New developments around the $\mu$CRL tool set

Stefan Blom$^a$ Jan Friso Groote$^{b,a}$ Izak van Langevelde$^a$
Bert Lisser$^a$ Jaco van de Pol$^{a,1}$

$^a$ Centrum voor Wiskunde en Informatica, Dept. of Software Engineering
Amsterdam, The Netherlands

$^b$ Eindhoven University of Technology, Computer Science Group
Eindhoven, The Netherlands

Abstract
Some recent developments in the $\mu$CRL tool set are presented. New analysis techniques are a symbolic model checker, and a visualizer for huge state spaces. Also various transformations are presented. At symbolic level, theorem proving, data flow analysis, and confluence checking are used to obtain considerable state space reductions. At the concrete level, distributed implementations of state space generation and minimization are recent. We mention the successful application of the tools to the verification of large data-intensive distributed systems.

1 Introduction

The $\mu$CRL specification language [12] is used to specify data-intensive protocols and distributed systems. System behavior is described in process algebra, using non-determinism, sequential and parallel composition, synchronous communication, hiding and recursion. Data structures are modeled using abstract data types. The data is linked to the processes by means of parameterized actions and recursion, conditionals, and a choice operator.

The $\mu$CRL tool set$^2$ [3] can be used to generate the state space as a labeled transition system (LTS) of a $\mu$CRL system specification. In order to combat the state space explosion at the symbolic level, an intermediate format of linear process is introduced (Figure 1). This is a symbolic and concise description of the state space. Emphasizing new developments, we will review some transformation and analysis capabilities on linear processes (Section 2) and LTSs (Section 3). Some applications are mentioned in Section 4.

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1 Email: Jaco.van.de.Pol@cwi.nl
2 URL: http://www.cwi.nl/~mcrl

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2 Symbolic Manipulation and Analysis

The symbolic analysis and transformations are applied to linear processes. Linearization can be applied to any $\mu$CRL specification, although only a subset has been implemented. A linear process is a concise, symbolic description of a possibly infinite transition system. It consists of a vector of state parameters, and a number of summands. Each summand specifies a set of labeled transitions by means of a condition, an atomic action and a state update. A simulator is available that allows the user to step through the states, by choosing one of the enabled actions.

Data flow analysis. Various simplifications of linear processes have been implemented [9], e.g. the identification and substitution of constant parameters, and the elimination of unused parameters. These have proved to be widely applicable, and combining them results in an effective state space reduction. Recently, a tool has been developed to identify typical control flow parameters in a linear process, and to construct their control flow graph. These graphs can be visualized and animated via the simulator. The control flow is also the basis of a live-variable analysis. The state space is reduced by resetting temporarily unused parameters to a dummy value. Future developments will use control flows for automated invariant generation and confluence marking.

Invariants and Confluence. Owing to the linear process format, propositions like “$\phi$ is an invariant” can be expressed in terms of universally quantified data formulas. An automated theorem prover, based on extending BDDs with equations and interpreted functions, has been developed to solve such formulas. Invariants are useful for verifying desired properties, but they can also be used in combination with transformations, for instance to identify more constant state parameters, or to prove that certain summands are never enabled.
The theorem prover is also used to detect confluent summands [5], possibly using separately proven invariants. Confluent summands can be given priority, giving rise to enormous state space reductions in many cases, similar to partial-order reduction. This prioritization can be approximated at symbolic level, or applied during state space generation, using an on-the-fly algorithm for strongly connected components, to avoid problems with \( \tau \)-loops.

Symbolic Model Checking. Recently, a prototype symbolic model checker has been built [13]. It can be used to check first-order modal \( \mu \)-calculus properties on infinite state spaces, represented by linear processes. The model checking problem is reduced to boolean equation systems with parameterized recursion operators [10]. These are solved using the aforementioned extension of BDDs. In this approach, it is for instance possible to express and prove that an infinite-state merger process transforms increasing input streams to an increasing output stream.

3 Explicit State Space Minimization and Analysis

Within the \( \mu \)CRL toolset a library for “on-the-fly” state space exploration has been implemented. Using this library, we have implemented both single-threaded and distributed state space generation tools. This exploration library has also been used to implement the Open/Cæsar interface. Thus, we can use all Open/Cæsar based tools from the CADP toolset \(^3\) [7]. The confluence-based partial-order reduction will soon be moved from the single-threaded state space generation tool to this exploration library, allowing both the Open/Cæsar tools and distributed state space generation tools to benefit from partial order reduction.

State Space Minimization. Model checking requires a significant amount of memory. As distributed generation can easily generate state spaces which do not fit on a single machine, it is essential to first reduce large LTSs modulo an equivalence which preserves the desired properties. To allow the reduction of very big state spaces, distributed strong and branching bisimulation reduction tools have been written for the \( \mu \)CRL tool set [4]. As usual, the reduction tools are based on partition refinement. The refinement strategy is quite unusual though: instead of refining a single block of the current partition in each iteration, all blocks are refined in parallel. In theory this leads to bad performance, but in practice the single-threaded versions of the tools are quite competitive with the \texttt{bcg\_min} tool in the CADP toolset. The parallelism is of course also a major advantage for distributed implementations. Another unusual feature is that the distributed branching bisimulation reduction works on LTSs which contain \( \tau \)-cycles. The framework used by the tools also allows

\(^3\) URL: \url{http://www.inrialpes.fr/vasy/cadp/}
reduction modulo safety equivalence. Another direction for future work is the implementation of a distributed model checking tool in the style of XTL [7].

State Space Analysis. The LTS is generated in a format recognized by the CADP tool set. This makes the following analysis techniques available at the state space level: equivalence checking, explicit state model checking and visualization. Usually, visualization of state spaces is limited to small systems (< 1K nodes). However, by using modern visualization techniques, such as clustering, very large graphs of >1M nodes can be visualized as cone trees [8]. The resulting drawings can be analyzed for symmetries, and individual parts can be inspected by zooming in. Also, various properties of the state vector of a distributed system can be translated to highlighted regions in the visualization.

4 Applications

Since its conception, the development of the \( \mu \)CRL tool set has been inspired and stimulated by its application in industrial case studies. Conversely, these industrial applications have benefitted from the analytical strength offered by the tools, which more than once revealed fatal bugs in the systems under scrutiny.

An optimization of Transaction Capabilities Procedures [2], lent itself well for verification through \( \mu \)CRL. The dimensions of the state spaces generated were relatively small, involving no more than a few thousand transitions. The success of this case study resulted in several more case studies in the telecommunication industry [1].

A distributed lift system [11], presented more of a challenge for the tool set, in that it was only possible to analyse systems of up to 5 lifts (or 6 on a cluster of 8 machines). Despite of this restriction, the analysis through \( \mu \)CRL revealed bugs that lead to possibly dangerous situations.

The Splice coordination architecture [6] was a killer for the tool set. The architecture involves a number of applications which have access to a distributed data space, but the systems for which the state space could be generated were limited to those consisting of 2 relatively simple applications; anything more complex hit the limit of several millions of transitions. Recent experiments show that confluence reduction is very effective on such architectures.

A cache coherence protocol for Java [14] crashed into the same limits. However, this verification greatly benefits from the young development of distributed tools for \( \mu \)CRL which opened new horizons by generating explicit state spaces of more than 800 million transitions.

The control system of the Dutch railroad trajectory Woerden-Harmelen is waiting to be verified. At the moment, its complexity grossly surpasses the limits of the \( \mu \)CRL tool set. As these limits are pushed further and further, it is realistic to expect that one day this case study can be successfully tackled.
References


