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A Probability Model of Covering Key Trace during Captring Volatile Memory

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Abstract

In this paper, we give a clear description of the running memory aq ing tool on a target system, especially for possibility covering the key trace during capturing volatile memor of offender may still in the Some key t running memory after the scene of a crime and have critical role in and secur applications. However, some key trace, such as rootkits in the memory, memory occupied by their co onding cess will probably be covered/ reallocated during the procedure of obtaining evidence of ime. There overed ratio (lost data) should be evaluated and investigated after the forensic tools run. del the distribution of key trace exacted in the unallocated memory space, then form a formula to eva e rate of the key trace in which the te th corresponding process has just been killed. At last we give es to analyze the evidence coverage ratio which can be estimated by the new allocated memor

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Keywords: Computer forensics; k

memory caper r Microsoft Windows XP SP2

1. Introduction

Physical 1 ensice has gained a lot of attention over the past years [1], and various methods for c2 ing and examining the volatile storage of a target machine. Memory have been invest. analysi fic during incident response and more traditional forensic investigations. And, ven nation from the physical memory, such as extracting the content of windows we extra iseful n ck ard f vical memory[2], command lines in the DOSKEY[3] and registry information [4,5]. memory acquired methods, one is based on hardware, and the other is based on Ther The hardware method needs installing the related device before, which is not useful in the softwar

* Corresponding author. Tel.: +0-86-13515319380 *E-mail address*: lihengj@keylab.net real cases. Therefore, the software-based acquired memory tools are actually employed in digital investigation. However, when running the software-based acquired memory tools, uploading the related driver files would probability alter the content of target memory.

On one hand, the volatility memory changes as the OS is running in a natural way. On the other hand, the memory content is varied by the uploading drivers when capturing the target machine memory. During a live forensics-investigation one rule of best practice is to minimize the impact on the target system, and it is an unavoidable action. And this leads to the trustable of acquired memory, that is, whether the evidence information is changed or covered because of running a memory acquired process. At present, the main research in memory forensics is how to extract information from memory as mu possible, such as network connection state, registry and login password. However, the effect of nory captured tool on the system has not well been investigated yet. In [6], Aaron Waters discuss the tent of memory changes over time. On the basis of a comprehensive summarizing and reviewing of ting results, Wang proposed a novel Model of Computer Live Forensics [7], and firstly took credibin digital evidence as a starting point, the issue of credibility of live forensic is then put for ard for rtudv.

Memory analysis is a key element of digital forensics. Leveraging memory to rmir e state of the machine at the time of the incident is often critical to success. While the e h 1 signif nt research into memory forensics, to date there has not been research into co trac ing the ring capturing memory. The memory content of a process is not lost immediat killed. en the pro The acquired memory tool may cover or disturb this part of memory. In we estimate the cover s pa possibility for the loading acquired tool.

The remainder of the paper is organized as follows. In Section we briefly devibe the memory management process and give an overview of the most important data structures that are required for this task. Modeling the Covering key trace of evidence is proposed in section 3. And the probability computation algorithm of covering on key trace memory zone to so descript 1 in detail in this section. Experimental results and analysis is presented in section 4. We concurrent a summary of our work and indicate opportunities for future research in this area and prior 5.

2. Process and virtual address to physical address in

KPCR) Based on Kernel Processor Contra ang propose an effective process analysis legn method which uses a combination of anning ad list the ersing techniques[8]. Brendan Dolan-Gavitt proposed that the Virtual Address Des , can provide such an abstraction layer over the page directory and page tables the memory ranges allocated by a process as they might be describ seen by the process – as m files, load LLs, or privately allocated regions[9]. VADs are data structures linked to each **PROCES** Jock that the memory manager uses to keep track of JCE which virtual addresse have been yed for each process. Programs usually operate on virtual memory the respective physical data, the Memory Manager must regions only, there e, to manipul e (many virtual into physical addresses [10]. continuously trap

cal addresses procedure works as follows: At the hardware level, volatile p^{1} This virtual o units storage is organize ed pages. A common size of such pages is 4kB on x86-platform. To system implements a two-level approach: For every process, the operating atir reference the rectory that saves pointers (Page Directory Entries PDEs 4 bytes each, syster a pa aint ing ter and several flags) to 1024 links (Page Table Entries, 4 bytes each) to the COL corre the main memory. Thus, in order to translate a virtual to a physical address, the memory ager first needs to recover the base address of the page directory. It is stored in the CR3 processor and is reloaded from the KPROCESS block of the process at every context registry of switch. The hast 10 bits of the virtual address can then be used as an index into the page directory to retrieve the desired PDE. With the help of the PDE and the page table index, i.e, the subsequent 10 bits of the virtual address, the page table and PTE in question are identified in the next step. To find the

approximate page and data in RAM eventually, the PTE and the 12-bit byte index of the virtual address are parsed.

The Virtual Address Descriptor tree is used by the Windows memory manager to describe memory ranges used by a process as they are allocated. When a process allocates memory with VirutalAlloc, the memory manager creates an entry in the VAD tree. A node in the tree is associated with a pool tag and is of type _MMVD_SHORT("VadS"), _MMVAD("Vad"), or _MMVAD_LONG("VadL"). The latter two store a pointer to a _Control_Area structure that, in turn, points to a _File_Object that holds the unique file name. Consequently, the entire list of loaded modules can be retrieved by traversing the VAD tree from top to bottom and following the corresponding _Control_Area and _File_Object references.

3. Model and Evaluation the Covering key trace of evidence

3.1. Model and Evaluation the influence of the key trace caused by acquiring memory to

When performing memory analysis, there are two primary components; a rnel emory and b)userland memory. In a Windows environment, kernel memory is comprised, evice d harn rs. the NT operating system executable, and HAL.DLL. The majority of userlap lemory is pr/ sed of ain code individual process, and the process' address primarily consists of files log m disk that or data needed for process execution, typically Portable Executables (E) and Dynamic Link Libraries(DLLs). Most of the virtual address in kernel memory is global regardless he process context, whereas the other virtual address space is generally specific to e cular process



Fig.1 / ct on the sector system memory while running acquiring tool

Techniques used in the sics will mevitably change the system under investigation because they must be conduct s on the system. And as such any findings may be problematic as by running to collected evidence included the probability of covering key evidence at court. ects of live fore. ol kit d affecting region in digital evidence. As showed in Fig.1, with the running trace by forensi the unallocated memory will be allocated for the new process. And, some acquiring tools. ne ansferr memory data may nto the pagefile.sys.

simply a matter of identifying the EPROCESS structure for the process of e VA ot member (0x11c in all versions of XP), and then following each link to the the Va inte locat l rig¹ until the entire tree is traversed. All addresses are virtual, so the page directory for lei as also muded in order to successfully read the tree. For affecting region, it can be obtained by the p ical memory occupied by forensic tool through its VadRoot. The probability of covering key finding sulated by probability statistics, which can be inferred in the following section as follows. trace can be (1) Supposed the key traces distributed uniformly in the unallocated memory pages. The number of

the unallocated memory page is m, and the symbol of these pages noted as $\Omega_m = \{B_1, B_2, \dots, B_m\}$.

(2) The pages of the unallocated memory increased as running the forensic tools. After the memory acquired tool, supposed the number of novel unallocated page is n (of course, n<m). We noted these pages as $\Omega_n = \{B'_1, B'_2, \dots, B'_n\}$ ($\Omega_n \subset \Omega_m$). Then, we can simply treat the $\Omega_r = \Omega_m - \Omega_n$ as the forensics tools occupied pages. And we noted the number of the containing the key traces memory pages as *e*, of course $\Omega_e \subseteq \Omega_r$.

(3) Suppose the event $A'_x =$ "the key trace contained in Ω_r ". Therefore, it can be inferred as $P(A'_x) = \frac{e}{m-n}$. This formulation approximate the key trace probability of the arbitrarily memory formulation.

contained in Ω_n . That is,

$$P(A_x) = P(A'_x) = \frac{e}{e+m-n}$$

(4) The probability of covering the key traces in unallocated memory can be some

$$p(\underbrace{i}_{i=1}^{n} A_{i}) = \sum_{i=1}^{n} R(A_{i}) - \sum_{1 \leq i_{1} < i_{2} \leq n} R(A_{i_{1}}A_{i_{2}}) + \dots + (-1)^{k-1} \sum_{1 \leq i_{1} < i_{2} < i_{k} \leq n} R(A_{i_{1}}A_{i_{2}}) + \dots + (-1)^{k-1} \sum_{1 \leq i_{1} < i_{2} < i_{k} \leq n} R(A_{i_{1}}A_{i_{2}}) + \dots + (-1)^{k-1} \sum_{1 \leq i_{1} < i_{2} < i_{k} \leq n} R(A_{i_{1}}A_{i_{2}}) + \dots + (-1)^{k-1} \sum_{1 \leq i_{1} < i_{2} < i_{k} \leq n} R(A_{i_{1}}A_{i_{2}}) + \dots + (-1)^{k-1} \sum_{1 \leq i_{1} < i_{2} < i_{k} \leq n} R(A_{i_{1}}A_{i_{2}}) + \dots + (-1)^{k-1} \sum_{1 \leq i_{1} < i_{2} < i_{k} \leq n} R(A_{i_{1}}A_{i_{2}}) + \dots + (-1)^{k-1} \sum_{1 \leq i_{1} < i_{2} < i_{k} \leq n} R(A_{i_{1}}A_{i_{2}}) + \dots + (-1)^{k-1} \sum_{1 \leq i_{1} < i_{2} < i_{k} \leq n} R(A_{i_{1}}A_{i_{2}}) + \dots + (-1)^{k-1} \sum_{1 \leq i_{1} < i_{2} < i_{k} \leq n} R(A_{i_{1}}A_{i_{2}}) + \dots + (-1)^{k-1} \sum_{1 \leq i_{1} < i_{2} < i_{k} \leq n} R(A_{i_{1}}A_{i_{2}}) + \dots + (-1)^{k-1} \sum_{1 \leq i_{1} < i_{2} < i_{k} \leq n} R(A_{i_{1}}A_{i_{1}}A_{i_{2}}) + \dots + (-1)^{k-1} \sum_{1 \leq i_{1} < i_{2} < i_{k} < n} R(A_{i_{1}}A_{i_{1}}A_{i_{2}}) + \dots + (-1)^{k-1} \sum_{1 \leq i_{1} < i_{2} < i_{k} < n} R(A_{i_{1}}A_{i_{1}}A_{i_{1}}A_{i_{1}}A_{i_{2}}) + \dots + (-1)^{k-1} \sum_{1 \leq i_{1} < i_{2} < i_{k} < n} R(A_{i_{1}}A_{i_{1}}A_{i_{1}}A_{i_{1}}A_{i_{1}}A_{i_{1}}A_{i_{1}}A_{i_{1}}A_{i_{1}}A_{i_{1}}A_{i_{1}}A_{i_{1}}A_{i_{1} < n} + \dots + (-1)^{k-1} \sum_{i_{1} < i_{1} < i_{1}$$

 $P(A_x) = \frac{e}{m-n}$ is replaced with above formula and it can be simpled as :

$$P(\bigcup_{i=1}^{n} A_{i}) = \sum_{i=1}^{n} (-1)^{i-1} C_{n}^{i} (\frac{e}{m-n}) \left[1 - (1 - \frac{1}{m-n})^{n}\right]$$
(3)

bability

3.2. The flow char of the computing the key trace co

irtual address p	hysical addre	ss size, V	Virtu ad .ss	nysical addr	ess size
0.470000	0vf109000 0v	-10	1000	0x92e6000	0x1000
0.0710000	0		2000	0x9367000	0x1000
0%371000	UXE IUSUUU		0006 £0	0x1e3a8000	0x1000
0x372000	OxtCa7000	10	3f0000	0x18ebd000	0x1000
0x373000	OxfOa ^c 0a		0x3f1000	0x92be000	0x1000
0x400000	0 10 0	.100.	0x400000	0x17b12000	0x1000
0x401000	1 01	:1000	0x401000	0xa4a2000	0x1000
0×402000	0xf174 0x	:1000	0x402000	0x11663000	0x1000
0x402	0xf175000	1000	0x403000	0x13de4000	0x1000
P (0)	0vf176000		0x404000	0x548c000	0x1000
405000		1000	0x405000	0x648d000	0x1000
400000	0.40110000 03	1000	0x40a000	0xa5a5000	0x1000
	UXEUM US	(1000	0x40b000	0x1fa26000	0x1000
U XXX	0 300 05	:1000	0x40c000	0x1b610000	0x1000
0x4080c	_0ec000 0x	:1000	0x40e000	0x9fd1000	0x1000
0x409000	0xf0ed000 0x	:1000	0x40f000	0x1da52000	0x1000
	of memory ma	p of the pi	ocess "hedef100.e	exe" and "user	_load.ex

The tree describes memory ranges is used by a process and enables reconstruction of a process virtue address space. A node in this tree can have a number of different pool tags, depending on the type of Virtual Address Descriptor. Common tags are VadS, Vad and VadL. The latter two objects contain pointers to Control Areas, which are described below. This object contains, among other things,

the file size of the mapped file. Fig. 2 illustrates the virtual address, physical address and the page size. In each process, An essential part of the operating system on the suspect machine and distinguishing legitimate components from suspicious and potentially malicious applications.



Fig.3 The flow char of the computing the key trace covering probability

During the process, we employ volatility framework 2.0 to analyze the memory[12] 2.3 ilh the flow chart of the computing the key trace covering probability. Firstly, we capture whole men of the target system. Later, we search the rootkit process, and find the Pid. Accord the , we fin addres out the virtual address using VAD tools. Then, we translate the virtual address into nd emp¹ record them. At last, we compare the physical address employed in the root √ith th d in the memory acquired tool. In the real running system, there are some r ts, such as 0.exe. Usually, we kill the process using icesword tool, and capture the system ce again. ALC:

4. Experiments and results

In order to make clear which part of memory content has b ing the evidence acquired changed process. And which part of memory content would be covered ause of lo ing the memory acquired tool, we make some experiments using the following Environ Tb periments are performed o5536 on a Windows 7 host. using a Windows XP Service Pack 2 VMware® W on 7.1.4 The host OS is Windows 7 Ultimate, 32-bit (Build 00, 600, with 3G RAM. The live response toolkit used in our experiments is the user load.exe, ch bed by ourselves and based on the absolute driver file (MemDump.sys).

The comparing files function of the Vienes pusing panalysis the impact on progress and drive of the key traces, by comparing the front of back pemory to ge files of running forensics tools. A txt file will be generated if they are difference this progress and drive place to be finder to be forensics tools have no effect on the progress and drive place to be finder (nxdef100.exe).

The size of the Hxdef 292KB on disk. However, the pages of hxdef100.exe exe occup ailablepages command using Windbg to find the basic occupied 136 in memory g x nt! Mh pages, moreover operating MemoryAnalyzer to obtain the offset addresses of the unallocated met address. Finally, get physical a. s of the undistributed memory pages by adding the above two addresses. We m different experiments using different rootkits, such as hack defender. Running some ared Dlls, and this Dlls memory is shared. Here, we only consider the part rookits also ne of .exe memory.

xdeflexe m n $m-n$ $P(A_i)$ 1360x015ad2(88786)0x0159ef(88559)2270.5991				ab. I the probability of	of covering key trace		
136 0x015ad2(88786) 0x0159ef(88559) 227 0.5991	vdef1	ovo		т	п	m-n	$P(A_i)$
		CAC	136	0x015ad2(88786)	0x0159ef(88559)	227	0.5991

Before equiring the system memory, the unallocated pages are 88786, and after acquiring the memory, the mallocated pages are 88559. Therefore, the new allocated memory page is 88786-88559=227. The pages of the Hxdef100 process occupied 136. In theory, the probability of covering key trace is 0.5991. From formula (3) in section 3.2, we can compute the covering probability 1-(1-0.5991)^136 is nearly to 1. That is, the integrity of the process hxdef100.exe is destroyed.

be

of

From the process physical memory, about 49 in 134 pages are the same, that is, 49 pages are reused in the process of the memory acquired tool. It can be noted that the hxdef100.exe contains 136pages, but 134 pages can be located in the physical memory. The results of Tab.1 show that part of the key trace is covered by loading the memory acquired tool. That is, the integrality of the original data using in the malware possibility in the memory can be destroyed. Some of physical memory employed in the original process may be allocated for running the new process.

5. Conclusions and future work

When the process has just been killed, not all the physical memory related the process we allocated for other process. In this paper, we analyze the allocated physical memory of the trace. Then, we kill the process and count the covering pages. Considering the relation betweer usage memory and model of the key trace, we computed the covering probability in the capture emory. of the data in process can not be very important in court. Therefore, we need further to vestig/ that t re th key data in the process would be covered or not in detail. In the future work, we will roblem.

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References

[8]

Intern

[1] Stefan V, Felix C. Freiling, A survey of main memory uism lysis techniques for the windows operating system, digital investigation 8(2011)3-22.

[2] James Okolica, Gilbert L. Peterson, Ex e wind clipboard from physical memory, digital investigation 8(2011)S118-S124

vs co [3] Stevens R, Casey E. Extracting W Is from Physical memory. In: Proceedings of the 2010 Det digital forensic research Workshop (DF

[4] Brendan Dolan-Gavitt, For Windows registry in memory, Digital Investigation 5 (2008)S26-S32 analysis c

[5] Shuhui Z, Lianhai W, tracting win registry information from physical memory, 2011 3rd International Conference on Computer Resea pment (ICCRD), Issue Date: 11-13 March 2011 On page(s): 85 - 89. n and

[6] Walters A, Pet Jr NL. Volat integrating volatile memory forensics into the digital investigation process. In: Black Hat DC 2007;

uicha [7] Lianhai Shuhui Z, A Model of Computer Live Forensics Based on Physical Memory Analysis. The 1st formatic sience and Engineering, pp. 4647-4649 International Confer

W. 2 di Z, Windows Memory Analysis Based on KPCR, ias, vol. 2, pp.677-680, 2009 Fifth Z, Ľ dion Assurance and Security, 2009 ence on al Ci

Brend Dolan-Gavit, The VAD tree: A process-eye view of physical memory, *digital investigation* 4(2007)S62-S64 K W, van A.R. Ballegooij, Forensic memory analysis: Files mapped in memory, digital investigation Γ **4**7. 5(2008)

cich ME, Solomon DA, Ionescu A. Microsoft windows internals. 5th ed. Microsoft Press; June 2009. [11] Ru

[12] The Volatility Framework: Volatile artifact extraction utility memory framework www.volatilesystems.com/default/volatilty,