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Energy Procedia 15 (2012) 30 - 39



International Conference on Materials for Advanced Technologies 2011, Symposium O

Advances in the Surface Passivation of Silicon Solar Cells

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Abstract

The surface passivation properties of aluminium oxide (Al₂O₃) on crystalline Si are compared with the traditional passivation system of silicon nitride (SiN_x). It is shown that Al₂O₃ has fundamental advantages over SiN_x when applied to the rear of *p*-type silicon solar cells as well as to the p^+ emitter of *n*-type silicon solar cells. Special emphasis is paid to the transfer of Al₂O₃ into industrial solar cell production. We compare different Al₂O₃ deposition techniques suitable for mass production such as ultrafast spatial atomic layer deposition, inline plasma-enhanced chemical vapour deposition and reactive sputtering. Finally, we review the most recent cell results with Al₂O₃ passivation and give a brief outlook on the future prospects of Al₂O₃ in silicon solar cell production.

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Keywords: Silicon; solar cells; surface passivation; aluminium oxide

1. Introduction

The most important advancement in the area of surface passivation of silicon solar cells in recent years was the re-introduction of aluminium oxide (Al_2O_3) [1-3]. As its properties are fundamentally different from that of other dielectric materials frequently used for the surface passivation of silicon solar cells – such as silicon nitride (SiN_x) or silicon dioxide $(SiO_2) - Al_2O_3$ fills a hitherto open gap in silicon-based photovoltaic technology. One major difference to most other dielectric passivation layers applied so far in photovoltaics – which usually contain a fixed positive charge density – is that Al_2O_3 contains a high density of fixed negative charges [1]. As a consequence, it shows a negligible injection level dependence

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of the effective surface recombination velocity on *p*-type silicon [3] and it also prevents any parasitic shunting at the rear of locally contacted and passivated *p*-type silicon solar cells [4]. The negative fixed charge density in combination with a relatively low interface state density also provides the best passivation achieved to date on boron-diffused p^+ -emitters as well as screen-printed Al- p^+ emitters for *n*type silicon solar cells [5, 6]. Hence, today the surface passivation of silicon solar cells by Al₂O₃ is considered a key technology in future industrial high-efficiency solar cell production.

The first study of the surface passivation properties of Al_2O_3 on silicon was in fact already published in 1989 by Hezel and Jaeger [1]. They used the relatively simple deposition technique of atmospheric pressure chemical vapour deposition (APCVD) and demonstrated a very good level of surface passivation on *p*-type silicon. The reported fixed negative charge density of $Q_f = -3 \times 10^{12} \text{ cm}^{-2}$ lies within the range of typical $Q_{\rm f}$ values of present-day well-passivating Al₂O₃ layers [7]. Hezel and Jaeger did not, however, measure the effective surface recombination velocity $S_{\rm eff}$, which is a figure of merit that includes the effects of chemical as well as field-effect passivation. Instead, they reported the surface recombination velocity parameter S_0 , which only includes the chemical interface passivation, to be at 210 cm/s. Considering the high negative Qf value of the films we conjecture that Hezel and Jaeger already achieved $S_{\rm eff}$ values well below 100 cm/s on 2 Ω cm p-type silicon. In the following two decades this important result was however buried in oblivion until in 2006 Al₂O₃ was re-discovered as dielectric passivation layer for silicon solar cells [2, 3]. Agostinelli et al. [2] reported that thermal atomic-layer-deposited (ALD) Al₂O₃ layers provide effective surface recombination velocities $S_{\rm eff}$ below 100 cm/s on 2 Ω cm ptype Cz-Si. Later the same year Hoex et al. [3] reported that plasma-assisted atomic-layer-deposited (PA-ALD) Al₂O₃ is capable of providing an S_{eff} value of 13 cm/s on 2 Ω cm *p*-type FZ-Si and S_{eff} = 2 cm/s on 2 Ω cm *n*-type Cz-Si. It should be noted that their $S_{\text{eff}}(\Delta n)$ measurements showed a very weak injection dependence for the *p*-type wafers and a more pronounced dependence for the *n*-type wafers – which is, as shown in later studies, a fundamental property of the silicon surface passivation by negative-chargedielectrics [8]. In the following year, the important finding was published that Al_2O_3 deposited by PA-ALD provides a hitherto unreached and stable level of surface passivation on boron-diffused p^+ emitters, where emitter saturation current densities J_{0c} in the order of 10-30 fA/cm² were achieved [5]. This was a major advancement compared to previously used passivation layers, such as thermally grown SiO₂, which is typically not long-term stable on boron-diffused p^+ emitters [9], and plasma-enhanced chemical vapour deposited (PECVD) SiN_v, which has problems concerning the passivation of highly doped p-type surfaces [10].

In this contribution, we focus on the fundamental differences between the silicon surface passivation by Al_2O_3 and the more traditional passivation layer SiN_x . Special emphasis is also paid to the transfer of Al_2O_3 into industrial solar cell production. This is a crucial point, as the conventional ALD deposition systems are not compatible with the high throughput required in solar cell production. Hence, we compare different Al_2O_3 deposition techniques suitable for mass production such as high-rate spatial ALD, PECVD and sputtering. Finally, we will summarise the latest results concerning lab-type small-area silicon solar cells as well as industrial-type large-area cells.

2. Negatively charged vs. positively charged dielectrics: Al₂O₃ vs. SiN_x

The surface passivation properties of dielectric layers depend crucially on the fixed charge density Q_f in the dielectric layer or at the interface, which will be discussed in more detail below. The most frequently used dielectric in photovoltaics is SiN_x deposited by PECVD. During deposition onto a silicon wafer, electrons are transferred from the growing SiN_x layer into the energetically favourable silicon

wafer. The positive charges extend about 20 nm into the SiN_r [11]. As a consequence, the field-effect passivation level provided by the fixed positive charges within the SiN_x increases until a SiN_x layer thickness of about 20 nm is reached. Note that an introduction of a SiO_2 layer between the Si wafer and the SiN_x layer hampers the charge transfer from the SiN_x into the Si and completely eliminates the charge transfer above a crucial thickness of about 5 nm. The SiO_2 layer itself also contains positive charges, however, they are typically well below the Q_f values detected in single SiN_x layers [12]. Deposition of Al_2O_3 leads already during the growth of the first nanometer to the formation of a large amount of negative charges. For Al₂O₃ on Si it has been shown that the charges are located extremely close to the interface [13, 14]. The nature of the charged defects within the two different dielectric layers seems to be fundamentally different. In the case of SiN_x the charged defects are homogeneously distributed throughout the bulk of the layer. The positively charged defects are microscopically assigned to silicon dangling bonds which are back-bonded to three nitrogen atoms, the so-called K^+ centres [15, 16]. In the case of Al₂O₃ on Si, the microscopic origin of the negative charges is still under discussion. An ultrathin SiO_x layer (up to ~ 1 nm) has been detected in some studies, which was conjectured to play a crucial role in the formation of the negative $O_{\rm f}$ [7]. In our samples, however, the SiO_x interfacial layer was found to be only a few monolayers thick and we detected instead a highly non-stoichiometric very oxygen-rich AlO_x within the first nanometer to the interface [14]. As Al vacancies and O interstitials are known to be negatively charged in aluminium oxide [17], we suggest that this non-stoichiometric AIO_x layer is the reason for the large negative $Q_{\rm f}$ detected close to the Si interface [14].



Fig. 1. Measured effective surface recombination velocity (SRV) S_{eff} as a function of the Corona charge density Q_{C} deposited onto PECVD-SiN_x and PA-ALD-Al₂O₃-coated 1- Ω cm *p*-type FZ Si wafer. The lines are guides to the eye.

Figure 1 shows a measurement of the effective surface recombination velocity (SRV) S_{eff} as a function of the Corona charge density Q_{C} deposited onto dielectric passivation layers of SiN_x and Al₂O₃. In the flatband case when $Q_{\text{C}} = -Q_{\text{f}}$, the recombination rate and hence $S_{\text{eff}}(Q_{\text{C}})$ shows a maximum. As has been shown by Dauwe *et al.* [18], a hypothetical asymmetry in the capture cross sections for holes and electrons of the interface states has a negligible influence on the peak position. From the peak position the fixed charge density Q_{f} within the dielectric layer can hence be directly deduced. Figure 1 shows typical Corona-lifetime measurements for PECVD-SiN_x and PA-ALD-Al₂O₃ layers deposited onto 1- Ω cm *p*-type FZ Si wafers, where the deposited amount of charges has been determined via successive Kelvin probe measurements. The S_{eff} values were deduced from the effective lifetimes, which were measured by the quasi-steady-state photoconductance (QSSPC) technique [18]. As can be seen from Fig. 1, the fixed charge density within the SiN_x layer is positive and amounts to $Q_{\text{f}} = +2 \times 10^{12}$ elementary charges/cm², whereas the fixed charge density within the Al₂O₃ layer is negative and amounts to $Q_{\text{f}} = -4 \times 10^{12} \text{ cm}^{-2}$. These are typical values for the respective dielectric passivation layers on silicon. The fixed negative charge density within the Al₂O₃ layer induces an accumulation layer at the *p*-type silicon surface in contrast to the SiN_x layer inducing and inversion layer. As will be shown below, this has a significant impact on the $S_{\text{eff}}(\Delta n)$ dependence provided by the two different dielectric materials, which is much weaker on *p*-Si in the case of Al₂O₃ passivation compared to SiN_x passivation (see Fig. 2). In addition, due to the formation of an accumulation layer instead of an inversion layer at the *p*-type silicon surface, the above-mentioned parasitic shunting effect at the solar cell rear does not occur for an Al₂O₃-rearpassivated cell [4]. Al₂O₃ is hence an optimal choice for the rear passivation of *p*-type silicon solar cells.



Fig. 2. Measured (symbols) and calculated (lines) $S_{\text{eff}}(\Delta n)$ dependence of PECVD-SiN_x and PA-ALD-Al₂O₃passivated (a) *p*-type silicon and (b) *n*-type silicon. The pronounced injection-level dependence of S_{eff} at low Δn for SiN_x in *p*-type silicon and Al₂O₃ in *n*-type silicon is assigned to an enhanced recombination in the space charge region (SCR).

In order to model the $S_{\text{eff}}(\Delta n)$ dependence at the Si/SiO₂ interface, the so-called Girisch model [19] has been successfully applied in the past [12]. The only recombination channel considered in this model is recombination via interface states. The model is based on the Shockley-Read-Hall (SRH) equation and includes the effect of band bending in the silicon towards the interface due to the presence of charges in the SiO_2 and at the interface. In the case of SiN_{x_2} where the band bending is much more pronounced compared to SiO₂ due to the more than one order of magnitude higher fixed positive charge density Q_{f_0} it turned out that a modelling of the $S_{\text{eff}}(\Delta n)$ dependence is difficult using the simple Girisch approach. Due to the high $Q_{\rm f}$ of around $+2 \times 10^{12}$ elementary charges/cm² the Girisch model results in a negligible $S_{\rm eff}(\Delta n)$ dependence (dashed line in Fig. 2(a)). The measurements on p-type silicon, however, typically show an increasing S_{eff} with decreasing Δn [16] (triangles up in Fig. 2(a)), a problematic behaviour for solar cell applications, as solar cells operate under various illumination conditions. It turned out, however, that assuming a surface-damaged region (SDR) of reduced lifetime in the silicon close to the interface (~100 nm) explains the observed behaviour. Due to the positive Q_f in the SiN_x layer, the SDR lies directly within the space charge region (SCR) and hence produces a significant contribution to the total recombination [20, 21]. The pronounced injection-level dependence of $S_{\rm eff}$ measured on SiN_x-passivated *p*-type silicon can hence mainly be attributed to recombination in the SCR (see dash-dotted line in Fig. 2(a)). The lines in Fig. 2 show calculated $S_{\text{eff}}(\Delta n)$ curves using the Girisch model and adding a SRH recombination channel within the SCR [20, 21]. More recently, Steingrube et al. [22] refined the model and were thereby able to consistently model the $S_{\text{eff}}(\Delta n)$ dependence of a broad variety of SiN_x-passivated p- and n-type silicon wafers of different resistivities. Importantly, as SiN_x produces no inversion layer, but an accumulation layer on *n*-type silicon, virtually no injection dependence is measured on *n*-type silicon, which is also in excellent agreement with the assumption of the presence of an SDR (triangles up in Fig.

2(b)). The physical cause of the SDR is still under discussion. Possible origins include hydrogen-induced recombination centres, as PECVD-SiN_x contains large amounts of hydrogen, or stress-induced defects due to the different thermal expansion coefficients of SiN_x and Si. Interestingly, we were able to show that the modelling of the $S_{eff}(\Delta n)$ dependence on Al₂O₃-passivated silicon wafers also requires the assumption of an SDR, although this SDR seems to be less deep than in the case of SiN_x [8]. The negative Q_{f} in the Al₂O₃ layer, however, reverses the $S_{eff}(\Delta n)$ behaviour concerning the conductance type. As now an accumulation layer is formed on *p*-type silicon, a negligible $S_{eff}(\Delta n)$ dependence is observed at the Al₂O₃/*p*-Si interface and a more pronounced $S_{eff}(\Delta n)$ dependence is observed at the Al₂O₃/*n*-Si interface (triangles down in Figs. 2 (a) and (b)). Concerning the absolute S_{eff} values, however, we observe lower S_{eff} values using ALD-Al₂O₃ compared to PECVD-SiN_x on *p*- as well as on *n*-type Si (see Figs. 2 (a) and (b)).



Fig. 3. Measured emitter saturation current densities J_{0e} as a function of the sheet resistance of boron-diffused p^+ emitters. SiN_x data were taken from Ref. [23]. The lines are guides to the eye.

It has been shown that Al_2O_3 also provides an excellent and stable level of surface passivation on boron-diffused p^+ -emitters [5]. Figure 3 shows a comparison of measured emitter saturation current densities J_{0e} of boron-diffused p^+ emitters of varying sheet resistances passivated by PA-ALD and thermal ALD-Al₂O₃ and in comparison with PECVD-SiN_x (SiN_x data were taken from Ref. [23]). Independent of the particular ALD technique used, very low J_{0e} values of ~20 fA/cm² (corresponding to a limiting open-circuit voltage of $V_{\text{oc.limit}} = 740 \text{ mV}$) are obtained for a sheet resistance of around 90 Ω/sq . In the same sheet resistance range, the J_{0e} of the SiN_x-passivated emitter amounts to 900 fA/cm² (corresponding to a limiting V_{oc} of only 640 mV). It has to be mentioned that optimised high-temperature treatments and special cleaning sequences to improve the SiN_x passivation quality on p^+ -emitters have recently been successfully developed [24]. It is a common misbelief that the high positive $O_{\rm f}$ within the SiN_x layer explains that SiN_x passivates phosphorus-diffused n^+ emitters quite well, but boron-diffused p^+ emitters to a significantly lesser extent. From simulation studies it has been shown that the high positive $Q_{\rm f}$ in the SiN_x layer is not sufficient to explain the poor surface passivation of p^+ emitters [9]. The main cause for the unsatisfactory passivation quality of PECVD-SiN_x on boron-diffused p^+ emitters is in fact most likely the highly asymmetric capture cross sections for electrons and holes of the interface states [9]. From this it can be concluded that the fact that Al_2O_3 passivates p^+ emitters extremely well cannot be explained by the high negative $Q_{\rm f}$ alone either, but additionally requires favourable capture cross section values of the Al₂O₃/Si interface states.

3. Industrially suitable Al₂O₃ deposition techniques

In a conventional ALD process, the separation of the two half-reactions is implemented by an alternate dosing of the process gases, which are usually trimethyl aluminium (TMA) and H₂O for thermal ALD or instead of H₂O a remote oxygen plasma for PA-ALD. Exposure times of only a few milliseconds are sufficient to ensure complete saturation of the growth surface. In between both precursor doses, however, the reactor chamber is purged with an inert gas and subsequently pumped to remove the residual process gas and reaction products. To prevent parasitic CVD processes and to ensure a true ALD process, pumping times of the order of a few seconds are required, severely limiting the growth rate to approximately 2 nm/min. This makes conventional ALD unsuitable for high-throughput industrial solar cell production. Recently, Poodt et al. [25] proposed a high-rate fast ALD concept based on spatially separated ALD ('spatial ALD'), enabling high deposition rates of 70 nm/min. In contrast to the conventional sequential separation, both half-reactions are *spatially* separated, thus eliminating the need for intermediate pumping steps. In a first proof-of-principle tool developed at TNO [25], the spatial separation was achieved by rotating the wafer underneath a round reactor head incorporating gas inlets for TMA and water vapour, separated by gas bearing planes formed by a flow of pressurised nitrogen. Since both reaction zones are sealed off by nitrogen flow, any unintentional interaction of the process gases is prevented and the deposition can be performed under atmospheric conditions, an additional advantage concerning the industrial applicability. We have recently demonstrated that using Al_2O_3 deposited by high-rate spatial ALD the same level of excellent surface passivation as obtained by conventional thermal ALD is achieved on p- and n-type silicon wafers [26]. As can be seen from Fig. 4 (a), S_{eff} values below 10 cm/s are achieved on 1- Ω cm *p*-type silicon over a broad injection range between 10¹³ and 10¹⁵ cm⁻³ by high-rate spatial ALD. High-throughput (up to 3,000 wafers per hour) reactors based on the spatial ALD approach are currently under development at two different companies, namely SoLayTec and Levitech, and will be commercially available in the near future with the extra option of an additional gas bearing at the wafer back side, thus enabling double-floating wafer transport in a reciprocating manner or in a single direction.



Fig. 4. Effective lifetime τ_{eff} and corresponding effective SRV S_{eff} as a function of the excess carrier concentration Δn measured on 1- Ω cm *p*-type FZ-Si (a) before and (b) after firing in an industrial conveyor-belt furnace (data taken from Ref. [30]).

Recently, two other techniques were demonstrated to be suitable for depositing surface-passivating Al_2O_3 layers. Plasma-enhanced chemical vapour deposition (PECVD) [27, 28] was shown to provide SRVs of 10 cm/s on 1- Ω cm *p*-type FZ-Si [28], whereas reactive sputtering [29] on comparable material

resulted in SRVs down to 55 cm/s. In a recent contribution [30], the passivation quality of Al₂O₃ deposited by inline-PECVD (Roth&Rau, SiNA) and by rf magnetron sputtering (homemade setup at ANU) was studied. The sputtering uses an aluminium target, which is reactively sputtered in an O₂/Ar atmosphere [29], while the PECVD uses TMA and nitrous oxide as process gases. Figure 4(a) compares the effective lifetimes measured on 1- Ω cm *p*-type FZ-Si wafers passivated by Al₂O₃ films deposited using the three, in our opinion, most promising industrial Al₂O₃ deposition techniques: (i) high-rate spatial ALD, (ii) PECVD, and (iii) rf magnetron sputtering. The direct lifetime comparison in Fig. 4(a) shows that both spatial ALD and PECVD provide *S*_{eff} values < 10 cm/s, clearly outperforming the sputtered Al₂O₃ passivation layer results in *S*_{eff} values between 35 and 70 cm/s in the relevant injection range, which would be still sufficient for the next generation of industrial high-efficiency solar cells.

A very important property concerning the industrial applicability of Al₂O₃ is the stability of the surface passivation during firing. Hence, we have annealed the lifetime samples of Fig. 4(a) in an industrial infrared conveyor-belt furnace at a measured peak temperature of ~800°C for a few seconds. Figure 4(b) shows the injection-dependent lifetimes and corresponding surface recombination velocities measured after firing. The Al₂O₃ deposited by high-rate spatial ALD shows clearly the best firing stability, providing SRVs of ~20 cm/s after firing over the entire relevant injection range. The Al₂O₃ layer deposited by inline-PECVD also results in a good passivation quality after firing, providing S_{eff} values between 30 and 80 cm/s in the relevant injection range. The sputtered Al₂O₃ shows the strongest increase in the surface recombination after firing, leading to SRVs between 300 and 800 cm/s. Obviously, the sputtered Al₂O₃ needs further optimisation, while the PECVD-Al₂O₃ and in particular the spatial ALD-Al₂O₃ layers can directly be implemented into an industrial-type screen-printing solar cell process.

As we have demonstrated recently, the firing stability of the Al₂O₃ layers can be further improved by adding a hydrogen-rich PECVD-SiN_x capping layer [31, 32]. Using such Al₂O₃/SiN_x stacks we have in fact achieved effective SRVs of ≤ 10 cm/s after firing for Al₂O₃ layers deposited by PA-ALD as well as by thermal ALD. An excellent firing stability of Al₂O₃/SiN_x stacks was also reported by other researchers [33, 34]. Al₂O₃/SiN_x stacks are hence perfectly suited for the surface passivation of industrial screen-printed silicon solar cells.

4. Application to solar cells

High cell efficiencies (> 20%) using Al₂O₃ surface passivation had first been realized on *p*-type [4] as well as *n*-type silicon [35] wafers only briefly after the very promising first lifetime results had been published [2, 3, 5]. Table 1 shows a summary of the currently highest efficiencies achieved on *p*-type silicon wafers using a PERC cell structure, where the rear is passivated by Al₂O₃ deposited by different deposition techniques such as ALD, PECVD and sputtering. All cell parameters listed in Table 1 were independently confirmed. The efficiencies achieved on 4 cm² lab-type cells using ALD [36] and PECVD [28] are practically identical within the measurement uncertainty (cells P1 and P2 in Table 1). For the best cell with 21.7% efficiency [36] we have implemented an additional contact passivation underneath the front Al grid. This contact passivation is realised by a PA-ALD-AlO_x layer, where only two ALD cycles were applied, which resulted in a 12 mV increase in V_{oc} [36]. Cell P3 with a sputtered AlO_x rear passivation still achieves an efficiency of 20.1% [30] despite the simple sputtering deposition technique [29], which does not require any expensive gases such as TMA. Importantly, cells P1 – P3 in Table 1 did not undergo any firing step, which is a standard high-temperature process step in the industrial solar cell production. Hence, at ISFH we have developed an industrial-type PERC solar cell with fully screen-

printed and fired contacts on 12.5×12.5 cm² pseudo-square boron-doped *p*-type Cz-Si [37]. This cell P4 shows the so far best cell results with an Al₂O₃/SiN_x rear passivation on large-area wafers. The 19.0% efficient cell P4 does, however, not include any selective emitter at the front and has a non-optimised rear geometry of the local laser contact openings [37]. Hence, we estimate an efficiency limit exceeding 20% for an optimised cell of this type including a selective emitter [38].

Table 1. p-type silicon PERC solar cells with Al₂O₃ rear passivation. The Al₂O₃ is deposited by different deposition techniques such as ALD, PECVD and sputtering. All cell parameters were independently confirmed at FhG-ISE CalLab.

	Metallisation	Rear passivation	Area [cm ²]	V _{oc} [mV]	$J_{\rm sc}$ [mA/cm ²]	FF [%]	η [%]	Ref.
P1	Shadow-mask evaporated Al	ALD-Al ₂ O ₃ /PECVD-SiN _x	4	673	40.3	79.9	21.7	[36]
P2	Evap. Ti/Pd/Ag, photolithography	PECVD-AlO _x	4	684	39.4	79.8	21.5	[28]
Р3	Shadow-mask evaporated Al	Sputtered AlO _x	4	651	39.1	79.1	20.1	[30]
P4	Screen-printed front and rear	ALD-Al ₂ O ₃ /PECVD-SiN _x	149	652	38.9	75.1	19.0	[37]

Table 2 summarises some of the best cell results achieved so far on high-lifetime *n*-type silicon, where the boron-diffused p^+ emitter has been passivated by ALD-Al₂O₃/PECVD-SiN_x stacks. Cell N1 is a passivated emitter and rear, locally diffused (PERL) cell, where the rear is passivated by high-temperature grown SiO₂ and a local phosphorus diffusion was implemented to create the local back surface field. Due to the excellent Al₂O₃ passivation of the p^+ front emitter [5, 35], this cell achieves a very high V_{oc} of 705 mV and the best efficiency of 23.9% [39]. Cell N2 is a back-contact emitter-wrap-through (EWT) solar cell, where the boron-diffused p^+ emitter covers the entire front and a large part of the cell rear [40]. Hence, the quality of the applied Al₂O₃/SiN_x passivation scheme is of utmost importance. The achieved V_{oc} of 661 mV and the efficiency of 21.6% [40] of this EWT cell on *n*-type Cz-Si clearly reveal the high potential of the Al₂O₃ passivation for this cell type. Table 2 also includes a large-area cell with borondiffused p^+ front emitter and a printed, fired and electroplated front contact grid [41]. The entire rear was phosphorus-diffused and metallised by evaporated aluminium. The front of this 19.6% [41] efficient *n*type cell was passivated by a firing-stable Al₂O₃/SiN_x stack. The cell results shown in Tables 1 and 2 prove that Al₂O₃/SiN_x stacks are perfectly suited for the next generation of industrial high-efficiency solar cells on *p*- as well as on *n*-type silicon.

Table 2. *n*-type silicon solar cells with Al_2O_3/SiN_x -passivated boron-diffused p^+ -emitter. The parameters of cells N1 and N2 were independently confirmed at FhG-ISE CalLab.

	Cell structure	Area [cm ²]	V _{oc} [mV]	$J_{\rm sc}$ [mA/cm ²]	FF [%]	η [%]	Ref.
N1	PERL; rear: SiO ₂ -passivated, local n^+	4	705	41.1	82.5	23.9	[35,39]
N2	EWT; locally contacted SiO ₂ -passivated n^+	4	661	40.4	80.8	21.6	[40]
N3	Front grid: printed, fired, plated; rear: evaporated Al	141	649	38.5	78.3	19.6	[41]

5. Conclusions

Lifetime as well as solar cell results demonstrate the enormous potential of Al_2O_3 as a surfacepassivating dielectric layer for the next generation of industrial silicon solar cells on *p*- as well as *n*-type silicon wafers. The question remains open what will be the best suited deposition technique and on what timescale it can be transferred to industry. The highest film quality is currently realised by means of PA-ALD, which is in its current form not able to deliver the throughput required in a solar cell production line. We have hence evaluated three different deposition techniques which are in principle suitable for the required throughput, namely high-rate spatial ALD, inline-PECVD, and sputtering. As large-area inline-PECVD systems are already available on the market, PECVD seems to be the preferred short-term deposition techniques for Al_2O_3 . If the firing stability of sputtered Al_2O_3 layers could be further improved (e.g. by using a hydrogen-rich SiN_x capping layer), this could become another option for the short-term. The preferred medium-term and long-term deposition technique might be the ultrafast spatial ALD due to its reduced TMA gas consumption compared to PECVD, the absence of parasitic deposition at the reactor wall and a smaller footprint of the deposition systems currently under development. Also, ALD provides highest-quality pinhole-free Al_2O_3 films and allows conformal film deposition, which might prove useful for advanced solar cell concepts such as back-contact EWT cells.

Acknowledgements

Funding was provided by the State of Lower Saxony and the German Ministry for the Environment, Nature Conservation and Nuclear Safety (BMU) under contract number 0325050 ("ALD").

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