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## Identification of lifetime-limiting defects in as-received and heat treated seed-end Czochralski wafers

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### Abstract

Major impurity-induced defects in Czochralski silicon are known to be related to oxygen and metallic elements. In this study we focus on seed-end wafers, and submit them to different solar cell process-related annealings. We find that in the as-received state, these seed-end wafers contain a central low carrier lifetime core. We demonstrate that the effective carrier lifetime in the as-received state is simultaneously governed by several defects including oxygen-related thermal donors and a defect deactivated at low temperature. After high temperature steps, a strong recombination center is formed in the central core. Preliminary results of effective carrier lifetime versus temperature measurements directly support oxygen precipitates to be limiting the carrier lifetime. In the light of all results, we propose a qualitative model for the high temperature behavior of these seed-end wafers.

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*Keywords:* Czochralski silicon; lifetime-limiting defects; oxygen; precipitation; thermal donors.

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### 1. Introduction

It has been reported that 10 to 20% of Czochralski (Cz) wafers are affected by high concentrations of oxygen (O)-related defects and may not match photovoltaic (PV) specifications for high efficiency solar cells [1]. Regarding homojunction (HJ) cells, the efficiency drop was attributed to O precipitation during emitter diffusion [2]. Nevertheless, to our knowledge, the role of O precipitates (OP) in the efficiency drop was not directly observed, but

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was rather inferred from the presence of high OP densities in areas where effective minority carrier lifetime ( $\tau$ ) is low. Complementary studies are thus needed. Recently, elegant techniques involving mid-temperature (T) annealings ( $\sim 600^\circ\text{C}$ ) were developed to passivate defects in HJ process [3]. The applicability of these techniques to all Cz wafers requires however more data. Moreover, it is difficult to transfer these steps to other processes such as low T amorphous Si (a-Si) – crystalline Si heterojunction cells, since a-Si degrades at  $T > 250^\circ\text{C}$ . Therefore the study of defects in as-grown wafers remains fully relevant.

In this work, we investigate  $\tau$  of seed-end Cz wafers, where formation of O-related defects such as OP and thermal donors (TD) is favored because of a high interstitial O concentration ( $[\text{O}_i]$ ) associated to a slow cooling during crystal growth. We attempt to identify the  $\tau$ -limiting defects in the as-received state as well as after different process-related annealings.

## 2. Experimental procedure

### 2.1. Sample preparation

Five sister wafers from the seed-end (solidified fraction = 4%) of an industrial phosphorus (P)-doped Cz ingot were studied. Using the OxyMap method described in [4] the following concentrations were measured at the center of a first wafer:  $[\text{P}] = 7 \times 10^{14} \text{ cm}^{-3}$ ,  $[\text{O}_i] = 1.15 \times 10^{18} \text{ cm}^{-3}$ . The as-grown TD concentration ( $[\text{TD}]$ ) varied from  $1.2 \times 10^{15} \text{ cm}^{-3}$  at the center to  $8 \times 10^{13} \text{ cm}^{-3}$  at the periphery. The 4 remaining wafers were submitted to the process steps described in Fig. 1.

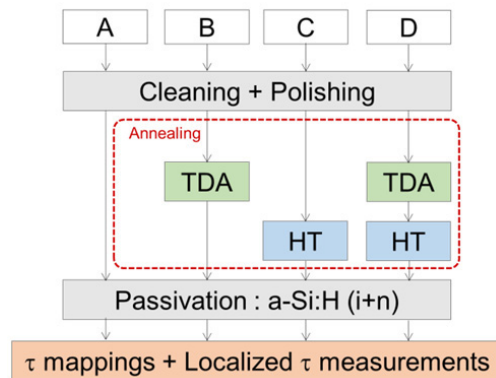


Fig. 1. Process flow. TDA stands for “Thermal Donors Annihilation” and HT for “High Temperature” annealing.

Polishing was performed using an alkaline solution. Identical annealings, either TD Annihilation (TDA) or high T annealing (HT), were performed simultaneously. TDA is a 10 min annealing at  $750^\circ\text{C}$ , allowing TD suppression [5]. The HT annealing ( $900^\circ\text{C}$ , 1h30) mimics the thermal profile of both boron and P diffusions. Passivation stacks of a-Si layers (intrinsic and n-type) on both wafer surfaces were deposited by PECVD ( $T \sim 200^\circ\text{C}$ ). This stack allows high passivation quality to be achieved ( $S_{\text{eff}} < 2 \text{ cm}\cdot\text{s}^{-1}$ ).

### 2.2. Sample characterization

$\tau$  mappings were obtained using Microwave Photoconductance Decay ( $\mu\text{PCD}$ ). At the wafer center, measurements of injection- and T- dependent  $\tau$  (IDLS and TDLS) were carried out using a SINTON-WCT120 equipment. TDLS was performed between  $25^\circ\text{C}$  and  $160^\circ\text{C}$ . Following the procedure detailed in [6], TDLS allows to get information on energy levels introduced by the dominant recombination-active centers in a wafer. The technique is based on a simplification of the  $\tau$  expression related to SRH recombination channels ( $\tau_{\text{SRH}}$ ) at low injection level ( $\Delta p$ ) as a function of T.  $\tau_{\text{SRH}}$  is obtained from  $\tau$  according to Eq. 1.

$$\frac{1}{\tau} = \frac{1}{\tau_{SRH}} + \frac{1}{\tau_{int}} + \frac{1}{\tau_{surf}} \quad (1)$$

Where  $\tau_{int}$  is the lifetime limited by intrinsic recombination channels (Auger and radiative recombinations) and  $\tau_{surf}$  is the lifetime limited by surface recombinations.

From [6], in the case of an energy level introduced close to the conduction or valence bands, the  $\tau_{SRH}$  limited by this recombination-active center can be expressed as Eq. 2. This expression is however valid only in low  $\Delta p$  conditions, at sufficiently high T, considering a shallow center (close to one of the bands) and if the density of recombinant centers is inferior to a threshold density  $N_{crit}$  whose value is a function of carriers densities, energy level position and ratio of capture cross sections [7].

$$\tau_{SRH}^{LLI}(T) \approx \frac{T}{\sigma_p(T)} \exp\left(-\frac{\Delta E_t}{k_B T}\right) \quad (2)$$

Here  $\sigma_p(T)$  represents the T-dependence of the capture cross section for holes, and  $\Delta E_t$  is the position of the energy level relative to the priori unknown closest band, i.e.  $\Delta E_t = E_t - E_v$  or  $\Delta E_t = E_c - E_t$  [6]. As we do not know a priori the recombination-active center in our samples, we assumed that capture cross-section for holes is T-independent. Under this assumption, the slope of the Arrhenius plot obtained by TDLS indicates the value of  $\Delta E_t$  introduced by the recombination-active center. TDLS alone does not however give information on the half of the band gap where the energy level is located.

The influence of intrinsic recombination channels on the carrier lifetime was calculated using Richter's model and subtracted from our measurements of  $\tau$  as a function of T [8]. Regarding the quality of passivation compared to the measured  $\tau$ ,  $\tau_{surf}$  was neglected. Estimated  $\tau_{surf}$  (>3 ms) is indeed largely higher than measured  $\tau$ , and we can thus consider that  $\tau$  is essentially limited by  $\tau_{SRH}$  and  $\tau_{int}$  (Eq. 1). The  $\tau_{SRH}$  was then extracted at low  $\Delta p$ , as required by the procedure detailed above:  $\Delta p = n_0/10 = 7 \times 10^{13} \text{ cm}^{-3}$ , where  $n_0$  is the electron concentration ( $n_0 = [P] + 2[DT]$ ). Prior to TDLS measurements, the wafers were annealed at 200°C during 7h in ambient atmosphere, to avoid any transient effect in the passivation quality.

### 3. Results

#### 3.1. Identification of defects in as-received samples

$\mu$ PCD mappings show the presence of a low  $\tau$  core (LLC) in as-received wafers (Fig. 2 (a)). Similar observations were recently reported for early bodies of Cz ingots [9]. After TDA,  $\tau$  is homogenized and reaches values higher than 1.5 ms all along the wafer (Fig. 2 (b)). The LLC in wafer A corresponds to the area where [TD] is the highest.

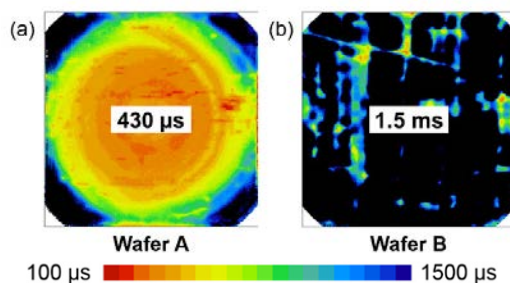


Fig. 2.  $\mu$ PCD  $\tau$  mappings performed on passivated wafers (a) as-received; (b) after TDA.  $\tau$  in the central zone measured with a SINTON-WCT120 at  $\Delta p = 1.10^{15} \text{ cm}^{-3}$  is indicated in white boxes.

Using an in-house model to simulate recombination at TD [10], we computed the  $\tau$  in the LLC of wafer A considering TD as the  $\tau$ -limiting defect (Fig. 3), using Richter's model for  $\tau_{\text{int}}$  calculation [8].  $\tau_{\text{surf}}$  and its variation with  $\Delta p$  were estimated from measurements on high-quality passivated Float-Zone samples. The associated error on computed  $\tau$  is related to the high lateral gradient of [TD] in the LLC. Below  $\Delta p = 1.10^{15} \text{cm}^{-3}$ , the recombinant centers density is higher than the critical concentration  $N_{\text{crit}}$  [7] leading to inapplicability of the model.

We observed a significant discrepancy between prediction and measurement, suggesting the presence of another defect. To identify this second defect, we performed specifically on wafer A isochronal annealings of 30 min between 100°C and 300°C (50°C step). Low  $[\text{O}_i]$  wafers (both Float-Zone and Cz) were used as references in order to monitor surface passivation variations. The slight observed variations at highest T were used to correct the  $\tau$  data by a passivation degradation factor. We found that  $\tau$  almost doubled in the LLC of wafer A after the 250°C annealing (Fig. 3). This low deactivation T points toward a vacancy-related defect [11]. The remaining gap between predictions and experiment could be due to a third defect. We did not however investigate this possibility any further, as the gap may as well be related to experimental and modelling uncertainties.

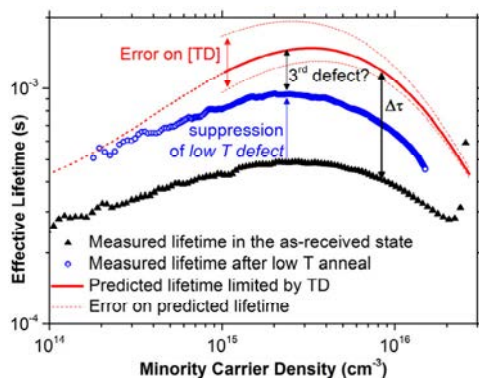


Fig. 3. Measured and predicted  $\tau$  in the LLC of wafer A.

### 3.2. Identification of defects in HT annealed samples (wafers C and D)

A HT step induced a strong material degradation in the region where the LLC was observed on as-received wafers (Fig. 4 (a)). Performed after TDA, this HT step leads to an even stronger  $\tau$  drop (Fig 4 (b)). Contrary to the results of Section 3.1, TD cannot be responsible for the LLC observed here since they were destroyed during the TDA and/or HT step. Therefore, we demonstrate that care must be taken when attributing a given defect to an observed LLC, as the responsible defect can be manifold.

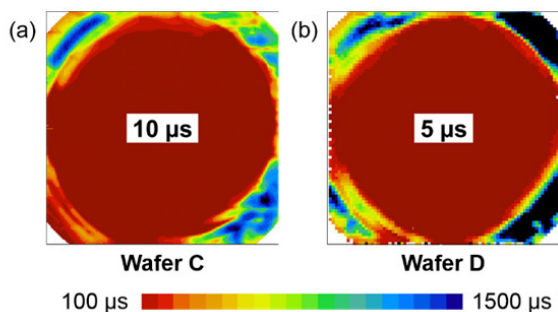


Fig. 4.  $\mu$ PCD  $\tau$  mappings performed on passivated wafers (a) after HT (b) after TDA+HT.  $\tau$  in the central zone measured with a SINTON-WCT120 at  $\Delta p = 1.10^{15} \text{cm}^{-3}$  is indicated in white boxes.

In order to investigate the underlying defect after HT steps, TDLS measurements were carried out (Fig. 5). As already mentioned in paragraph 2.2,  $\tau_{surf}$  was neglected and, as a first blind guess, capture cross sections were considered T-independent (Eq. 2). We concluded from Fig. 5 that the  $\tau$ -limiting defect in the LLC is the same in both wafers (same slopes). This defect introduces a recombination active level around  $\Delta E_t=0.20$  eV, which is compatible with OP level measured by Deep Transient Level Spectroscopy ( $\Delta E_t=0.2-0.3$  eV) [12]. Our results thus suggest that OP may be the  $\tau$ -limiting defect in these studied heat treated seed-end wafers. Nevertheless, it has been more recently reported that lifetime-killing OP, which were stronger recombinant centers when strained [13], introduce two independent recombinant centers, labeled defect 1 and defect 2, at the following energy levels:  $E_{t1}=E_v+0.22$  eV and  $E_{t2}=E_c-0.08$  eV [14,15].

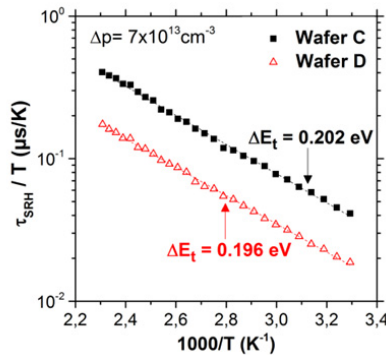


Fig. 5. Arrhenius plot of TDLS measurements in the LLC of wafers C and D. Energy levels position calculated from the slopes (Eq. 2) are indicated.

The consideration of both energy levels makes the usual TDLS approach difficult to apply. To circumvent this issue, we chose to compute the theoretical Arrhenius plot that one should get if these two levels were present in our samples and to compare the computed slope with measured ones, rather than extracting energy levels from the experimental plot. The  $\tau_{SRH}$  limited by recombinations related to the OP,  $\tau_{SRH,OP}$ , is defined by Eq. 3 where  $\tau_{SRH,1}$  is the carrier lifetime limited by SRH recombinations related to defect 1, and  $\tau_{SRH,2}$  the one related to defect 2.

$$\frac{1}{\tau_{SRH,OP}} = \frac{1}{\tau_{SRH,1}} + \frac{1}{\tau_{SRH,2}} \tag{3}$$

Rearranging the standard SRH statistics [7], the lifetime limited by SRH recombination in n-type Si can be written as:

$$\tau_{SRH}(T) = \tau_{p0}(T) \left( 1 + \frac{n_1(T)}{n_0(T)} + \frac{1}{k} \frac{p_1(T)}{n_0(T)} \right) \tag{4}$$

Where  $n_1$  and  $p_1$  are the equilibrium concentrations of electrons and holes, respectively, when the Fermi level coincides with the energy of the recombination center, and  $\tau_{p0}$  is defined as:

$$\tau_{p0}(T) = \frac{1}{\alpha_p(T)N_t} \tag{5}$$

$\alpha_p$  is the capture coefficient for holes, and  $k$  is the ratio of the capture coefficients ( $k=\alpha_n/\alpha_p$  with  $\alpha_n$  the capture coefficient for electrons).  $N_t$  is the density of recombination-active defects. We expressed directly the quantity  $N_t \alpha_p$  as a function of  $N_{\text{strained}}$  (density of strained OP) and  $\Delta[\text{O}_i]$  ( $[\text{O}_i]$  consumed during precipitation), as developed in [15]. We did not measure  $N_{\text{strained}}$  and  $\Delta[\text{O}_i]$ , but fortunately their absolute value has no effect on the slope of the simulated Arrhenius plot. Thus, for the computation, we picked arbitrary values. Finally, we checked for both levels that the defect densities  $N_{\text{crit}}$  above which SRH simplified model cannot be applied anymore [7] were significantly greater than the estimated densities in our samples. SRH recombination parameters of the two defects introduced by OP have been described in [14,15] and are summarized in Table 1.

Table 1. SRH parameters from [14,15].

Parameter	Value – Defect 1	Value – Defect 2
$E_t$	$E_v + 0.22 \text{ eV}$	$E_c - 0.08 \text{ eV}$
$k$	157	1/1200
$N_t \alpha_p$ (300 K)	$(3 \times 10^{-11} \text{ cm}^3/\text{s}) N_{\text{strained}}^{1/3} \Delta[\text{O}_i]^{2/3}$	$(3 \times 10^{-8} \text{ cm}^3/\text{s}) N_{\text{strained}}^{1/3} \Delta[\text{O}_i]^{2/3}$
Activation energy of $\alpha_p$	0.20 eV	-
Activation energy of $\alpha_n$	-	0.14 eV

TDLs measurements were performed at  $T < 200^\circ\text{C}$ . We thus considered  $n_0$  to be independent of  $T$ . We assumed the position of each level, the ratio of the capture coefficients  $k$  and the densities of each defect to be  $T$ -independent.

The resulting theoretical Arrhenius plot is presented in Fig. 6. A slope of 0.23 eV is obtained, in very close agreement with the experimental slopes. Among other uncertainty sources, the relative error of 15% can be related to the expression of  $N_t \alpha_p$  as a function of  $N_{\text{strained}}$  and  $\Delta[\text{O}_i]$ . Indeed, the parameterization that was used here was derived from measurements on samples where precipitation was forced and preceded by a tabula rasa annealing. In that way, the OP population in our sample can somehow differ from that used for SRH parametrization in [15], in terms of sizes and shapes of OP. Our results nevertheless bring direct evidence that OP are responsible for the  $\tau$  drop in the LLC of wafers C and D after HT step. It is the first time to our knowledge that the direct implication of OP in the  $\tau$  drop on process-related annealed wafer is reported.

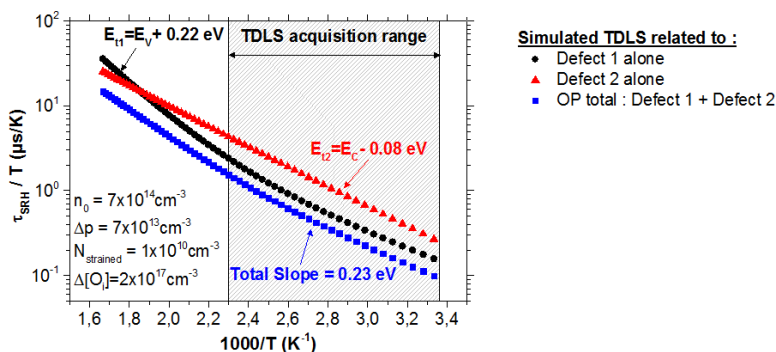


Fig. 6. Simulated Arrhenius plots representing  $T$ -behavior of  $\tau$  related to defect 1 alone (black circles), defect 2 alone (red triangles) and to OP (blue squares). Parameters used for the simulation are indicated in the white box. Energy level position calculated from the slope is indicated.

### 3.3. Qualitative precipitation model

We tentatively propose a qualitative model that explains our findings, in the light of all previous results. The TDA performed at  $750^\circ\text{C}$  would allow some small nuclei present in as-received wafers to grow (alongside some larger nuclei). In that way they could reach a sufficient size to survive a HT step whereas these smallest nuclei would be killed during the rising ramp of HT annealing on wafer C. Density of strained OP would thus be higher in wafer D leading to a smaller  $\tau$ . The high  $\tau$  in wafer B indicates that after TDA the nuclei have not reached a

sufficient size to be strained and therefore do not limit  $\tau$ . In addition, TD and the *low T defect* identified in wafer A are annihilated leading to an overall high  $\tau$ .

#### 4. Conclusions and outlook

It was shown that as-received seed-end wafers contain a LLC resulting from several defects including TD and *low T defect*. After TDA, a high and homogeneous  $\tau$  is obtained. The strong  $\tau$  drop after HT steps (with and without TDA) were qualitatively explained by the growth of large strained OP, the TDA enhancing the precipitation through drifting of the nuclei population to higher sizes, able to survive HT. To our knowledge, we evidenced for the first time the direct implication of OP in the  $\tau$  drop on wafers submitted to process-related annealing. We underlined that the attribution of a given defect to an observed LLC is not straightforward and requires care, as many different defects can lead to a similar  $\tau$  pattern (here: TD, OP, maybe the *low T defect*). Future work is planned in order to get further information about nuclei size and density in as-received wafers. We also intend to study the *low T defect*.

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