Improvement on industrial n-type bifacial solar cell with >20.6% efficiency

Hung-Chih Chang\textsuperscript{a}, Chih-Jeng Huang\textsuperscript{a}, Po-Tsung Hsieh\textsuperscript{a*}, Wei-Cheng Mo\textsuperscript{a}, Shu-Hung Yu\textsuperscript{a}, Chi-Chun Li\textsuperscript{a}

\textsuperscript{a}Motech Industries, Inc. Science Park Branch, 74145 Tainan, Taiwan

Abstract

Results of industrial n-type bifacial solar cells, with efficiency as high as 20.63%, are presented within this work. 6” n-Cz wafers were used as substrates. The front emitter and the back surface field were fabricated by boron and phosphorous diffusions respectively. Various surface structures and doping conditions were adopted for comparisons for their influence on cell performance. In addition, three Ag/Al pastes were tested and their related properties were discussed. With the integration of these improved processes, better electrical properties, including higher cell efficiency were obtained.

Keywords: n-type, bifacial, boron doping, texture.

1. Introduction

As is well known, a sizable reduction in performance of industrial p-type Si solar cells takes place after the cells are exposed to light. This light induced degradation (LID) effect has been shown to result from the boron-oxygen complex in the p-type Si wafer [1-3]. The doped boron atoms react with oxygen atoms under illumination, forming B-O complexes that degrade the bulk lifetime of Si wafer [4-6]. As such, one major approach to avoid LID is to use wafers without boron doping, and the most readily available option is the n-type Si material. Additional advantages of n-type wafers include longer minority carrier diffusion length and less sensitivity to metallic impurities [7]. A few
products using n-type Si substrates have been available, including interdigitated back-contact (IBC) cells from SunPower, and heterojunction with intrinsic thin layer (HIT) cells from Panasonic [8, 9]. Though much effort has been made to develop various n-type cell structures with high efficiency, the cost often increased significantly due to additional processing steps. Bifacial n-type cell structure holds the potential for both achieving high efficiency and keeping the production cost low, by utilizing industrial production processes. A number of institutions have reported high efficiency values of n-type bifacial cells, including Motech’s 20.23% announced last year [10-11]. In this work, an improvement to 20.63% cell efficiency is presented. The effect of different surface structures and doping conditions are discussed. Furthermore, three different Ag/Al pastes were applied to the bifacial n-type cells to compare their electrical performance.

2. Experiment

In this study, 6-inch n-type Cz silicon wafers with an initial thickness of 180 um were used. After texturing, the wafers were then doped with boron for front emitter, and with phosphorous for back surface field. Passivation films and SiNx ARC films were deposited on the doped surfaces. Front-side and back-side metallization were completed by screen-printing Ag/Al and Ag pastes respectively. Schematic cross section of N-type bifacial cell structure is shown in Fig. 1. Several processes improvement were addressed and investigated based on previous study [5, 6].

![Fig. 1. Schematic cross-section of N-type bifacial solar cell.](image)

3. Results and discussion

Figure 2 compares the electrical results of bifacial cells with three different rear side textures, TX1, TX2, and TX3. TX1 is the original texturing process. In order to reduce the surface recombination, additional smoothening processes were applied on TX2 and TX3. Longer process time was used on TX3 than TX2, which means the surface of TX3 should be smoother than TX2. As shown in the Fig. 2, higher open circuit voltage (Voc) was obtained by adopting TX2 and TX3. Relative 0.6% gain can be observed for TX3 process. Improvement in short circuit current (Isc) can also be obtained by TX2 and TX3, as compared to TX1. Over 1% relative gain was measured for TX3 sample. As shown in Fig. 3, the surface morphology of TX3 is smoother than textured surface, which is beneficial for Voc and Isc. Cell efficiency gain was over 3% gain, due to enhanced Voc, Isc, and also the fill factor (FF). As illustrated in the right figure of Fig. 2, series resistances (Rs) for TX2 and TX3 were lower than TX1, which resulted in FF gain. Comparing TX2 and TX3: TX2 exhibited lower Rs than TX3, which indicated better suitability of TX2 for metallization. However, FF remains the same for TX2 and TX3 as indicated in the figure. Using SunsVoc measurement we found that the pseudo fill factor (PFF) of TX3 was higher than that of TX2, which compensated the difference in Rs, and resulted in comparable FF for TX2 and TX3.
Boron doping is always a critical process for n-type cell for the formation of emitter layer. In order to evaluate boron doping quality, a symmetrical structure for QSSPC measurement was prepared. Figure 4 shows the emitter saturation current density (Jo) and effective lifetime (Teff) for different boron doping recipes (P1, P2 and P3). It is well-known that higher process temperature is always needed for boron diffusion than phosphorous diffusion based on different doping mechanism for boron and phosphorous atoms to Si surface. However, it is also important to control the thickness of boron rich layer during diffusion process. Therefore, the process adjustments from P1 to P3 are mainly on thermal budget, with P3 having the least thermal budget. It depicts that Jo and Teff were improved during the optimization, P1 to P2 to P3, indicating less surface recombination and defect for P3 compared to P1 and P2. Figure 5 presents the relative cell IV results. P1 is the original baseline and serves as the base of comparison. Higher Voc values were measured with P2 and P3 conditions, consistent with the QSSPC results. Nearly 2% of relative gain on Voc can be achieved with P3. In addition to Voc, it is worth mentioning that Rs was also reduced for P2 and P3 (not shown here). Due to the improvement on Rs, FF increased by 4% relative. As a result, better cell efficiency performance was observed by using P3 recipe with the enhanced Voc, Jsc and FF.
Front Ag/Al paste for boron emitter plays a crucial role in n-type bifacial solar cells. Fig. 6 shows the comparison of relative I-V values for the three Ag/Al pastes we tested and Figure 7 presents the comparisons on Rs and printed finger width for various pastes. Due to different paste compositions and viscosities, wider finger width was obtained with Paste A than the other two pastes using the same screen opening, and thus resulted in lower Isc for Paste A. Similar Isc results were observed for Paste B and Paste C due to similar metal finger widths. Figure 8 shows the image of printed finger by Paste C and less spread at sidewall of the finger was observed. Lower Voc for Paste B than Paste C could be ascribed to different Al content of pastes. As compared with Figure 6 and Figure 7, better fill factor obtained by Paste C resulted from the lower Rs. According to Transmission Line Measurement results (not shown here), Paste C exhibited the lowest contact resistance. Based on the results above, Paste C has better performance not only in Isc and Voc, but also in FF. The highest cell efficiency was obtained by Paste C.
Integrating the optimized processes discussed above, we were able to produce a bifacial cell with efficiency reaching 20.63%. Table 1 shows the basic electrical data of the best cell.

Table 1. I-V characteristics of the champion cell.

<table>
<thead>
<tr>
<th>Chuck type</th>
<th>Voc (mV)</th>
<th>Jsc (mA/cm²)</th>
<th>FF (%)</th>
<th>Eff. (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Metal coated chuck (ITRI_SCCL)</td>
<td>647</td>
<td>39.2</td>
<td>81.3</td>
<td>20.63</td>
</tr>
</tbody>
</table>
4. Conclusion

Efficiency up to 20.63\% for N-type bifacial solar cells was achieved by industrial processes. Investigations on rear surface texture formation, boron doping conditions and various metal pastes showed significant influence on Voc, Isc and FF. Results indicate that higher cell efficiency is reachable with optimized cell structure, and continued improvement in processes.

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References