



About synchronous programming and abstract interpretation¹

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Abstract

This paper intends to highlight the connection between synchronous programming and abstract interpretation. First, the specific technique for compiling synchronous programs into interpreted automata can be seen as a partial evaluation. The second point concerns program verification. Most critical properties of reactive systems are safety properties, which can be translated into invariants, through the use of *synchronous observers*. Invariants can be proved by means of approximate reachability analysis, which is probably the most studied application of abstract interpretation. © 1998 Elsevier Science B.V. All rights reserved.

1. Introduction

Synchronous programming has been proposed during the last decade, as a new point of view on concurrency. A family of synchronous languages [21, 16] (e.g., ESTEREL, STATECHARTS, ARGOS, LUSTRE, SIGNAL) has been based on this point of view, and devoted to the design of *reactive systems*, i.e., computer systems whose role is to maintain a continuous, real-time, interaction with their environment.

In the synchronous approach, concurrency is not considered as an implementation constraint, but as a programming facility for logical structuring of programs. Since synchronous concurrency has nothing to do with parallel or distributed execution, it can be defined without taking into account physical constraints, like communication or task execution delays. A synchronous process is supposed to be able to react *instantaneously* to incoming events, and to communicate *instantaneously* with other processes. The main consequence is that a synchronous system can be (and will be, in most synchronous languages) *deterministic*, a feature which is highly desirable in reactive programs, and which tremendously simplifies the design, debugging, and verification tasks.

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An abundant literature has been devoted to synchronous languages (e.g., [4, 5, 3, 7, 20, 26, 8, 17, 25, 2]). This paper intends to highlight the connection between synchronous programming and abstract interpretation. First, one of the compiling techniques which have been developed for synchronous languages consists in synthesizing the control structure of the sequential object code as a finite automaton. We will see that this technique is in fact a partial evaluation. The second point concerns program verification: for reactive systems, experience shows that an important practical goal is to ensure some critical properties, rather than to completely prove program correctness. Moreover, these properties are mainly *safety* properties. Now, the synchronous approach allows any safety property to be easily translated into an *invariant*: the property is expressed by means of an auxiliary program, called *synchronous observer* [19], which observes the input/output events of the program under verification, and detects any violation of the property. The verification then consists in showing that the parallel composition of the program and its observer never complains about property violation. Finally, invariants can be proved by means of approximate reachability analysis, which is probably the most studied application of abstract interpretation [11, 12]. Numerous specific analysis techniques (e.g., [24, 33, 10, 13, 14, 27]) are available, which provide conservative verification tools in this context.

The paper essentially consists of two parts: Section 2 is a brief introduction to synchronous languages, illustrated by aspects of the language LUSTRE. Section 3 concerns the use of abstract interpretation in compiling and verification.

2. Synchronous programming (or Concurrency can make programming easier)

The subtitle of this section is deliberately provocative: from the very first steps of time-sharing systems, concurrency has been identified as a tremendous increase in programming complexity, because of non-determinism and lack of global notion of *state*. However, after Milner's pioneering works [28, 29], the synchronous model was developed, which avoids these two problems at the expense of losing the idea of parallel implementation. In synchronous languages, concurrency must be understood as a way of describing the desired behavior of a system, and has nothing to do with parallel execution.

2.1. Basic principles

Basically, a reactive system can be viewed as an input/output automaton, reacting to incoming events by selecting a transition, computing and emitting output events, and changing its state for the next reaction. As a matter of fact, many reactive systems are implemented in that way (for instance on automatic controllers). However, building an automaton by hand is a complex and error-prone task. Automata are even harder to debug and modify, since the slightest modification in the specification may involve a complete change in the structure of the automaton. These problems are mainly due to

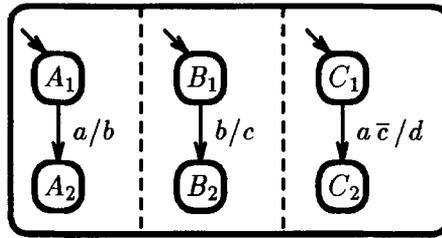


Fig. 1. Synchronous parallel composition.

the lack of structure and modularity. Synchronous languages are nothing but high-level, modular, languages to describe automata, exactly as LEX and YACC are languages to describe scanners and parsers.

The main structuring feature, which exists in all the synchronous languages, is synchronous parallel composition. Intuitively, when two automata are composed in parallel, each of them perceives the outputs of the other as its own inputs. A transition of the whole program, in response to an input event, consists of simultaneous transitions of all active parallel processes in the program, each process reacting to the current event, which is the input event enriched by the outputs of the other processes. So, the current event is the solution of a fixpoint equation

$$\text{“current_event} = \text{input_event} + \text{response_to}(\text{current_event})\text{”}$$

For instance, Fig. 1 shows a fragment of an ARGOS program [26] which is the parallel composition of three automata:³ The first one, in state A_1 , waits for an input event where the “signal” a is present. Whenever such an event occurs, it executes its transition to state A_2 and emits the signal b . Now, the presence of b in the current event involves the reaction $B_1 \rightarrow B_2$ of the second automaton, which emits the signal c . The third automaton does not react, since, in C_1 it waits for an event containing a but not c . So, in presence of the input event $\{a\}$, the resulting current event is $\{a, b, c\}$ and the whole program moves from the global state (A_1, B_1, C_1) to the global state (A_2, B_2, C_1) .

2.2. An example: programming in Lustre

We will use the synchronous data-flow language Lustre as an example of synchronous language. As for any synchronous program, a behavior of a Lustre program is an infinite sequence of reactions to input events. The data-flow point of view [23, 1] is the following: at each reaction, each variable of the program has a value, so we associate with each variable x an infinite sequence $(x_1, x_2, \dots, x_n, \dots)$, where x_n is the value

³ Argos basic components are input/output automata (Mealy machines): transition labels have two parts, separated by a “/”: an input guard, which is a condition on the presence/absence of *signals* in the current event, and an output part, which is a set of signals emitted when the transition is fired. When a transition is enabled by the current state and the current event, its firing in the current reaction is compulsory.

of x at the n th reaction of the program. Such a sequence will be called a *flow*, and a program can be viewed as computing output flows (i.e., flows associated with output variables) from input flows. In fact, any operator in the language can be viewed as such a function on flows: for instance, if x and y are variables, respectively, associated with flows (x_1, \dots, x_n, \dots) and (y_1, \dots, y_n, \dots) , then $x + y$ denotes the flow $(x_1 + y_1, \dots, x_n + y_n, \dots)$; that is, “+” is an operator on flows, which returns the point-wise sum of its operands. Usual arithmetic, Boolean, conditional, operators are extended this way to operate on flows; they are called *data operators*. Constants are considered as constant flows. Two specific operators will be used: the “previous” (**pre**) and the “followed-by” (\rightarrow) operators. Let $x = (x_1, \dots, x_n, \dots)$ and $y = (y_1, \dots, y_n, \dots)$ be two flows, then $\text{pre}(x) = (\text{nil}, x_1, \dots, x_{n-1}, \dots)$ where *nil* denotes the undefined value – and $x \rightarrow y = (x_1, y_2, \dots, y_n, \dots)$. Now, a program basically consists of a system of equations defining output (and possibly local) variables, as expressions of constants and variables. More precisely, such a system of equations is encapsulated into a *node*, which is a user-defined operator on flows. Let us start with two simple examples which will be used later on.

- The first one is a “switch”, which receives two Boolean flows, ON and OFF, and returns a Boolean flow S. Whenever ON is true, S becomes true; otherwise, if OFF is true, S becomes false; when neither ON nor OFF is true, the value of S does not change (its initial value is false):

```
node SWITCH (ON, OFF: bool) returns (S: bool);
let
  S =    if ON then true
        else if OFF then false
        else (false  $\rightarrow$  pre(S));
tel
```

Once such a node is declared, it can be used as an operator, anywhere in a program.

- The second example is an incrementer–decrementer, whose result is always the difference between the number of times the input INCR has been true, and the number of times the input DECR has been true:

```
node DIFF (INCR, DECR: bool) returns (DIFF: int);
let
  DIFF = (0  $\rightarrow$  pre(DIFF)) +
         (if INCR and not DECR then 1
          else if not INCR and DECR then -1
          else 0);
tel
```

Now, let us consider a (slightly) more realistic example, extracted from a subway traffic regulation system: a train detects beacons that are placed along the track, and receives the “second” from a central clock. Ideally, it should encounter one beacon each second; so the space left between beacons rules the speed of the train. Here, we

only consider the way the train detects it is early or late. To avoid shaking, a hysteresis is applied: whenever the number $\#b$ of beacons encountered exceeds the number $\#s$ of seconds received plus 10, the train detects it is early, and remains early until $\#b$ becomes equal to $\#s$. Conversely, whenever $\#b < \#s - 10$, the train detects it is late until $\#b = \#s$.

The program makes use of the nodes defined above. We use a local variable `advance` which counts the difference ($\#b - \#s$) by means of the node `DIFF`. The thresholds on `advance` are used to control two switches, which, respectively, compute the “early” and “late” states.

```

node train (beacon, second: bool) returns (early, late: bool);
var advance: int;
let
  advance = DIFF(beacon, second);
  early = SWITCH(advance > 10, advance=0);
  late = SWITCH(advance <- 10, advance=0);
tel

```

Notice that the three equations can be written in any order, as in mathematics. They can also be considered as three parallel processes, synchronously composed: at each reaction, the process computing `advance` instantaneously computes its result and broadcasts it to the other processes.

2.3. Natural semantics of Lustre

Let us give (a simplified version of) the semantics of Lustre, in structural operational semantics [30], in order to show that a Lustre program behaves as an automaton. In particular, we have to identify the notion of *state* of a program. Let Var ($\ni x$) be the set of variable identifiers appearing in a program. A *memory* is a function σ from Var to the set Val ($\ni v$) of values.⁴ Let σ_{In} and σ_{Out} , respectively, denote the restriction of a memory σ to input and output variables. Basically, a reaction of a program computes an output memory σ_{Out} from an input memory σ_{In} ; but, for doing so, it needs also some information about the past execution, to compute the results of the operators “pre” and “ \rightarrow ”. Without loss of generality, let us assume that any “pre” operator in a program, is applied to a single variable. Then, the value of “pre(x)” at a given reaction is the value of x at the previous reaction, i.e., in the previous memory. Thus, we have to keep track of the previous memory (denoted π) to compute the current reaction (initially, the previous memory associates the undefined value *nil* with each identifier). Moreover, the result of “ $e_1 \rightarrow e_2$ ” is the result of e_1 if the reaction is the first one, otherwise it is the result of e_2 . So, we also need a Boolean flag, say *init*, which is true only at the first reaction of the program. For simplicity, assume *init* is an auxiliary variable, the value of which is given by the memory. This value is true only in the initial memory.

⁴ We do not detail the type rules, which are standard.

Now, the past memory π constitutes the state of the program. So, a reaction of the program will be denoted $\pi \xrightarrow[\sigma_{Out}]{\sigma_{In}} \sigma$ to express that, in the state π the reaction to the input memory σ_{In} computes the whole memory σ , whose restriction to outputs is σ_{Out} . A behavior of the program will be a sequence $(\langle \sigma_{In}^1, \sigma_{Out}^1 \rangle, \langle \sigma_{In}^2, \sigma_{Out}^2 \rangle, \dots)$, where the sequence of memories $(\sigma^1, \sigma^2, \dots)$ satisfies

$$\sigma^n \xrightarrow[\sigma_{Out}^{n+1}]{\sigma_{In}^{n+1}} \sigma^{n+1} \quad \forall n \geq 0$$

where the initial memory σ_0 satisfies $\sigma^0(x) = nil, \forall x \neq init$, and $\sigma^0(init) = true$.

We start from a simplified abstract syntax:

```

program ::= node(In)(Out)equations
equations ::= equation | equations ; equation
equation ::= x=exp
exp ::= x | k | op(exp1, ..., expn) | pre(x) | exp1 -> exp2

```

where **In** and **Out** are lists of input and output identifiers, **x** stands for any identifier, **k** for any constant, and **op** for any n -ary data operator.

Fig. 2 gives the semantic rules, defining the following predicates:

program : $\pi \xrightarrow[\sigma_{Out}]{\sigma_{In}} \sigma$, which expresses that $\pi \xrightarrow[\sigma_{Out}]{\sigma_{In}} \sigma$ is a reaction of **program**.

equations : $\pi \rightarrow \sigma$, which expresses that σ is a current memory consistent with the evaluation of **equations** in the state π .

exp : $(\pi, \sigma) \rightarrow v$, which expresses that in the state π , v is the value of the expression **exp**, evaluated in the current memory σ .

Lustre static semantics (which is not described here) ensures that these rules define a deterministic semantics: in each state π , the current memory σ is a *function* of the input memory σ_{In} (and thus, so are the current outputs and the next state).

$$\begin{array}{c}
 \frac{\text{equations} : \pi \longrightarrow \sigma}{\text{node (In)(Out) equations} : \pi \xrightarrow[\sigma_{Out}]{\sigma_{In}} \sigma} \\
 \\
 \frac{\text{equations} : \pi \longrightarrow \sigma \quad , \quad \text{equation} : \pi \longrightarrow \sigma}{\text{equations; equation} : \pi \longrightarrow \sigma} \quad \frac{\text{exp} : (\pi, \sigma) \rightarrow \sigma(x)}{x = \text{exp} : \pi \longrightarrow \sigma} \\
 \\
 x : (\pi, \sigma) \rightarrow \sigma(x) \quad k : (\pi, \sigma) \rightarrow k \quad \frac{\text{exp}_i : (\pi, \sigma) \rightarrow v_i, \quad i = 1..n}{\text{op}(\text{exp}_1, \dots, \text{exp}_n) : (\pi, \sigma) \rightarrow \text{op}(v_1, \dots, v_n)} \\
 \\
 \frac{}{\text{pre}(x) : (\pi, \sigma) \rightarrow \pi(x)} \quad \text{init} : (\pi, \sigma) \rightarrow \text{false} \\
 \\
 \frac{\pi(\text{init}) = \text{true} \quad , \quad \text{exp}_1 : (\pi, \sigma) \rightarrow v_1}{\text{exp}_1 \rightarrow \text{exp}_2 : (\pi, \sigma) \rightarrow v_1} \quad \frac{\pi(\text{init}) = \text{false} \quad , \quad \text{exp}_2 : (\pi, \sigma) \rightarrow v_2}{\text{exp}_1 \rightarrow \text{exp}_2 : (\pi, \sigma) \rightarrow v_2}
 \end{array}$$

Fig. 2. Semantic rules.

3. Compilation and verification (or synchronous programming and abstract interpretation nicely fit together)

3.1. Control structure synthesis

In this section, we consider the problem of compiling synchronous languages. The code of a reactive system is generally subject to dramatic constraints, concerning either the reaction time (real-time systems), or the size of the code (embedded systems), or both. So, a wide range of compiling techniques is needed, so as to adjust the size and performances of the code to the requirements.

In the preceding section, we have seen that a synchronous program behaves as a deterministic automaton. There are two extreme ways for implementing such an automaton:

- It can be implemented as a single function, taking the current state and input as parameters, and returning the current output and the next state. In this solution, the code executed at each reaction is the same. The structure of the object code is a single infinite loop, whose body consists of 3 steps: (1) waiting for inputs (“get” statement), (2) computing and emitting (“put” statement) outputs, and (3) computing and storing the memory for the next step. As a very simple example, consider

```

init := true;
loop
  get(ON,OFF);
  if init then
    S := if ON then true else false;
    init:=false;
  else
    S := if ON then true elsif OFF then false else pre_S
  endif
  emit(S); pre_S := S;
endloop
    
```

Fig. 3. Single-loop code of the node SWITCH.

State.1 init	State.2 not init and pre_S	State.3 not init and not pre_S
<pre> get(ON,OFF); if ON then S := true; goto State.2 else S := false; goto State.3 </pre>	<pre> get(ON,OFF); if ON then S := true; goto State.2 elsif OFF then S := false; goto State.3 else S := true; goto State.2 </pre>	<pre> get(ON,OFF); if ON then S := true; goto State.2 else S := false; goto State.3 </pre>

Fig. 4. Detailed automaton code of the node SWITCH.

the node SWITCH presented in Section 2.2. The single loop code could have the structure shown in Fig. 3.

- Another solution is to produce a different function for each state that can be reached by the automaton (provided this set of states is finite). The current input is the only parameter of each function, which returns the current output and selects the function to be applied at the next reaction. For the SWITCH example, we get 3 reachable states, so 3 functions⁵ as shown in Fig. 4.

The latter solution produces a more efficient code, but can only be applied when the set of reachable states is finite. Moreover, even in the finite case, the number of states (and thus, the size of the code) grows exponentially with the number of variables. Intermediate solutions can be defined as follows:

Let \mathcal{S} be a set of *state variables*, with finite domains (generally Boolean), selected by the user. Let $\sigma_{\mathcal{S}}$ be the restriction of the memory σ to \mathcal{S} , and $[\sigma]_{\mathcal{S}}$ be the equivalence class of σ for the relation $\sigma \stackrel{\mathcal{S}}{\approx} \sigma' \iff \sigma_{\mathcal{S}} = \sigma'_{\mathcal{S}}$. Obviously, $\stackrel{\mathcal{S}}{\approx}$ is an equivalence relation with finitely many classes.

The idea is to distinguish the code to be executed according to the value of state variables – i.e., to the class $[\pi]_{\mathcal{S}}$ of the previous memory —, while the other variables are stored in the actual memory. Let $\mathcal{D} = \text{Var} \setminus \mathcal{S}$. The code corresponding to a class $[\pi]_{\mathcal{S}}$ is a function of $(\pi_{\mathcal{D}}, \sigma_{in})$ returning the output σ_{out} , the next class $[\sigma]_{\mathcal{S}}$ and the memory $\sigma_{\mathcal{D}}$. Ideally, this code should be generated only for reachable classes, and we would have a transition $[\pi]_{\mathcal{S}} \rightarrow [\sigma]_{\mathcal{S}}$ if and only if

$$\exists \pi' \stackrel{\mathcal{S}}{\approx} \pi, \exists \sigma' \stackrel{\mathcal{S}}{\approx} \sigma, \quad \text{with } \pi' \xrightarrow[\sigma'_{out}]{\sigma'_{in}} \sigma'$$

But, in general, the reachability of a class cannot be determined statically, without taking fully into account the behavior of variables in \mathcal{D} . So, the reachability is only approximated, and some unreachable states can be considered. The construction of the control automaton can be viewed as a *partial evaluation* [22] of the program⁶ by abstracting a memory π into

$$\alpha(\pi) = \lambda x. \begin{cases} \pi(x) & \text{if } x \in \mathcal{S} \\ \top & \text{if } x \in \mathcal{D} \end{cases}$$

where \top is a non-determined value, with respect to which most operators are strict. So, state variables are “static variables” of partial evaluation, and the code executed in each state is a specialization of the single loop code.

For instance, Fig. 5 shows the resulting code for the program “train” when the state variables are all the Boolean variables. The control automaton has 5 states: the initial state (State₀), the states where the train is on time (State₄), early (State₂), late

⁵ Notice that the code executed in states 1 and 3 is the same, so the automaton has actually only 2 states. Algorithms to compile synchronous programs into *minimal* automata are available [6].

⁶ More precisely, [9] connects this technique with Wadler’s *deforestation* [32].

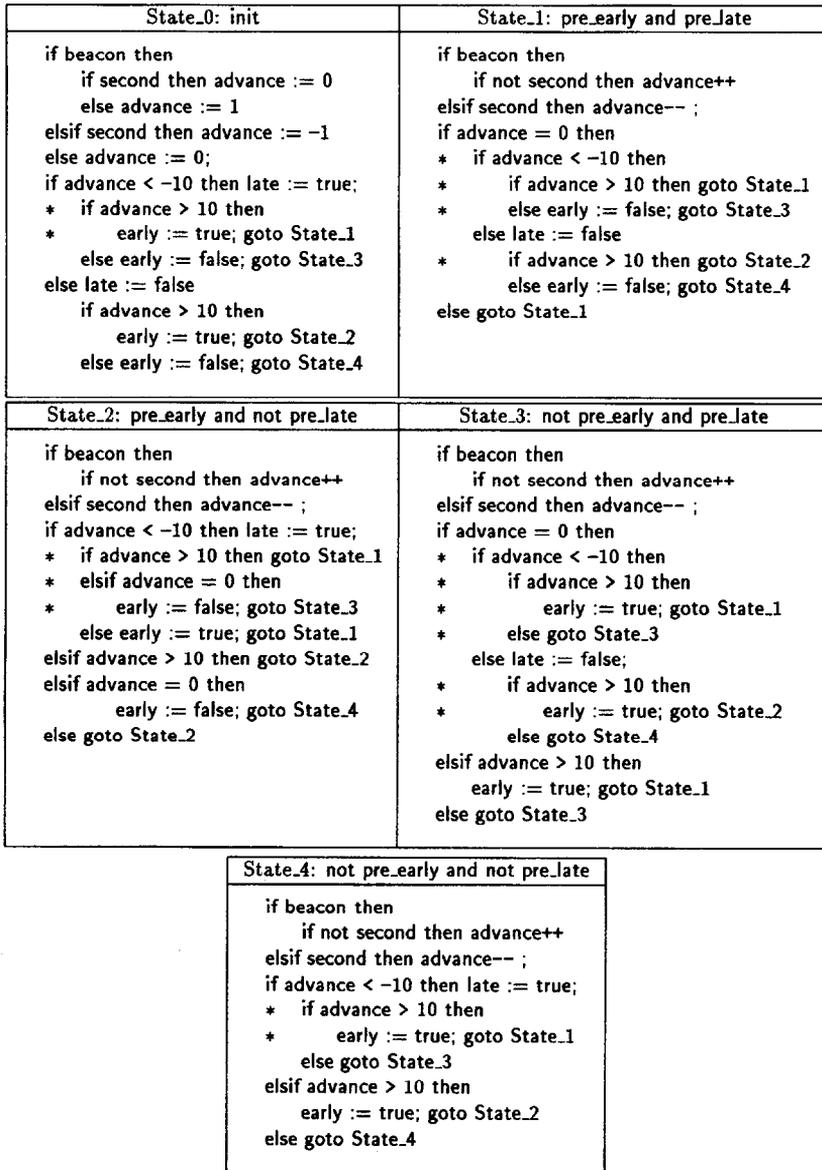


Fig. 5. Control structure of the “train” example.

(State₃), and a clearly unreachable state where it is both early and late (State₁). Also, many transitions are clearly irrelevant. In this example, state variables highly depend on numerical variables, so the structure of the resulting control automaton is rather poor. Notice that all the lines marked with a “*” would be removed by a straightforward numerical analysis of path condition satisfiability.

3.2. Program verification

Synchronous programming and abstract interpretation nicely cooperate for reactive system verification. This is due to 3 reasons, that we will develop in turn:

1. In the field of reactive systems, the main goal, in practice, is to prove some critical *safety* properties.
2. In addition to being well-suited to reactive system programming, synchronous programming makes possible an easy and modular expression of safety properties in terms of *invariants*.
3. Abstract interpretation provides an approximate, conservative, verification of invariance properties.

3.2.1. Safety properties of reactive systems

It is a commonplace to say that reactive systems are those whose reliability is the most critical, because of the dramatic consequences that failures can have in this field. However, experience shows that, in practice, the most important goal is to ensure some critical properties, rather than to completely prove program correctness. Moreover, it was noted elsewhere [18, 31] that, almost always, these properties are *safety properties*, i.e., properties expressing that something bad never happens. As a matter of fact, in non-reactive systems, *liveness properties* often result from the abstraction of a real-time property; in reactive systems, real-time properties cannot be abstracted. For instance, nobody cares that a train *eventually* stops; it must stop within a given delay or distance.

3.2.2. Translating safety into invariance

Let us recall that, if a program behavior violates a safety property, it does so at a precise step: one can identify the first reaction which violates the property.

As a consequence, given a safety property P about the behavior of a program Π , one can write another program Ω_P – called an *observer* of P –, which takes as input the input/output variables of Π , and emits an “alarm” whenever P is violated (see Fig. 6).

Now, instead of proving P about Π , we can prove that the parallel composition of Π and Ω_P never emits an alarm. In other words, the safety property P has been

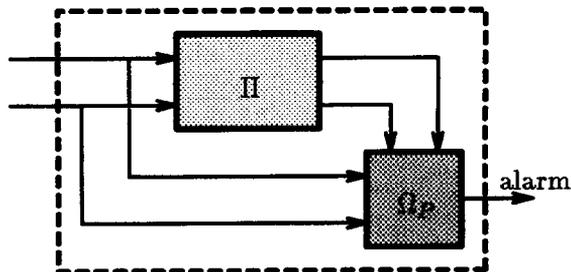


Fig. 6. Observing a safety property.

changed into an invariant. Notice that the synchronous composition mechanism allows this translation to be *modular*. In an asynchronous model, in order to ensure that the observer accurately perceives the behavior of Π , one would have to add explicit synchronizations in Π : it would not be modular, and, worse, it could modify the behavior of Π , thus invalidating the result of the proof.

For instance, consider the property of our program “train”, stating that it cannot move directly from a state where it is “early” to a state where it is “late”. An observer of this property will take as input the variables **early** and **late** computed by the program **train**, and compute a variable **alarm** as follows: **alarm** is true whenever **late** is true and **early** was true at the previous instant:

```
node observer (early, late: bool) returns (alarm: bool);
let
  alarm = late and (false -> pre (early));
tel
```

The verification of the property comes down to proving that the following program never returns a true value:

```
node verify (beacon, second: bool) returns (alarm: bool);
var early, late: bool;
let
  (early, late) = train(beacon, second);
  alarm = observer(early, late);
tel
```

3.2.3. Approximate verification of invariants

Let Π be a program, considered as a transition system $(S, \mathcal{I}, \rightarrow)$ (where S is a set of states, $\mathcal{I} \subseteq S$ is the set of initial states, and $\rightarrow \subseteq S \times S$ is the transition relation). Let *pre* and *post* be the classical *precondition* and *postcondition* functions, from 2^S to 2^S :

$$pre = \lambda X. \{s \in S \mid \exists s' \in X, s \rightarrow s'\}, \quad post = \lambda X. \{s' \in S \mid \exists s \in X, s \rightarrow s'\}$$

If X is a set of states, let $\mathcal{R}(X)$ be the set of states which are reachable from X , and $\mathcal{A}(X)$ be the set of states from which X is reachable. We have:

$$\mathcal{R}(X) = \mu Y. X \cup post(Y), \quad \mathcal{A}(X) = \mu Y. X \cup pre(Y)$$

To prove that a given set \mathcal{P} of states is an invariant of a program, we can show that the set $\mathcal{R}(\mathcal{I})$ of reachable states of the program is included in \mathcal{P} (forward verification) or, equivalently, that the set \mathcal{I} of initial states of the program does not intersect the set $\mathcal{A}(\neg\mathcal{P})$ of states which can lead outside of \mathcal{P} (backward verification). In general, neither $\mathcal{R}(\mathcal{I})$ nor $\mathcal{A}(\neg\mathcal{P})$ can be automatically computed. However, both are least fixpoints that can be upper-approximated using available abstract interpretations [24, 33, 10, 13, 14, 27]. Let $\widehat{\mathcal{R}}(\mathcal{I})$ and $\widehat{\mathcal{A}}(\neg\mathcal{P})$ be upper approximations

of $\mathcal{R}(\mathcal{I})$ and $\mathcal{A}(\neg\mathcal{P})$, respectively; then the invariance of \mathcal{P} is ensured as soon as either $\mathcal{R}(\mathcal{I}) \subseteq \mathcal{P}$ or $\mathcal{I} \cap \mathcal{A}(\neg\mathcal{P}) = \emptyset$. Obviously, these conditions are only necessary conditions, so the verification method is only partial, and nothing can be inferred when it fails. However the results can be strengthened by combining both approaches: for instance, when $\mathcal{R}(\mathcal{I})$ intersects $\neg\mathcal{P}$, one can compute an upper approximation of $\mathcal{A}(\mathcal{R}(\mathcal{I}) \cap \neg\mathcal{P})$, and show that it does not intersect \mathcal{I} , and so on.

Several authors [18, 7] simply use the control automaton built by synchronous language compilers, to prove logical properties of programs. As a matter of fact, the control automaton is also an abstraction of the program behavior, through the abstraction/concretization pair:

$$\{\sigma \mid \sigma_{\mathcal{P}} = s\} \xrightleftharpoons[\gamma]{\alpha} s$$

Obviously, if the automaton produced from the verification program (i.e., the composition of the initial program and its observer, as in Fig. 6) never emits an alarm – or, more concretely, if the code generated by the compiler does not contain any statement “alarm := true” –, the property is satisfied.

This approach fails in proving our example property (Section 3.2.2), because it strongly depends on the behavior of numerical variables.

Here, we show a more powerful technique [15], combining the control automaton with the abstract interpretation proposed in [13]: in this interpretation, a set of numerical states (vectors) is approximated by its *convex hull*, i.e., the least convex polyhedron containing it. With each state s of the control automaton is associated a polyhedron P_s , such that (with the notations of Section 3.1), for each reachable memory π , $\pi_{\mathcal{P}} = s \Rightarrow \pi_{\mathcal{Q}} \in P_s$. So, if $P_s = \emptyset$, the state s is not reachable.

As in any abstract interpretation, each concrete program statement is given an abstract counterpart, working on polyhedra. So, for each state s_i , P_{s_i} is defined as a function $F_i(P_{s_1}, \dots, P_{s_n})$, where F_i is defined by means of abstract operations on polyhedra. Moreover, in order to avoid infinite iterations when solving this abstract system of equations, a widening operation is applied, which extrapolates the limit of iterations.

Without explaining the technique in more detail (see [15]), we show the results on the complete subway example.

3.2.4. The complete subway example

Let us come back to our subway traffic regulation system. We assume that

- when a train is early, it puts on the brake. Continuously braking stops the train before encountering 10 beacons.
- the “second” signal is broadcast from a central clock. A train signals that it is late to the central clock, which does not emit the “second” as long as at least one train is late.

From a LUSTRE program simulating the whole system, the compiler generates a control automaton with 9 states. The approximate convex analysis shows that only 5 states are

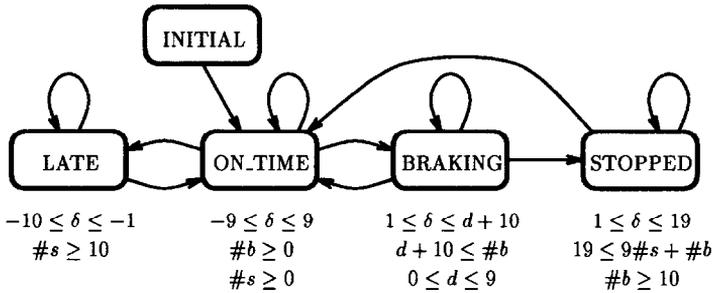


Fig. 7. Linear analysis of the subway program.

reachable, and gives the results shown by Fig. 7 (where $\#b$ and $\#s$ are the numbers of beacons and of seconds, and where δ stands for $\#b - \#s$).

The property expressed in Section 3.2.2 is obvious on this automaton. Moreover, the absolute difference $|\#b - \#s|$ is shown to be bounded. With two trains, the analysis shows that the absolute difference $|\#b_1 - \#b_2|$ between the numbers of beacons encountered by each train is also bounded, which means that if the number of beacons initially separating the trains is greater than the bound, then the trains cannot collide.

The whole approach can be viewed as a compound abstract interpretation: first, the control structure is generated, which takes into account the behavior of (some) Boolean variables, and provides a finite partition to the numerical analysis. The more detailed is the partition, the more precise is the numerical analysis.

4. Conclusion

After recalling the principles of synchronous programming, we have shown two connections with abstract interpretation techniques:

- When the compilers of synchronous programs into interpreted automata were first designed, nobody seemed to have been aware that this technique was a standard application of partial evaluation. Synchronous language compilers constitute an interesting application of this technique, which permits to choose a flexible compromise between code size and performances.
- Until now, abstract interpretation has been only seldom used in program verification. Such applications are developing now, since the finite-state-based methods are reaching their limits. We showed that synchronous programs are a good application field for verification based on abstract interpretation, since (1) synchronous languages are used for programming critical systems, where (2) almost all critical properties are safety properties; (3) synchronous observers allow these properties to be readily translated into invariants, which (4) can be conservatively verified by abstract interpretation.

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