

Theoretical Computer Science

Theoretical Computer Science 274 (2002) 277

www.elsevier.com/locate/tcs

## Author index volume 274 (2002)

(The issue number is given in front of the page numbers)

de Boer, F.S., A Hoare logic for dynamic networks of asynchronously communicating	
deterministic processes	(1-2) 3-41
de Simone, R., see D. Sangiorgi	(1-2) 1- 1
Henzinger, T.A., see PY Schobbens	(1-2) 151-182
Hermanns, H., U. Herzog and JP Katoen, Process algebra for performance evaluation	(1-2) 43- 87
Herzog, U., see H. Hermanns	(1-2) 43- 87
Katoen, JP, see H. Hermanns	(1-2) 43- 87
Lugiez, D. and Ph. Schnoebelen, The regular viewpoint on PA-processes	(1-2) 89-115
Madhusudan, P. and P.S. Thiagarajan, Branching time controllers for discrete event	
systems	(1-2) 117-149
Raskin, JF, see PY Schobbens	(1-2) 151-182
Sangiorgi, D. and R. de Simone, Editorial	(1-2) 1- 1
Schnoebelen, Ph., see D. Lugiez	(1-2) 89-115
Schobbens, PY, JF Raskin and T.A. Henzinger, Axioms for real-time logics	(1-2) 151-182
Sewell, P., From rewrite rules to bisimulation congruences	(1-2) 183-230
Thiagarajan, P.S., see P. Madhusudan	(1-2) 117-149
Yoshida, N., Minimality and separation results on asynchronous mobile processes -	
representability theorems by concurrent combinators	(1-2) 231-276