

# Design and Real-time Implementation of Perturbation Observer based Sliding-mode Control for VSC-HVDC Systems

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## Abstract

This paper develops a perturbation observer based sliding-mode control (POSMC) scheme for voltage source converter based high voltage direct current (VSC-HVDC) systems. The combinatorial effect of nonlinearities, parameter uncertainties, unmodelled dynamics and time-varying external disturbances is aggregated into a perturbation, which is estimated online by a sliding-mode state and perturbation observer. POSMC does not require an accurate system model and only one state measurement is needed. Moreover, a significant robustness can be provided through the real-time compensation of the per-

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turbation. Four case studies are carried out on the VSC-HVDC system, such as active and reactive power tracking, AC bus fault, system parameter uncertainties, and weak AC grid connection. Simulation results verify its advantages over vector control and feedback linearization sliding-mode control. Then a hardware-in-the-loop (HIL) test is undertaken to validate the implementation feasibility of the proposed approach.

*Keywords:* sliding-mode control, perturbation observer, VSC-HVDC systems, HIL test

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## 1. Introduction

Voltage source converter based high voltage direct current (VSC-HVDC) systems using insulated gate bipolar transistor (IGBT) technology have attracted increasing attentions due to the interconnection between the mainland and offshore wind farms, power flow regulation in alternating current (AC) power systems, long distance transmission (Flourentzou, Agelidis and Demetriades, 2009), and introduction of the supergrid, which is a large scale power grid interconnected between national power grids (Hertema and Ghandhari, 2010). The main feature of the VSC-HVDC system is that no external voltage source is needed for communication, while active and reactive power at each AC grid can be independently controlled (Zhang, 2011).

12 Traditionally, control of the VSC-HVDC system utilizes a nested-loop  
13  $d$ - $q$  vector control (VC) approach based on linear proportional-integral (PI)  
14 methods (Haileselassie, Molinas and Undeland, 2008), whose control perfor-  
15 mance may be degraded with the change of operation conditions as its con-  
16 trol parameters are tuned from one-point linearization model (Li, Haskew  
17 and Xu, 2010). As VSC-HVDC systems are highly nonlinear resulting from  
18 converters and also operate in power systems with modelling uncertainties,  
19 many advanced control approaches are developed to provide a consistent  
20 control performance under various operation conditions, such as feedback  
21 linearization control (FLC) (Ruan, Li, Peng, Sun and Lie, 2007), which fully  
22 compensated the nonlinearities with the requirement of an accurate system  
23 model. Linear matrix inequality (LMI)-based robust control was developed  
24 in (Durrant, Werner and Abbott, 2004) to maximize the size of the uncer-  
25 tainty region within which closed loop stability is maintained. In addition,  
26 adaptive backstepping control was designed to estimate the uncertain pa-  
27 rameters by (Ruan, Li, Jiao, Sun and Lie, 2007). In (Zhang, Harnfors and  
28 Nee, 2011), power-synchronization control was employed to greatly increase  
29 the short-circuit capacity to the AC system. However, the aforementioned  
30 methods may not be adequate to simultaneously handle perturbations such

31 as modelling uncertainties and time-varying external disturbances .

32       Based on the variable structure control strategy, sliding-mode control  
33 (SMC) is an effective and high-frequency switching control for nonlinear sys-  
34 tems with modelling uncertainties and time-varying external disturbances.  
35 The main idea of SMC is to maintain the system sliding on a surface in  
36 the state space via an appropriate switching logic, it features the simple  
37 implementation, disturbance rejection, fast response and strong robustness  
38 (Lordelo and Fazzolari, 2014). While the malignant effect of chattering phe-  
39 nomenon can be reduced by predictive variable structure (Huo, 2008) and  
40 self-tuning sliding mode (Zong, Zhao and Zhang, 2010), SMC has been ap-  
41 plied on electrical vehicles (Gokasan, Bogosyan and Goering, 2006), power  
42 converters (Kessal and Rahmani, 2014), induction machines (Lascu, Boldea  
43 and Blaabjerg, 2004), wind turbines (Beltran, Ahmed-Ali and Benbouzid,  
44 2008), ect. Moreover, a feedback linearization sliding-mode control (FLSMC)  
45 has been developed for the VSC-HVDC system to offer invariant stability to  
46 modelling uncertainties by (Moharana and Dash, 2010). Basically, SMC as-  
47 sumes perturbations to be bounded and the prior knowledge of these upper  
48 bounds is required. However, it may be difficult or sometimes impossible  
49 to obtain these upper bounds, thus the supreme upper bound is chosen to

50 cover the whole range of perturbations. As a consequence, SMC based on  
51 this knowledge becomes over-conservative which may cause a poor tracking  
52 performance and undesirable control oscillations (Edwards and Spurgeon,  
53 1998).

54 During the past decades, several elegant approaches based on observers  
55 have been proposed to estimate perturbations, including the unknown input  
56 observer (UIO) (Johnson, 1971), the disturbance observer (DOB) (Chen, Bal-  
57 lance, Gawthrop and O'Reilly, 2000), the equivalent input disturbance (EID)  
58 based estimation (She, Fang, Ohyama, Hashimoto and Wu, 2008), enhanced  
59 decentralized PI control via advanced disturbance observer (Sun, Li and Lee,  
60 2015), the extended state observer (ESO) based active disturbance rejection  
61 control (ADRC) (Han, 2009), and practical multivariable control based on  
62 inverted decoupling and decentralized ADRC (Sun, Dong, Li and Lee, 2016).  
63 Among the above listed approaches, ESO requires the least amount of sys-  
64 tem information, in fact, only the system order needs to be known (Guo and  
65 Zhao, 2011). Due to such promising features, ESO based control schemes  
66 have become more and more popular. Recently, ESO based SMC has been  
67 developed to remedy the over-conservativeness of SMC via an online per-  
68 turbation estimation. It observes both system states and perturbations by

69 defining an extended state to represent the lumped perturbation, which can  
70 be then compensated online to improve the performance of system. Related  
71 applications can be referred to mechanical systems (Kwon and Chung, 2004),  
72 missile systems (Xia, Zhu and Fu, 2011), spherical robots (Yue, Liu, An and  
73 Sun, 2014), and DC-DC buck power converters (Wang, Li, Yang, Wu and Li,  
74 2015).

75 This paper uses an ESO called sliding-mode state and perturbation ob-  
76 server (SMSPO) (Jiang, Wu and Wen, 2002; Liu, Wu, Zhou and Jiang, 2014)  
77 to estimate the combinatorial effect of nonlinearities, parameter uncertain-  
78 ties, unmodelled dynamics and time-varying external disturbances existed  
79 in VSC-HVDC systems, which is then compensated by the perturbation ob-  
80 server based sliding-mode control (POSMC). The motivation to use POSMC  
81 in this paper rather than SMC and our previous work (Jiang, Wu and Wen,  
82 2002; Liu, Wu, Zhou and Jiang, 2014; Yang, Jiang, Yao and Wu, 2015) can  
83 be summarized as follows:

- 84 • The robustness of POSMC to the perturbation mostly depends on  
85 the perturbation compensation while the ground of the robustness in SMC  
86 (Gokasan, Bogosyan and Goering, 2006; Kessal and Rahmani, 2014; Lascu,  
87 Boldea and Blaabjerg, 2004; Beltran, Ahmed-Ali and Benbouzid, 2008; Mo-

88 harana and Dash, 2010) is the discrete switching input. Furthermore, the  
89 upper bound of perturbation is replaced by the smaller bound of its estima-  
90 tion error, thus an over conservative control input is avoided and the tracking  
91 accuracy is improved.

92 • POSMC can provide greater robustness than that of nonlinear adap-  
93 tive control (NAC) (Jiang, Wu and Wen, 2002; Liu, Wu, Zhou and Jiang,  
94 2014) and perturbation observer based adaptive passive control (POAPC)  
95 (Yang, Jiang, Yao and Wu, 2015) due to its inherent property of disturbance  
96 rejection.

97 Compared to VC (Li, Haskew and Xu, 2010), POSMC can provide a  
98 consistent control performance under various operation condition of the VSC-  
99 HVDC system and improve the power tracking by eliminating the power  
100 overshoot. Compared to FLSMC (Moharana and Dash, 2010), POSMC only  
101 requires the measurement of active and reactive power and DC voltage, which  
102 can provide a significant robustness and avoid an over-conservative control  
103 input as the real perturbation is estimated and compensated online. Four  
104 case studies are carried out to evaluate the control performance of POSMC  
105 through simulation, such as active and reactive power tracking, AC bus fault,  
106 system parameter uncertainties and weak AC gird connection. Compared

107 to the author's previous work on SMSPO (Jiang, Wu and Wen, 2002; Liu,  
108 Wu, Zhou and Jiang, 2014), a dSPACE simulator based hardware-in-the-loop  
109 (HIL) test is undertaken to validate its implementation feasibility.

110 The rest of the paper is organized as follows. In Section 2, the model  
111 of the two-terminal VSC-HVDC system is presented. In Section 3, POSMC  
112 design for the VSC-HVDC system is developed and discussed. Section 4 and  
113 5 present the simulation and HIL results, respectively. Finally, conclusions  
114 are drawn in Section 6.

## 115 **2. VSC-HVDC System Modelling**

116 There are two VSCs in the VSC-HVDC system shown in Fig. 1, in which  
117 the rectifier regulates the DC voltage and reactive power, while the inverter  
118 regulates the active and reactive power. Only the balanced condition is  
119 considered, e.g., the three phases have identical parameters and their voltages  
120 and currents have the same amplitude while each phase shifts  $120^\circ$  between  
121 themselves. The rectifier dynamics can be written at the angular frequency



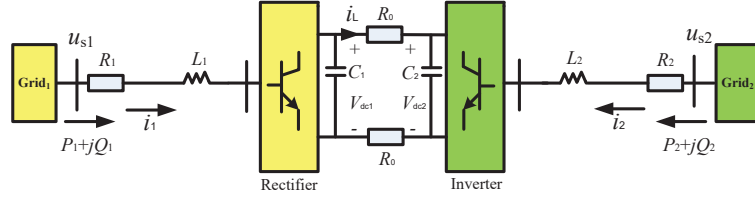


Figure 1: A standard two-terminal VSC-HVDC system

122  $\omega$  as (Ruan, Li, Jiao, Sun and Lie, 2007)

$$\left\{ \begin{array}{l} \frac{di_{d1}}{dt} = -\frac{R_1}{L_1}i_{d1} + \omega i_{q1} + u_{d1} \\ \frac{di_{q1}}{dt} = -\frac{R_1}{L_1}i_{q1} - \omega i_{d1} + u_{q1} \\ \frac{dV_{dc1}}{dt} = \frac{3u_{sq1}i_{q1}}{2C_1V_{dc1}} - \frac{i_L}{C_1} \end{array} \right. \quad (1)$$

123 where the rectifier is connected with the AC grid via the equivalent resistance  
 124 and inductance  $R_1$  and  $L_1$ , respectively.  $C_1$  is the DC bus capacitor,  $u_{d1} =$   
 125  $\frac{u_{sd1} - u_{rd}}{L_1}$  and  $u_{q1} = \frac{u_{sq1} - u_{rq}}{L_1}$ .

126 The inverter dynamics is written as

$$\left\{ \begin{array}{l} \frac{di_{d2}}{dt} = -\frac{R_2}{L_2}i_{d2} + \omega i_{q2} + u_{d2} \\ \frac{di_{q2}}{dt} = -\frac{R_2}{L_2}i_{q2} - \omega i_{d2} + u_{q2} \\ \frac{dV_{dc2}}{dt} = \frac{3u_{sq2}i_{q2}}{2C_2V_{dc2}} + \frac{i_L}{C_2} \end{array} \right. \quad (2)$$

127 where the inverter is connected with the AC grid via the equivalent resistance  
 128 and inductance  $R_2$  and  $L_2$ , respectively.  $C_2$  is the DC bus capacitor,  $u_{d2} =$

129  $\frac{u_{sd2}-u_{id}}{L_2}$  and  $u_{q2} = \frac{u_{sq2}-u_{iq}}{L_2}$ .

The interconnection between the rectifier and inverter through DC cable is given as

$$V_{dc1}i_L = V_{dc2}i_L + 2R_0i_L^2 \quad (3)$$

130 where  $R_0$  represents the equivalent DC cable resistance.

131 The phase-locked loop (PLL) (Jovcic, 2003) is used during the transfor-  
132 mation of the  $abc$  frame to the  $dq$  frame. In the synchronous frame,  $u_{sd1}$ ,  
133  $u_{sd2}$ ,  $u_{sq1}$ , and  $u_{sq2}$  are the  $d$ ,  $q$  axes components of the respective AC grid  
134 voltages;  $i_{d1}$ ,  $i_{d2}$ ,  $i_{q1}$ , and  $i_{q2}$  are that of the line currents;  $u_{rd}$ ,  $u_{id}$ ,  $u_{rq}$ , and  
135  $u_{iq}$  are that of the converter input voltages.  $P_1$ ,  $P_2$ ,  $Q_1$ , and  $Q_2$  are the active  
136 and reactive powers transmitted from the AC grid to the VSC;  $V_{dc1}$  and  $V_{dc2}$   
137 are the DC voltages; and  $i_L$  is the DC cable current.

138 At the rectifier side, the  $q$ -axis is set to be in phase with the AC grid  
139 voltage  $u_{s1}$ . Correspondingly, the  $q$ -axis is set to be in phase of the AC grid  
140 voltage  $u_{s2}$  at the inverter side. Hence,  $u_{sd1}$  and  $u_{sd2}$  are equal to 0 while  
141  $u_{sq1}$  and  $u_{sq2}$  are equal to the magnitude of  $u_{s1}$  and  $u_{s2}$ . Note that this paper  
142 adopts such framework from (Moharana and Dash, 2010; Ruan, Li, Jiao, Sun

143 and Lie, 2007; Ruan, Li, Peng, Sun and Lie, 2007) to provide a consistent  
 144 control design procedure and an easy control performance comparison, other  
 145 framework can also be used as shown in (Li, Haskew and Xu, 2010; Zhang,  
 146 Harnefors and Nee, 2011). The only difference of these two alternatives is  
 147 the derived system equations, while the control design is totally the same.  
 148 In addition, it is assumed that the VSC-HVDC system is connected to suf-  
 149 ficiently strong AC grids, such that the AC grid voltage remains as an ideal  
 150 constant. The power flows from the AC grid can be given as

$$\left\{ \begin{array}{l} P_1 = \frac{3}{2} (u_{sq1}i_{q1} + u_{sd1}i_{d1}) = \frac{3}{2}u_{sq1}i_{q1} \\ Q_1 = \frac{3}{2} (u_{sq1}i_{d1} - u_{sd1}i_{q1}) = \frac{3}{2}u_{sq1}i_{d1} \\ P_2 = \frac{3}{2} (u_{sq2}i_{q2} + u_{sd2}i_{d2}) = \frac{3}{2}u_{sq2}i_{q2} \\ Q_2 = \frac{3}{2} (u_{sq2}i_{d2} - u_{sd2}i_{q2}) = \frac{3}{2}u_{sq2}i_{d2} \end{array} \right. \quad (4)$$

### 151 3. POSMC Design for the VSC-HVDC System

#### 152 3.1. Perturbation observer based sliding-mode control

153 Consider an uncertain nonlinear system which has the following canonical  
 154 form

$$\left\{ \begin{array}{l} \dot{x} = Ax + B(a(x) + b(x)u + d(t)) \\ y = x_1 \end{array} \right. \quad (5)$$

155 where  $x = [x_1, x_2, \dots, x_n]^T \in \mathbb{R}^n$  is the state variable vector,  $u \in \mathbb{R}$  and  
156  $y \in \mathbb{R}$  are the control input and system output, respectively.  $a(x) : \mathbb{R}^n \mapsto \mathbb{R}$   
157 and  $b(x) : \mathbb{R}^n \mapsto \mathbb{R}$  are unknown smooth functions, and  $d(t) : \mathbb{R}^+ \mapsto \mathbb{R}$   
158 represents the time-varying external disturbance. The  $n \times n$  matrix  $A$  and  
159 the  $n \times 1$  matrix  $B$  are of the canonical form as follows

$$A = \begin{bmatrix} 0 & 1 & 0 & \cdots & 0 \\ 0 & 0 & 1 & \cdots & 0 \\ \vdots & & & & \vdots \\ 0 & 0 & 0 & \cdots & 1 \\ 0 & 0 & 0 & \cdots & 0 \end{bmatrix}_{n \times n}, \quad B = \begin{bmatrix} 0 \\ 0 \\ \vdots \\ 0 \\ 1 \end{bmatrix}_{n \times 1}$$

160 The perturbation of system (5) is defined as (Jiang, Wu and Wen, 2002; Liu,  
161 Wu, Zhou and Jiang, 2014; Yang, Jiang, Yao and Wu, 2015)

$$\Psi(x, u, t) = a(x) + (b(x) - b_0)u + d(t) \quad (6)$$

162 From the original system (5), the last state  $x_n$  can be rewritten in the presence  
 163 of perturbation (6) as follows

$$\dot{x}_n = a(x) + (b(x) - b_0)u + d(t) + b_0u = \Psi(x, u, t) + b_0u \quad (7)$$

164 Define a *fictitious state*  $x_{n+1} = \Psi(x, u, t)$ . Then, system (5) can be extended  
 165 as

$$\left\{ \begin{array}{l} y = x_1 \\ \dot{x}_1 = x_2 \\ \vdots \\ \dot{x}_n = x_{n+1} + b_0u \\ \dot{x}_{n+1} = \dot{\Psi}(\cdot) \end{array} \right. \quad (8)$$

166 The new state vector becomes  $x_e = [x_1, x_2, \dots, x_n, x_{n+1}]^T$ , and following  
 167 assumptions are made (Jiang, Wu and Wen, 2002)

168 • A.1  $b_0$  is chosen to satisfy:  $|b(x)/b_0 - 1| \leq \theta < 1$ , where  $\theta$  is a positive  
 169 constant.

170 • A.2 The functions  $\Psi(x, u, t) : \mathbb{R}^n \times \mathbb{R} \times \mathbb{R}^+ \mapsto \mathbb{R}$  and  $\dot{\Psi}(x, u, t) : \mathbb{R}^n \times$   
 171  $\mathbb{R} \times \mathbb{R}^+ \mapsto \mathbb{R}$  are bounded over the domain of interest:  $|\Psi(x, u, t)| \leq \gamma_1$ ,  
 172  $|\dot{\Psi}(x, u, t)| \leq \gamma_2$  with  $\Psi(0, 0, 0) = 0$  and  $\dot{\Psi}(0, 0, 0) = 0$ , where  $\gamma_1$  and  $\gamma_2$

173 are positive constants.

- 174 • A.3 The desired trajectory  $y_d$  and its up to  $n$ th-order derivative are
- 175 continuous and bounded.

176 The above three assumptions ensure the effectiveness of such perturbation  
 177 estimation based approach. In particular, assumptions A.1 and A.2 guar-  
 178 antee the closed-loop system stability with perturbation estimation, while  
 179 assumption A.3 ensures POSMC can drive the system state  $x$  to track a  
 180 desired state  $x_d = [y_d, y_d^{(1)}, \dots, y_d^{(n-1)}]^T$  (Jiang, 2001). In the consideration  
 181 of the worst case, e.g.,  $y = x_1$  is the only measurable state, an  $(n+1)$ th-  
 182 order SMSPO (Jiang, Wu and Wen, 2002; Liu, Wu, Zhou and Jiang, 2014)  
 183 for the extended system (8) is designed to estimate the system states and  
 184 perturbation, shown as follows

$$\left\{ \begin{array}{l} \dot{\hat{x}}_1 = \hat{x}_2 + \alpha_1 \tilde{x}_1 + k_1 \text{sat}(\tilde{x}_1) \\ \vdots \\ \dot{\hat{x}}_n = \hat{\Psi}(\cdot) + \alpha_n \tilde{x}_1 + k_n \text{sat}(\tilde{x}_1) + b_0 u \\ \dot{\hat{\Psi}}(\cdot) = \alpha_{n+1} \tilde{x}_1 + k_{n+1} \text{sat}(\tilde{x}_1) \end{array} \right. \quad (9)$$

185 where  $\tilde{x}_1 = x_1 - \hat{x}_1$ ,  $k_i$  and  $\alpha_i$ ,  $i = 1, 2, \dots, n + 1$ , are positive coefficients,  
 186 function  $\text{sat}(\tilde{x}_1)$  is defined as  $\text{sat}(\tilde{x}_1) = \tilde{x}_1/|\tilde{x}_1|$  when  $|\tilde{x}_1| > \epsilon$  and  $\text{sat}(\tilde{x}_1) =$   
 187  $\tilde{x}_1/\epsilon$  when  $|\tilde{x}_1| \leq \epsilon$ . The effect and setting of the SMSPO parameters are  
 188 provided as follows:

- 189 • **The Luenberger observer constants  $\alpha_i$ .** Which are chosen to place  
 190 the observer poles at the desired locations in the open left-half complex  
 191 plane. In other words,  $\alpha_i$  are chosen such that the root of  $s^{n+1} +$   
 192  $\alpha_1 s^n + \alpha_2 s^{n-1} + \dots + \alpha_{n+1} = (s + \lambda_\alpha)^{n+1} = 0$  is in the open left-  
 193 half complex plane. A larger value of  $\alpha_i$  will accelerate the estimation  
 194 rate of SMSPO but also result in a more significant effect of peaking  
 195 phenomenon. Thus a trade-off between the estimation rate and effect of  
 196 peaking phenomenon must be made through trial-and-error. Normally  
 197 they are set to be much larger than the root of the closed-loop system  
 198 to ensure a fast online estimation (Yang, Jiang, Yao and Wu, 2015).
- 199 • **The sliding surface constants  $k_i$ .**  $k_1 \geq |\tilde{x}_2|_{\max}$  must be chosen to  
 200 guarantee the estimation error of SMSPO (9) will enter into the sliding  
 201 surface  $S_{\text{spo}}(\tilde{x}) = \tilde{x}_1 = 0$  at  $t > t_s$  and thereafter remain  $S_{\text{spo}} = 0$ ,  $t \geq$   
 202  $t_s$  (Jiang, 2001; Jiang, Wu and Wen, 2002). While the poles of the sliding  
 203 surface  $\lambda_k$  are determined by choosing the ratio  $k_i/k_1$  ( $i = 2, 3, \dots, n +$

204 1) to put the root of  $p^n + (k_2/k_1)p^{n-1} + \dots + (k_n/k_1)p + (k_{n+1}/k_1) = (p +$   
 205  $\lambda_k)^n = 0$  to be in the open left-half complex plane. Under Assumption  
 206 A.2, SMSPO converges to a neighbourhood of the origin if gains  $k_i$  are  
 207 properly selected, which has been proved in (Jiang, Wu and Wen, 2002;  
 208 Hernandez and Barbot, 1996). For a given  $k_1$ , a larger  $k_i$  will accelerate  
 209 the estimation rate of SMSPO but also result in a degraded observer  
 210 stability. Thus a trade-off between the estimation rate and observer  
 211 stability must be made through trial-and-error (Jiang, 2001).

- 212 • **The layer thickness constant of saturation function  $\epsilon$ .** Which  
 213 is a positive small scalar to replace the sign function by the saturation  
 214 function, such that the chattering effect can be reduced. A larger  $\epsilon$   
 215 will result in a smoother chattering but a larger steady-state estima-  
 216 tion error. Consequently, a trade-off between the chattering effect and  
 217 steady-state estimation error must be made through trial-and-error. In  
 218 practice, a value closes to 0 is recommended.

219 **Remark 1.** When SMSPO is used to estimate the perturbation, the  
 220 upper bound of the derivative of perturbation  $\gamma_2$  is required to guarantee  
 221 the estimation accuracy, and such upper bound will result in a conservative  
 222 observer gain. However, the conservative gain is only included in the observer



223 loop, not in the controller loop.

224 Define an estimated sliding surface as

$$\hat{S}(x, t) = \sum_{i=1}^n \rho_i (\hat{x}_i - y_d^{(i-1)}) \quad (10)$$

225 where the estimated sliding surface gains  $\rho_i = C_{n-1}^{i-1} \lambda_c^{n-i}$ ,  $i = 1, \dots, n$ , place  
 226 all poles of the estimated sliding surface at  $-\lambda_c$ , where  $\lambda_c > 0$ .

227 The POSMC for system (5) is designed as

$$u = \frac{1}{b_0} \left[ y_d^{(n)} - \sum_{i=1}^{n-1} \rho_i (\hat{x}_{i+1} - y_d^{(i)}) - \zeta \hat{S} - \varphi \text{sat}(\hat{S}) - \hat{\Psi}(\cdot) \right] \quad (11)$$

228 where  $\zeta$  and  $\varphi$  are control gains which are chosen to fulfill the attractiveness  
 229 of the estimated sliding surface  $\hat{S}$ .

230 Note that POSMC does not require an accurate system model and only  
 231 one state measurement  $y = x_1$  is needed. As the upper bound of perturbation  
 232  $\Psi(\cdot)$  is replaced by the smaller bound of its estimation error  $\tilde{\Psi}(\cdot)$ , a smaller  
 233 control gain is needed such that the over-conservativeness of SMC can be  
 234 avoided (Jiang, Wu and Wen, 2002).

235 **Remark 2.** The motivation to use SMSPO is due to the fact that the  
 236 sliding-mode observer potentially offers advantages similar to those of sliding-

237 mode controllers, in particular, inherent robustness to parameter uncertainty  
 238 and external disturbances (Slotine and Li, 1991). It is a high-performance  
 239 state estimator with a simple structure and is well suited for uncertain nonlin-  
 240 ear systems (Kwon and Chung, 2004). Moreover, it has the merits of simple  
 241 structure and easy analysis of the closed-loop system stability compared to  
 242 that of ADRC which uses a nonlinear observer (Han, 2009), while they can  
 243 provide almost the same performance of perturbation estimation.

244 The overall design procedure of POSMC for system (5) can be summa-  
 245 rized as follows:

246 Step 1: Define perturbation (6) for the original  $n$ th-order system (5);

247 Step 2: Define a *fictitious state*  $x_{n+1} = \Psi(\cdot)$  to represent perturbation (6);

248 Step 3: Extend the original  $n$ th-order system (5) into the extended  $(n + 1)$ th-  
 249 order system (8);

250 Step 4: Design the  $(n + 1)$ th-order SMSPO (9) for the extended  $(n + 1)$ th-order  
 251 system (8) to obtain the state estimate  $\hat{x}$  and the perturbation estimate  
 252  $\hat{\Psi}(\cdot)$  by the only measurement of  $x_1$ ;

253 Step 5: Design controller (11) for the original  $n$ th-order system (5), in which  
 254 the estimated sliding surface  $\hat{S}$  is calculated by (10).

255 *3.2. Rectifier controller design*

256 Choose the system output  $y_r = [y_{r1}, y_{r2}]^T = [Q_1, V_{dc1}]^T$ , let  $Q_1^*$  and  $V_{dc1}^*$   
 257 be the given references of the reactive power and DC voltage, respectively.  
 258 Define the tracking error  $e_r = [e_{r1}, e_{r2}]^T = [Q_1 - Q_1^*, V_{dc1} - V_{dc1}^*]^T$ , differentiate  
 259  $e_r$  for rectifier (1) until the control input appears explicitly, yields

$$\begin{bmatrix} \dot{e}_{r1} \\ \ddot{e}_{r2} \end{bmatrix} = \begin{bmatrix} f_{r1} - \dot{Q}_1^* \\ f_{r2} - \ddot{V}_{dc1}^* \end{bmatrix} + B_r \begin{bmatrix} u_{d1} \\ u_{q1} \end{bmatrix} \quad (12)$$

260 where

$$\begin{cases} f_{r1} = \frac{3u_{sq1}}{2} \left( -\frac{R_1}{L_1} i_{d1} + \omega i_{q1} \right) \\ f_{r2} = \frac{3u_{sq1}}{2C_1 V_{dc1}} \left[ -\omega i_{d1} - \frac{R_1}{L_1} i_{q1} - \frac{i_{q1}}{V_{dc1}} \left( \frac{3u_{sq1} i_{q1}}{2C_1 V_{dc1}} - \frac{i_L}{C_1} \right) \right] \\ - \frac{1}{2R_0 C_1} \left( \frac{3u_{sq1} i_{q1}}{2C_1 V_{dc1}} - \frac{i_L}{C_1} - \frac{3u_{sq2} i_{q2}}{2C_2 V_{dc2}} - \frac{i_L}{C_2} \right) \end{cases}$$

261 and

$$B_r = \begin{bmatrix} \frac{3u_{sq1}}{2L_1} & 0 \\ 0 & \frac{3u_{sq1}}{2C_1 L_1 V_{dc1}} \end{bmatrix}$$

262 The determinant of matrix  $B_r$  is obtained as  $|B_r| = 9u_{sq1}^2 / (4C_1 L_1^2 V_{dc1})$ , which  
 263 is nonzero within the operation range of the rectifier, thus system (12) is  
 264 linearizable.

265 Assume all the nonlinearities are unknown, define the perturbations  $\Psi_{r1}(\cdot)$

266 and  $\Psi_{r2}(\cdot)$  as

$$\begin{bmatrix} \Psi_{r1}(\cdot) \\ \Psi_{r2}(\cdot) \end{bmatrix} = \begin{bmatrix} f_{r1} \\ f_{r2} \end{bmatrix} + (B_r - B_{r0}) \begin{bmatrix} u_{d1} \\ u_{q1} \end{bmatrix} \quad (13)$$

267 where the constant control gain  $B_{r0}$  is given by

$$B_{r0} = \begin{bmatrix} b_{r10} & 0 \\ 0 & b_{r20} \end{bmatrix}$$

268 Then system (12) can be rewritten as

$$\begin{bmatrix} \dot{e}_{r1} \\ \ddot{e}_{r2} \end{bmatrix} = \begin{bmatrix} \Psi_{r1}(\cdot) \\ \Psi_{r2}(\cdot) \end{bmatrix} + B_{r0} \begin{bmatrix} u_{d1} \\ u_{q1} \end{bmatrix} - \begin{bmatrix} \dot{Q}_1^* \\ \ddot{V}_{dc1}^* \end{bmatrix} \quad (14)$$

269 Define  $z'_{11} = Q_1$ , a second-order sliding-mode perturbation observer (SMPO)

270 is used to estimate  $\Psi_{r1}(\cdot)$  as

$$\begin{cases} \dot{z}'_{11} = \hat{\Psi}_{r1}(\cdot) + \alpha'_{r1} \tilde{Q}_1 + k'_{r1} \text{sat}(\tilde{Q}_1) + b_{r10} u_{d1} \\ \dot{\hat{\Psi}}_{r1}(\cdot) = \alpha'_{r2} \tilde{Q}_1 + k'_{r2} \text{sat}(\tilde{Q}_1) \end{cases} \quad (15)$$

271 where observer gains  $k'_{r1}$ ,  $k'_{r2}$ ,  $\alpha'_{r1}$ , and  $\alpha'_{r2}$  are all positive constants.

272 Define  $z_{11} = V_{\text{dc}1}$  and  $z_{12} = \dot{z}_{11}$ , a third-order SMSPO is used to estimate

273  $\Psi_{r2}(\cdot)$  as

$$\begin{cases} \dot{\hat{z}}_{11} = \hat{z}_{12} + \alpha_{r1}\tilde{V}_{\text{dc}1} + k_{r1}\text{sat}(\tilde{V}_{\text{dc}1}) \\ \dot{\hat{z}}_{12} = \hat{\Psi}_{r2}(\cdot) + \alpha_{r2}\tilde{V}_{\text{dc}1} + k_{r2}\text{sat}(\tilde{V}_{\text{dc}1}) + b_{r20}u_{q1} \\ \dot{\hat{\Psi}}_{r2}(\cdot) = \alpha_{r3}\tilde{V}_{\text{dc}1} + k_{r3}\text{sat}(\tilde{V}_{\text{dc}1}) \end{cases} \quad (16)$$

274 where observer gains  $k_{r1}$ ,  $k_{r2}$ ,  $k_{r3}$ ,  $\alpha_{r1}$ ,  $\alpha_{r2}$ , and  $\alpha_{r3}$  are all positive constants.

275 The above observers (15) and (16) only need the measurement of reactive  
276 power  $Q_1$  and DC voltage  $V_{\text{dc}1}$  at the rectifier side, which can be directly  
277 obtained in practice.

278 The estimated sliding surface of system (12) is defined as

$$\begin{bmatrix} \hat{S}_{r1} \\ \hat{S}_{r2} \end{bmatrix} = \begin{bmatrix} \hat{z}'_{11} - Q_1^* \\ \rho_1(\hat{z}_{11} - V_{\text{dc}1}^*) + \rho_2(\hat{z}_{12} - \dot{V}_{\text{dc}1}^*) \end{bmatrix} \quad (17)$$

279 where  $\rho_1$  and  $\rho_2$  are the positive sliding surface gains. The attractiveness of  
280 the estimated sliding surface (17) ensures reactive power  $Q_1$  and DC voltage  
281  $V_{\text{dc}1}$  can track to their reference.

282 The POSMC of system (12) is designed as

$$\begin{bmatrix} u_{d1} \\ u_{q1} \end{bmatrix} = B_{r0}^{-1} \begin{bmatrix} -\hat{\Psi}_{r1}(\cdot) + \dot{Q}_1^* - \zeta_r' \hat{S}_{r1} - \varphi_r' \text{sat}(\hat{S}_{r1}) \\ -\hat{\Psi}_{r2}(\cdot) + \dot{V}_{dc1}^* - \zeta_r \hat{S}_{r2} - \varphi_r \text{sat}(\hat{S}_{r2}) \end{bmatrix} \quad (18)$$

283 where positive control gains  $\zeta_r$ ,  $\zeta_r'$ ,  $\varphi_r$ , and  $\varphi_r'$  are chosen to ensure the at-  
284 tractiveness of estimated sliding surface (17).

285 During the most severe disturbance, both the reactive power and DC  
286 voltage reduce from their initial value to around zero within a short period  
287 of time  $\Delta$ . Thus the boundary values of the system state and perturbation  
288 estimates can be obtained as  $|\hat{z}'_{11}| \leq |Q_1^*|$ ,  $|\hat{\Psi}_{r1}(\cdot)| \leq |Q_1^*|/\Delta$ ,  $|\hat{z}_{11}| \leq |V_{dc1}^*|$ ,  
289  $|\hat{z}_{12}| \leq |V_{dc1}^*|/\Delta$ , and  $|\hat{\Psi}_{r2}(\cdot)| \leq |V_{dc1}^*|/\Delta^2$ , respectively.

### 290 3.3. Inverter controller design

291 Choose the system output  $y_i = [y_{i1}, y_{i2}]^T = [Q_2, P_2]^T$ , let  $Q_2^*$  and  $P_2^*$  be  
292 the given references of the reactive and active power, respectively. Define  
293 the tracking error  $e_i = [e_{i1}, e_{i2}]^T = [Q_2 - Q_2^*, P_2 - P_2^*]^T$ , differentiate  $e_i$  for  
294 inverter (2) until the control input appears explicitly, yields

$$\begin{bmatrix} \dot{e}_{i1} \\ \dot{e}_{i2} \end{bmatrix} = \begin{bmatrix} f_{i1} - \dot{Q}_2^* \\ f_{i2} - \dot{P}_2^* \end{bmatrix} + B_i \begin{bmatrix} u_{d2} \\ u_{q2} \end{bmatrix} \quad (19)$$

295 where

$$\begin{cases} f_{i1} = \frac{3u_{sq2}}{2} \left( -\frac{R_2}{L_2} i_{d2} + \omega i_{q2} \right) \\ f_{i2} = \frac{3u_{sq2}}{2} \left( -\frac{R_2}{L_2} i_{q2} - \omega i_{d2} \right) \end{cases}$$

296 and

$$B_i = \begin{bmatrix} \frac{3u_{sq2}}{2L_2} & 0 \\ 0 & \frac{3u_{sq2}}{2L_2} \end{bmatrix}$$

297 The determinant of matrix  $B_i$  is obtained as  $|B_i| = 9u_{s2}^2/(4L_2^2)$ , which is  
 298 nonzero within the operation range of the inverter, thus system (19) is lin-  
 299 earizable.

300 Assume all the nonlinearities are unknown, define the perturbations  $\Psi_{i1}(\cdot)$   
 301 and  $\Psi_{i2}(\cdot)$  as

$$\begin{bmatrix} \Psi_{i1}(\cdot) \\ \Psi_{i2}(\cdot) \end{bmatrix} = \begin{bmatrix} f_{i1} \\ f_{i2} \end{bmatrix} + (B_i - B_{i0}) \begin{bmatrix} u_{d2} \\ u_{q2} \end{bmatrix} \quad (20)$$

302 where the constant control gain  $B_{i0}$  is given by

$$B_{i0} = \begin{bmatrix} b_{i10} & 0 \\ 0 & b_{i20} \end{bmatrix}$$

303 Then system (19) can be rewritten as

$$\begin{bmatrix} \dot{e}_{i1} \\ \dot{e}_{i2} \end{bmatrix} = \begin{bmatrix} \Psi_{i1}(\cdot) \\ \Psi_{i2}(\cdot) \end{bmatrix} + B_{i0} \begin{bmatrix} u_{d2} \\ u_{q2} \end{bmatrix} - \begin{bmatrix} \dot{Q}_2^* \\ \dot{P}_2^* \end{bmatrix} \quad (21)$$

304 Similarly, define  $z'_{21} = Q_2$  and  $z_{21} = P_2$ , two second-order SMPOs are used  
 305 to estimate  $\Psi_{i1}(\cdot)$  and  $\Psi_{i2}(\cdot)$ , respectively, as

$$\begin{cases} \dot{z}'_{21} = \hat{\Psi}_{i1}(\cdot) + \alpha'_{i1} \tilde{Q}_2 + k'_{i1} \text{sat}(\tilde{Q}_2) + b_{i10} u_{d2} \\ \dot{\hat{\Psi}}_{i1}(\cdot) = \alpha'_{i2} \tilde{Q}_2 + k'_{i2} \text{sat}(\tilde{Q}_2) \end{cases} \quad (22)$$

306 where observer gains  $k'_{i1}$ ,  $k'_{i2}$ ,  $\alpha'_{i1}$ , and  $\alpha'_{i2}$  are all positive constants.

$$\begin{cases} \dot{z}_{21} = \hat{\Psi}_{i2}(\cdot) + \alpha_{i1} \tilde{P}_2 + k_{i1} \text{sat}(\tilde{P}_2) + b_{i20} u_{q2} \\ \dot{\hat{\Psi}}_{i2}(\cdot) = \alpha_{i2} \tilde{P}_2 + k_{i2} \text{sat}(\tilde{P}_2) \end{cases} \quad (23)$$

307 where observer gains  $k_{i1}$ ,  $k_{i2}$ ,  $\alpha_{i1}$ , and  $\alpha_{i2}$  are all positive constants .

308 The above observers (22) and (23) only need the measurement of reactive  
 309 power  $Q_2$  and active power  $P_2$  at the inverter side, which can be directly  
 310 obtained in practice.



311 The estimated sliding surface of system (19) is defined as

$$\begin{bmatrix} \hat{S}_{i1} \\ \hat{S}_{i2} \end{bmatrix} = \begin{bmatrix} \hat{z}'_{21} - Q_2^* \\ \hat{z}_{21} - P_2^* \end{bmatrix} \quad (24)$$

312 Similarly, the attractiveness of the estimated sliding surface (24) ensures the  
313 reactive power  $Q_2$  and active power  $P_2$  can track to their reference.

314 The POSMC of system (19) is designed as

$$\begin{bmatrix} u_{d2} \\ u_{q2} \end{bmatrix} = B_{i0}^{-1} \begin{bmatrix} -\hat{\Psi}_{i1}(\cdot) + \dot{Q}_2^* - \zeta'_i \hat{S}_{i1} - \varphi'_i \text{sat}(\hat{S}_{i1}) \\ -\hat{\Psi}_{i2}(\cdot) + \dot{P}_2^* - \zeta_i \hat{S}_{i2} - \varphi_i \text{sat}(\hat{S}_{i2}) \end{bmatrix} \quad (25)$$

315 where positive control gains  $\zeta_i$ ,  $\zeta'_i$ ,  $\varphi_i$ , and  $\varphi'_i$  are chosen to ensure the attrac-  
316 tiveness of estimated sliding surface (24).

317 Similarly, the boundary values of the system state and perturbation es-  
318 timates can be obtained as  $|\hat{z}'_{21}| \leq |Q_2^*|$ ,  $|\hat{\Psi}_{i1}(\cdot)| \leq |Q_2^*|/\Delta$ ,  $|\hat{z}_{21}| \leq |P_2^*|$ , and  
319  $|\hat{\Psi}_{i2}(\cdot)| \leq |P_2^*|/\Delta$ , respectively.

320 Note that control outputs (18) and (25) are modulated by the sinusoidal  
321 pulse width modulation (SPWM) technique (Nikolas, Vassilios, and Georgios,  
322 2009) in this paper. The overall controller structure of the VSC-HVDC  
323 system is illustrated by Fig. 2, in which only reactive power  $Q_1$  and DC

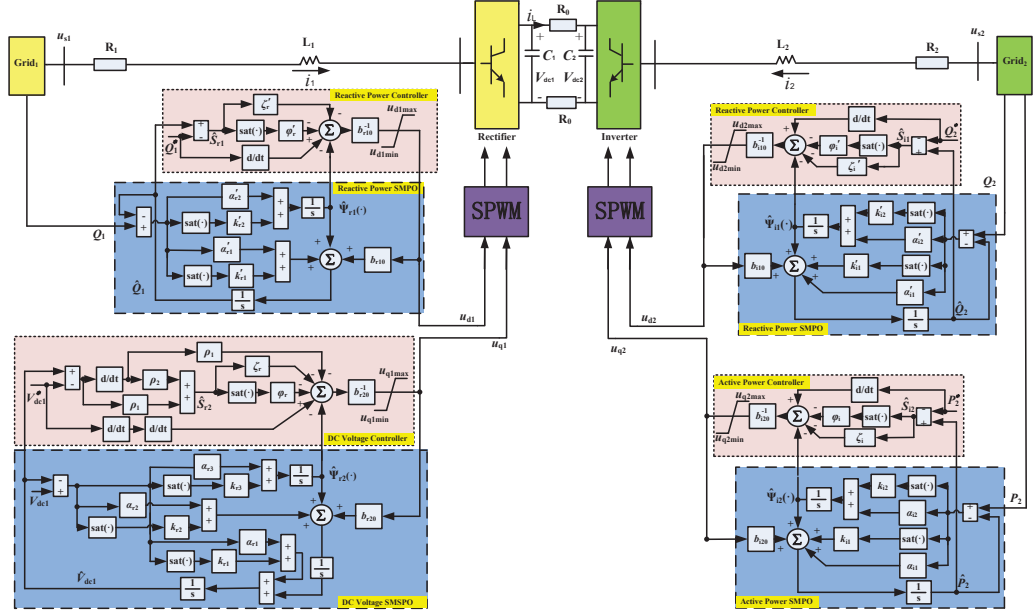


Figure 2: The overall controller structure of the VSC-HVDC system.

324 voltage  $V_{dc1}$  need to be measured for rectifier controller (18), while active  
 325 power  $P_2$  and reactive power  $Q_2$  for inverter controller (25).

#### 326 4. Simulation Results

327 POSMC is applied on the VSC-HVDC system illustrated in Fig. 1. The  
 328 AC grid frequency is 50 Hz and VSC-HVDC system parameters are given in  
 329 Table 1. POSMC parameters are provided in Table 2, in which the observer  
 330 poles are allocated as  $\lambda_{\alpha_r} = 100$  and  $\lambda_{\alpha'_r} = \lambda_{\alpha_i} = \lambda_{\alpha'_i} = 20$ , while control  
 331 inputs are bounded as  $|u_{qi}| \leq 80$  kV and  $|u_{di}| \leq 60$  kV, where  $i = 1, 2$ . The

Table 1: The VSC-HVDC system parameters

AC system based voltage	$V_{AC_{base}}$	132 kV
DC cable base voltage	$V_{DC_{base}}$	150 kV
System base power	$S_{base}$	100 MVA
AC system resistance (25 km)	$R_1, R_2$	0.05 $\Omega$ /km
AC system inductance (25 km)	$L_1, L_2$	0.026 mH/km
DC cable resistance (50 km)	$R_0$	0.21 $\Omega$ /km
DC bus capacitance	$C_1, C_2$	11.94 $\mu$ F

332 switching frequency is 1620 Hz for both rectifier and inverter, which is taken  
 333 from (Moharana and Dash, 2010). The control performance of POSMC is  
 334 compared to that of VC (Li, Haskew and Xu, 2010) and FLSMC (Moharana  
 and Dash, 2010) by the following four cases.

Table 2: POSMC parameters for the VSC-HVDC system

Rectifier controller gains			
$b_{r10} = 100$	$b_{r20} = 7000$	$\rho_1 = 800$	$\rho_2 = 1$
$\zeta_r = 20$	$\zeta'_r = 10$	$\varphi_r = 20$	$\varphi'_r = 20$
Rectifier observer gains			
$\alpha_{r1} = 300$	$\alpha'_{r1} = 40$	$\alpha_{r2} = 3 \times 10^4$	$\alpha'_{r2} = 400$
$\alpha_{r3} = 10^6$	$\Delta = 0.01$	$\epsilon = 0.1$	$k_{r1} = 100$
$k'_{r1} = 75$	$k_{r2} = 10^5$	$k'_{r2} = 3.75 \times 10^4$	$k_{r3} = 2.5 \times 10^7$
Inverter controller gains			
$b_{i10} = 50$	$b_{i20} = 50$	$\zeta_i = 10$	$\zeta'_i = 10$
$\varphi_i = 10$	$\varphi'_i = 10$		
Inverter observer gains			
$\alpha_{i1} = 40$	$\alpha'_{i1} = 40$	$\alpha_{i2} = 400$	$\alpha'_{i2} = 400$
$k_{i1} = 75$	$k'_{i1} = 75$	$k_{i2} = 3.75 \times 10^4$	$k'_{i2} = 3.75 \times 10^4$

335

336 1) *Case 1: Active and reactive power tracking:* The references of active  
 337 and reactive power are set to be a series of step change occurs at  $t = 0.2$  s,

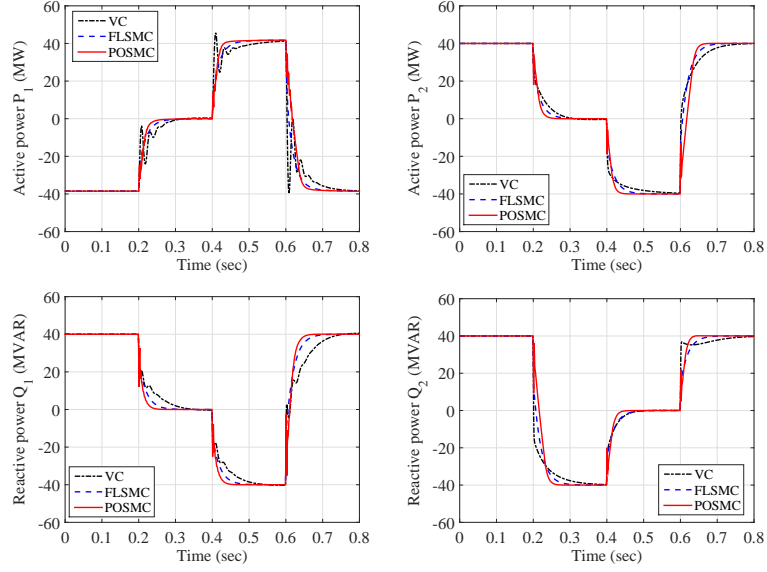


Figure 3: System responses obtained under the active and reactive power tracking.

338  $t = 0.4$  s, and restores to the original value at  $t = 0.6$  s, while DC voltage  
 339 is regulated at the rated value  $V_{dc1}^* = 150$  kV. The system responses are  
 340 illustrated by Fig. 3. One can find that POSMC has the fastest tracking  
 341 rate and maintains a consistent control performance under different operation  
 342 conditions.

343 2) *Case 2: 5-cycle line-line-line-ground (LLLG) fault at AC bus 1.* A  
 344 5-cycle LLLG fault occurs at AC bus 1 when  $t = 0.1$  s. Due to the fault, AC  
 345 voltage at the corresponding bus is decreased to a critical level. Fig. 4 shows  
 346 that POSMC can effectively restore the system with smallest active power  
 347 oscillations. Response of perturbation estimation is demonstrated in Fig. 5,

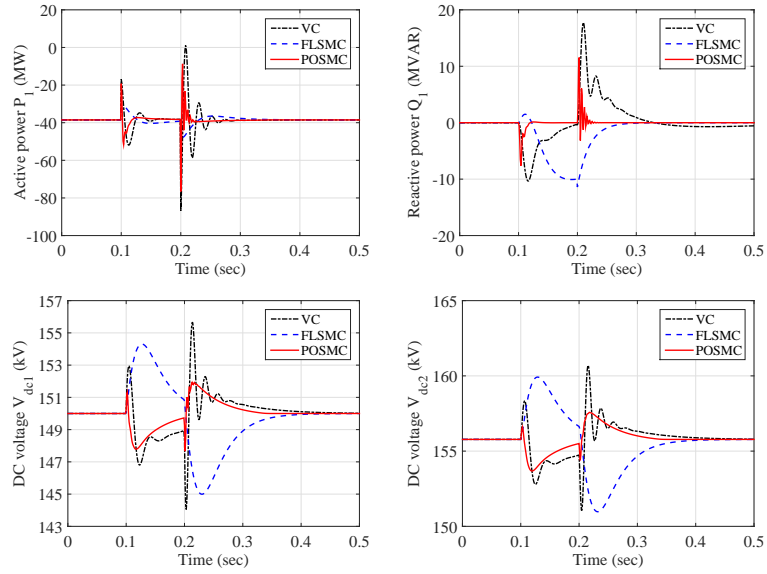


Figure 4: System responses obtained under the 5-cycle LLLG fault at AC bus 1.

348 which shows SMSPO and SMPO can estimate the perturbations with a fast  
 349 tracking rate.

350 3) *Case 3: Weak AC grid connection:* The AC grids are assumed to be  
 351 sufficiently strong such that AC bus voltages are ideal constants. It is worth

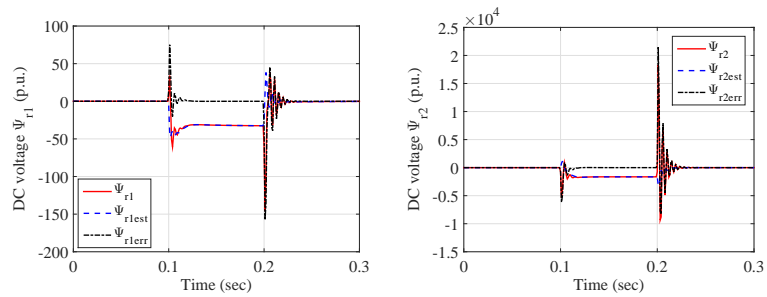


Figure 5: Estimation errors of the perturbations obtained under the 5-cycle LLLG fault at AC bus 1.

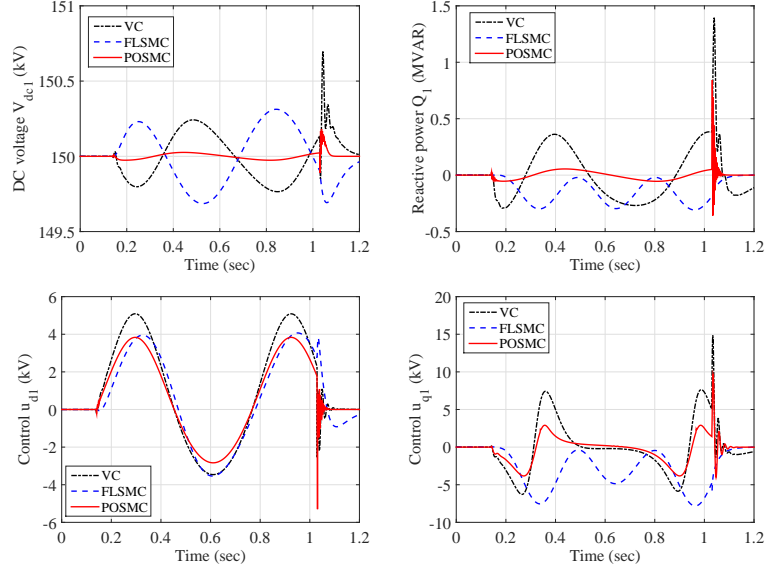


Figure 6: System responses obtained with the weak AC grid connection.

352 considering a weak AC grid connected to the rectifier, e.g., offshore wind  
 353 farms, which voltage  $u_{s1}$  is no longer a constant but a time-varying function.  
 354 A voltage fluctuation occurs from 0.15 s to 1.05 s caused by the wind speed  
 355 variation is applied, which corresponds to  $u_{s1} = 1 + 0.15 \sin(0.2\pi t)$ . System  
 356 responses are presented in Fig. 6, it illustrates that both DC voltage and  
 357 reactive power are oscillatory, while POSMC can effectively suppress such  
 358 oscillation with the smallest fluctuation of DC voltage and reactive power.

359 4) *Case 4: System parameter uncertainties:* When there is a fault in the  
 360 transmission or distribution grid, the resistance and inductance values of the  
 361 grid may change significantly. Several tests are performed for plant-model

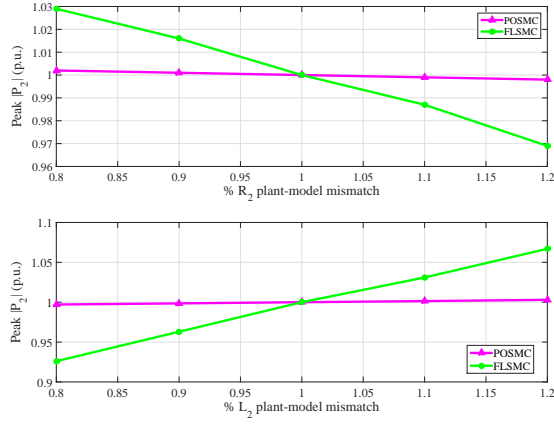


Figure 7: The peak active power  $|P_2|$  (in p.u.) to a  $-120$  A in the DC cable current  $i_L$  obtained at nominal grid voltage for plant-model mismatches in the range of 20% (one parameter changes and others keep constant).

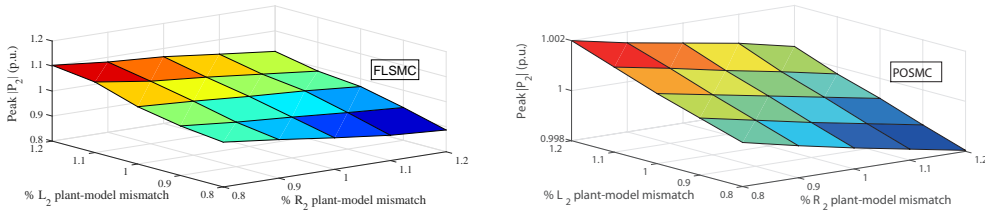


Figure 8: The peak active power  $|P_2|$  (in p.u.) to a  $-120$  A in the DC cable current  $i_L$  obtained at nominal grid voltage for plant-model mismatches in the range of 20% (different parameters may change at the same time).

362 mismatches of  $R_2$  and  $L_2$  with  $\pm 20\%$  uncertainties. All tests are undertaken  
 363 under the nominal grid voltage and a corresponding -120 A in the DC cable  
 364 current  $i_L$  at 0.1 s. The peak active power  $|P_2|$  is recorded which uses per  
 365 unit (p.u.) value for a clear illustration of system robustness. It can be  
 366 found from Fig. 7 that the peak active power  $|P_2|$  controlled by POSMC is  
 367 almost not affected, while FLSMC has a relatively large range of variation,  
 368 i.e., around 3% to  $R_2$  and 8% to  $L_2$ , respectively. Responses to mismatch  
 369 of  $R_2$  and  $L_2$  changing at the same time are demonstrated in Fig. 8. The  
 370 magnitude of changes is around 10% under FLSMC and almost does not  
 371 change under POSMC. This is because POSMC estimates all uncertainties  
 372 and does not need an accurate system model, thus it has better robustness  
 373 than that of FLSMC which requires accurate system parameters.

374 The integral of absolute error (IAE) indices of each approach calculated  
 375 in different cases are tabulated in Table 3. Here  $IAE_{Q_1} = \int_0^T |Q_1 - Q_1^*| dt$ ,  
 376  $IAE_{V_{dc1}} = \int_0^T |V_{dc1} - V_{dc1}^*| dt$ ,  $IAE_{Q_2} = \int_0^T |Q_2 - Q_2^*| dt$  and  $IAE_{P_2} = \int_0^T |P_2 -$   
 377  $P_2^*| dt$ . The simulation time  $T=3$  s. Note that POSMC has a little bit higher  
 378 IAE than that of FLSMC in the power tracking due to the estimation error,  
 379 while it can provide much better robustness in the case of 5-cycle LLLG fault  
 380 and weak AC grid connection. In particular, its  $IAE_{Q_1}$  and  $IAE_{V_{dc1}}$  are only



Table 3: IAE indices (in p.u.) of different control schemes calculated in different cases

Method \ Case	Power tracking			
	IAE <sub>Q<sub>1</sub></sub>	IAE <sub>V<sub>dc1</sub></sub>	IAE <sub>Q<sub>2</sub></sub>	IAE <sub>P<sub>2</sub></sub>
VC	3.83E-02	4.44E-03	2.13E-02	2.71E-02
FLSMC	2.19E-02	1.73E-03	2.23E-02	2.18E-02
POSMC	2.33E-02	2.00E-03	2.42E-02	2.33E-02
Method \ Case	5-cycle LLLG fault		Weak AC grid connection	
	IAE <sub>Q<sub>1</sub></sub>	IAE <sub>V<sub>dc1</sub></sub>	IAE <sub>Q<sub>1</sub></sub>	IAE <sub>V<sub>dc1</sub></sub>
VC	2.62E-02	2.15E-03	4.53E-03	4.13E-03
FLSMC	1.13E-02	4.13E-03	4.08E-03	3.33E-03
POSMC	5.64E-03	1.38E-03	3.88E-04	6.78E-04

381 8.57% and 9.51% of those of VC, 16.42% and 20.36% of those of FLSMC with  
382 the weak AC grid connection. The overall control costs are illustrated in Fig.  
383 9, with  $IAE_u = \int_0^T (|u_{d1}| + |u_{q1}| + |u_{d2}| + |u_{q2}|) dt$ . It is obvious that POSMC  
384 has the lowest control costs in all cases, which is resulted from the merits  
385 that the upper bound of perturbation is replaced by the smaller bound of its  
386 estimation error, thus an over-conservative control input can be avoided.

## 387 5. Hardware-in-the-loop Test Results

388 A dSPACE simulator based HIL test is used to validate the implementa-  
389 tion feasibility of POSMC, which configuration and experiment platform are  
390 given by Fig. 10 and Fig. 11, respectively. The rectifier controller (18) and

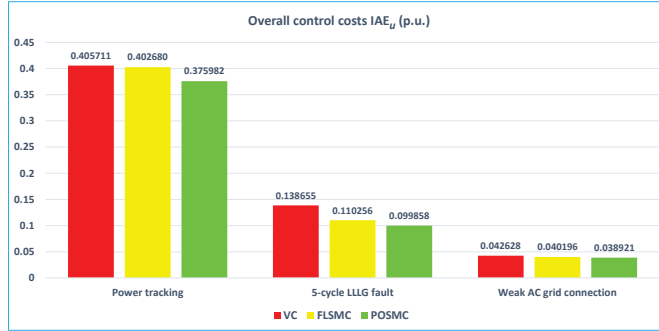


Figure 9: Overall control costs  $IAE_u$  (in p.u.) obtained in different cases

391 inverter controller (25) are implemented on one dSPACE platform (DS1104  
392 board) with a sampling frequency  $f_c = 1$  kHz, and the VSC-HVDC system  
393 is simulated on another dSPACE platform (DS1006 board) with the limit  
394 sampling frequency  $f_s = 50$  kHz to make HIL simulator as close to the real  
395 plant as possible. The measurements of the reactive power  $Q_1$ , DC voltage  
396  $V_{dc1}$ , active power  $P_2$  and reactive power  $Q_2$  are obtained from the real-time  
397 simulation of the VSC-HVDC system on the DS1006 board, which are sent  
398 to two controllers implemented on the DS1104 board for the control inputs  
399 calculation.

400 It follows from (Yang, Jiang, Yao and Wu, 2015) that an unexpected high-  
401 frequency oscillation in control inputs may emerge as the large observer poles  
402 would result in high gains, which lead to highly sensitive observer dynamics to  
403 the measurement disturbances in the HIL test . Note that this phenomenon

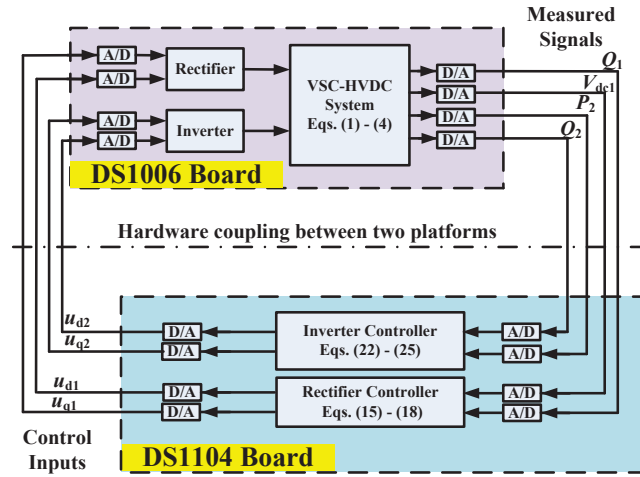


Figure 10: The configuration of the HIL test.

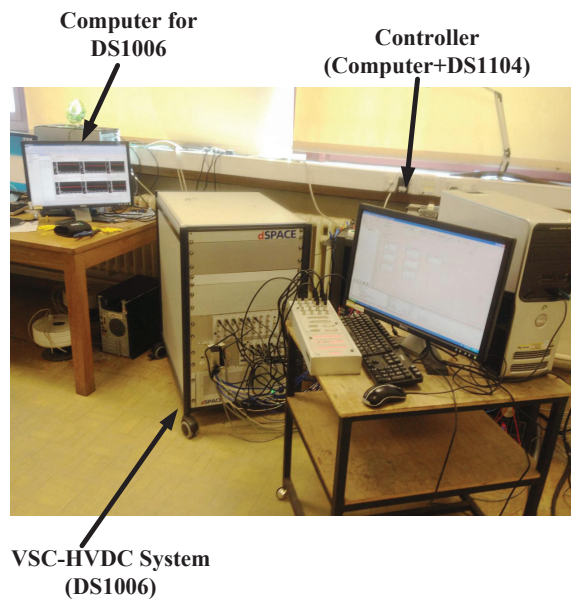


Figure 11: The experiment platform of the HIL test.

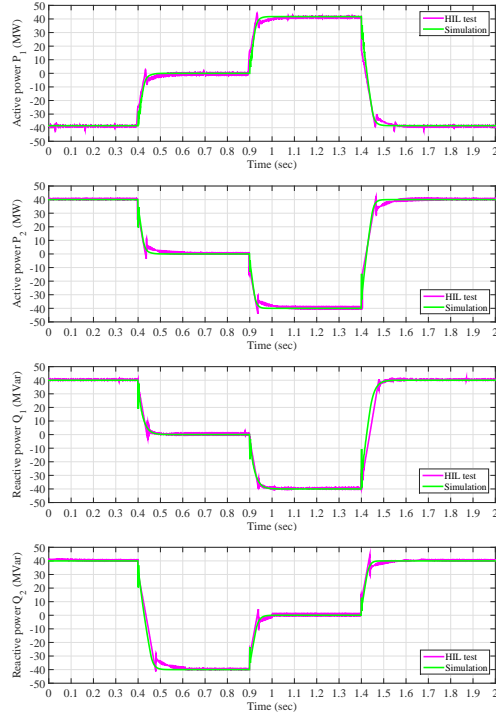


Figure 12: HIL test results of system responses obtained under the active and reactive power tracking.

404 does not exist in the simulation. One effective way to alleviate such malignant  
 405 effect is to reduce the observer poles. Through trial-and-error, an observer  
 406 pole in the range of  $\lambda_{\alpha_r} \in [15, 25]$  and  $\lambda_{\alpha'_r} = \lambda_{\alpha_i} = \lambda_{\alpha'_i} \in [3, 10]$  can avoid such  
 407 oscillation but with almost similar transient responses, thus the reduced poles  
 408  $\lambda_{\alpha_r} = 20$  and  $\lambda_{\alpha'_r} = \lambda_{\alpha_i} = \lambda_{\alpha'_i} = 5$ , with  $b_{r10} = 50$ ,  $b_{r20} = 5000$ ,  $b_{i10} = 20$ , and  
 409  $b_{i20} = 20$ , are chosen in the HIL test. Furthermore, a time delay  $\tau = 3$  ms  
 410 has been assumed in the corresponding simulation to consider the effect of  
 411 the computational delay of the real-time controller.

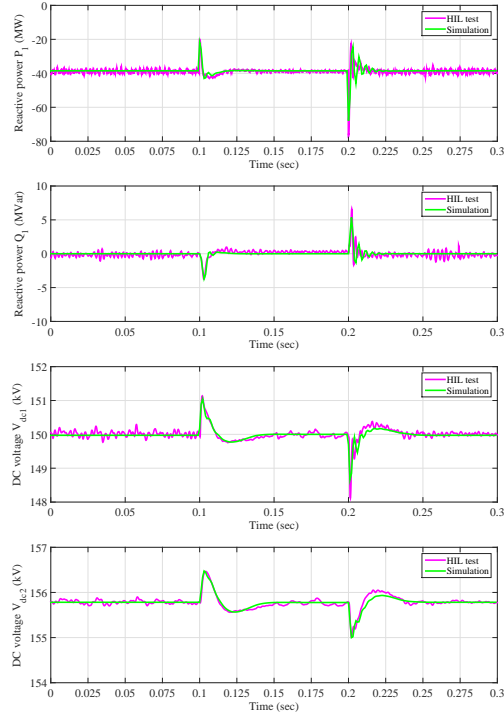


Figure 13: HIL test results of system responses obtained under the 5-cycle LLLG fault at AC bus 1.

412     1) *Case 1: Active and reactive power tracking:* The reference of active  
413 and reactive power changes at  $t = 0.4$  s,  $t = 0.9$  s, and restores to the original  
414 value at  $t = 1.4$  s, while DC voltage is regulated at the rated value  $V_{dc1}^* = 150$   
415 kV. The system responses obtained under the HIL test and simulation are  
416 compared by Fig. 12, which shows that the HIL test has almost the same  
417 results as that of the simulation.

418     2) *Case 2: 5-cycle line-line-line-ground (LLLG) fault at AC bus 1.* A  
419 5-cycle LLLG fault occurs at AC bus 1 when  $t = 0.1$  s. Fig. 13 demonstrates

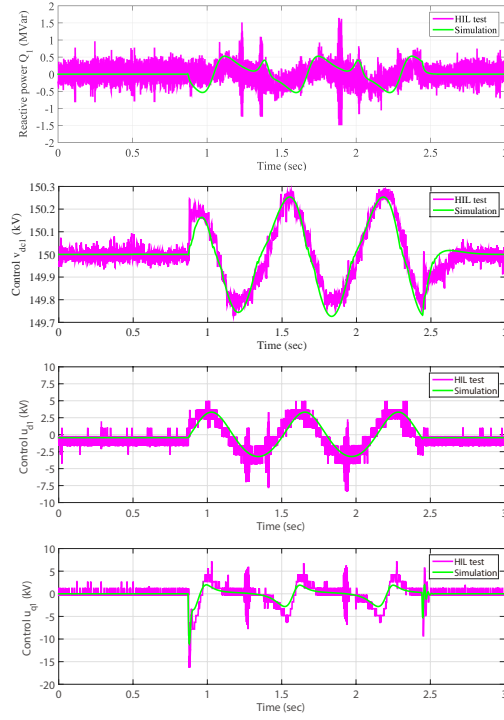


Figure 14: HIL test results of system responses obtained with the weak AC grid connection.

420 that the system can be rapidly restored and the system responses obtained  
 421 by the HIL test is similar to that of simulation.

422 3) *Case 3: Weak AC grid connection:* The same voltage variation  $u_{s1} =$   
 423  $1 + 0.15 \sin(0.2\pi t)$  is applied between 0.87 s to 2.45 s. It can be readily seen  
 424 from Fig. 14 that the results of the HIL test and simulation match very well.

425 The difference of the obtained results between the HIL test and simulation  
 426 is possibly due to the following two reasons:

- 427
- There exist measurement disturbances in the HIL test which are how-

428 ever not taken into account in the simulation, a filter could be used  
429 to remove the measurement disturbances thus the control performance  
430 can be improved.

431 • The sampling frequency of VSC-HVDC model and POSMC is the same  
432 in simulation ( $f_s=f_c=1$  kHz) as they are implemented in Matlab of the  
433 same computer. In contrast, the sampling frequency of VSC-HVDC  
434 model ( $f_s=50$  kHz) is significantly increased in the HIL test to make  
435 VSC-HVDC model as close to the real plant as possible. Note the  
436 sampling frequency of POSMC remains the same ( $f_c=1$  kHz) due to  
437 the sampling limit of the practical controller.

## 438 6. Conclusion

439 A POSMC scheme has been developed for the VSC-HVDC system to  
440 rapidly compensate the combinatorial effect of nonlinearities, parameter un-  
441 certainties, unmodelled dynamics and time-varying external disturbances.  
442 As the upper bound of perturbation is replaced by the smaller bound of its  
443 estimation error, an over-conservative control input is avoided such that the  
444 tracking accuracy can be improved.

445 Four case studies have been undertaken to evaluate the control perfor-

446 mance of the proposed approach, which verify that POSMC can maintain a  
447 consistent control performance with less power overshoot during the power  
448 reversal, restore the system rapidly after the AC fault, suppress the oscilla-  
449 tion effectively when connected to a weak AC grid, and provide significant  
450 robustness in the presence of system parameter uncertainties. At last, an  
451 HIL test has been carried out which validates the implementation feasibility  
452 of POSMC.

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