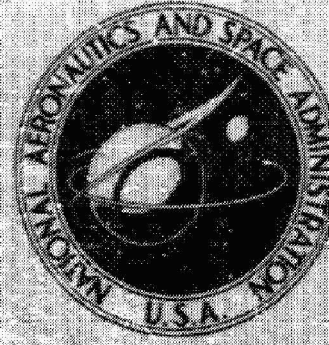


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FACILITY FOR A 607	N70-39358	(ACCESSION NUMBER)	(THRU)
	46	(PAGES)	(CODE)
		(NASA CR OR TMX OR AD NUMBER)	(CATEGORY)

REVIEW OF THE BRAYTON ENGINE
ELECTRICAL SUBSYSTEM DESIGN
AND COMPUTERIZED TECHNIQUE
USED TO DOCUMENT WIRING

by James Nestor and Pierre A. Thollot

Lewis Research Center
Cleveland, Ohio 44135



1. Report No. NASA TM X-2079	2. Government Accession No.	3. Recipient's Catalog No.	
4. Title and Subtitle REVIEW OF THE BRAYTON ENGINE ELECTRICAL SUBSYSTEM DESIGN AND COMPUT- ERIZED TECHNIQUE USED TO DOCUMENT WIRING		5. Report Date September 1970	6. Performing Organization Code
		8. Performing Organization Report No. E-5621	
7. Author(s) James Nestor and Pierre A. Thollot		10. Work Unit No. 120-27	11. Contract or Grant No.
9. Performing Organization Name and Address Lewis Research Center National Aeronautics and Space Administration Cleveland, Ohio 44135		13. Type of Report and Period Covered Technical Memorandum	
		14. Sponsoring Agency Code	
12. Sponsoring Agency Name and Address National Aeronautics and Space Administration Washington, D.C. 20546			
15. Supplementary Notes APPENDIX A - FORTRAN PROGRAM USED TO DOCUMENT BRAYTON ENGINE ELECTRICAL SUBSYSTEM by Donald R. Packe			
16. Abstract This report presents a general description of the Brayton-B engine space power system, with special emphasis on the electrical subsystem. The interval between delivery of individual components and the initiation of systems testing is covered in detail, with particular attention to the computer-oriented approach used to document all the many interconnections of the electrical components. Applications of this documentation technique are recommended for development of other complex electrical - electronic systems.			
17. Key Words (Suggested by Author(s)) Space power; Brayton engine; Electrical subsystem; Electrical inter- face; Wire list, computerized; Wiring harness; Electrical documentation		18. Distribution Statement Unclassified - unlimited	
19. Security Classif. (of this report) Unclassified	20. Security Classif. (of this page) Unclassified	21. No. of Pages 41	22. Price* \$3.00

*For sale by the Clearinghouse for Federal Scientific and Technical Information
Springfield, Virginia 22151

REVIEW OF THE BRAYTON ENGINE ELECTRICAL SUBSYSTEM DESIGN AND COMPUTERIZED TECHNIQUE USED TO DOCUMENT WIRING

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SUMMARY

The Brayton-B engine is being developed as a reliable source of continuous electric power for future space missions. NASA-Lewis Research Center has assumed full system engineering responsibility for its design and development. The engine has been built and is presently undergoing systems testing in a vacuum environment.

This report includes a general description of the major subsystems and their interrelation. The electrical subsystem is covered in more detail to include the function and operation of the major components. The application of computer technology as an aid for developing wire lists for the electrical harnesses is discussed. Advantages of computerized wiring tabulations over detailed wiring diagrams include simplified maintenance, lower cost, convenience, accuracy, and flexibility.

This report is intended primarily to document the approach used to develop the Brayton engine electrical subsystem and to serve as a guide and reference source for possible application of these principles to the development of other complex electrical-electronic systems.

INTRODUCTION

NASA-Lewis has assumed full system engineering responsibility to develop, build, and test the Brayton-B engine for use as a reliable source of electrical power on future space missions. Design criteria for the engine are that it would operate continuously for at least 5 years, control and regulate itself automatically, and provide a minimum of 10 kWe (kilowatts electric) of usable three-phase, 120-volt, 1200-hertz electrical energy.

Design and engineering analyses, material testing, and preliminary design studies conducted at Lewis began in 1963. A review of this phase of the program is presented in reference 1. Functions, design conditions, operating parameters, and electrical and

thermodynamic characteristics of the major components were defined, and detailed specifications generated. By 1968 related technology had been developed to the point where contracts could be awarded to individual vendors to design and build the major components based on these specifications. Very close liaison was maintained throughout the life of each contract by the responsible contract manager to insure that the specifications were being rigidly followed and that the vendors complied with the intent of the contract. A comprehensive report covering the analysis and selection of design conditions for the Brayton engine was presented by Klann (ref. 2) in 1968.

Concurrently with fabrication, delivery, and checkout of the major components, Lewis prepared for the next phase: to assemble the components into an integral functioning engine. Key elements in this effort were the total electrical subsystem configuration design, development of interface requirements related to additional development instrumentation, provision for backup power supply interconnections for engine checkout, and, finally, documentation of the entire electrical subsystem wiring. The technique used to document the engine electrical subsystem wiring, as well as the instrumentation and electrical interfaces with the test facility, has proved particularly flexible and effective and is discussed in detail.

A progress report on the prototype engine during early system testing at the NASA-Lewis Space Power Facility was given by Brown (ref. 3) in 1969. Preliminary results of engine performance tests in a vacuum environment are presented in a work entitled "Experimental Performance of a 2 - 15 Kilowatt Brayton Power System in the Space Power Facility Using Krypton" by Fenn, Deyo, Miller, and Vernon of Lewis.

DESCRIPTION OF THE BRAYTON-B ENGINE

Figure 1 shows a schematic diagram of the engine with the four major subsystems enclosed by dashed lines. The engine as shown in the schematic is a completely self-contained system capable of automatic startup and shutdown cycles with steady-state operation at from 0 to 10 kilowatts of user electrical output power.

The heat source subsystem adds heat to an inert working gas through its heat exchanger. At present, the two heat sources being considered for the Brayton engine are radioisotope and a nuclear reactor. The hot gas in the recirculating primary gas loop flows through a single-stage radial-inflow turbine. Expansion of the gas through the turbine spins the shaft and produces useful work. About two-thirds of this work is absorbed in driving a single-stage radial-outflow compressor. The remaining one-third of the shaft work is available to a four-pole brushless alternator. The single shaft of this Brayton Rotating Unit (BRU) is supported by gas journal and thrust bearings and is lubricated by the working gas itself. After expansion in the turbine, the gas flows through a recuperator where a majority of the unused heat energy is transferred back to the cooler

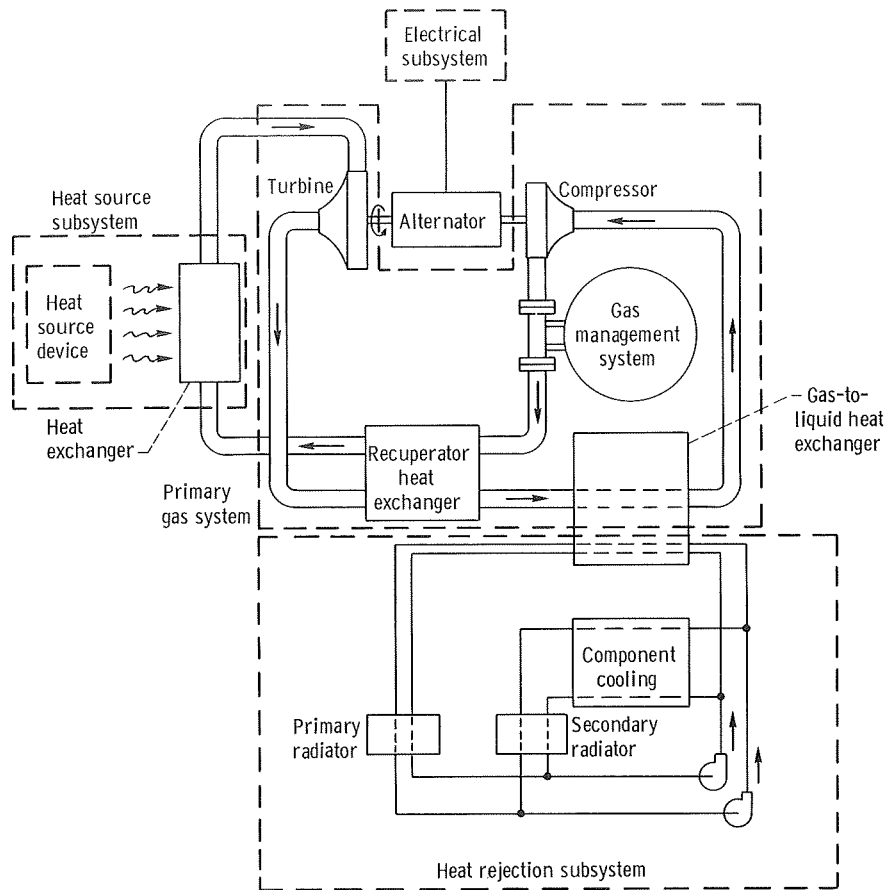


Figure 1. - Brayton engine schematic.

gas flow leaving the compressor. The gas leaving the recuperator is further cooled by the gas-to-liquid heat exchanger which removes the waste heat from the gas loop and transfers it to the liquid heat-rejection subsystem. A gas management system supplies the gas for engine startup by injecting gas into an evacuated loop at the compressor discharge. It also provides jacking gas to the BRU bearings during the startup cycle and controls total gas inventory in the system during steady-state operation. A more detailed description of the events related to startup and shutdown is given by Cantoni and Thomas (ref. 4).

The heat rejection subsystem removes excess engine heat from the gas-to-liquid heat exchanger and provides cooling for the electrical-electronic components. Cooling is provided by redundant liquid loops of recirculating Dow Corning DC-200 fluid. Either loop satisfies the necessary heat-transfer requirements. Each loop circulates in a split path: one path removes heat from the heat exchanger and dissipates it by means of the primary (hot) radiator; the second path cools the alternator and the electronic components, dissipating heat through the secondary (cool) radiator. The pumps for the two coolant loops are powered from the engine electrical subsystem.

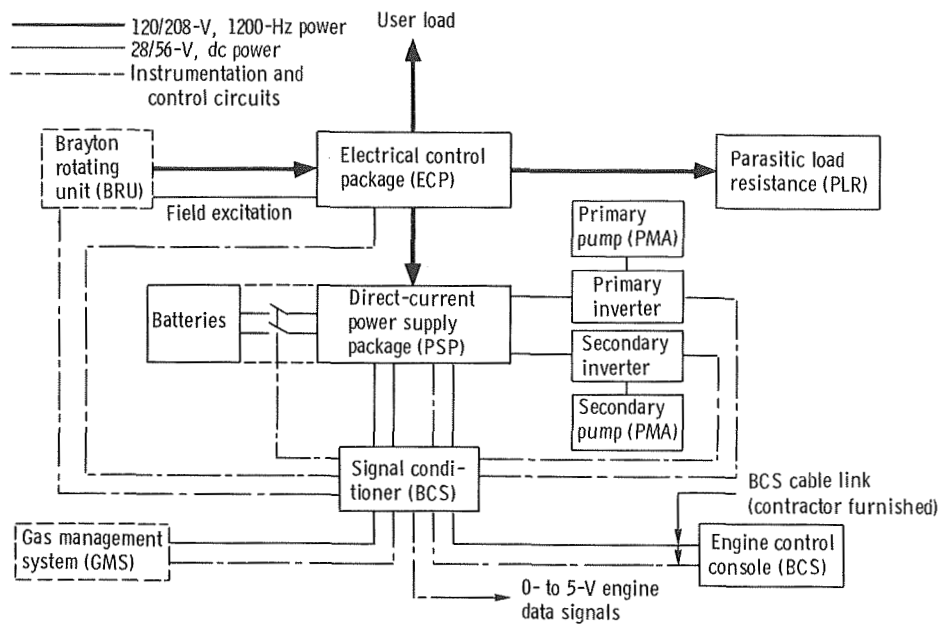
Finally, the electrical subsystem regulates and distributes the electrical output of the alternator and provides all the engine control and logic functions. The electrical output is 208/120 volts, 1200 hertz, three-phase power. The maximum alternator output power depends on the heat source mated to the engine and the pressure level at which the system is operating. The alternator is capable of up to 15 kilowatts power output. Between 1.5 and 2 kilowatts of this power is used for "housekeeping" purposes: operation of the engine control system, speed control and field excitation for the alternator, operation of the coolant pumps, and for battery charging.

DETAILS OF THE ELECTRICAL SUBSYSTEM

The BRU, as the principal turbomachinery package of the engine, is an integral unit which includes the turbine, the compressor, and the alternator. The alternator is a four-pole, solid rotor, modified Lundell type, three-phase brushless machine whose output is 208 volts line-to-line at 1200 hertz. The rotor of the alternator consists of two separate magnetic sections joined together by a nonmagnetic metallic separator. Advantages of a machine of this type are the smooth rotor which minimizes windage losses and the incorporation of short armature coils (compared with a homopolar inductor alternator). Further, the elimination of slip rings or commutator contacts and the absence of rotating windings increase the reliability and life expectancy of the machine. Alternator cooling is accomplished by dual liquid passages in the alternator jacket for circulation of the heat-rejection system coolant. Either liquid passage can provide the needed cooling. Detailed information related to alternator design and performance characteristics is given by Ingle and Corcoran in reference 5.

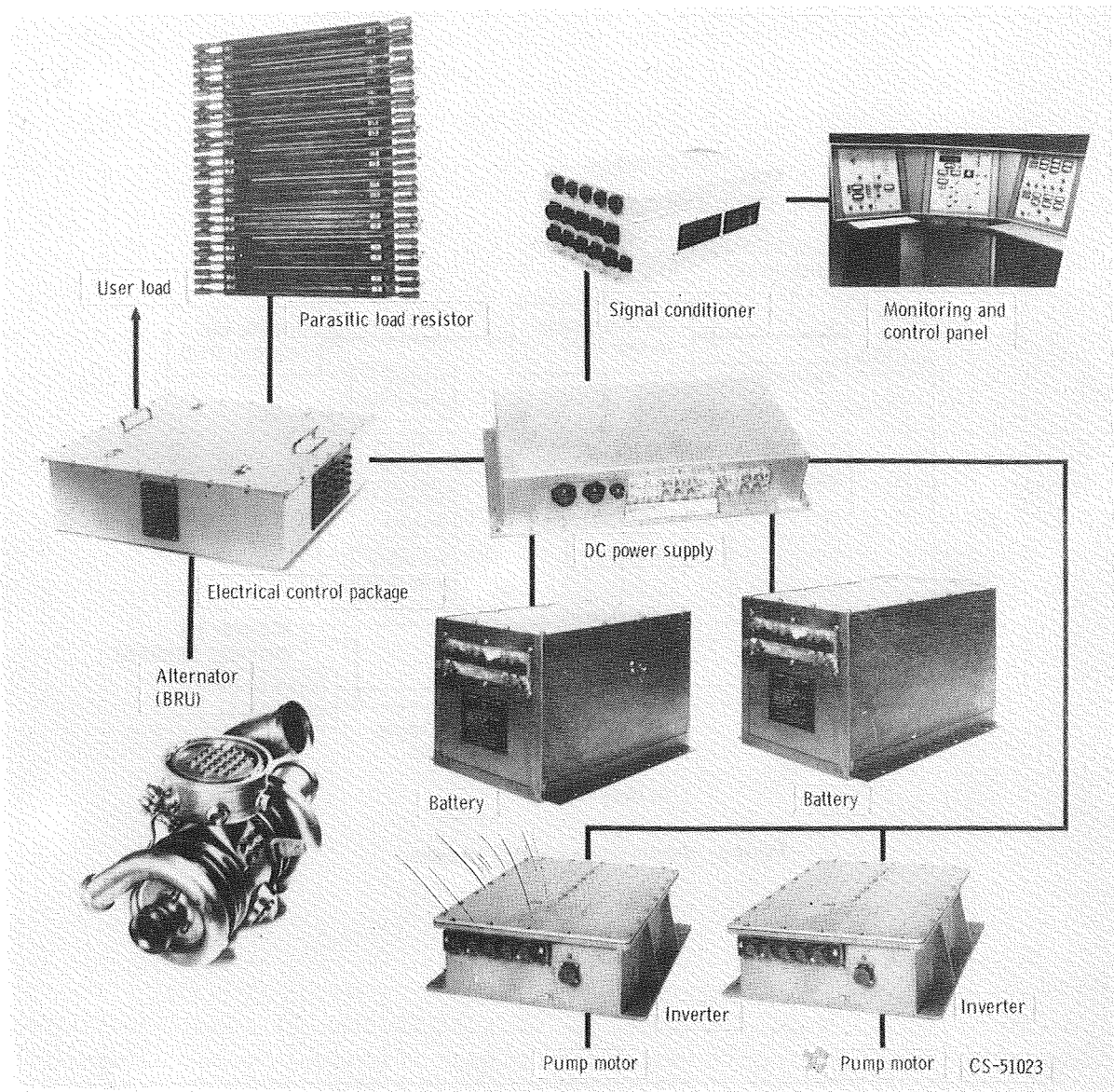
The remainder of the electrical subsystem regulates and distributes engine power and provides all control and logic functions required to operate the Brayton engine. The major packages which comprise the electrical subsystem are the Power Supply Package (PSP), the Electrical Control Package (ECP), the Brayton-Engine Control System (BCS), the inverters, and the pump motors. All power required to operate the electrical subsystem during startup, normal operation, and shutdown is supplied by the engine. Figure 2 is a schematic representation of the electrical subsystem showing individual components and their control and power interconnections.

The PSP supplies the engine with required dc power both during normal operation and when the BRU is shut down. During normal operation, the dc supply rectifies the 1200-hertz alternator output to provide 56 volts dc (± 28 V dc from ground reference). When the BRU is inactive, two silver-cadmium batteries provide the required power for system startup and shutdown. Direct current is used to power all control and monitoring functions, to recharge the batteries, and to supply the two 400-hertz inverters which



(a) Block diagram.

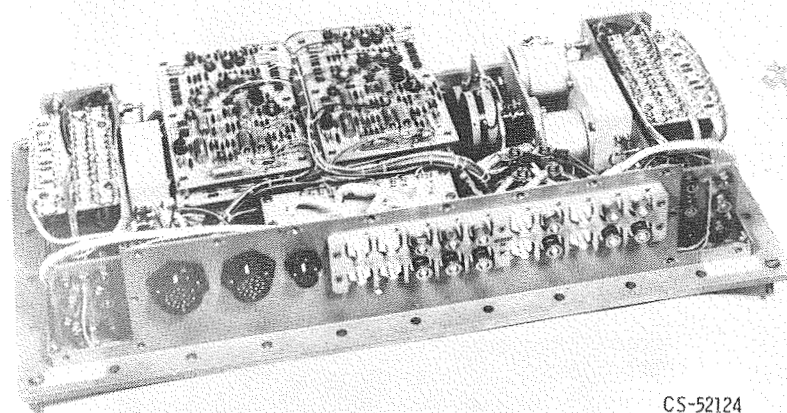
Figure 2. - Brayton engine-electrical subsystem.



(b) Electrical subsystem components.

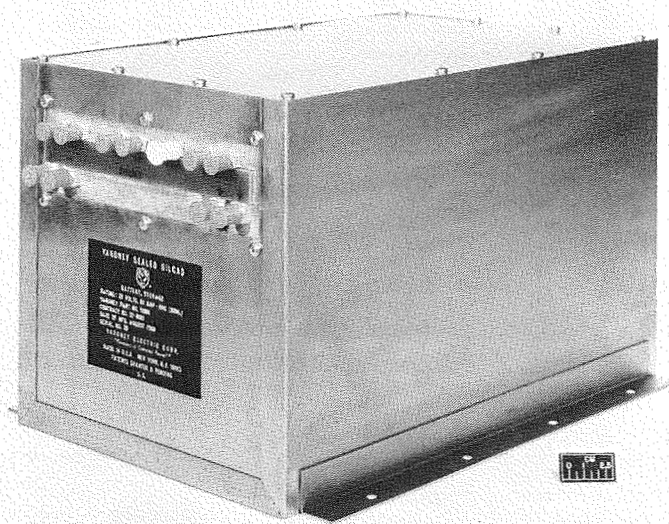
Figure 2. - Concluded.

drive the liquid-loop pumps. Specifically, the PSP consists of a positive and a negative 28-volt battery, and an electronic unit (see fig. 3). Packaged within this electronic unit are the following: two multiple winding transformers and diode assemblies which convert the 208/120 volts ac input to ± 28 volts dc bus power and ± 42 volts dc for battery charging; two identical series regulator current limiting circuits used for battery charging; two separate ampere-hour integrator circuits which continuously depict the approximate charge status of each of the batteries; a bistable latching power relay which connects or removes the batteries from the dc bus; control and logic circuitry, which accepts commands from the BCS and dictates the priority and relation of the PSP internal functions; and finally, a system which senses and conditions a number of PSP data



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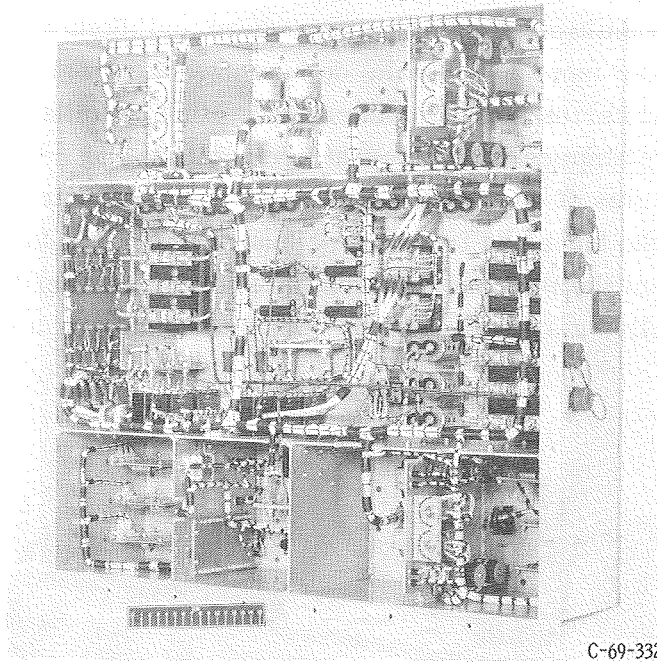
(a) Direct-current power supply (cover removed).



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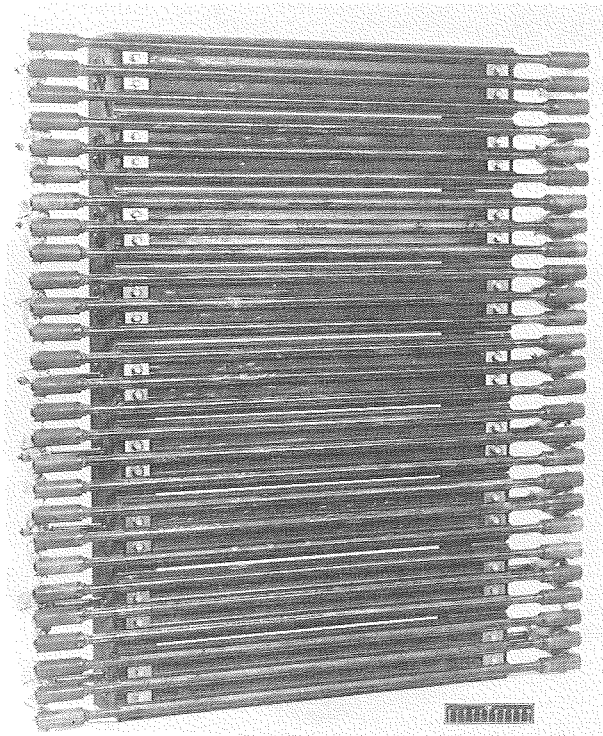
(b) Battery (two required).

Figure 3. - Direct current power supply package.



C-69-332

(a) Speed control-voltage regulator (cover removed).



C-70-1259

(b) Parasitic load resistor.

Figure 4. - Electrical control package.

parameters, converting the information to 0 to 5 volts dc signals for external monitoring.

The ECP provides alternator excitation, regulates the 1200-hertz output voltage, controls the BRU speed, and contains the engine circuit breakers which distribute the electrical power (see fig. 4). Remotely mounted, but still a functional part of the ECP, is the parasitic load resistor which dissipates excess electrical energy as commanded by the speed control in order to maintain the BRU speed within its design range. The alternator field excitation circuitry consists of a series field supply, supplemented as required by a shunt field supply. The series field amplitude is directly proportional to alternator output line current, while the shunt field is varied automatically to maintain constant alternator output voltage. The speed control senses alternator frequency and maintains it within design limits by varying the amount of power being dissipated in the parasitic load resistors.

The BCS provides all control and monitoring functions necessary to operate the engine. It consists of three individual elements: an engine-mounted signal conditioner, a cable link, and a remote control and monitoring console. The signal conditioner is an electronic unit which interfaces between engine electronics (instrumentation and control devices) and the control and monitoring console. The cable system links these two elements of the BCS. The three elements of the BCS are pictured in figure 5. The signal conditioner accepts all engine instrument and control signals and converts them to a common 0 to 5 volts dc output. It also acts as the interface for the returning command and control functions originating from the control console. The control console is a highly compact and specialized system which incorporates human factors engineering in

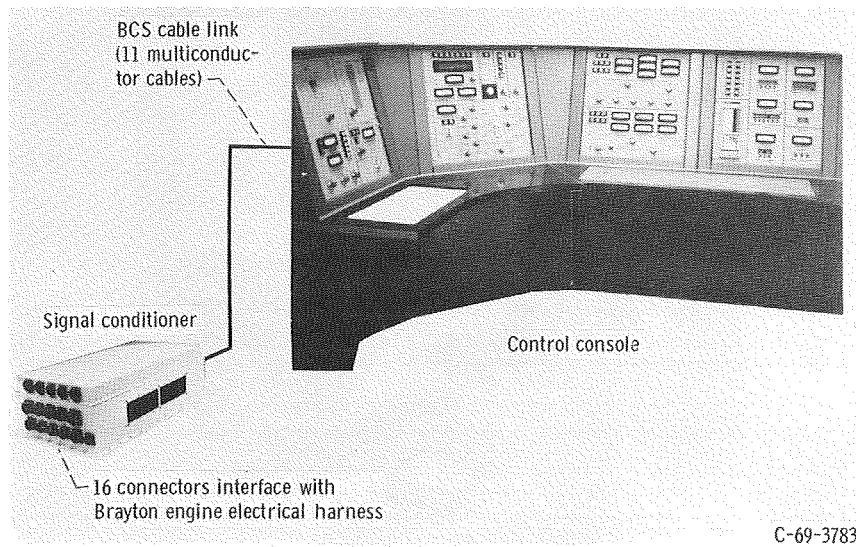
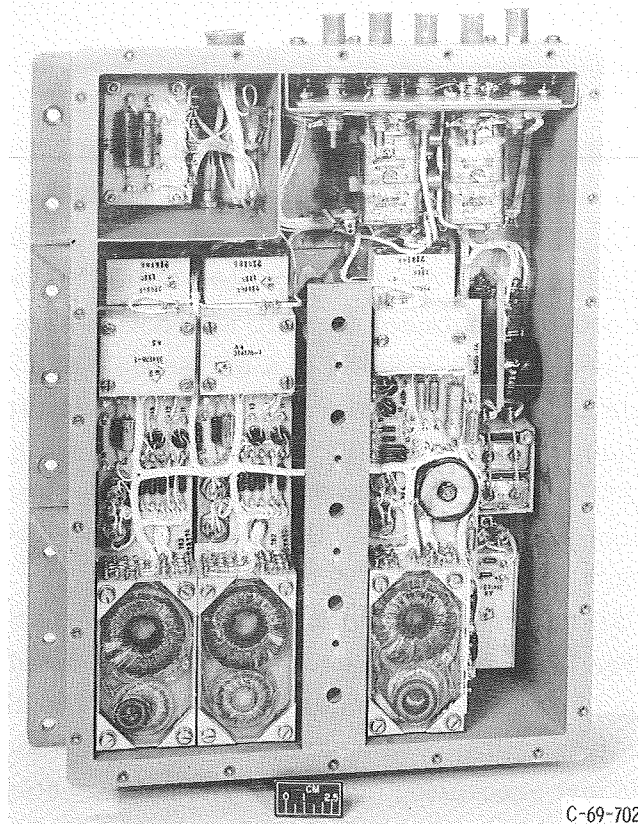
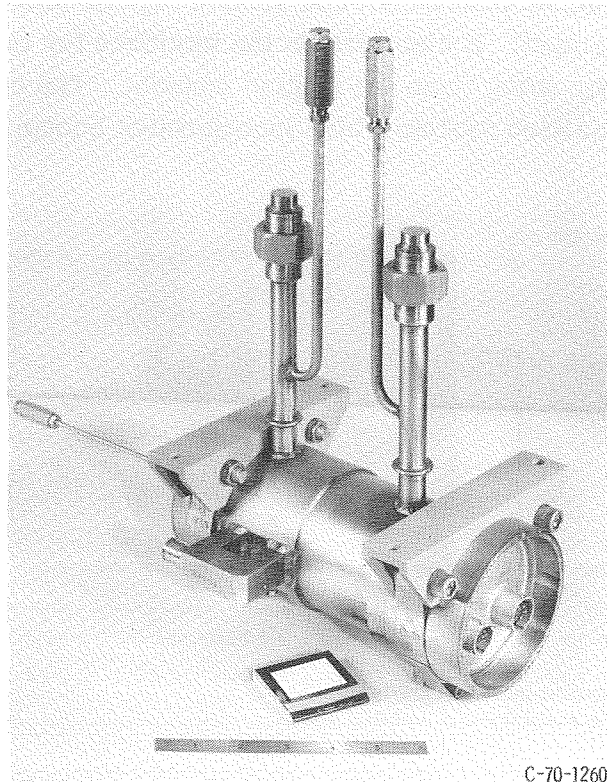


Figure 5. - Brayton-engine control system.



C-69-702

(a) Inverter (cover removed).



C-70-1260

(b) Coolant pump-motor assembly.

Figure 6. - Inverter and pump-motor assembly.

its design. It could, with minimum modification, be used for a mission application. The cable link between the signal conditioner and the control and monitoring console consists of 11 multiconductor cables, which collectively contain 183 sensing signals, 43 return signals, 32 dc power leads, 43 shields, and 66 spare conductors. All console circuits are wired on printed circuit cards which are essentially self-diagnostic and indicate a malfunction directly. Most of these cards can be replaced in the event of a malfunction with the system still in operation. Specific criteria incorporated in the design of the ECS include: (1) redundancy whenever appropriate, (2) no interruption in engine operation as a result of a single component failure, (3) totally self-sufficient, being independent of spacecraft or outside sources of power during engine operation, startup, or shutdown.

Two dc-to-ac inverters each provide up to 25 amperes of three-phase, 400-hertz power to operate the two pump-motor assemblies (PMA) in the two coolant loops (see fig. 6). Each inverter takes its input power from the dc power supply and converts it to a quasi-square wave, approximately 45 volts rms output, which is directly connected to the input of each PMA. A 5-volt pulse from the signal conditioner starts or stops either inverter. The inverters are protected against overload by an internal cutoff circuit which turns the inverter off, but will permit restart after the overload is removed.

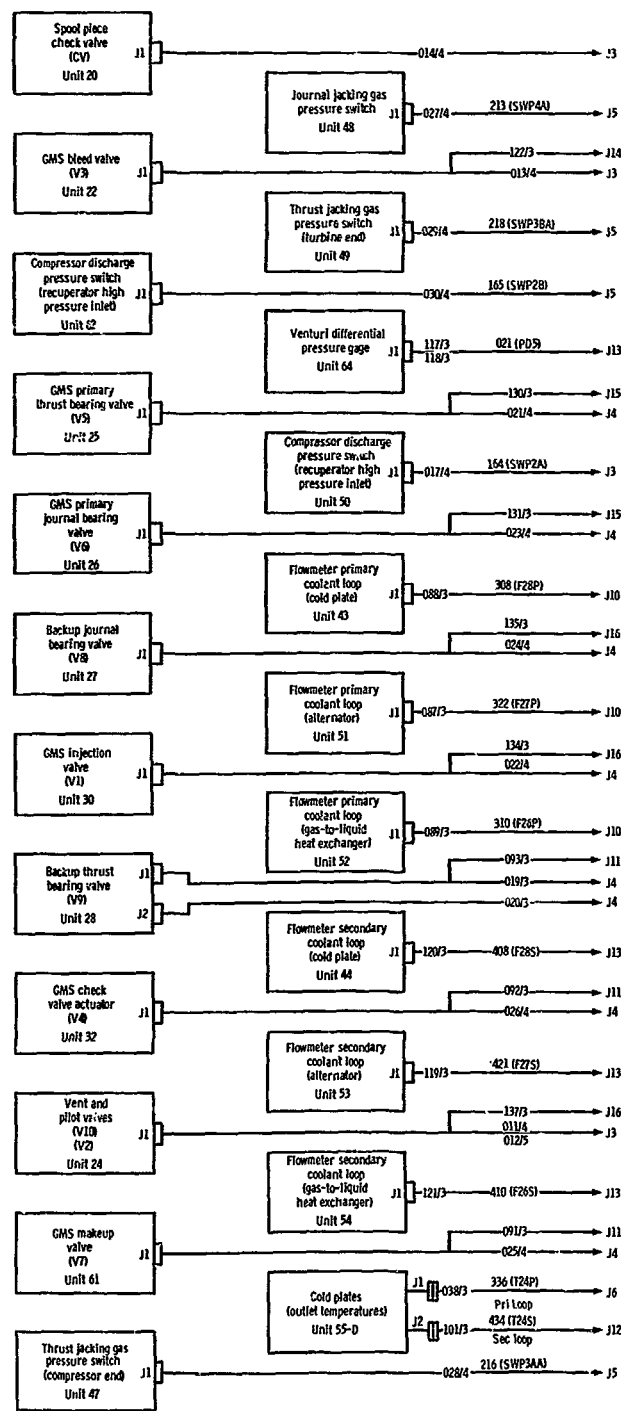
Each PMA is an integral unit consisting of a centrifugal pump and a three-phase, 400-hertz, fractional horsepower induction motor mounted within the same housing. Either pump will operate whenever its respective inverter is turned on. The pumps have a nominal speed of 11 000 rpm at rated pressure and flow. The same coolant liquid which circulates through the heat-rejection subsystem flows within the motor housing and is in direct contact with the electrical windings. The liquid thus also removes heat from the motor windings and lubricates the bearings.

With the exception of the BCS cable link between the engine-mounted signal conditioner and the remote control and monitoring console, no interconnecting electrical subsystem wiring was developed by any of the component manufacturers. All drawings required to integrate the electrical components were developed by Lewis from information furnished by the individual vendors. This effort is discussed in the following section.

DOCUMENTATION OF THE ELECTRICAL SUBSYSTEM WIRING

Drawings

The block diagram of the engine power and control system (fig. 2) describes the conceptual version of the electrical subsystem; however, far more detail was required before engine wiring could be started. Vendor drawings were examined carefully to



Signal conditioner

J1
J2
J3
J4
J5
J6
J7
J8
J9
J10
J11
J12
J13
J14
J15
J16

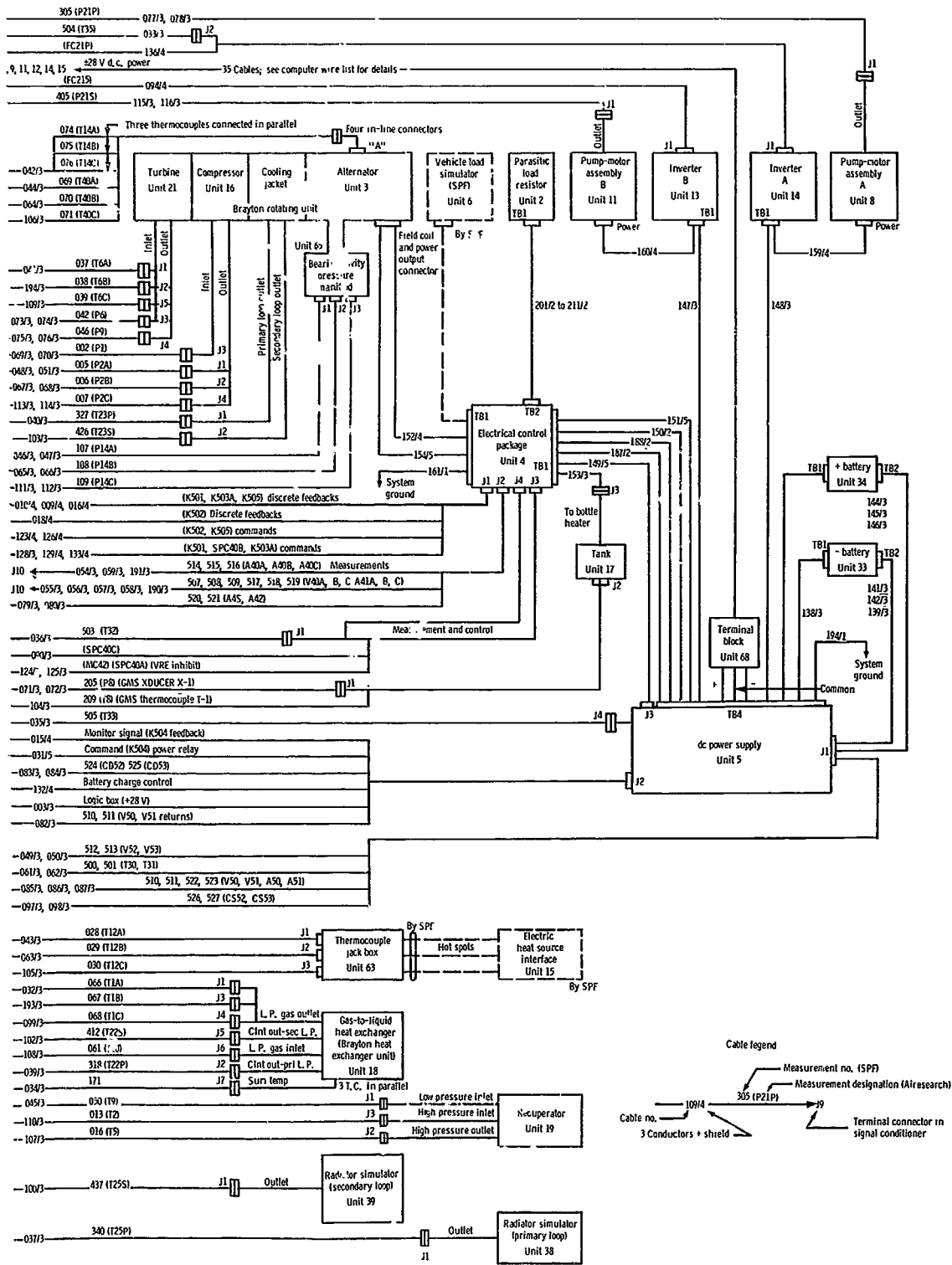


Figure 7. - Brayton power system electrical block diagram.

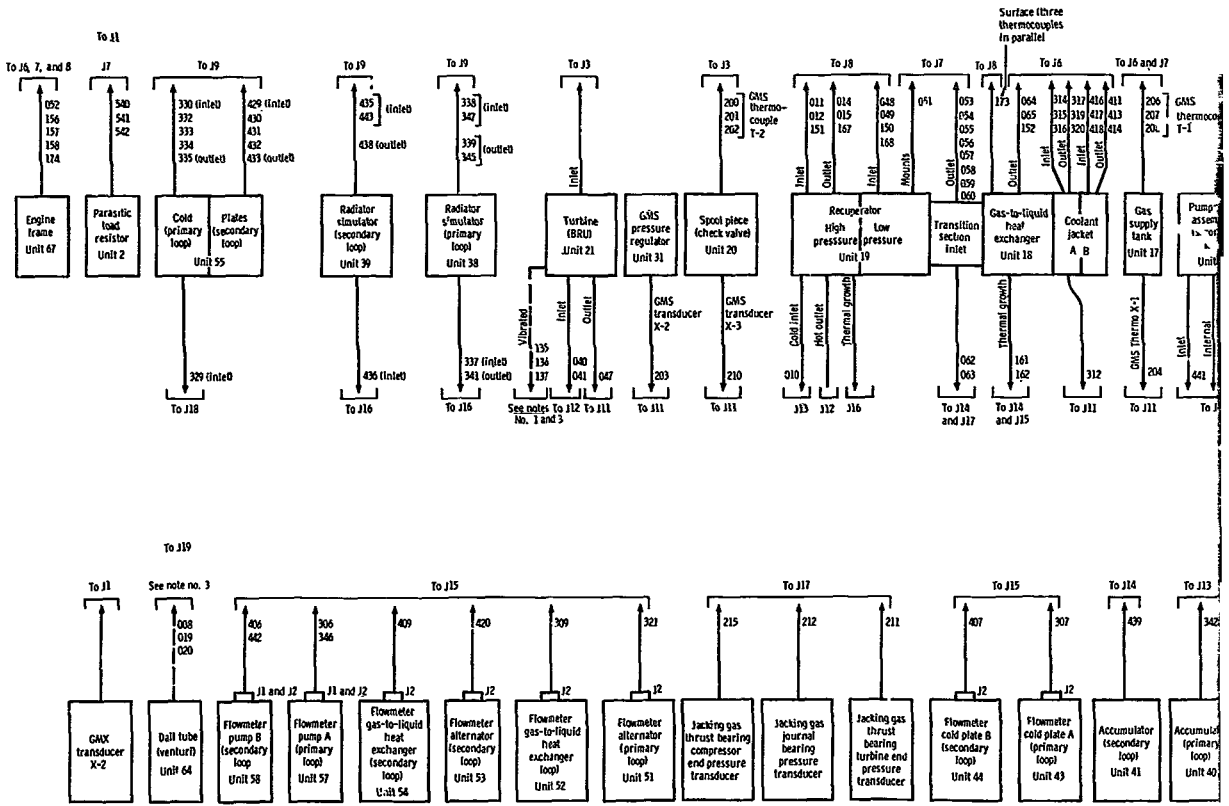
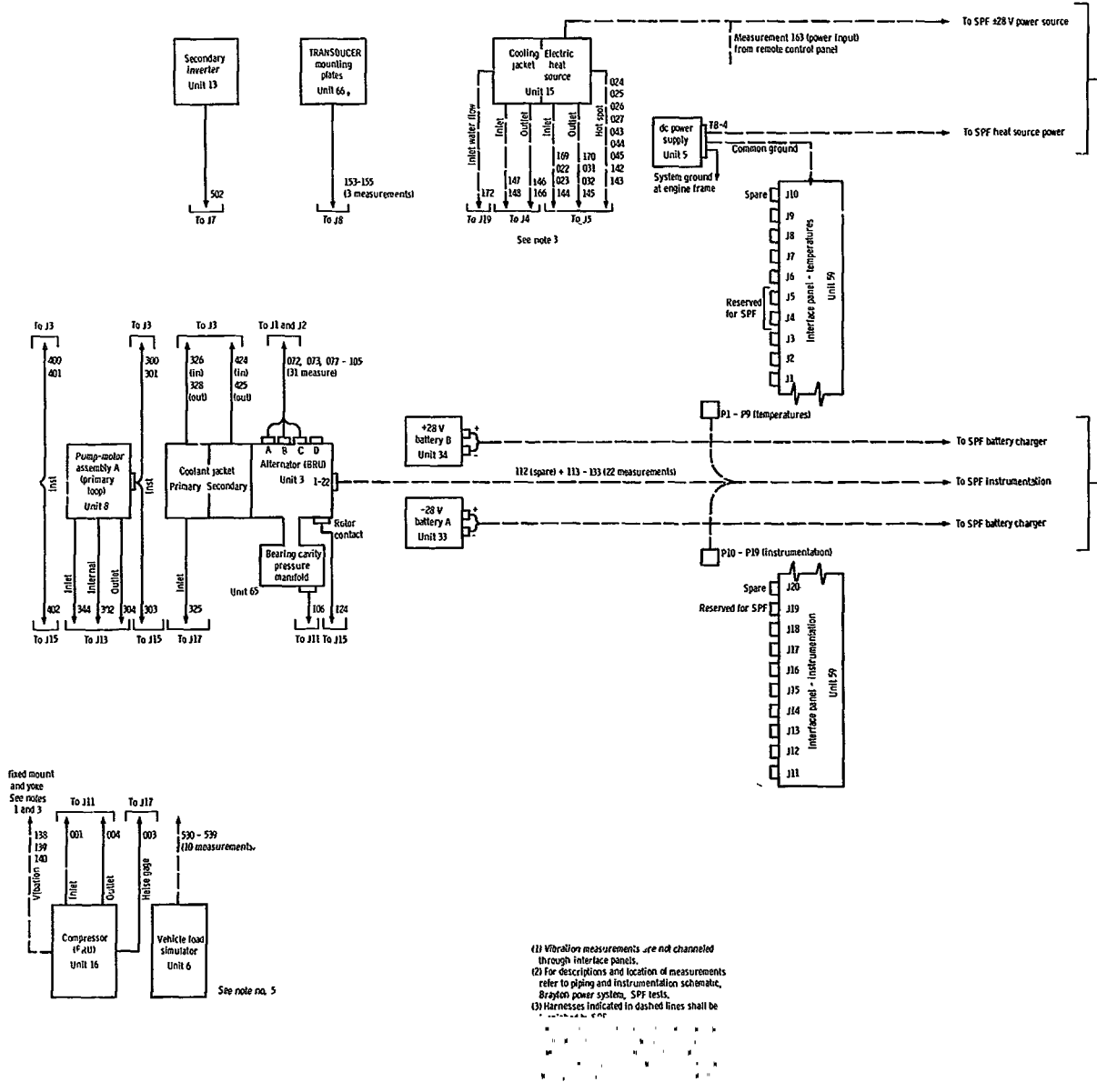


Figure 8.



identify all interfacing connectors and terminals. Then, using this added information, the block diagram of figure 7 was developed. Except for the BCS cable link, which was furnished by the contractor, this drawing shows all the cables required to complete the electrical subsystem. In addition, it gives the number of active conductors per cable, identifies the cables by number, provides shielding and grounding information, and indicates circuit designations for all the control and monitoring circuits.

In the same manner, a second block diagram was developed (see fig. 8) to show the interfaces associated with instrumentation and related support systems outside of the basic engine. These peripheral circuits, referred to in this report as the Development System, include thermocouples, flowmeters, pressure transducers, accelerometers, etc., which were installed to better evaluate engine performance during ground testing.

Concurrent with development of the block diagrams, a grounding philosophy was devised to reduce or eliminate the possibility of stray or extraneous voltages. All conductors were shielded, and all shields were grounded at one end only, to a single point on the engine frame, which, in turn, was grounded to earth.

Wire Lists

Detailed wiring diagrams to show point-to-point connections for each conductor were not developed. The number of cables and components that make up the complete electrical subsystem would require many drawings to show all the terminations and would prove too cumbersome. Furthermore, maintaining an up-to-date series of these drawings would be time consuming and costly. As an alternative, computerized wire lists for each of the electrical subsystems were developed, based on the block diagrams of figures 7 and 8. Wire lists in themselves are not new. However, tabulations in a computer-oriented format are a much more recent technique which has found acceptance in both aerospace and military applications. It is particularly adaptable to complex systems where a large number of components are to be interconnected.

Two wire lists were established: one detailing the engine electrical subsystem, and the other, the development system. The tabulations included each conductor in the subsystem with both termination points; in addition, they gave the circuit characteristics, cabling and shielding information, and a brief description of the function of that conductor in the system. In order to facilitate the identification of components, each was assigned a unit number as defined in table I. In addition, a sequential signal number was assigned to identify all conductors from each unit.

The wire lists, one page of which is reproduced in figure 9, show the origin of each conductor (unit number, signal number, connector, and pin), its destination (unit number, signal number, connector, and pin), the cable designation number, electrical char-

TABLE I. - BRAYTON ENGINE, LIST OF UNIT NUMBER DESIGNATIONS

Unit number	Component	Unit number	Component
1	Signal conditioner (engine)	35	Emergency shutoff valve (manual)
2	Parasitic load resistor (PLR)	36	Pilot valve (part of GMS vent valve, unit 24)
3	Alternator (BRU)	37	Emergency vent device
4	Electrical control package (ECP)	38	Primary radiator simulator (A)
5	dc power supply	39	Secondary radiator simulator (B)
6	Vehicle load bank (load simulator)	40	Accumulator - primary coolant loop (A)
7	Engine monitor panel	41	Accumulator - secondary coolant loop (B)
8	Primary pump-motor assembly (PMA A)	42	GMS relief valve
9	SPF (Space Power Facility) aluminum bulkhead interface	43	Flowmeter - primary coldplate
10	SPF concrete wall interface	44	Flowmeter - secondary coldplate
11	Secondary pump-motor assembly (PMA B)	45	Not used
12	Engine control panel	46	Pressure switch - relief valve
13	Secondary inverter (B)	47	Pressure switch - thrust bearing jacking gas (compressor end)
14	Primary inverter (A)	48	Pressure switch - journal bearing jacking gas
15	Electric heat source interface	49	Pressure switch - thrust bearing jacking gas (turbine end)
16	Compressor (BRU)	50	Pressure switch - compressor discharge - primary
17	Gas supply tank	51	Flowmeter - alternator primary coolant
18	Gas-to-liquid heat exchanger (BHXU)	52	Flowmeter - BHXU primary coolant
19	Recuperator (BHXU)	53	Flowmeter - alternator secondary coolant
20	Spool piece check valve (CV)	54	Flowmeter - BHXU secondary coolant
21	Turbine (BRU)	55	Coldplates (four sections)
22	GMS (Gas Management System) bleed valve (V3)	56	GMS extra valve
23	GMS orifice filter transducer A	57	Flowmeter - primary pump coolant
24	GMS vent valve (dump valve - V2)	58	Flowmeter - secondary pump coolant
25	GMS primary thrust bearing valve (V5)	59	Interface panel, Brayton engine to SPF
26	GMS primary journal bearing valve (V6)	60	Interface panel, Brayton engine to SPF
27	GMS backup journal bearing valve (V8)	61	GMS makeup valve (V7)
28	GMS backup thrust bearing valve (V9)	62	Pressure switch - compressor discharge - backup
29	GMS orifice filter transducer B	63	Electric heat source thermocouple jack box
30	GMS injection valve (V1)	64	Dall tube differential pressure
31	GMS pressure regulator	65	Bearing cavity pressure manifold (BRU)
32	GMS check valve actuator (V4)	66	Transducer mounting plates
33	Negative 28 V battery (A)	67	Engine frame
34	Positive 28 V battery (B)	68	Terminal block for BCS power

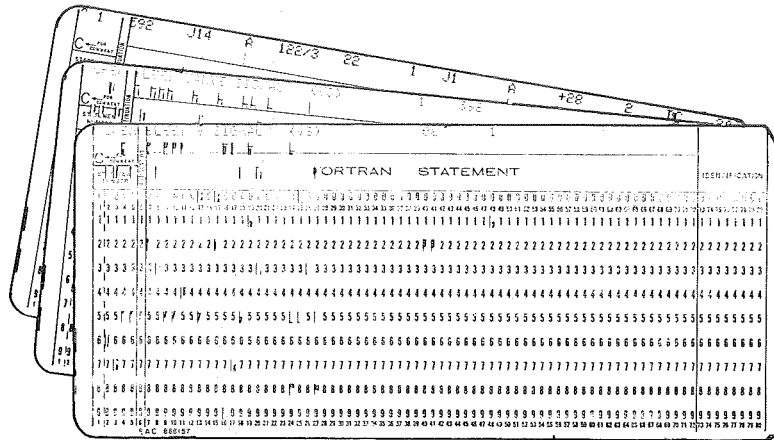
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FROM					CABLE					TO					CHARACTERISTICS																																																																
UNIT	SIG	CONN	PIN	NO.	UNIT	SIG	CONN	PIN	VOLTS	AMPS	FREQ.	AWG.																																																																			
1	592	J14	A	122/3	22	1	J1	A	+28	2		DC 20																																																																			
1	593	J14	B	122/3	22	2	J1	B	COMM	2		DC 20																																																																			
1	594	J14	C	122/3	22																																																																										
1	595	J14	D	123/4	4	38	J1	M	COMM			DC 20																																																																			
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1	597	J14	F	123/4	4	36	J1	K	COMM			DC 20																																																																			
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1	599	J14	H	124/3	4	73	J3	L	+28			DC 20																																																																			
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CIRCUIT FUNCTION/REMARKS										UNIT		SIG																																																																			
OPEN	BLEED	VALVE	SIGNAL	(V3)						1	592																																																																				
OPEN	BLEED	VALVE	RETURN	(V3)						1	593																																																																				
OPEN	BLEED	VALVE	SHIELD	(V3)						1	594																																																																				
VRE	AUX. CONT.	CLOSE	COMND	(K502)						1	595																																																																				
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VRE	INHIBIT	MC-42	SIGNAL							4	73																																																																				
VRE	INHIBIT	MC-42	RETURN							4	80																																																																				
OPEN	BLEED	VALVE	SIGNAL	(V3)						22	1																																																																				
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(a) Preliminary data sheets.
Figure 10. - Wire list development.



(b) Typical data cards for one conductor.

Figure 10. - Concluded.

for point B. The completed tabulations for the Brayton engine listed 644 actual conductors in the electrical subsystem harness and 812 in the development harness. A detailed listing of the computer program used to generate the two wire lists is given in appendix A.

Many advantages have become evident in the use of these wire lists over the more conventional use of wiring diagrams:

(1) The computer was programmed to include several self-checking capabilities which flagged potential errors that could result in miswiring. For example, if two wires are identified by the same unit and signal number, the duplications are automatically flagged in the right-hand margin. Should a function card be keyed improperly to a characteristics card, the error appears as a separate printout at the beginning of the wire list. Similarly, a conductor for which one end is connected and the other end is unaccounted for will be printed automatically out of sequence at the head of the list where it can be quickly found for corrective action.

(2) The format is flexible and can be easily modified by a simple program change. For example, wiring can be listed in order by unit and signal number (as shown in fig. 9) or the listing might be in sequence by cable number. Or, if desired, specified columns may be left blank to suit the user's applications. Several other variations are easily available as shown in appendix B (table II).

(3) Revisions are easily accomplished. For each rerouted conductor, no more than three new cards are required: a characteristics and the two function cards. Cards can be punched, and a new printout run off and distributed the same day. The revision date is printed automatically on each page.

(4) More detailed information appears on the wire list than normally appears in the typical wiring diagram: voltages, currents, frequencies, circuit functions, etc.

(5) The wire list is an ideal check list for technicians to install cables and to check out the installation. Further, all the needed information pertaining to a conductor appears on one line, eliminating the need to trace a circuit through a series of drawings.

(6) A particularly useful option performed by the computer is to automatically search the wire list for potential subharness assemblies which will permit separating the harness into smaller increments for fabrication and installation.

In order to further acquaint the reader with the utility of the wire list program, additional details together with a complete example problem are presented in appendix B.

ELECTRICAL SUBSYSTEM BUILDUP

A wood mockup, which had been built earlier to develop physical configuration of the engine, was made available and presented the ideal form on which to fabricate the electrical harnesses. Cable routing was planned so that, when wiring was completed, the two harnesses could be removed from the mockup and reinstalled as separate units on the engine. The wire lists were used to complete the cable terminations and connector assemblies, to ring out the completed wiring, and to serve as a check list for measurement of insulation resistances. The harness assembly during installation on the mockup is shown in figure 11.

Mechanical assembly of the basic engine was accomplished concurrently with fabrication of harnesses on the mockup. At the time the engine was ready for installation of the electrical system, a detailed procedure was provided to expedite mounting and connecting the electrical components and harnesses.

On completion of the electrical installation, a static checkout of the electrical subsystem was performed to verify that all components were connected properly and functioned normally. A checkout procedure, developed for this purpose, detailed the sequence for final interconnection of units and specified precautionary safeguards to prevent damage to any component during initial application of power. Interconnection of units and power buildup were accomplished step-by-step until, at the last phases of checkout, the entire electrical subsystem was connected in its final configuration and functioned normally with an external source of 1200-hertz, three-phase power instead of the BRU alternator.

In performing this procedure, the control console was connected to the engine for the first time. This presented the first opportunity to verify operation of the engine electrical system from a remote station. This also was the first time the engine interfaced with the external development system.

Completion of these tests established confidence in the entire electrical subsystem and verified that the engine was ready to undergo systems testing. Figure 12 shows the

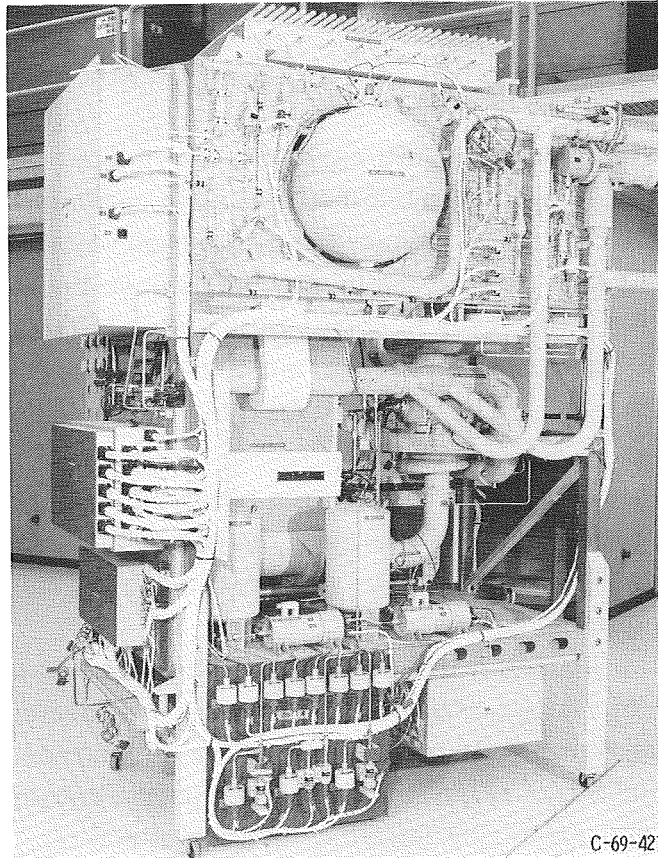


Figure 11. - Engine mockup with electrical harnesses installed.

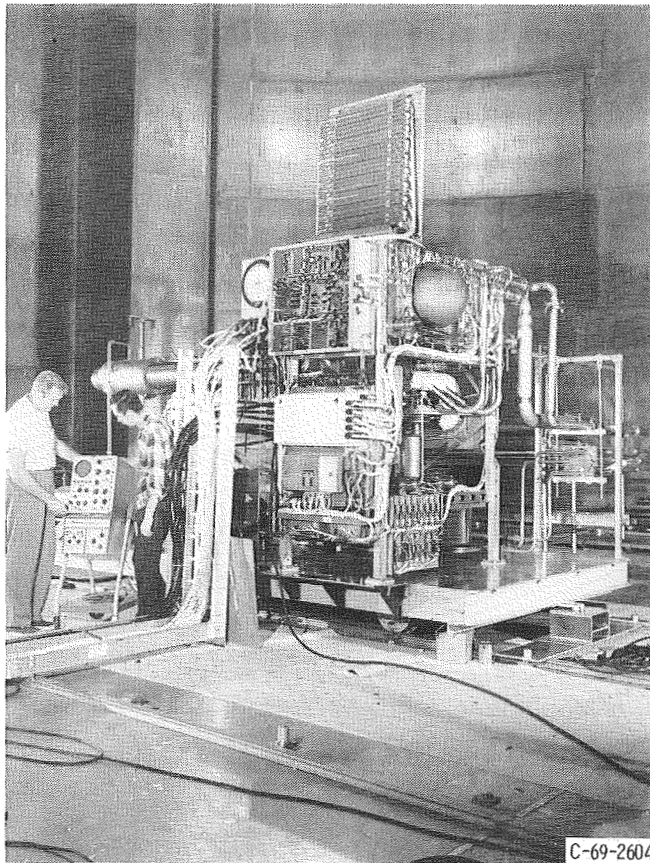


Figure 12. - Engine undergoing systems tests at Space Power Facility.

Brayton-B engine during early checkout operations at the Lewis Space Power Facility at Plum Brook. Subsequent engine testing under vacuum conditions verified that the objective of proper electrical subsystem performance had been achieved.

CONCLUDING REMARKS

Despite its complexity, wiring of the engine system proceeded smoothly and without significant discrepancies. Some minor problems resulting from the integration of information from drawings of many vendors, were easily corrected. The expediency and success with which the component interconnections were completed is attributed directly to the use of the computer. The principal benefits derived from this form of documentation are as follows:

1. The built-in self-checking capability assures a lower probability of error than either drawings or noncomputerized lists.
2. All needed information for any conductor appears on only one line in the tabulation, eliminating the need to trace a circuit through a series of drawings.
3. Ease in accomplishing revisions and reissuing wire lists results in lower maintenance costs.
4. Greater detail can be shown for all electrical conductors as compared with wiring diagrams.
5. Information is condensed into a simple, convenient, flexible format which facilitates installation, checkout, configuration control, and troubleshooting of the system.

Lewis Research Center,
National Aeronautics and Space Administration,
Cleveland, Ohio, April 27, 1970,
120-27.

APPENDIX A

FORTRAN PROGRAM USED TO DOCUMENT BRAYTON ENGINE ELECTRICAL SUBSYSTEM

by Donald R. Packe

The following printout is a copy of the FORTRAN program developed to tabulate the Brayton engine electrical harnesses and to print the wire lists. The program is included in this report as a model which, with appropriate modifications, can be adapted at other computer facilities to document the wiring for any complex electrical-electronic system.

```
$1BFTC WIRE LIST,DECK WIRE 1
C THIS PROGRAM ORGANIZES WIRE LIST INFORMATION TO ASSIST IN WIRE 2
C THE DESIGN AND FABRICATION OF ELECTRICAL HARNESSSES. WIRE 3
C IN ADDITION TO PRESENTING THE INFORMATION IN CONVENIENT FORM, WIRE 4
C IT SEARCHES THE DATA FOR DUPLICATION OF CONNECTIONS,AND FLAGS WIRE 5
C EACH DUPLICATE CONNECTION AS A POSSIBLE ERROR (NORMALLY EACH WIRE 6
C TERMINAL CONTAINS ONLY A SINGLE WIRE). THE PROGRAM SORTS ALL WIRE 7
C INPUT DATA INTO ALPHABETICAL AND NUMERICAL ORDER. FINALLY, THE WIRE 8
C PROGRAM SEARCHES FOR SELF CONTAINED SUBHARNESSSES WITHIN THE WIRE 9
C TOTAL HARNESS. THESE SUBHARNESSSES WOULD BE CANDIDATES FOR WIRE 10
C SEPARATE FABRICATION. WIRE 11
C WIRE 12
C WIRE 13
C LIST OF SYMBOLS WIRE 14
C U1='FROM' UNIT NUMBER WIRE 15
C S1='FROM' SIGNAL NUMBER WIRE 16
C C1='FROM' CONNECTOR NUMBER WIRE 17
C P1='FROM' PIN NUMBER WIRE 18
C HN=CABLE NUMBER WIRE 19
C U2='TO' UNIT NUMBER WIRE 20
C S2='TO' SIGNAL NUMBER WIRE 21
C C2='TO' CONNECTOR NUMBER WIRE 22
C P2='TO' PIN NUMBER WIRE 23
C VO=VOLTAGE ON WIRE WIRE 24
C AM=CURRENT ON WIRE WIRE 25
C FR=FREQUENCY ON WIRE WIRE 26
C AW=WIRE SIZE WIRE 27
C CFR=DESCRIPTION OF WIRE FUNCTION AT 'FROM' TERMINAL WIRE 28
C WIRE 29
C DIMENSION HED(4) WIRE 30
C DIMENSION U1(1400), S1(1400), C1(1400), P1(1400), U2(1400), S2(140 WIRE 31
C 10), C2(1400), P2(1400), VO(1400), AM(1400), FR(1400), AW(1400), CF WIRE 32
C 2R(1400,6), HN(1400), CFRX(6) WIRE 33
C COMMON U1,S1,C1,P1,U2,S2,C2,P2,VO,AM,FR,AW,CFR,HN,J,KY WIRE 34
C DATA E1,E2/6HDUP US,6HDUP CP/ WIRE 35
C DATA TST1,TST2/6H000001,6H000002/ WIRE 36
C DIMENSION HD(13), EROR(2) WIRE 37
C DATA AST,DOTS/1H#,6H...../ WIRE 38
101 FORMAT (A3,3X,A3,3X,A4,3X,A3,2XA5,2X,A3,4X,A3,3X,A4,3X,A3,2X,A6,1X WIRE 39
C 1A5,1X,A5,1X,A5) WIRE 40
C WIRE 41
102 READ (5,104) KY,KZ,HD WIRE 42
C THE RUN OPTIONS ARE AS FOLLOWS WIRE 43
C KY=0 OR BLANK- THE DATA IS SORTED IN ORDER OF 'FROM' UNIT AND WIRE 44
C SIGNAL NUMBER -- ALL DATA IS PRINTED OUT WIRE 45
```

C	KY=1 - - - - -	THE DATA IS SORTED IN ORDER OF 'FROM' UNIT AND CABLE NUMBER -- ALL DATA IS PRINTED OUT	WIRE	46
C			WIRE	47
C	KY=2 - - - - -	THE DATA IS SORTED IN ORDER OF 'FROM' UNIT AND CABLE NUMBER -- ONLY 'FROM' UNIT, CONNECTOR, AND PIN AND 'TO' UNIT, CONNECTOR, AND PIN ARE PRINTED OUT. THIS LISTING IS USEFUL AS A CHECK-OFF LIST FOR TESTING A COMPLETED HARNESS.	WIRE	48
C			WIRE	49
C			WIRE	50
C			WIRE	51
C	KZ=0 JK BLANK-	EACH 'FROM' AND 'TO' LIST OF UNIT, SIGNAL, CONNECTOR, AND PIN IS INVERTED AND ADDED TO THE LIST IN THE INVERTED FORM (I.E. 'FROM' IS INVERTED TO 'TO' AND VISA VERSA). THIS CUTS IN HALF THE NUMBER OF INPUT DATA CARDS REQUIRED TO DEFINE THE HARNESS.	WIRE	52
C			WIRE	53
C			WIRE	54
C			WIRE	55
C			WIRE	56
C			WIRE	57
C	KZ=1 - - - - -	THE INPUT IS NOT INVERTED -- THIS IS USEFUL FOR LISTING THE CONTENTS OF THE DATA DECK WHILE ORGANIZING THE HARNESS INFORMATION.	WIRE	58
C			WIRE	59
C			WIRE	60
C	KZ=2 - - - - -	THE INPUT DATA IS INVERTED AS IN KZ=0 AND IN ADDITION, ALL POSSIBLE COMPLETE AND SEVERABLE SUBHARNESSSES WITHIN THE MAIN HARNESS ARE IDENTIFIED AND PRINTED OUT.	WIRE	61
C			WIRE	62
C			WIRE	63
C			WIRE	64
C			WIRE	65
C	HD - - - - -	A 78 CHARACTER FIELD FOR A HEADING TO BE PRINTED AT THE TOP OF EACH OUTPUT PAGE	WIRE	66
C			WIRE	67
			WIRE	68
			WIRE	69
C	READ (5,103) HED	HED IS THE IDENTIFIER HEADING OF THE INFORMATION FIELD OF EACH WIRE -- NORMALLY THIS WILL BE VOLTS, AMPS, FREQUENCY, AND WIRE SIZE	WIRE	70
C			WIRE	71
C			WIRE	72
			WIRE	73
103	FORMAT (A6,1X,A5,1X,A5,1X,A5)		WIRE	74
104	FORMAT (Z11,13A6)		WIRE	75
	J=1		WIRE	76
			WIRE	77
105	READ (5,101) U1(J),S1(J),C1(J),P1(J),HN(J),U2(J),S2(J),C2(J),P2(J),V1(J),AM(J),FR(J),AW(J)		WIRE	78
C		THIS STATEMENT READS IN THE (FROM) AND (TO) UNIT, SIGNAL, CONNECTOR, AND PIN INFORMATION AS WELL AS CABLE NUMBER AND WIRE OPERATING CONDITIONS SUCH AS VOLTAGE, CURRENT, FREQUENCY, AND WIRE SIZE.	WIRE	79
C			WIRE	80
C			WIRE	81
C			WIRE	82
			WIRE	83
	IF (U1(J).EQ.AST) GO TO 108		WIRE	84
	CALL PACKUP (C1(J))		WIRE	85
	CALL PACKUP (C2(J))		WIRE	86
	CALL PACKUP (P1(J))		WIRE	87
	CALL PACKUP (P2(J))		WIRE	88
	IF (KZ.EQ.1) GO TO 106		WIRE	89
	J=J+1		WIRE	90
	U1(J)=U2(J-1)		WIRE	91
	S1(J)=S2(J-1)		WIRE	92
	C1(J)=C2(J-1)		WIRE	93
	P1(J)=P2(J-1)		WIRE	94
	U2(J)=U1(J-1)		WIRE	95
	S2(J)=S1(J-1)		WIRE	96
	C2(J)=C1(J-1)		WIRE	97
	P2(J)=P1(J-1)		WIRE	98
	HN(J)=HN(J-1)		WIRE	99
	V1(J)=V1(J-1)		WIRE	100
	AM(J)=AM(J-1)		WIRE	101
	FR(J)=FR(J-1)		WIRE	102
	AW(J)=AW(J-1)		WIRE	103
106	J=J+1		WIRE	104
	IF (J.LT.1400) GO TO 105		WIRE	105
	WRITE (6,107)		WIRE	106
107	FORMAT (54H0 LIST CONTAINS MORE THAN 1400 TERMINATIONS--PROGRAM TERMINATED)		WIRE	107
	STOP		WIRE	108
			WIRE	109
			WIRE	110
108	J=J-1		WIRE	111
	WRITE (6,109) J		WIRE	112
109	FORMAT (14H0 LIST CONTAINS,14,13H TERMINATIONS)		WIRE	113

		WIRE 114
	CALL ORDER	WIRE 115
C	SORTS DATA INTO ORDER	WIRE 116
	IF (KZ.EQ.2) CALL SEARCH	WIRE 117
C	SEE KZ=2 OPTION DESCRIPTION ABOVE	WIRE 118
	CALL DUPCHK	WIRE 119
C	CHECKS FOR DUPLICATE TERMINATIONS	WIRE 120
	DATA BLANK/1H /	WIRE 121
	DO 110 L=1,J	WIRE 122
	DO 110 K=1,6	WIRE 123
110	CFR(L,K)=BLANK	WIRE 124
		WIRE 125
		WIRE 126
C	DESCRIPTION OF WIRE FUNCTION AT 'FROM' TERMINAL	WIRE 127
111	READ (5,112) (CFRX(K),K=1,6),UX,SX	WIRE 128
112	FORMAT (6A6,2X,A3,4X,A3)	WIRE 129
	IF (CFRX(1).EQ.AST) GO TO 116	WIRE 130
	DO 114 JX=1,J	WIRE 131
	IF (UX.NE.U1(JX)) GO TO 114	WIRE 132
	IF (SX.NE.S1(JX)) GO TO 114	WIRE 133
	DO 113 KX=1,6	WIRE 134
113	CFR(JX,KX)=CFRX(KX)	WIRE 135
	GO TO 111	WIRE 136
114	CONTINUE	WIRE 137
	WRITE (6,115) (CFRX(K),K=1,6),UX,SX	WIRE 138
115	FORMAT (50H THIS FUNCTION/REMARKS CARD HAS NO UNIT/SIG MATCH(,6A6, 12X,A3,2X,A3,1H))	WIRE 139
	GO TO 111	WIRE 140
116	JX=0	WIRE 141
117	WRITE (6,118) KY,KZ,4D	WIRE 142
118	FORMAT (1H1,212,2X,13A6,A2)	WIRE 143
	WRITE (6,119) HEJ,(DUTS,KX=1,22)	WIRE 144
119	FORMAT (2H0*,10X,4HFROM,9X,7H*CABLE*,12X,2HTU,11X,1H*,4X,15HCHARAC ITERISTICS,5X,1H*,36X,1H*/58H *UNIT. SIG . CONN . PIN *NUMBR* UNIT 2. SIG . CONN . PIN *,A6,1H.,A5,1H.,A5,1H.,A5,1H.,A5,38H* CIRCUIT FUNC TION/REMARKS */1H ,21A6,A5)	WIRE 145
	KQ=0	WIRE 146
120	KQ=KQ+1	WIRE 147
	JX=JX+1	WIRE 148
	EROR(1)=BLANK	WIRE 149
	EROR(2)=BLANK	WIRE 150
	IF (AND(TST2,U2(JX)).NE.TST2) GO TO 121	WIRE 151
	EROR(2)=E2	WIRE 152
121	IF (AND(TST1,U2(JX)).NE.TST1) GO TO 122	WIRE 153
	EROR(1)=E1	WIRE 154
122	IF (U1(JX).EQ.U1(JX-1)) GO TO 123	WIRE 155
	WRITE (6,124) (BLANK,KQ=1,40)	WIRE 156
	KQ=KQ+2	WIRE 157
123	IF (KY.LT.2) WRITE (6,124) U1(JX),S1(JX),C1(JX),P1(JX),HN(JX),U2(J X),S2(JX),C2(JX),P2(JX),VD(JX),AM(JX),FR(JX),AW(JX),(CFR(JX,JY),JY 2=1,6),EROR	WIRE 158
	IF (KY.EQ.2) WRITE (6,124) U1(JX),BLANK,C1(JX),P1(JX),BLANK,U2(JX) 1,BLANK,C2(JX),P2(JX),(BLANK,K3Q=1,12)	WIRE 159
124	FORMAT (2H *,A3,3H . ,A3,3H . ,A4,3H . ,A3,2H *,A5,2H* ,A3,4H . , 1A3,3H . ,A4,3H . ,A3,2H *A6,1H.,A5,1H.,A5,1H.,A5,1H*,6A6,1H*,2A6)	WIRE 160
	IF (JX.EQ.J) GO TO 125	WIRE 161
	IF (KQ.LT.51) GO TO 120	WIRE 162
	GO TO 117	WIRE 163
125	GO TO 102	WIRE 164
	END	WIRE 165
		WIRE 166
		WIRE 167
		WIRE 168
		WIRE 169
		WIRE 170
		WIRE 171
		WIRE 172
		WIRE 173
		WIRE 174

\$IBFTC DUP	LIST,DECK	DUP	1
		DUP	2
	SUBROUTINE DUPCHK	DUP	3
C	THIS SUBROUTINE SEARCHES WITHIN EACH UNIT NUMBER FOR DUPLICATE	DUP	4
		DUP	5
C	SIGNAL NUMBERS AND FOR DUPLICATE PIN NUMBERS ON EACH CONNECTOR.	DUP	6
	DATA ONE/6H000001/	DUP	7
	DATA TWO/6H000002/	DUP	8
	DATA BLANK/1H /	DUP	9
	DIMENSION U1(1400), S1(1400), C1(1400), P1(1400), U2(1400), S2(1400),	DUP	10
	C2(1400), P2(1400), V0(1400), AM(1400), FR(1400), AW(1400), CF	DUP	11
	2R(1400,6), HN(1400), CFRX(6)	DUP	12
	COMMON U1, S1, C1, P1, U2, S2, C2, P2, V0, AM, FR, AW, CFR, HN, J	DUP	13
		DUP	14
	DO 202 K=2, J	DUP	15
	IF (S1(K).EQ.BLANK) GO TO 202	DUP	16
		DUP	17
C	BLANK SIGNAL WORDS OR BLANK PIN WORDS ARE IGNORED.	DUP	18
	IF (U1(K).EQ.U1(K-1).AND.S1(K).EQ.S1(K-1)) GO TO 201	DUP	19
		DUP	20
C	CHECK FOR DUPLICATE UNIT-SIGNAL	DUP	21
		DUP	22
	GO TO 202	DUP	23
201	U2(K)=OR(U2(K),ONE)	DUP	24
	U2(K-1)=OR(U2(K-1),ONE)	DUP	25
		DUP	26
C	A DUPLICATE UNIT-SIGNAL EXISTS. FLAG BOTH LIST ENTRIES.	DUP	27
202	CONTINUE	DUP	28
	KEND=0	DUP	29
203	KSTT=1+KEND	DUP	30
	DO 204 M=KSTT, J	DUP	31
	IF (U1(M).EQ.U1(KSTT)) GO TO 204	DUP	32
	KEND=M-1	DUP	33
	GO TO 205	DUP	34
204	CONTINUE	DUP	35
	KEND=J	DUP	36
205	K1=KEND-1	DUP	37
	DO 208 M=KSTT, K1	DUP	38
	M1=M+1	DUP	39
	IF (P1(M).EQ.BLANK) GO TO 208	DUP	40
		DUP	41
	DO 207 MX=M1, KEND	DUP	42
	IF (C1(M).EQ.C1(MX).AND.P1(M).EQ.P1(MX)) GO TO 206	DUP	43
		DUP	44
C	CHECK FOR DUPLICATE CONNECTOR-PIN	DUP	45
		DUP	46
	GO TO 207	DUP	47
206	U2(M)=OR(U2(M),TWO)	DUP	48
	U2(MX)=OR(U2(MX),TWO)	DUP	49
C	A DUPLICATE CONNECTOR-PIN EXISTS. FLAG BOTH LIST ENTRIES.	DUP	50
207	CONTINUE	DUP	51
208	CONTINUE	DUP	52
	IF (KEND.NE.J) GO TO 203	DUP	53
	RETURN	DUP	54
	END	DUP	55

\$IBFTC ORDR	LIST,DECK	ORDR	1
		ORDR	2
	SUBROUTINE ORDER	ORDR	3
C	THIS SUBROUTINE SORTS THE WIRE LIST INTO ORDER ACCORDING TO	ORDR	4
C	TWO OPTIONS	ORDR	5
C	KQ.EQ.0 OPTION -- THE LIST IS SORTED ACCORDING TO UNIT	ORDR	6
C	AND SIGNAL NUMBER	ORDR	7

C	KQ.NE.0 OPTION -- THE LIST IS SORTED ACCORDING TO UNIT	ORDR	8
C	AND CABLE NUMBER	ORDR	9
	DIMENSION U1(1400), S1(1400), C1(1400), P1(1400), U2(1400), S2(1400), C2(1400), P2(1400), VO(1400), AM(1400), FR(1400), AW(1400), CFR(1400,6), HN(1400), CFRX(6)	ORDR	10
	COMMON U1,S1,C1,P1,U2,S2,C2,P2,VO,AM,FR,AW,CFR,HN,J,KQ	ORDR	11
	DO 301 JX=1,J	ORDR	12
		ORDR	13
	HN(JX)=ARS(1,HN(JX))	ORDR	14
	CALL PACK1 (U1(JX),S1(JX),CFR(JX,1))	ORDR	15
		ORDR	16
C	PACK1 PACKS UNIT AND SIGNAL NUMBER INTO ONE WORD FOR SORTING	ORDR	17
301	U1(JX)=ARS(1,U1(JX))	ORDR	18
302	KCHECK=0	ORDR	19
	DO 305 K=2,J	ORDR	20
	IF (KQ.EQ.0) GO TO 303	ORDR	21
	IF (U1(K).GT.U1(K-1).OR.(U1(K).EQ.U1(K-1).AND.HN(K).GE.HN(K-1))) GO TO 305	ORDR	22
	GO TO 304	ORDR	23
		ORDR	24
303	IF (CFR(K,1).GE.CFR(K-1,1)) GO TO 305	ORDR	25
C	THE FOLLOWING GROUPS EXCHANGE THE (K) AND (K-1) LIST ENTRIES	ORDR	26
		ORDR	27
C	KCHECK.EQ.1 INDICATES LIST IS NOT YET IN ORDER	ORDR	28
304	KCHECK=1	ORDR	29
	TEMP=U1(K)	ORDR	30
	U1(K)=U1(K-1)	ORDR	31
	U1(K-1)=TEMP	ORDR	32
C		ORDR	33
	TEMP=S1(K)	ORDR	34
	S1(K)=S1(K-1)	ORDR	35
	S1(K-1)=TEMP	ORDR	36
C		ORDR	37
	TEMP=C1(K)	ORDR	38
	C1(K)=C1(K-1)	ORDR	39
	C1(K-1)=TEMP	ORDR	40
C		ORDR	41
	TEMP=P1(K)	ORDR	42
	P1(K)=P1(K-1)	ORDR	43
	P1(K-1)=TEMP	ORDR	44
C		ORDR	45
	TEMP=U2(K)	ORDR	46
	U2(K)=U2(K-1)	ORDR	47
	U2(K-1)=TEMP	ORDR	48
C		ORDR	49
	TEMP=S2(K)	ORDR	50
	S2(K)=S2(K-1)	ORDR	51
	S2(K-1)=TEMP	ORDR	52
C		ORDR	53
	TEMP=C2(K)	ORDR	54
	C2(K)=C2(K-1)	ORDR	55
	C2(K-1)=TEMP	ORDR	56
C		ORDR	57
	TEMP=P2(K)	ORDR	58
	P2(K)=P2(K-1)	ORDR	59
	P2(K-1)=TEMP	ORDR	60
C		ORDR	61
	TEMP=VO(K)	ORDR	62
	VO(K)=VO(K-1)	ORDR	63
	VO(K-1)=TEMP	ORDR	64
C		ORDR	65
	TEMP=AM(K)	ORDR	66
	AM(K)=AM(K-1)	ORDR	67
	AM(K-1)=TEMP	ORDR	68
C		ORDR	69
	TEMP=FR(K)	ORDR	70
	FR(K)=FR(K-1)	ORDR	71
	FR(K-1)=TEMP	ORDR	72
		ORDR	73
		ORDR	74
		ORDR	75

C	TEMP=AW(K)	ORDR	76
	AW(K)=AW(K-1)	ORDR	77
	AW(K-1)=TEMP	ORDR	78
	TEMP=CFR(K,1)	ORDR	79
	CFR(K,1)=CFR(K-1,1)	ORDR	80
	CFR(K-1,1)=TEMP	ORDR	81
C	TEMP=HN(K)	ORDR	82
	HN(K)=HN(K-1)	ORDR	83
	HN(K-1)=TEMP	ORDR	84
C		ORDR	85
305	CONTINUE	ORDR	86
	IF (KCHECK.EQ.1) GO TO 302	ORDR	87
	DO 306 JX=1,J	ORDR	88
	UI(JX)=ALS(1,UI(JX))	ORDR	89
306	HN(JX)=ALS(1,HN(JX))	ORDR	90
	RETURN	ORDR	91
	END	ORDR	92
		ORDR	93
		ORDR	94
		ORDR	95

\$IBFTC	PACK	LIST,DECK	PACK	1
			PACK	2
		SUBROUTINE PACK1 (A,B,C)	PACK	3
C		THIS SUBROUTINE COMBINES UNIT AND SIGNAL WORDS INTO A SINGLE	PACK	4
C		WORD FOR SORTING BY ORDER SUBROUTINE	PACK	5
		DATA B1,B3/0770C00000000,6H 00000/	PACK	6
		AX=A	PACK	7
		K=0	PACK	8
401		IF (AND(B1,AX).NE.B3) GO TO 402	PACK	9
		K=K+6	PACK	10
		AX=ALS(6,AX)	PACK	11
		GO TO 401	PACK	12
402		AX=ARS(K,AX)	PACK	13
		BX=B	PACK	14
		K=0	PACK	15
403		IF (AND(B1,BX).NE.B3) GO TO 404	PACK	16
		K=K+6	PACK	17
		BX=ALS(6,BX)	PACK	18
		GO TO 403	PACK	19
404		BX=ARS(K,BX)	PACK	20
		C=OR(ALS(18,ARS(18,AX)),ARS(18,BX))	PACK	21
		RETURN	PACK	22
		END	PACK	23

\$IBFTC	PACKP	LIST,DECK	PACKP	1
			PACKP	2
		SUBROUTINE PACKUP (E)	PACKP	3
C		THIS SUBROUTINE LEFT ADJUSTS DATA WORDS, I.E. UNIT, SIGNAL,	PACKP	4
C		CONNECTOR, AND PIN DESIGNATORS. LEFT HAND BLANKS ARE PURGED	PACKP	5
			PACKP	6
C		TO ALIGN DATA PRIOR TO SORTING.	PACKP	7
		DIMENSION F(4)	PACKP	8
		DATA BLANK,S1/6H 00000,0770000000000/	PACKP	9
		DO 501 K=1,4	PACKP	10
		F(K)=AND(S1,E)	PACKP	11

501	E=ALS(6,E)	PACKP	12
	DO 502 L=1,3	PACKP	13
	DO 502 K=1,3	PACKP	14
	IF (F(K).NE.BLANK) GO TO 502	PACKP	15
	T=F(K)	PACKP	16
	F(K)=F(K+1)	PACKP	17
	F(K+1)=T	PACKP	18
502	CONTINUE	PACKP	19
	DO 503 K=1,4	PACKP	20
	L=5-K	PACKP	21
503	E=OR(ARS(6,E),F(L))	PACKP	22
	RETURN	PACKP	23
	END	PACKP	24

\$IBFTC SEARCH LIST,DECK

		SEARC	1
		SEARC	2
	SUBROUTINE SEARCH	SEARC	3
C	THIS SUBROUTINE TRACES THROUGH THE DATA TO IDENTIFY WIRE	SEARC	4
C	SUB-HARNESS POSSIBILITIES. IN SEARCHING THROUGH IT DISCOUNTS	SEARC	5
C	WIRES TERMINATED ON A THREADED STUD (WHICH MAY ACCOMODATE MORE	SEARC	6
C	THAN ONE WIRE) BY IGNORING THE DESIGNATION TB (FOR TERMINAL BLOCK)	SEARC	7
C	IN EITHER CONNECTOR COLUMN. THE TERMINAL BLOCK DESIGNATION MAY	SEARC	8
C	CONTAIN FOUR CHARACTERS TBXX WHERE XX MAY BE ANY ARBITRARY	SEARC	9
C	IDENTIFIER.	SEARC	10
	COMMON U1,S1,C1,P1,U2,S2,C2,P2,VO,AM,FR,AW,CFR,HN,J,KQ	SEARC	11
	DIMENSION U1(1400), S1(1400), C1(1400), P1(1400), U2(1400), S2(1400),	SEARC	12
	C2(1400), P2(1400), VO(1400), AM(1400), FR(1400), AW(1400), CF	SEARC	13
	2R(1400,6), HN(1400), CFRX(6)	SEARC	14
	N=1	SEARC	15
	CFR(1,3)=U1(1)	SEARC	16
	CFR(1,4)=C1(1)	SEARC	17
	CFR(1,5)=U2(1)	SEARC	18
	CFR(1,6)=C2(1)	SEARC	19
	DO 602 K=2,J	SEARC	20
	DO 601 KN=1,N	SEARC	21
	IF (U1(K).EQ.CFR(KN,3).AND.C1(K).EQ.CFR(KN,4).AND.U2(K).EQ.CFR(KN,	SEARC	22
	15).AND.C2(K).EQ.CFR(KN,6)) GO TO 602	SEARC	23
601	CONTINUE	SEARC	24
	N=N+1	SEARC	25
	CFR(N,3)=U1(K)	SEARC	26
	CFR(N,4)=C1(K)	SEARC	27
	CFR(N,5)=U2(K)	SEARC	28
	CFR(N,6)=C2(K)	SEARC	29
602	CONTINUE	SEARC	30
	DATA TB,SQ/6HTB0000,0777700000000/	SEARC	31
	DATA BLANK/IH /	SEARC	32
	DU 603 K=1,N	SEARC	33
603	CFR(K,2)=BLANK	SEARC	34
	DO 604 K=1,N	SEARC	35
	IF (CFR(K,4).EQ.BLANK.OR.CFR(K,6).EQ.BLANK) CFR(K,2)=TB	SEARC	36
604	IF (AND(SQ,CFR(K,4)).EQ.TB.AND.AND(SQ,CFR(K,5)).NE.TB) CFR(K,2)=TB	SEARC	37
	WRITE (6,605)	SEARC	38
605	FORMAT (97H1THE FOLLOWING IS A CONDENSED LIST OF UNITS AND CONNECT	SEARC	39
	IURS BETWEEN WHICH AT LEAST ONE WIRE EXISTS/96H TERMINAL BLOCKS (TB	SEARC	40
	2) AND BLANKS ARE TREATED AS CABLE END POINTS AND ARE MARKED WITH A	SEARC	41
	3N ASTERISK/IH0,6X,22HUNIT-CONN*TO*UNIT-CONN)	SEARC	42
	DATA AST/6H */	SEARC	43
	DU 606 K=1,N	SEARC	44
606	CFR(K,1)=BLANK	SEARC	45
	DO 607 K=1,N	SEARC	46
	IF (AND(SQ,CFR(K,4)).EQ.TB.OR.AND(SQ,CFR(K,6)).EQ.TB) CFR(K,1)=AST	SEARC	47
	IF (CFR(K,2).EQ.TB) CFR(K,1)=AST	SEARC	48

607	CONTINUE	SEARC	49
	WRITE (6,608) (CFR(K,1),(CFR(K,KN),KN=3,6),K=1,N)	SEARC	50
608	FJRMAT (1X,5A6)	SEARC	51
	WRITE (6,609)	SEARC	52
609	FORMAT (66HLTHE FOLLOWING SUB HARNESSSES HAVE BEEN GLEANED FROM THE	SEARC	53
	1 ABOVE LIST)	SEARC	54
	KF=0	SEARC	55
	KB=N+1	SEARC	56
610	KA=0	SEARC	57
	KC=KB-1	SEARC	58
611	KE=0	SEARC	59
	DO 615 K=1,N	SEARC	60
	IF (CFR(K,2).EQ.TB) GO TO 615	SEARC	61
	IF (KA.EQ.1) GO TO 612	SEARC	62
	KA=1	SEARC	63
	GO TO 614	SEARC	64
612	DO 613 KD=KB,KC	SEARC	65
	IF (CFR(K,3).EQ.CFR(KD,3).AND.CFR(K,4).EQ.CFR(KD,4).AND.AND(SQ,CFR	SEARC	66
	1(KD,4)).NE.TB) GO TO 614	SEARC	67
	IF (CFR(K,3).EQ.CFR(KD,5).AND.CFR(K,4).EQ.CFR(KD,6).AND.AND(SQ,CFR	SEARC	68
	1(KD,6)).NE.TB) GO TO 614	SEARC	69
	IF (CFR(K,3).EQ.CFR(KD,5).AND.CFR(K,4).EQ.CFR(KD,6).AND.CFR(K,5).E	SEARC	70
	1Q.CFR(KD,3).AND.CFR(K,6).EQ.CFR(KD,4).AND.AND(SQ,CFR(K,4)).EQ.TB.A	SEARC	71
	2ND.AND(SQ,CFR(K,6)).EQ.TB) GO TO 614	SEARC	72
613	CONTINUE	SEARC	73
	GO TO 615	SEARC	74
614	KC=KC+1	SEARC	75
	CFR(KC,3)=CFR(K,3)	SEARC	76
	CFR(KC,4)=CFR(K,4)	SEARC	77
	CFR(KC,5)=CFR(K,5)	SEARC	78
	CFR(KC,6)=CFR(K,6)	SEARC	79
	CFR(K,2)=TB	SEARC	80
	KE=1	SEARC	81
	GO TO 615	SEARC	82
615	CONTINUE	SEARC	83
	IF (KA.EQ.0) RETURN	SEARC	84
	IF (KE.EQ.1) GO TO 611	SEARC	85
	KF=KF+1	SEARC	86
	WRITE (6,616) KF	SEARC	87
616	FORMAT (12HLSUB HARNESSI4,5X,24HUNIT-CONN* TO *UNIT-CONN)	SEARC	88
	WRITE (6,617) ((CFR(K,KD),KD=3,6),K=KB,KC)	SEARC	89
617	FORMAT (22X,A3,1X,A4,6X,A3,1X,A4)	SEARC	90
	GO TO 610	SEARC	91
	END	SEARC	92

APPENDIX B

HYPOTHETICAL EXAMPLE TO ILLUSTRATE USE OF FORTRAN PROGRAM

The electrical system shown in figure 13 will be used to illustrate how the FORTRAN program is used. The three major components, identified as units 1, 2, and 3 are interconnected by six cables (101 to 106). The cables terminate in connectors (J1 or J2) or terminal boards (TB-1, TB-3). In all, there are 14 conductors, each with two terminations and a shield over cable 101. The conductor information was listed on data sheets shown in figures 14 and 15, and data cards were key punched accordingly.

The card file for the wire lists is organized as shown in figure 16. The following brief explanation of the key cards will clarify their function:

(1) The deck of cards comprising the FORTRAN program (appendix A) or the shorter binary deck is in front.

(2) The title card, figure 17(a), provides the title of the program plus any information the author may wish to include in the heading such as a distribution list, revision information, limitations as to use of the printout, etc. A 78-character field is available for use in the heading which is automatically printed at the top of each page. The first two characters in the heading, at the extreme left of the card, are reserved to specify the run option to the computer. These two spaces are punched with either a zero (or blank), 1, or 2. Table II describes the choice of options.

(3) Following the title card, a Headings card (fig. 17(b)) is inserted to call out the column headings under CHARACTERISTICS. The headings used for the Brayton engine (VOLTS, AMPS, FREQ, and AWG) are optional and can be modified to suit the individual application.

(4) This deck of cards (CHARACTERISTICS) lists all conductor data except FUNCTION.

(5) The card with an asterisk (*) in the first column signals the end of the CHARACTERISTICS deck.

(6) The following deck comprises the FUNCTION information for the individual conductors. Each card in this deck is keyed to its counterpart by the UNIT and SIGNAL NO.

(7) The final asterisk card signals the end of data to be fed to the computer.

The printout of figure 18 was obtained by running the data cards of figure 16 through the computer. Option 02 was selected for this run to illustrate the subharness routine and the wiring tabulation. Page 1 of the printout calls out the three subharnesses that may be fabricated individually to make up the complete harness. At the top of page 2 of the printout is the run option as well as all the other information punched on the title card. On the same page under CHARACTERISTICS the optional subheadings punched on the card of figure 17(b) are printed out.

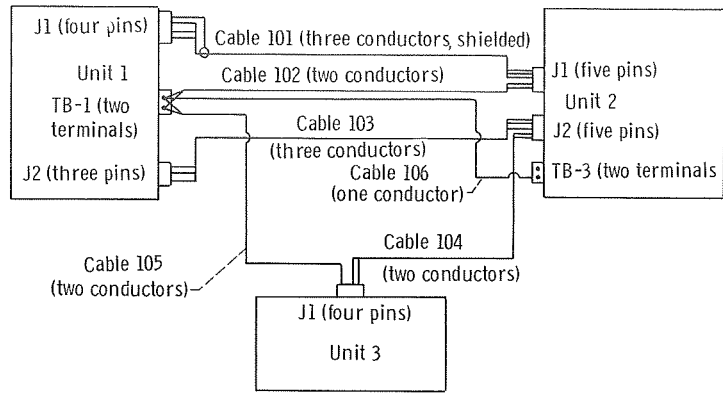


Figure 13. - Electrical system block diagram.

TITLE										PROJECT NUMBER										ANALYST										SHEET 1 OF 2																																																	
HYPOTHETICAL SYSTEM															FORTRAN STATEMENT															IDENTIFICATION																																																	
STATEMENT NUMBER	CONT																																																																														
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80
		FROM			CABLE			TO			CHARACTERISTICS																																																																				
UNIT	SIG	CONN	PIN	NO	UNIT	SIG	CONN	PIN	VOLTS	AMPS	REQ	AWG																																																																			
1	1	J1	A	101/4	2	1	J1	A	120	12	400	14																																																																			
1	2	J1	B	101/4	2	2	J1	B	120	12	400	14																																																																			
1	3	J1	C	101/4	2	3	J1	C	120	12	400	14																																																																			
1	4	J2	A	103/3	2	6	J2	A	+28	18	DC	10																																																																			
1	5	J2	B	103/3	2	7	J2	B	COMM	10	DC	10																																																																			
1	6	J2	C	103/3	2	8	J2	C	-28	8	DC	10																																																																			
1	7	TB1	1	102/2	2	4	J1	D	120		400	16																																																																			
1	8	TB1	2	102/2	2	5	J1	E	120		400	16																																																																			
1	9	TB1	1	105/2	3	1	J1	A	120		400	16																																																																			
1	10	TB1	2	105/2	3	2	J1	B	120		400	16																																																																			
2	9	J2	D	104/2	3	3	J1	C	+28		DC	16																																																																			
2	10	J2	E	104/2	3	4	J1	D	-28		DC	16																																																																			
1	11	TB1	1	106/1	2	11	TB3	2	120		400	16																																																																			
1	12	J1	D	101/4	2				GND																																																																						

Figure 14. - Characteristics data sheet.

TABLE II. - LIST OF AVAILABLE OPTIONS FOR PRINTOUT OF THE WIRE LIST

Run option		Computer printout
Column 1	Column 2	
0	0	UNIT/SIGNAL NO. sequence, <u>with</u> inversion of FROM and TO data (This option prints out both terminations of each conductor in alpha-numerical sequence.)
0	1	UNIT/SIGNAL NO. sequence, <u>without</u> inversion of FROM and TO data (This option prints out only the origin of each conductor in sequence. Destination of each conductor will print out only on the same line as the origin.)
1	0	UNIT/CABLE NO. sequence <u>with</u> UNIT/SIGNAL NO. inversion
1	1	UNIT/CABLE NO. sequence <u>without</u> UNIT/SIGNAL NO. inversion
2	0	UNIT/SIGNAL NO. sequence <u>with</u> UNIT/SIGNAL NO. inversion (All columns are blank except UNIT, CONNECTOR, PIN. Useful for continuity measurement, resistance, and insulation tests.)
2	1	UNIT/SIGNAL NO. sequence <u>without</u> UNIT/SIGNAL NO. inversion (Similar to 2-0.)
0	2	Subharness Routine (Printout is in the same format as in option 0-0, but tabulation is preceded by a listing of all the individual subharnesses into which the list may be separated. Each subharness may be fabricated as a separate unit for individual installation to complete the electrical system. See example in fig. 18.)

The wire list prints out all the conductor information in alpha-numerical sequence starting with UNIT 1, SIGNAL 1, and continues through to the last conductor, UNIT 3, SIGNAL 4. In all, there are 28 lines of wiring information printed out for the 14 conductors including the shield. If option 0-1 were used instead, the inversion feature would be omitted and only 14 lines of information would appear for the 14 conductors.

In the column at the extreme right on page 2, DUP CP (duplicate connector and pin) point out that terminal board TB-1 has more than one conductor terminating on pins 1 and 2 and may be in error. Examination of figure 13 shows that there are actually two conductors connected to each of the two terminals of TB-1 and, therefore, this is not an error.

In the same manner, if DUP US appears in the right-hand column, it indicates a duplication of unit and signal numbers which should also be verified for possible error.

It may also be noted that the shield on Cable 101 (unit 1, signal 12) runs to unit 2 but is not terminated at that end. It, therefore, appears again under the unit 2 listing as the first line since it lacks a signal, connector, and pin number, as well as the function information. These omissions signal that a connection is missing. In this case, the omission is intentional; however, all such omissions are similarly identified as possible wiring errors.

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