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Published in:
IEEE Transactions on Industrial Electronics

DOI (link to publication from Publisher):
10.1109/TIE.2016.2580525

Publication date:
2016

Document Version
Peer reviewed version

Link to publication from Aalborg University

Citation for published version (APA):
Droop Control with Improved Disturbance Adaption for PV System with Two Power Conversion Stages

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Abstract—Droop control has commonly been used with distributed generators for relating their terminal parameters with power generation. The generators have also been assumed to have enough capacities for supplying the required power. This is however not always true, especially with renewable sources with no or insufficient storage for cushioning climatic changes. In addition, most droop-controlled literatures have assumed a single dc-ac inverter with its input dc source fixed. Front-end dc-dc converter added to a two-stage photovoltaic (PV) system has therefore usually been ignored. To address these unresolved issues, an improved droop scheme for a two-stage PV system has been developed in the paper. The developed scheme uses the same control structure in both grid-connected and islanded modes, which together with properly tuned synchronizers, allows mode transfer to be seamlessly triggered. Moreover, the proposed scheme adapts well with internal PV and external grid fluctuations, and is hence more precise with its tracking, as compared with the traditional droop scheme. Simulation and experimental results have verified these expectations, and hence the effectiveness of the proposed scheme.

Index Terms—Microgrid, PV system, droop control, islanded mode, grid-connected mode, seamless transfer.

I. INTRODUCTION

Formation of microgrid (MG) is an attractive approach for solving energy challenges experienced around the globe [1], [2]. It has thus been widely studied in either its grid-connected or islanded mode [3]-[5]. In the former, interfacing inverters are normally operated as controlled current sources, injecting active and reactive powers to the grid [6]. Alternatively, they can be voltage-controlled like proposed in [7]-[20]. For example, in [7], a single-phase PV system has been voltage-controlled to support the fundamental voltage, while eliminate harmonics at its point of common coupling (PCC). The approach however adds an extra inductor in series with the grid to make it more inductive at the expense of size and efficiency.

Another well-received approach is indirect current control, where the grid current has been controlled by regulating magnitude and phase of the ac filter capacitor voltage [8], [9]. Its design is however complex because of nonlinear factors, and sine and cosine tables included in the control. A simplification has subsequently been developed in [10] without the sine and cosine tables. The controller can then be designed using classical control techniques. Despite that, the load voltage waveform may still be distorted since it is not regulated directly by a voltage control loop. A further improvement can be found in [11], where the control system has been enhanced by connecting a local load to the filter capacitor, before feeding back the capacitor current for control. The method works fine, but its dc-link dynamic has not been addressed.

The next voltage-controlled technique proposed is the droop method, which in effect, has mapped the generator terminal parameters with its active and reactive power generations [12]-[14]. The droop method is originally borrowed from large synchronous generators connected to the power grids [15], but when applied to smaller distributed generators (DGs), is prone to fluctuations because of the absence of large inertias and fuel reserves. Despite that, the droop concept has been commonly used with [16] and [17] presenting a droop controller for DG that can guarantee zero steady-state error for its output reactive power. Its output active power may however deviate from the desired reference value. To solve the problem, integral terms have been added to the conventional droop scheme for forcing the DG active and reactive powers to track their references closely [18].

The droop principle has also been recommended by the Consortium for Electric Reliability Technology Solutions (CERTS) for regulating photovoltaic (PV) inverters with a stable bus voltage, even during load transient [19]. More recently, a universal controller has been proposed in [20], where maximum-power-point-tracking (MPPT), droop control
and dc-link voltage regulation have been managed simultaneously without major control reconfiguration. The limitation introduced is other non-renewable sources or storage units must be present for balancing supply and demand, which if not catered, will severely narrow variation range of the loads. Despite these differences, the studies in [16] to [20] have not considered possible fluctuations of the dc source connected to the inverter. Such consideration is relevant to renewable sources like PV, where variations may be unintentionally caused by changes of irradiation or intentionally activated by the MPPT scheme. In addition, the studies have only focused on single-stage converters, which make them not directly applicable to two-stage converters used with some PV systems.

The other concern investigated is the uninterrupted supply of power, which will then require seamless transfer between grid-connected and islanded modes [21]-[28]. For example, [23] has proposed a high-performance inverter controlled by an adaptive sliding-mode scheme. Sliding-mode control is generally fast, and can hence ride through the transfer smoothly. However, it is nonlinear and mathematically complex, which may make analysis difficult. In [24], a voltage-current-weighted control has been proposed with different weights used for its grid-connected and islanded modes during the transfer. It uses four controllers in total, and is hence equally complex. In [25] and [26], the transfer is accompanied by a reconfiguration of controllers, which supposedly, is a non-optimized solution when compared with schemes that use a single common control structure throughout the transfer. Such common control schemes are however not widely discussed at present.

Moreover, seamless transfer requires synchronization with the grid, prior to connecting an islanded DG with the mains [29], [30]. For single-phase inverters, zero-crossing detection of the grid voltage is a possible technique, but sensitive to grid voltage distortion [31]. Another synchronization technique has therefore been presented in [32] for \( P-f \) and \( Q-V \) droop-controlled DG. The technique uses one synchronizer for eliminating voltage magnitude difference and another for eliminating phase difference between the DG and grid. It is however formulated for an inductive grid only, and not the predominantly resistive low-voltage (LV) distribution network. Similar synchronization concepts can however still be used, but only after some minor modifications are added to account for its resistive characteristics.

Summarizing the above, presently unresolved issues are listed, as follows.

- Studies, including [12] to [19], have considered single-stage inverters with sufficient generation capacities. This may not be true for PV systems, where two-stage converters with no or limited storage may sometimes be used.
- Effects of internal and external fluctuations on the droop performances have usually been neglected. This may not be appropriate for PV systems, where climatic changes are common.
- A common droop structure with the first two issues resolved has not been developed for ensuring seamless transfer between operating modes.

These challenges have been addressed in the paper by proposing an improved droop scheme for two-stage PV systems. Simulation and experimental results have verified the expected performances of the proposed scheme.

II. GENERAL POWER TRANSFER AND POSSIBLE VARIATIONS

Fig. 1 shows a simple circuit for formulating power transfer. The notations used are \( V \leq 0, Z \leq \theta \) and \( E \leq \delta (V \text{ and } E \text{ are peak values}) \) for representing output voltage from the PV system, line impedance and voltage at the PCC, respectively. In the grid-connected mode, \( E \leq \delta \) is decided by the grid, while in the islanded mode, it is determined by the local generators and loads. Regardless of that, power angle \( \delta \) is usually small, which means \( \sin \delta \approx \delta \) and \( \cos \delta \approx 1 \) can be used for simplification. In addition, for a LV distribution network, the line impedance is mainly resistive, which means \( \theta \approx 0 \). The active and reactive powers \((P \text{ and } Q)\) transferred can thus be expressed as:

\[
P = \frac{V(V - E)}{Z} \tag{1}
\]

\[
Q = -\frac{VE}{Z} \delta. \tag{2}
\]

These equations show that active power can be controlled by adjusting \( V \), while reactive power can be controlled by varying \( \delta \), even though it is also affected by \( V \). Additionally, both powers are affected by disturbances from \( E \text{ and } Z \), which strictly, are external of the PV system. Internally within the PV system, active power limitation can also happen because of a drop in irradiation, for example. Specific details related to these issues are discussed separately in the next two sections for the islanded and grid-connected modes.

III. ISLANDED MODE

A. Control Principle

Beginning with active power transfer represented by (1), a \( P-V \) curve can be plotted, as shown in Fig. 2(a). This curve will shift when \( E \text{ and } Z \) vary, as understood from (1). Regardless of that, the \( P-V \) curve alone is not sufficient for defining a specific operating point for the network. To mark out this stable operating point, the PV system must be controlled to add a second \( P-V \) curve to Fig. 2(a), which conventionally, has assumed the linear droop characteristic represented by (3) [33].

\[
V = V_0 - k_p (P - P_r) \tag{3}
\]

\[
f = f_0 + k_q (Q - Q_r) \tag{4}
\]

where \( f \) and \( f_0 \) are the measured and rated frequencies of the PV system, \( V \) and \( V_0 \) are the measured and rated output voltage
amplitudes, \( P_a \) and \( Q_a \) are the rated active and reactive powers, and \( k_a \) and \( k_b \) are the active and reactive droop coefficients, respectively. An intersection at point \( a \) is thus created, whose corresponding operating point on the PV curve is also marked with the same notation of \( a \). In the steady state, both operating points must have the same power value \( P_a \), in order to keep the dc-link voltage stabilized.

The same reasoning can be applied to reactive power transfer, which then necessitates the \( Q_f \) droop expression in (4) for controlling the PV system. It is however not possible to draw (2) and (4) on a single diagram because of their differences in one of the parameters (\( \delta \) and \( f \), where \( \delta = 2\pi t + \delta_0 \) and \( \delta_0 \) is the initial power angle). Implementation of (3) and (4) then results in a control scheme similar to that shown in Fig. 3 for a two-stage PV system when in its islanded mode. The only difference is Fig. 3 includes a modified active droop expression, instead of (3). The modified droop expression will be explained later. Regardless of that, Fig. 3 shows the output voltage \( v_{ac} \) and current \( i_{ac} \) of the rear-end inverter being measured for computing \( P \) and \( Q \). The active and reactive droop expressions can then be used for mapping out the desired \( V \) and \( f \), from which the demanded voltage reference \( v_{ref} \) is computed for tracking by the usual double-loop controller. On the other hand, the front-end boost converter is controlled by a single-loop voltage converter notated as \( G_k(s) \). Input to \( G_k(s) \) is the difference between the measured dc-link voltage \( V_{DC} \) and its reference \( V_{DCref} \). To nullify this difference, \( G_k(s) \) is usually a proportional-integral (PI) controller, which may not be necessary for the proposed scheme because of reasons explained next.

As described earlier, the \( P-V \) curve and line in Fig. 2(a) help to define a common operating point with \( P = P_a \). In the literature, this active power has always been assumed as supplied by the DG without difficulty [14], [34]. Such assumption is however true only when a large reserve of fuel or storage is available for cushioning variations of parameters, which for a PV system, are its irradiation and temperature. Assuming now that the reserve is not catered and the PV maximum power \( P_{Pmax} \) has been reduced such that \( P_{Pmax} < P_a \) in Fig. 2(a) (see point \( b_1 \)), power delivered to the dc-link by the boost converter will then be lesser than power drawn out from it by the inverter. The dc-link voltage will therefore drop until the inverter power is reduced to \( P = P_b = P_{Pmax} \), which in Fig. 2(a), is achieved by lowering the droop line by \( \Delta V_f \) (see point \( b_2 \)).

The challenge is finding the value for \( \Delta V_f \), which is the simplest method is to compute it using a PI controller. Input to the PI controller can be the difference between power delivered by the dc-dc converter to the dc-link capacitor and power extracted from the same capacitor by the inverter. However, in the steady state, this power imbalance will reduce to zero (ignoring losses) before the dc-link capacitor voltage can stabilize. Despite that, the integral term of the PI controller will still output a finite \( \Delta V_f \) for lowering the droop line in Fig. 2(a). Value of the PI controller output is however not specific, because of its single-to-multiple mapping characteristic, which is usually not encouraged when multiple converters are connected in parallel. Because of that, the proportional droop scheme without any integral term is used instead for finding \( \Delta V_f \).

Instead, the dc-link voltage \( V_{DC} \) is allowed to vary within a very small range, notated as \( V_{DCmin} \leq V_{DC} \leq V_{DCref} \). The lower limit of the range must at least be higher than the maximum inverter output voltage to avoid over-modulation. The necessary expression governing it can thus be derived from (3) as

\[
V_{DC} = \begin{cases} 
V_{DCref}, & \text{if } V_{DC} \geq V_{DCref} \\
0, & \text{if } V_{DC} < V_{DCref}
\end{cases}
\]

which the simplest \( k_f \) droop coefficient for computing \( \Delta V_f \) is obtained by subtracting the maximum of (3) from its minimum \((V_0 = (V_0 + k_f P_0))\). The actual \( V_{DC} \), when fallen below \( V_{DCref} \), represents a lack of PV capacity (e.g. insufficient irradiation) for restoring the dc-link voltage. The inverter power must hence be lowered by introducing a negative \( \Delta V_f \).

The actual \( V_{DC} \), when risen above \( V_{DCref} \), represents an excess PV capacity (e.g. strong irradiation), which can be lowered to meet the lower load demand. No offset \((\Delta V_f = 0)\) is thus added for changing the inverter power.

As \( V_{DC} \) is now required to vary, controller \( G_k(s) \) for the
boost converter cannot be a PI controller. It should be a proportional droop controller with gain given by \(V_{DCref} - V_{DCnom}/P_0\).

- Coefficient \(k_v\) will usually be high if only a narrow dc-link voltage range is permitted (partly due to the large dc-link capacitor \(C\) included for decoupling purposes).

The modified P-V droop expression is then written as (6), where the first term is the usual droop expression for power sharing, and the second term is for regulating internal power limitation within the generator.

\[
V = V_0 - k_P (P - P_0) + k_V (V_{DC} - V_{DCref}) \tag{6}
\]

The \(Q-f\) expression, on the other hand, remains unchanged, since internal active power variation will not limit the amount of reactive power that the rear-end inverter can deliver. This explains why the reactive power droop expression in Fig. 3 has been retained as (4), while the active power droop expression has changed to (6). Moreover, it should be emphasized that Fig. 3 works even with a limited amount of storage (mainly for catering much lower night usage) added to the dc-link of each PV system through a second dc-dc converter. This storage can charge when its charger detects \(V_{DC}\) above \(V_{DCref}\), to reinforce the lack of PV capacity, if necessary. The dc-link voltage will then stabilize, but if the interval of insufficient PV capacity is longer than that the storage capacity can support, the dc-link voltage will eventually continue its drop, allowing (6) to work as intended. The control scheme in Fig. 3 with (6) included can thus be used without changes, even with some storage introduced through a second dc-dc converter at the dc-link.

**B. Small-Signal Analysis**

Small-signal analysis can next be performed on (4) and (6) for studying the behaviors of paralleled converters, whose number has been kept at two to avoid excessive mathematical complexity. Output voltages of these two converters can further be noted as \(V_1 = V_{d1} + jV_{q1}\) and \(V_2 = V_{d2} + jV_{q2}\), respectively. Their angles and magnitudes can then be expressed as:

\[
\delta_i = \arctan \left( \frac{V_{qi}}{V_{di}} \right) \tag{7}
\]

\[
V_i = V_{di} + jV_{qi} = \sqrt{V_{di}^2 + V_{qi}^2} \tag{8}
\]

where \(i = 1\) or 2 is the assigned converter index number.

The variation of energy \(E_i\) in each dc-link capacitor of the converter can also be written as:

\[
E_i = \int P_i(t) dt = \frac{1}{2} CV_{DCi}^2 - \frac{1}{2} CV_{DCi0}^2 \tag{9}
\]

where \(V_{DCi}\) and \(C\) are the dc-link voltage and capacitance, respectively. The linearly perturbed expression for relating the dc-link voltage and its power can thus be derived as:

\[
\Delta V_{DCi} = -\frac{1}{mC} \Delta P_i - \frac{k_P}{s} \Delta P_i \tag{10}
\]

where \(m\) is the equilibrium value of \(V_{DCi}\) and \(\Delta\) is for representing perturbation. Additionally, \(\Delta f_i = s\Delta \delta_i/2\pi\). Combining these perturbed expressions with the small-signal expressions of (4) and (6) then results in the state equation (11) for representing each converter.

\[
\begin{bmatrix}
\Delta V_{DCi} \\
\Delta f_i \\
\Delta V_{d_i} \\
\Delta V_{q_i}
\end{bmatrix} = A_i \begin{bmatrix}
\Delta P_i \\
\Delta Q_i
\end{bmatrix} + B_i \begin{bmatrix}
\Delta P_i \\
\Delta Q_i
\end{bmatrix} \tag{11}
\]

where \(A_i\) and \(B_i\) represent the coefficient matrices.

To again avoid excessive complexity, line impedances of the two converters are assumed equal, which together with the load impedance, are expressed as \(Z = r + jX\) and \(Z_L = r_L + jX_L\), respectively. The output current expressions of the converters can thus be expressed as:

\[
\begin{bmatrix}
\Delta I_1 \\
\Delta I_2
\end{bmatrix} = \begin{bmatrix}
Y_{11} & Y_{12} \\
Y_{21} & Y_{22}
\end{bmatrix} \begin{bmatrix}
\Delta V_1 \\
\Delta V_2
\end{bmatrix} \tag{12}
\]

with

\[
\begin{bmatrix}
\Delta I_1 \\
\Delta I_2
\end{bmatrix} = \begin{bmatrix}
\Delta I_{d1} & \Delta I_{q1} & \Delta I_{d2} & \Delta I_{q2}
\end{bmatrix} \tag{13}
\]

where \([Y]\) is the nodal admittance matrix, and \(I_{d0}\) and \(I_{q0}\) are real and imaginary components of each converter output current.

Active and reactive powers of each converter can then be computed using (13), which upon perturbed, leads to (14).

\[
\begin{bmatrix}
P_i \\
Q_i
\end{bmatrix} = \begin{bmatrix}
V_{di}I_{di} & V_{qi}I_{qi}
\end{bmatrix} \tag{14}
\]

where \([\Delta S] = [\Delta P_i] \Delta Q_i = [\Delta P_j] \Delta Q_j\).

The state equation of the whole system with two converters can hence be represented as:

\[
\begin{bmatrix}
\Delta X\end{bmatrix} = \begin{bmatrix}
A_1 & 0 \\
0 & A_2
\end{bmatrix} \begin{bmatrix}
\Delta P_i \\
\Delta Q_i
\end{bmatrix} + \begin{bmatrix}
B_1 \\
B_2
\end{bmatrix} \begin{bmatrix}
[I] + [V][Y][k]
\end{bmatrix} \begin{bmatrix}
\Delta X
\end{bmatrix} \tag{15}
\]

where

\[
\begin{bmatrix}
\Delta X
\end{bmatrix} = \begin{bmatrix}
\Delta V_{DC1} & \Delta f_1 & \Delta V_{d1} & \Delta V_{q1} & \Delta V_{DC2} & \Delta f_2 & \Delta V_{d2} & \Delta V_{q2}
\end{bmatrix} \tag{16}
\]

\[
[k] = \begin{bmatrix}
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 1
\end{bmatrix}
\]

From (15), the state space matrix \(M\) for the whole system can eventually be extracted as:

\[
M = \begin{bmatrix}
A_1 & 0 \\
0 & A_2
\end{bmatrix} + \begin{bmatrix}
B_1 \\
B_2
\end{bmatrix} \begin{bmatrix}
[I] + [V][Y][k]
\end{bmatrix}. \tag{16}
\]

Using (16), root loci can be plotted for analyzing the system response with \(V_L = V_L = 218 + j30\Omega\), for the converters, \(Z_L = 50+j0.2\Omega\) for the load, \(Z = 2+j0.1\Omega\) for the distribution lines, and \(\omega_f = 3.141\) rad/s for the common cut-off frequency of the low-pass filters used for computing the average powers. With droop coefficients next set as \(k_p = 0.004\), \(k_q = 0.001\) and \(k_f\) changing from 0.05 to 0.5, Fig. 4 shows the first set of root loci plotted. The dominant poles \(\lambda_2\) and \(\lambda_3\) noted in the figure are...
found to shift away from the imaginary axis, as \( k_q \) increases. The expectation is thus an improvement of system dynamics, while retaining stability since all poles are still in the left-half s-plane. Fig. 5 follows with another set of root loci obtained with \( k_p = 0.001 \), \( k_q = 0.4 \) and \( k_q \) varying from 0.0001 to 0.1. Only two poles \( \lambda_1 \) and \( \lambda_3 \) are again found to affect the system dynamics, since the other poles are far away from the imaginary axis. Despite that, the system is still stable.

Finally, Fig. 6 shows the last set of root loci, where the coefficient changed is \( k_p \) from 0.0001 to 0.1, while keeping the others constant at \( k_p = 0.004 \) and \( k_q = 0.4 \). Since this combination of coefficients can lead to instability if not designed properly, it is plotted for both improved and traditional droop schemes for comparison. The traditional scheme referred to here is the scheme, which uses (3) and (4) for droop purposes. It is thus the same basic scheme used in [19] and many others found in the literature. Fig. 6(a) shows the root loci of the improved droop scheme, where it can be seen that dominant poles \( \lambda_2 \) and \( \lambda_3 \) shift closer to the imaginary axis, as \( k_q \) increases. The system response will therefore become more oscillatory, and eventually destabilize, as \( k_q \) rises above 0.0377 (\( \lambda_2 \) and \( \lambda_3 \) enter the right-half s-plane). In contrast, response of the traditional droop scheme is dominated by the single pole \( \lambda_3 \), which will move closer to the imaginary axis, as \( k_q \) increases. The system eventually becomes unstable, as \( k_q \) rises above 0.0112. The stable range of \( k_q \) of the improved droop scheme is thus wider than that of the traditional droop scheme. Besides this, stabilities of both droop schemes are not affected by normal variations of the converter and inverter output filters, since both schemes use only low-pass filtered or average power values.

### IV. GRID-CONNECTED MODE

For smoother operation, the same droop expressions should be used for the grid-connected mode [16], which also requires the PV inverter to output the maximum power \( P_{PV_{max}} \). This maximum power can however be lower than the rated power \( P_q \) in (3), depending on the operating conditions. The traditional droop expressions in (3) and (4) are therefore not directly applicable, and must hence be changed accordingly. One straightforward approach is given in (17):

\[
V = V_G \left( k_p(P - P_0) + \left( k_{pp} + k_{iq} \right) \frac{k_p}{s} (P_{PV_{max}} - P) \right)
\]

where \( k_{pp} \) and \( k_{iq} \) are proportional and integral gains, respectively. The modification noted here is the second term of (17), which in effect, has added an offset \( \Delta V_G (-k_p P_0 \leq \Delta V_G \leq 0) \) to the original droop line. The purpose is to make the system output the PV maximum power \( P_{PV_{max}} \), rather than its rated
power $P_0$ ($P_{PVT_{\max}} \leq P_0$). Moreover, unlike the islanded mode, power sharing is not the main concern in the grid-connected mode, which hence permits an integral term to be used in (17).

The same offset in (17) can also help to filter off influences from grid voltage and line impedance variations. For example, as shown in Fig. 2(b), the PV system is initially operating at point $c$ with maximum PV power harnessed. The grid voltage $E$ is subsequently assumed to change slightly, causing the power transfer curve from (1) to lower slightly. The new operating point of the inverter will then be at $c_1$, while the dc-dc converter remains at $c$. This condition is however unsustainable, since an imbalance in power exists between the dc-dc converter and inverter. The response from (17) will then be the lowering of droop line by $\Delta V_{dc}$, until the final operating point $c_2$ is reached. Finding $\Delta V_{dc}$ from (17) is however not direct, since $P_{PVT_{\max}}$ is unknown, if irradiation and temperature are not measured. Moreover, (17) does not permit the rear-end inverter, controlled indirectly forcing the inverter output power to follow the PV maximum power.

Expression (17) must hence be changed to (18), where its second term can be used for regulating the dc-link voltage and inverter. The response from (17) will then be the lowering of droop line by $\Delta V_{dc}$, until the final operating point $c_2$ is reached. Finding $\Delta V_{dc}$ from (17) is however not direct, since $P_{PVT_{\max}}$ is unknown, if irradiation and temperature are not measured. Moreover, (17) does not permit the rear-end inverter, controlled by it, to regulate the dc-link voltage $V_{DC}$, which strictly, is necessary, since the front-end dc-dc converter is already tasked to track the PV maximum power point.

Expression (17) must hence be changed to (18), where its second term can be used for regulating the dc-link voltage by indirectly forcing the inverter output power to follow the PV maximum power.

$$V = V_0 - k_P (P - P_0) + (k_{pp} + \frac{k_{pp}}{s})(V_{DC} - V_{DCref})$$  \hspace{1cm} (18)

Additionally, the gain $k_P$ in (18) should be chosen such that the maximum transient energy flowing through the dc-link capacitor does not cause its voltage to rise above its trip level. The gain $k_P$ should, on the other hand, be chosen to minimize the steady-state error. For the implemented system, they are respectively chosen as $k_{pp} = 0.0003$ and $k_{pq} = 0.005$, which are comparably low gains suitable for the slower outer droop control loop.

The same reasoning can be applied to (4), leading to the following modified $Q$-f expression:

$$f = f_0 + k_r (Q - Q_0) + (k_{pq} + \frac{k_{pq}}{s})(Q - Q_{ref})$$  \hspace{1cm} (19)

where $k_{pq}$ and $k_{pq}$ are proportional and integral gains, respectively. Again, the first term of (19) is the usual droop expression, shifted vertically by the added second term to follow the demanded reference $Q_{ref}$ (set to zero if unity power factor is preferred). Additionally, $k_{pq}$ should be chosen to give a satisfactory dynamic response, while $k_{pq}$ should be chosen to minimize the steady-state error. For the implemented PV system, these gains are set as $k_{pq} = 0.0001$ and $k_{pq} = 0.0004$ to give a sufficiently long response time needed for decoupling the slower outer droop power loop from the faster inner voltage and current tracking loops.

Fig. 7 shows the eventual grid-connected scheme implemented for the PV system. As usual, the boost converter is controlled by measuring $V_{pv}$ and $I_{pv}$ from the PV panel for tracking in the MPPT block. The inverter is however droop-controlled using almost the same control blocks as in Fig. 3 for the islanded mode, except with the improved droop expressions in (18) and (19) used instead. In addition, a harmonic detection block based on instantaneous power theory [35] has been used for extracting harmonic voltage $v_h$ from the inverter terminal voltage $V_{inv}$. The voltage reference for tracking is then obtained by summing $v_f$ and $v_h$, where $v_f$ is the fundamental voltage reference produced by the droop controller. Voltage $v_h$ is therefore not compensated, which according to (20), will result in a smaller grid current distortion $i_f$ caused by the grid voltage harmonics $v_{gh}$:

$$|i_f| = \frac{|v_h - v_{gh}|}{Z} \leq \frac{|v_f|}{Z_{inv}} \text{ forced to zero}$$  \hspace{1cm} (20)

The grid-connected scheme in Fig. 7 will hence function well, even though at times, it may encounter problems caused by large delivered PV power. More specifically, the large delivered PV power may cause the inverter output voltage $V_{inv}$ to rise higher than threshold permitted by the local loads. When that happens, the front dc-dc boost converter should stop its rise higher than threshold permitted by the local loads. When that happens, the front dc-dc boost converter should stop its rise higher than threshold permitted by the local loads.

Fig. 7 shows the eventual grid-connected scheme implemented for the PV system. As usual, the boost converter is controlled by measuring $V_{pv}$ and $I_{pv}$ from the PV panel for tracking in the MPPT block. The inverter is however droop-controlled using almost the same control blocks as in Fig. 3 for the islanded mode, except with the improved droop expressions in (18) and (19) used instead. In addition, a harmonic detection block based on instantaneous power theory [35] has been used for extracting harmonic voltage $v_h$ from the inverter terminal voltage $V_{inv}$. The voltage reference for tracking is then obtained by summing $v_f$ and $v_h$, where $v_f$ is the fundamental voltage reference produced by the droop controller. Voltage $v_h$ is therefore not compensated, which according to (20), will result in a smaller grid current distortion $i_f$ caused by the grid voltage harmonics $v_{gh}$:

$$|i_f| = \frac{|v_h - v_{gh}|}{Z} \leq \frac{|v_f|}{Z_{inv}} \text{ forced to zero}$$  \hspace{1cm} (20)

The grid-connected scheme in Fig. 7 will hence function well, even though at times, it may encounter problems caused by large delivered PV power. More specifically, the large delivered PV power may cause the inverter output voltage $V_{inv}$ to rise higher than threshold permitted by the local loads. When that happens, the front dc-dc boost converter should stop its rise higher than threshold permitted by the local loads. When that happens, the front dc-dc boost converter should stop its rise higher than threshold permitted by the local loads. When that happens, the front dc-dc boost converter should stop its rise higher than threshold permitted by the local loads.
autonomously generated vertical offset, expressed as follows.

\[
\Delta V_{\text{ref}} = (k_{qf} + \frac{k_i}{s})(E - V)
\]

where \(k_{qf}\) and \(k_i\) are proportional and integral gains of the voltage synchronizer.

The same applies to Fig. 9(b), where the initial reactive power \(Q_c\) must be maintained, while lifting the inverter frequency from \(f_c\) to the grid frequency \(f_{\text{grid}}\). Lifting the frequency alone is however not sufficient, since the phase of the inverter \(\theta\) may not be equal to that of the grid \(\theta_{\text{grid}}\). It is therefore important for the inverter to track the phase of the grid using (24), which upon achieved, will indirectly force the inverter to have the same frequency as the grid.

\[
\Delta f_{\text{ref}} = (k_{qf} + \frac{k_i}{s})(\theta_{\text{grid}} - \theta)
\]

Fig. 9. Droop lines for illustrating (a) grid voltage and (b) grid frequency synchronizations.

Together with (1) and (2), block diagrams representing (21) and (22) can subsequently be drawn, as shown in Fig. 8(a) and (b). The same droop expressions in (21) and (22) can therefore be used for both modes with seamless transfer between them expected, since there are no major changes or swopping of control schemes. The boost converter will however experience changes from MPPT to dc-link voltage control, and vice versa. These changes are however decoupled by the dc-link capacitor, and will hence not disturb the grid significantly.

In addition, seamless transfer from the islanded to grid-connected mode requires synchronization with the grid, before static switch between the PV system and grid can be closed. The necessary synchronization can be explained with Fig. 9. Before synchronization, Fig. 9(a) shows the inverter operating at point \(d_1\), with output voltage \(V_d\) and output power \(P_d\) generated for the local load. After initiating synchronization, \(P_d\) must be held constant to continuously meet the local load demand, while lifting the inverter voltage from \(V_d\) to the grid voltage \(E\). Such lifting can again be realized by adding an autonomously generated vertical offset, expressed as follows.

\[
\Delta V_{\text{ref}} = (k_{qf} + \frac{k_i}{s})(E - V)
\]

Grid-connected to islanded mode:
- Detection of disconnection command.
- MPPT to dc-link voltage control for the boost converter.
- Same improved droop control for the inverter, but with parameters changed appropriately (see (21) and (22)).

Islanded to grid-connected mode:
- Detection of connection command.
- Activation of synchronizers in (23) and (24).
- Closing of static switch after synchronization.
- Deactivation of synchronizers.
- DC-link voltage to MPPT control for the boost converter.
- Same droop control for the inverter, but with parameters changed appropriately (see (21) and (22)).
VI. SIMULATION RESULTS

Two similarly rated PV systems, tied to a LV distribution network, have been simulated in Matlab/Simulink using parameters listed in Table I for each system. Results obtained from them are described below.

A. Islanded Mode for Evaluation of Internal Power Changes

Fig. 11 shows the results of the two islanded PV systems when controlled by the improved droop scheme. Before 0.1s, the two systems are assumed to have the same maximum rating of 1kW. They should hence share the active load demand evenly with their respective dc-link voltages kept at 400V (nominal value read from Table I). After 0.1s, the maximum power of one system has been reduced from 1kW to 60W, while that of the others remains unchanged. The compromised system, being unable to provide sufficient active power, will then have its dc-link voltage pulled down. The drop causes the droop line of the compromised system to be shifted down, like in Fig. 2. The droop-demanded output active power of the compromised system is then lowered until it reaches 60W. In the meantime, the second uncompromised system supplies more active power to the loads, which strictly, is necessary before supply and demand can be balanced. The overall network is therefore stable with only slight droops observed with the dc-link and inverter output voltages, even though the PV terminal voltages may change more prominently.

B. Grid-Connected Mode for Evaluation of External Changes

As explained with (1) and (2), external disturbances are introduced through $E$ and $Z$. Both parameters are prominently influenced by the grid. Results from the grid-connected mode are therefore chosen for demonstration (even though the same testing has also been performed in the islanded mode). As shown in Fig. 12(a), the grid voltages read are $v_{grid} = 220V$ from 0 to 0.2s, 225V from 0.2s to 0.4s, and 220V from 0.4s to 0.6s. Throughout the three intervals, the maximum PV power has been set to $P_{PPVmax} = 1000W$. However, with the traditional droop scheme, $P_{PPVmax}$ is not harnessed when the grid voltage increases in the second interval. The actual power generated is only 800W, delivered by a much smaller grid current $i_{grid}$. On the other hand, Fig. 12(b) shows the results obtained with the improved droop scheme when subject to the same changes. The harnessed power is now always 1000W, even though some slight variations can be observed at the instants of voltage changes. The proposed scheme is therefore more effective in terms of active power tracking.

<table>
<thead>
<tr>
<th>TABLE I</th>
<th>PARAMETERS OF TESTED SYSTEM</th>
</tr>
</thead>
<tbody>
<tr>
<td>Parameters</td>
<td>Values</td>
</tr>
<tr>
<td>Panel voltage variation ($VPV$)</td>
<td>190V-300V</td>
</tr>
<tr>
<td>Grid voltage ($E$)</td>
<td>220V (rms)</td>
</tr>
<tr>
<td>Grid frequency ($f_{grid}$)</td>
<td>50Hz</td>
</tr>
<tr>
<td>DC-link voltage ($V_{DC}$)</td>
<td>400V</td>
</tr>
<tr>
<td>DC-link voltage capacitance ($C_{DC}$)</td>
<td>4mH</td>
</tr>
<tr>
<td>Boost inductance ($L_{B}$)</td>
<td>940 μF</td>
</tr>
<tr>
<td>Output filter inductance ($L_{in}$)</td>
<td>5mH</td>
</tr>
<tr>
<td>Output filter capacitance ($C_{in}$)</td>
<td>10μF</td>
</tr>
<tr>
<td>Inverter switching frequency ($f_s$)</td>
<td>10kHz</td>
</tr>
<tr>
<td>Impedance of transmission line ($Z$)</td>
<td>2Ω, 0.8μH</td>
</tr>
</tbody>
</table>

Fig. 11. Simulated results of two paralleled PV inverters using improved droop schemes.

Fig. 12. Simulated results with grid voltage fluctuations when controlled with (a) traditional and (b) improved droop schemes.
Fig. 13(a) and (b) next show the results obtained when the grid frequency increases from 50Hz to 50.2Hz during the interval from 0.2s to 0.4s. The former uses the traditional droop scheme, while the latter uses the improved droop scheme. The traditional droop scheme is obviously not able to regulate the reactive power at zero during the second interval \(Q = 500\text{Var}\), which the improved scheme can achieve after some slight transient variations. The proposed scheme is thus less affected by grid frequency fluctuations, which surely, is encouraged.

Some results are then included in Fig. 14(a) and (b) for showing how the traditional controlled system responds during mode transfers. Quite obviously, Fig. 14(a) shows the inverter voltage \(v_{\text{inv}}\) surging during the islanded to grid-connected transfer at 0.115s, while Fig. 14(b) shows the inverter current \(i_{\text{inv}}\) surging during the reverse grid-connected to islanded transfer. Such surging is however not seen in Fig. 15(a) and (b) at the transfer time of 0.2s. The transfers are hence seamless with the improved droop scheme.
Experimental testing with a 1-kW PV system has been performed for proving the practicality of the proposed scheme. The hardware setup is shown in Fig. 16, while its parameters are given in Table I. The dc source used is a Chroma 62150H-600S programmable supply, programmed to output typical PV characteristics, while the controller used is a 32-bit STM32F103VBT6 microcontroller from STMicroelectronics. Results from the setup when first islanded are shown in Fig. 17 when the improved droop scheme is used, together with a load increase and then decrease. The load power is more specifically read as $P_{\text{Load}} = 280\text{W}$ from 0 to 7.42s, 560W from 7.42s to 21.35s, and 280W from 21.35s to 25s. These powers are supplied by the PV source, whose terminal voltage will hence change from $V_{\text{PV}} = 250\text{V}$ to 240V, and then back to 250V. Despite the changes, the results show that the PV inverter operates as intended with its dc-link voltage $V_{\text{DC}}$ kept at the nominal of 400V, since no drop in PV capacity has been introduced yet.

Fig. 18 shows power fluctuations from the PV simulator, whose values have been read as $P_{\text{PVmax}} = 700\text{W}$ from 0 to 9s, 500W from 9s to 19s, and 700W from 19s to 25s. These power changes, in turn, cause the panel voltage to change from $V_{\text{PV}} = 246\text{V}$ to 240V and then back to 246V. Regardless of that, the

VII. EXPERIMENTAL RESULTS

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dc-link voltage is well-regulated at 400V by the improved droop scheme, while harnessing $P_{\text{PPVmax}}$ during the three intervals.

Fig. 19(a) next shows the results without the harmonic detection block in Fig. 7 for filtering out the grid voltage distortion. The observed inverter current and grid current are obviously distorted, as anticipated. These current distortions are however not seen in Fig. 19(b) when the harmonic detection block in Fig. 7 is enabled.

Fig. 20 then shows the results obtained with grid voltage fluctuations when controlled by the traditional droop scheme from (3) and (4) ($v_{\text{grid}} = 220V$ from 0 to 14.48s, 225V from 14.48s to 24.38s, and 220V from 24.38s to 30s). As observed, maximum PV power of $P_{\text{PPVmax}} = 700W$ is only generated in the first and third intervals. Power generated in the second interval is only 560W because of the higher $v_{\text{grid}}$. Grid voltage fluctuations will therefore affect the maximum power harvesting ability of the system when controlled by the traditional droop scheme.

For comparison, Fig. 21 repeats the experiment with grid voltage fluctuations when controlled by the improved droop scheme from (21) and (22) ($v_{\text{grid}} = 220V$ from 0 to 14.74s, 230V from 14.74s to 24.34s, and 220V from 24.34s to 30s). At the instant of grid voltage increase, the inverter power in Fig. 21(b) is noted to drop to 520W, before gradually returning to the maximum PV power of $P_{\text{PPVmax}} = 700W$. In contrast, the decrease of grid voltage in Fig. 21(c) has caused the inverter power to increase to 880W, before returning to 700W. The extra power of 180W at the transition is supplied by the dc-link capacitor since the maximum PV power is only 700W. The proposed scheme is thus able to harness maximum active power even when subjected to grid voltage variations.

Instead of grid voltage, Fig. 22 shows the results obtained with the traditional droop scheme when subjected to grid

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**Fig. 20.** Experimental results with grid voltage fluctuations when using traditional droop scheme. (a) Full view, (b) zoomed-in view of first transition, and (c) zoomed-in view of second transition.

**Fig. 21.** Experimental results with grid voltage fluctuations when using improved droop scheme. (a) Full view, (b) zoomed-in view of first transition, and (c) zoomed-in view of second transition.
frequency fluctuations. As seen from Fig. 22(a), the increase of grid frequency from 50Hz to 50.1Hz has caused the grid current to gradually lag behind the grid voltage. The output reactive power from the inverter is thus non-zero. It only returns to zero in Fig. 22(b) after the grid frequency returns from 50.1Hz to 50Hz. The same grid frequency testing has been repeated in Fig. 23, where Fig. 23(a) shows the grid current lagging initially, before returning to its in-phase position when the grid frequency increases from 50Hz to 50.1Hz. Fig. 23(b) also shows the gradual returning of reactive power to zero when the grid frequency decreases from 50.1Hz to 50 Hz. The improved droop scheme is thus able to track reactive power more accurately even when the grid frequency fluctuates.

Last but not least, Fig. 24 shows the experimental transfer from islanded to grid-connected mode at 5.47s and the reverse transfer from grid-connected to islanded mode at 23.35s. Their zoomed-in views are provided in Fig. 24(b) and (c), respectively. As seen from Fig. 24(b), the inverter with its
stable dc-link voltage is found to supply only 280W to the local load, before the transfer. The grid current read is also at zero initially. The output voltage of the inverter is then activated to track the grid voltage, which upon synchronized, permits the static switch to close to start the transfer. After the transfer, the inverter and grid currents are noted to increase since the full maximum PV power is harnessed with 280W flowing to the load and 420W flowing to the grid (total \( P_{\text{max}} = 700\text{W} \)). During this time, the PV terminal voltage is also observed to drop from 250V to 225V, caused by its larger amount of power generated.

Based on the same reasons, the reverse transfer in Fig. 24(c) also correctly shows the inverter current decreasing and grid current falling to zero after entering the islanded mode. The inverter now generates only 280W for the local load even though the maximum power available is 700W. Overall, the two transfers have been seamless with no disturbing transients observed. This is partly due to the synchronizers used, and partly due to the common improved droop structure used for both operating modes.

VIII. CONCLUSION

An improved droop scheme has been proposed in the paper for a PV system connected to a resistive LV distribution network. The approach is developed for a two-stage PV system, permitting it to transfer seamlessly between the islanded and grid-connected modes, after ensuring proper synchronization. It also allows the PV system to generate maximum active power, while remain not affected by power fluctuations, and other grid voltage and frequency disturbances when in the grid-connected mode. Such immunity is realized by dynamically shifting the droop lines in both grid-connected and islanded modes. Simulation and experimental results have verified the performances of the proposed method.

REFERENCES

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