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# On the mechanisms of InGaN electron cooler in InGaN/GaN light-emitting diodes

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**Abstract:** Electron overflow limits the quantum efficiency of InGaN/GaN light-emitting diodes. InGaN electron cooler (EC) can be inserted before growing InGaN/GaN multiple quantum wells (MQWs) to reduce electron overflow. However, detailed mechanisms of how the InGaN EC contributes to the efficiency improvement have remained unclear so far. In this work, we theoretically propose and experimentally demonstrate an electron mean-free-path model, which reveals the InGaN EC reduces the electron mean free path in MQWs, increases the electron capture rate and also reduces the valence band barrier heights of the MQWs, in turn promoting the hole transport into MQWs.

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OCIS codes: (230.3670) Light-emitting diodes; (230.5590) Quantum-well, wire, and-dot devices; (160.6000) Semiconductor materials.

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## 1. Introduction

High-efficiency InGaN/GaN multiple quantum well (MQW) light-emitting diodes (LEDs) have been investigated with tremendous efforts as an energy-saving solid-state lighting source in the past decades [1–9]. However, the InGaN/GaN LED performance is still limited by efficiency droop, in which the quantum efficiency is reduced at an elevated injection current level. It is reported that Auger recombination causes the droop since the Auger recombination rate scales with the cubic power of the carrier density [10]. In addition, the electron overflow from the InGaN/GaN MQW region is also responsible for the efficiency droop [7]. One of the solutions proposed is to increase the electron confinement in the InGaN/GaN MQWs by using a polarization-matched AlGaInN electron blocking layer (EBL) [7] and an AlGaN or InAlN cap layer for the quantum barriers [8]. The increased band barrier height in the EBL and the cap layers suppresses the electron escape from the MQW region. Another solution is to grow InGaN intermediate layer before MQWs, which proves effective in improving the InGaN/GaN LED external quantum efficiency (EQE) and optical output power [11–15]. The reasons of the effectiveness of InGaN intermediate layer were tentatively attributed to either the improved current spreading effect promoted by the InGaN intermediate layer [14,15], or the electron cooler (EC) effect [11–13] that the hot electrons are thermalized by interacting with longitudinal optical (LO) phonons [16]. However, the exact mechanisms of the InGaN EC contributing to the reduction of electron overflow and the efficiency improvement have still remained unclear thus far. This inhibits the optimization of the InGaN EC and the

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maximization of its effectiveness. In addition, the n-InGaN EC has shown the effectiveness in reducing the electron overflow for double heterostructure (DH) active region [11–13]. However, it is not clear if electron cooler is still effective for a typical MQW stack with thick GaN barriers of 12 nm. Also different from the previous works [14,15] where the improved current spreading effect is assigned as the reason of the efficiency improvement by using InGaN insertion layer, here it is found that the improved device performance is actually owing to the promoted hole transport into the MQWs by reducing the valance band barrier height of MQWs. Thus, in this work, we studied InGaN/GaN MQW LEDs with the InGaN intermediate layer below the MQW region as the EC layer both experimentally and theoretically. An electron mean-free-path model is developed to understand the effect of the InGaN EC layer.

## 2. Experiments

Two InGaN/GaN LED wafers were grown by a metal-organic chemical vapor deposition (MOCVD) system. LED I is the reference sample while LED II is the sample with InGaN as the EC layer. The growth of the two samples was initiated on *c*-sapphire patterned substrates with periodic cone patterns (with a diameter of 2.4 μm, a height of 1.5 μm and a pitch of 3 μm). First, a 30 nm low-temperature GaN nucleation layer was grown. Then a 4 μm u-GaN layer was deposited as the template for the subsequent epitaxial growth. In LED I, a 2 μm n-GaN layer with a Si doping concentration of $5 \times 10^{18}$ $cm^{-3}$ was grown before the five-period $In_{0.15}Ga_{0.85}N$/GaN MQW region. The thicknesses of quantum wells and quantum barriers are 3 and 12 nm, respectively. For LED II, a 20 nm n-type $In_{0.10}Ga_{0.90}N$ EC layer of $5 \times 10^{18}$ $cm^{-3}$ Si doping concentration was grown before growing InGaN/GaN MQWs, for which the growth temperature was 748 °C while the quantum well growth temperature was 742 °C in our MOCVD chamber. Both LEDs I and II have a 20 nm p-$Al_{0.15}Ga_{0.85}N$ as the EBL layer. Finally, the LED samples were both covered with 0.2 μm p-GaN layer as the hole injector. The effective hole concentration in EBL and p-GaN layers for LEDs I and II are estimated to be $3 \times 10^{17}cm^{-3}$.

The electroluminescence (EL) spectra and the optical output power were measured for the two LED samples using the calibrated integrating sphere attached to an Ocean Optics spectrometer (QE65000). The metal contacts were made by indium balls on LED dies with a diameter of 2.0 mm.

## 3. Results and discussion

The measured EL spectra are shown in Figs. 1(a) and 1(b) for LEDs I and II, respectively. It can be seen that the EL intensity for LED II is stronger than that for LED I from lower injection current density of 10 $A/cm^2$ to higher injection current density of 40 $A/cm^2$. We also observed a red shift of the emission wavelengths as a function of the increased injection current for both LED samples, and we attributed the red shift of the emission wavelength to the increased junction temperature during testing [3–5].

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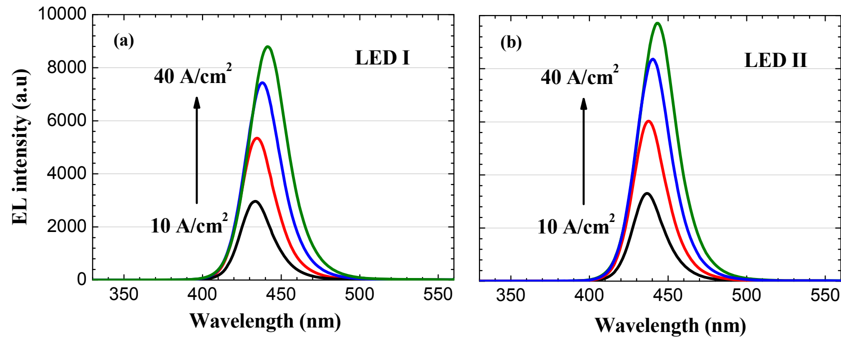


Fig. 1. EL spectra for (a) LED I and (b) LED II under various injection current levels of 10, 20, 30 and 40 A/cm$^2$.

The experimentally measured integrated optical output power and the EQE are depicted in Fig. 2(a) for both LED samples. Being consistent with the EL spectra in Figs. 1(a) and 1(b), LED II emits stronger optical output power than LED I and the optical output power enhancement is 7.76% at 35 A/cm$^2$ as an example. Correspondingly the EQE of LED II is enhanced compared to that of LED I from low current density of 2 A/cm$^2$ to high current density of 45 A/cm$^2$. More importantly, the efficiency droop [$droop = (EQE_{2A/cm^2} - EQE_{test}) / EQE_{2A/cm^2}$] for LED II is reduced significantly compared to LED I as shown in Fig. 2. For example, at 35 A/cm$^2$, the *droop* is 24.01% and 18.03% for LEDs I and II, respectively. The improved performance of LED II over LED I is attributed to the InGaN EC layer inserted before the InGaN/GaN MQW region. The detailed mechanisms of the InGaN EC layer on the improvement of the LED performance are theoretically investigated through the proposed mean-free-path model as follows. However, according to Fig. 2(a), both LEDs start the droop even when current density is smaller than 3 A/cm$^2$, this is likely due to the less optimized InGaN/GaN MQW region. In addition, the experimental efficiency of LED II is worse than that of LED I when the current density is lower than 2 A/cm$^2$, which is due to the electron blocking effect by the InGaN EC layer when the electron energy is small under the low bias. The simulated optical output power and EQE for LEDs I and II have also been illustrated in Fig. 2(b), and the simulated results are quite consistent with Fig. 2(a), in terms of the improved optical output power and reduced efficiency droop. However, the uncertainty of the III-nitrides such as the temperature dependence of the energy band offset, Auger recombination coefficient and Shockley-Read-Hall recombination coefficient causes the slightly different trending details between the simulated and measured optical properties.

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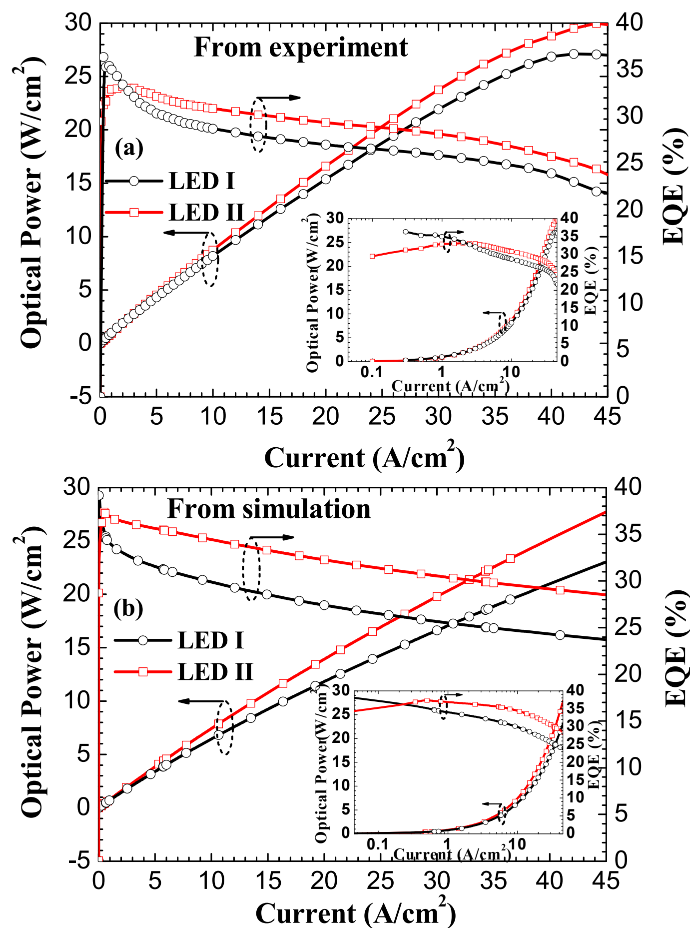


Fig. 2. (a) Experimentally measured and (b) numerically simulated optical output power and EQE for LEDs I and II, along with the optical output power and EQE in the semi-log scale in the insets.

The schematic drawings of energy band diagrams of LED I and LED II are depicted in Figs. 3(a) and 3(b), respectively, along with four electron transport/transition processes in the InGaN/GaN MQWs. Note that the active region was grown directly after n-InGaN EC in the previous works [11–13]. Yet, the gained energy due to the conduction band offset between the n-GaN layer and n-InGaN EC (i.e., $\Delta E_c$) may accelerate the electrons again. Hence, in this work, we have purposely grown a 12 nm thick GaN barrier of $5 \times 10^{17}$ cm$^{-3}$ Si doping concentration between the n-InGaN EC and the first quantum well. Then the gained $\Delta E_c$ can be consumed by those electrons climbing over the next $\Delta E_c$ between the n-InGaN EC and the adjacent GaN barrier. Moreover, our design is different from the one reported by Rebane *et al*. [17] In their design, they have grown a thin GaN barrier (0.5-5 nm) between the n-InGaN EC and the DH active region, hence transparent for electrons to tunnel through. However, as mentioned, the $\Delta E_c$ between the n-GaN layer and the n-InGaN EC may accelerate the electrons again. On the other hand, we grow our LED samples along [0001] orientation, and thus we have to consider the acceleration effect on electrons through the polarization induced electric field within the n-InGaN EC, which is sandwiched between two GaN layers.

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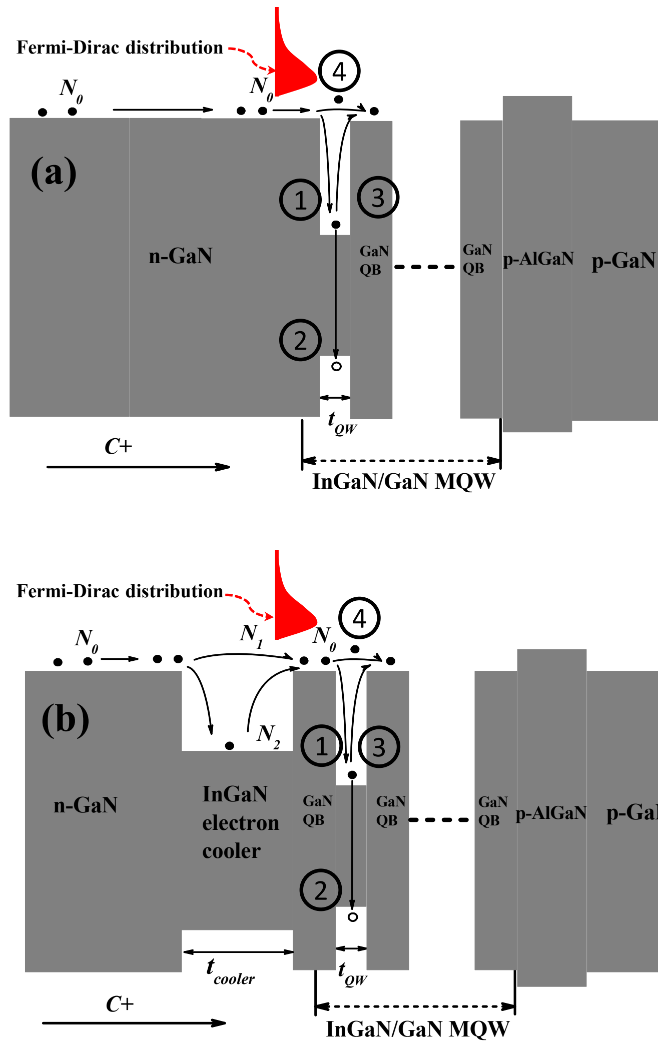


Fig. 3. Schematic energy diagrams for (a) LED I and (b) LED II, along with which four electron transport/transition processes are depicted in the InGaN/GaN MQWs: ① electrons are captured into the quantum well, ② electrons recombine with holes and at defects, ③ electrons re-escape from the quantum well and ④ electrons directly fly over to a remote position without being captured by the quantum well.

According to Figs. 3(a) and 3(b), the incoming electrons are scattered and fall into the quantum wells (i.e., process ①) with $\tau_{cap}$ being the electron capture time, and a value of $4 \times 10^{-12}$ s is used for electrons in the following simulations [18]. Those fallen electrons thereafter on one hand are trapped onto the quantum energy levels and become bound electrons. Then, the recombination with holes and also in crystal defects takes place and it is depicted by process ②. The radiative recombination rates within the quantum wells can be generally expressed by $R_{rad} = (n - n_0)/\tau_{rad}$, where $n$ is the electron concentration received by process ① while $n_0$ is the thermal-equilibrium electron concentration and $\tau_{rad}$ is the radiative recombination lifetime. Therefore, an increased $n$ favors the radiative recombination processes. However, there is also a thermionic electron re-escape from the quantum wells and electrons become free again as illustrated by process ③ in Figs. 3(a) and 3(b). The re-escape process is modeled by the electron escape time, i.e., $\tau_{esc}$, and it can be expressed by

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$\tau_{esc} = e \cdot t_{QW} \cdot n / J_e$ [19], where $n$ presents the electron concentration received by process ① while $J_e$ is the electron current caused by thermionic emission in any heterojunction. Process ④ denotes those electrons of a longer mean free path traveling to a remote position without being captured by quantum wells, which has to be suppressed for an enhanced $n$.

Now we have to find an approach to increase the electron concentration in the quantum wells. We set the number of the electrons injected into the n-GaN region to $N_0$ for both LED samples. We neglect the electron loss through Shockley-Read-Hall (SRH) recombination in the n-GaN and n-$In_{0.10}Ga_{0.90}N$ EC layers to simplify our model since the crystal quality of the two samples is identical. Furthermore, the hole concentration in the n-$In_{0.10}Ga_{0.90}N$ EC is much lower than the electron concentration, so the electron loss through radiative recombination with holes is also negligible. For LED II with n-$In_{0.10}Ga_{0.90}N$ EC layer, we assumed electrons of $N_2$ are captured by the n-$In_{0.10}Ga_{0.90}N$ EC layer with LO phonon emission, while the remaining electrons of $N_1$ directly fly over the EC layer without undergoing thermalization. The electrons of $N_2$ are then injected into InGaN/GaN MQW region after undergoing thermalization. Here, we correlate the quantum well captured electrons [i.e., process ① in Figs. 3(a) and 3(b)] with the electron mean free path (MFP) by Eq. (1) and Eq. (2) for LEDs I and II, respectively [20]. Note that the electron loss due to processes ③ and ④ contribute to the electron overflow from the MQW region.

$$N_t = N_0 \times \left[1 - \exp(-t_{QW} / l_{MFP})\right] = \left(N_1 + N_2\right) \times \left[1 - \exp(-t_{QW} / l_{MFP})\right] \quad (1)$$

$$M_t = N_1 \times \left[1 - \exp(-t_{QW} / l_{MFP})\right] + N_2 \times \left[1 - \exp(-t_{QW} / l_{MFP}^{cooler})\right] \quad (2)$$

where $t_{QW}$ is the thickness of the quantum well, $l_{MFP}$ is the mean free path of electrons within the InGaN/GaN MQWs without electron thermalization and $l_{MFP}^{cooler}$ is the mean free path of electrons in the InGaN/GaN MQWs with electron thermalization in the n-$In_{0.10}Ga_{0.90}N$ EC layer. Here, the relationship between $N_0$ and $N_2$ in Fig. 3(b) can be expressed in Eq. (3), in which we assume the mean free path of electrons in the n-GaN layer before entering the n-$In_{0.10}Ga_{0.90}N$ EC layer is $l_{MFP}$. It is shown that, in order to have more electrons thermalized, it is useful to properly increase the thickness of the n-$In_{0.10}Ga_{0.90}N$ EC layer ($t_{cooler}$).

$$N_2 = N_0 \times \left[1 - \exp(-t_{cooler} / l_{MFP})\right] \quad (3)$$

The comparison between Eqs. (1) and (2) reveals that, to increase the number of the quantum well captured electrons, the electron mean free path within the InGaN/GaN MQW region must be reduced such that $l_{MFP}^{cooler} < l_{MFP}$. Therefore, one has to understand the working mechanisms of the InGaN EC layer in reducing the electron mean free path. The electron mean free path is a function of the thermal velocity (i.e., $v_{th}$ - electron thermal velocity before undergoing thermalization and $v_{th}^{cooler}$ - electron thermal velocity after undergoing thermalization) and the scattering time ($\tau_{SC}$), which is set to 0.0091ps [11,12], as shown in Eqs. (4.1) and (4.2), respectively. Moreover, $v_{th}$ and $v_{th}^{cooler}$ can be expressed in Eqs. (5.1) and (5.2), respectively.

$$l_{MFP} = v_{th} \times \tau_{SC} \quad (4.1)$$

$$l_{MFP}^{cooler} = v_{th}^{cooler} \times \tau_{SC} \quad (4.2)$$

$$v_{th} = \sqrt{2 \times [E + qV] / m_e} \quad (5.1)$$

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$$v_{th}^{cooler} = \sqrt{2\times[E+\Delta E_c - \hbar\omega_{LO} + qV - \Delta E_c]/m_e} = \sqrt{2\times[E+qV-\hbar\omega_{LO}]/m_e} \quad (5.2)$$

where $E$ is the excess kinetic energy in the n-GaN layer referenced to the conduction band of the n-GaN layer, and $m_e$ is the effective mass of electrons. The first $\Delta E_c$ represents the kinetic energy received by the electrons when jumping over the conduction band offset between n-GaN and n-$In_{0.10}Ga_{0.90}N$ EC layer. $-\hbar\omega_{LO}$ means the energy loss by phonon emission. $qV$ is the work done to the electrons by the polarization induced electric field in the in-plane compressive n-$In_{0.10}Ga_{0.90}N$ EC layer. The $-\Delta E_c$ in Eq. (5.2) depicts the energy loss for electrons when climbing over the conduction band offset between the n-$In_{0.10}Ga_{0.90}N$ EC layer and the first quantum barrier. In our calculation, in order to consider the crystal relaxation by generating misfit dislocations, we only assumed 40% of the theoretical polarization induced charge density [21]. Meanwhile, we assume the energy band offset ratio between InGaN and GaN to be 70:30 [22], and thus $\Delta E_c$ between n-GaN and n-$In_{0.10}Ga_{0.90}N$ EC layer is 379.64 meV. Here we also assume the thermionic emission process dominates over the intra-band tunneling in the process of the electrons transport into the first quantum well. Thus $\Delta E_c$ can be eliminated as shown in Eq. (5.2). The energy loss through LO phonon emission is 92 meV, i.e., $\hbar\omega_{LO}$ = 92 meV [23]. Since the electric field within the EC layer is not linear and varies with position, we use APSYS simulator to calculate it [3]. The calculated electric field is shown in Fig. 4. Since $qV = \int_0^{t_{cooler}} q\times E(y)dy$ , $qV$ equals to 27.82 meV in our case. When the carrier temperature is 500 K, $l_{MFP}$ is 14.47 nm while $l_{MFP}^{cooler}$ is 1.32 nm. Obviously the $In_{0.10}Ga_{0.90}N$ EC layer has a significant effect in reducing the electron mean free path in InGaN/GaN MQWs, and therefore increasing the quantum well capture efficiency of electrons, i.e., $M_t > N_t$ . Here we only consider the constant mean free path in InGaN/GaN MQWs and did not consider its position dependence since doing so will not change the conclusion but only add more complexity to the calculation. Note, the electrons follow Fermi-Dirac distribution, and therefore Eq. (5.2) is valid when $E+qV-\hbar\omega_{LO} > 0$ for those hot electrons with a high carrier temperature, while for those with $E+qV-\hbar\omega_{LO} < 0$ (i.e., $E+\Delta E_c-\hbar\omega_{LO}+qV < \Delta E_c$ ) will be blocked by the conduction band offset between the EC layer and the first quantum barrier. However, the electrons will be accumulated in the EC layer until a high electron density is obtained, thus according to $J_e = \frac{4\cdot\pi\cdot e\cdot(k_BT)^2}{h^3}\cdot m_e^* \exp\left[-\Delta E_c/k_BT + \ln\left(n/N_c\right)\right]$ [24], where $\Delta E_c$ is the conduction band offset between GaN and the EC layer, and $N_c$ is the effective density of state for electrons, while $k_B$ is Boltzmann constant, $m_e^*$ is the electron effective mass, $h$ is the Planck constant and $n$ is the electron density, the electrons still can transport into the active region.

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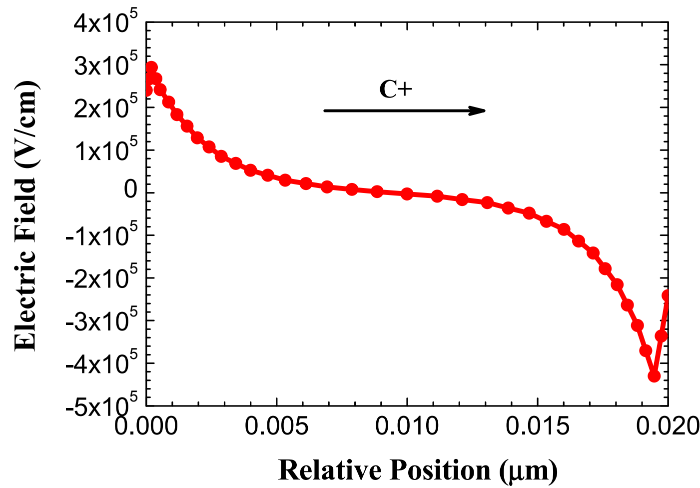


Fig. 4. Calculated electric filed as a function of position within the EC layer at 20 A/cm$^2$. The positive direction of the electric field is along the growth orientation, i.e., C + .

With the above calculated values of the electron mean free path, we performed numerical simulations on the energy band diagrams, electron and hole distributions, electron currents and the radiative recombination rates for the two samples to confirm that the reduction of the electron mean free path by the $In_{0.10}Ga_{0.90}N$ EC layer can enhance the optical output power performance of LEDs. In our simulation, APSYS simulator is used, which can well model the carrier transport processes [i.e., processes ①, ②, ③, and ④ in Figs. 3(a) and 3(b)] within the InGaN/GaN MQW region. The model of electron tunneling through the GaN layer between the n-InGaN EC and the first quantum well has not been used purposely to study the thermionic process for electron transport within that region. Besides the previously mentioned band offset ratio and polarization charge level, we also assumed $1 \times 10^{-30}$ cm$^6$/s as the Auger recombination coefficient [25]. The SRH recombination lifetime in the InGaN/GaN MQW region is set to be 43 ns [25]. Other parameters for nitrogen-containing compounds used in the simulation can be found somewhere else [26].

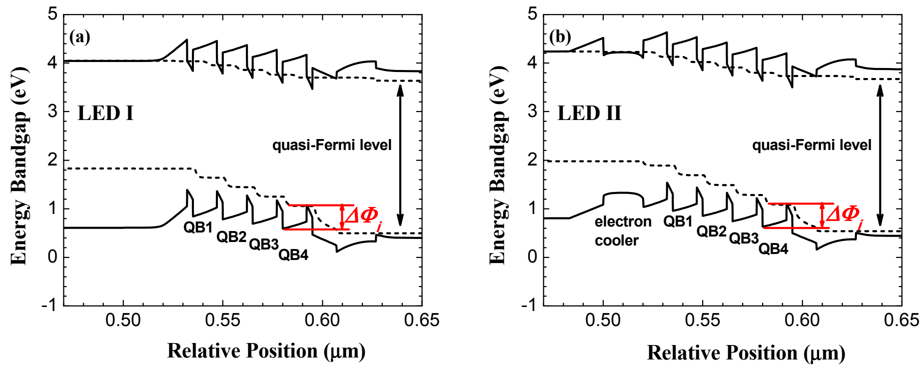


Fig. 5. Energy band diagrams for (a) LED I and (b) LED II.

The simulated energy band diagrams at 20 A/cm$^2$ for LEDs I and II are shown in Figs. 5(a) and 5(b), respectively. We defined the effective valance band barrier height ($\Delta\Phi_i$) for

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different quantum barriers (QB1, QB2, QB3 and QB4). The value of $\Delta\Phi_i$ are extracted and demonstrated in Table 1, from which we can see the effective valence band barrier heights of the quantum barriers for LED II is smaller than those for LED I. It has been reported that the effective valance band barrier height for the p-EBL can be reduced by employing GaN/InGaN as the last quantum barrier, hence promoting the hole injection into InGaN/GaN MQWs [27, 28]. However, as found in this work, the same physical principle can be applied to the case when the n-type $In_{0.10}Ga_{0.90}N$ layer is inserted between n-GaN layer and InGaN/GaN MQW region. As the polarization induced electric field within the n-type $In_{0.10}Ga_{0.90}N$ layer opposes the built-in electric field of the diode, and thus the n-type $In_{0.10}Ga_{0.90}N$ layer "pulls up" the valance band of the MQWs for a better hole transport across the active region.

**Table 1. Effective Valence Band Barrier Heights of InGaN/GaN MQWs for LEDs I and II**

| | $\Delta\Phi_1$ | $\Delta\Phi_2$ | $\Delta\Phi_3$ | $\Delta\Phi_4$ |
|---|---|---|---|---|
| **LED I** | 792.98 meV | 666.84 meV | 553.80 meV | 489.90 meV |
| **LED II** | 742.00 meV | 638.99 meV | 532.50 meV | 447.29 meV |

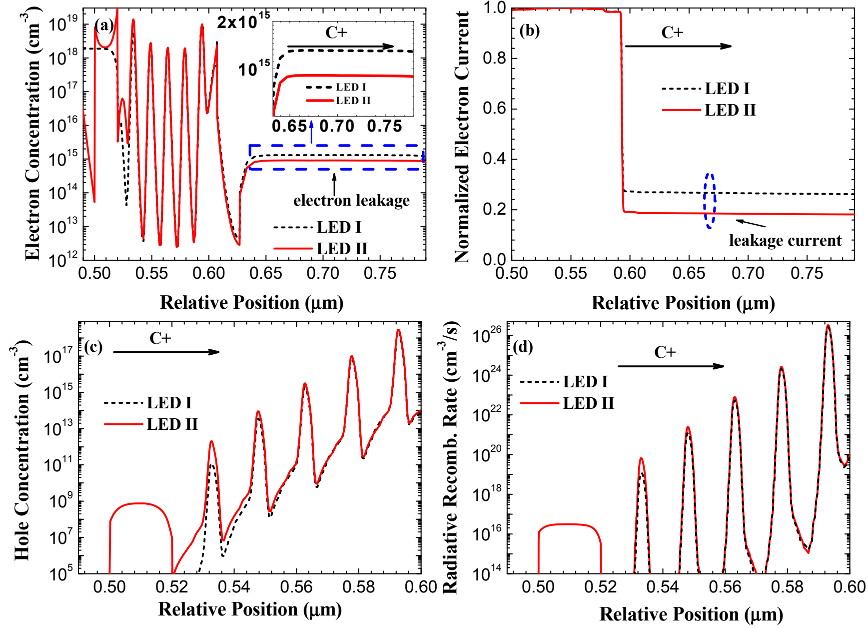


Fig. 6. Simulated (a) electron concentration along with the inset depicting the electron leakage out of the active region, (b) normalized electron current, (c) hole concentration, and (d) radiative recombination rates at 20 A/cm$^2$ across the InGaN/GaN MQW region for LEDs I and II, respectively.

The simulated electron profiles for LEDs I and II are shown in Fig. 6(a). We can see that the electron overflow is reduced in LED II with the n-type $In_{0.10}Ga_{0.90}N$ EC layer, compared to that in LED I. Meanwhile, the electron current distribution is also depicted in Fig. 6(b). Being consistent with Fig. 6(a), the electron leakage current into the p-type region is reduced from 26.56% to 18.86% at 20 A/cm$^2$, if we compare LED II to LED I. It should be noteworthy that the thermionic emission for process ③ in Figs. 3(a) and 3(b) can also be expressed by $J_e = \frac{4\cdot\pi\cdot e\cdot(k_BT)^2}{h^3}\cdot m_e^* \exp\left[-\Delta E_{c_wb}/k_BT + \ln\left(n/N_c\right)\right]$ [24], where $\Delta E_{c_wb}$ is

the conduction band offset for InGaN/GaN MQWs, and $N_c$ is the effective density of state for electrons, while $k_B$ is Boltzmann constant, $m_e^*$ is the electron effective mass and $h$ is the Planck constant. We can conclude that an increased electron concentration (i.e., $n$) within the InGaN/GaN MQWs enhances the electron re-escape process in Figs. 3(a) and 3(b) [29]. However, we know that $M_t > N_t$ , and thus LED II has a more severe electron re-escape process than LED I in Figs. 3(a) and 3(b). As a result, the reduced electron leakage in LED II is well attributed to the reduced electron mean free path by the n-type $In_{0.10}Ga_{0.90}N$ EC layer that suppresses those electrons directly flying over the quantum wells. In addition, we also showed the hole profiles for LEDs I and II in Fig. 6(c), respectively. According to Fig. 6(c), we can see that LED II exhibits a more homogenous hole distribution across the InGaN/GaN MQWs than LED I, which is due to the reduced valence band barrier heights of InGaN/GaN MQWs by the InGaN EC layer as shown in Fig. 5. The radiative recombination rates for LEDs I and II are shown in Fig. 6(d). The increased electron capture efficiency and the improved hole transport in the InGaN/GaN MQWs due to the InGaN EC layer account for the enhanced radiative recombination rate for LED II, as indicated in Fig. 6(d).

## 4. Conclusions

In conclusion, the InGaN/GaN LED with an n-type $In_{0.10}Ga_{0.90}N$ electron cooler layer has been demonstrated and investigated. The enhanced electron capture efficiency by the multiple quantum wells is attributed to a reduced mean free path after electrons undergo thermalization by phonon emission in the electron cooler layer. Moreover, we found the n-type $In_{0.10}Ga_{0.90}N$ electron cooler layer also promotes the hole transport by "pulling up" the valence band of the quantum barriers. Thus, the increased electron capture efficiency and the improved hole transport across the multiple quantum wells lead to the improvement of the radiative recombination rate, and thus the enhanced optical output power and the reduced efficiency droop. Therefore, the InGaN electron cooler holds great promise for achieving better-performance InGaN/GaN LEDs and can be optimized using the electron mean-free-path model.


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