



This is a repository copy of *Extremely low excess noise in InAs electron avalanche photodiodes*.

White Rose Research Online URL for this paper:  
<http://eprints.whiterose.ac.uk/9032/>

---

**Article:**

Marshall, A., Tan, C.H, Steer, M. et al. (1 more author) (2009) Extremely low excess noise in InAs electron avalanche photodiodes. *IEEE Photonics Technology Letters*, 21 (13). pp. 866-868. ISSN 1041-1135

<https://doi.org/10.1109/LPT.2009.2019625>

---

**Reuse**

Unless indicated otherwise, fulltext items are protected by copyright with all rights reserved. The copyright exception in section 29 of the Copyright, Designs and Patents Act 1988 allows the making of a single copy solely for the purpose of non-commercial research or private study within the limits of fair dealing. The publisher or other rights-holder may allow further reproduction and re-use of this version - refer to the White Rose Research Online record for this item. Where records identify the publisher as the copyright holder, users can verify any specific terms of use on the publisher's website.

**Takedown**

If you consider content in White Rose Research Online to be in breach of UK law, please notify us by emailing [eprints@whiterose.ac.uk](mailto:eprints@whiterose.ac.uk) including the URL of the record and the reason for the withdrawal request.



[eprints@whiterose.ac.uk](mailto:eprints@whiterose.ac.uk)  
<https://eprints.whiterose.ac.uk/>

# Extremely Low Excess Noise in InAs Electron Avalanche Photodiodes

Andrew R. J. Marshall, Chee Hing Tan, Mathew J. Steer, and John P. R. David

**Abstract**—Measurements of the avalanche multiplication noise in InAs p-i-n and n-i-p diodes at room temperature demonstrate unambiguously that the avalanche multiplication process is dominated by impact ionization of electrons. This results in the excess noise factor for electron initiated multiplication asymptotically approaching a maximum value just less than two and becoming virtually gain-independent for higher gains. Measurements for predominantly hole initiated multiplication show corresponding high excess noise factors suggesting the electron to hole ionization coefficient ratios are comparable to those reported for  $\text{Hg}_{1-x}\text{Cd}_x\text{Te}$  electron avalanche photodiodes.

**Index Terms**—Avalanche photodiodes (APDs), impact ionization.

## I. INTRODUCTION

PREVIOUS characterization of III–V semiconductors has shown that in general these materials are not ideally suited to high performance avalanche photodiode (APD) applications. An APD's avalanche gain is accompanied by an increase in noise, characterized by the gain-dependent excess noise factor  $F$ . Under the local model of impact ionization [1] to minimize  $F$  the ionization coefficients for electrons ( $\alpha$ ) and holes ( $\beta$ ) should be as disparate as possible. Ideally either  $\alpha$  or  $\beta$  should be equal to zero, such that the ionization coefficient ratio  $k = \alpha/\beta$  or  $k = \beta/\alpha$  also becomes zero, resulting in  $F \sim 2$  at high gains. Unfortunately, most III–V materials exhibit an ionization coefficient ratio in the range  $0.2 < k < 1$  for the field range in which practical APDs have been demonstrated [2], [3].

Extremely low excess noise, close to the lower limit predicted by the local model when only one carrier type undergoes impact ionization, has been observed in  $\text{Hg}_{1-x}\text{Cd}_x\text{Te}$  diodes made from both short-wave infrared and mid-wave infrared sensitive compositions [4]. It would be useful if similar low noise behavior could also be found in the more widely used III–V material system. The first indication that this might be possible came from recent photomultiplication measurements which showed that avalanche gain in the III–V material InAs is dominated by electron impact ionization, with holes playing almost no part in the avalanche process [5]. The band structure of InAs has some similarities to that of  $\text{Hg}_{1-x}\text{Cd}_x\text{Te}$  at the compositions for which low noise has been reported, with the first conduction band intervalley separations being two or more times the

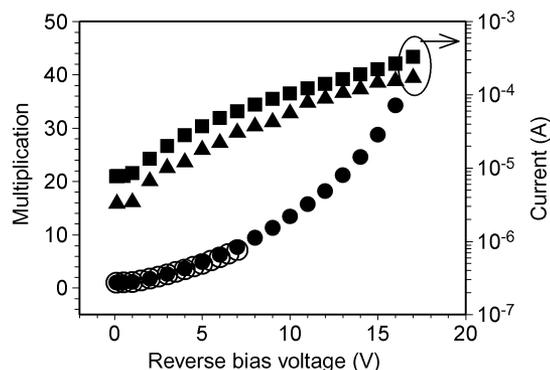


Fig. 1. Multiplication ( $\bullet$ ), reverse leakage current ( $\blacktriangle$ ), and photocurrent ( $\blacksquare$ ) measured on a 50- $\mu\text{m}$  diameter P2 diode, under top illumination during noise measurement. Reported  $M_e$  ( $\circ$ ) [5].

bandgap energy. This letter reports on a systematic study of excess noise undertaken on InAs diodes.

## II. EXPERIMENTAL DETAIL AND RESULTS

InAs p-i-n and n-i-p diode structures were grown by molecular beam epitaxy and processed into mesa diodes as described in earlier work [5], [6]. Measurement results are reported for two p-i-n diodes with intrinsic region widths of 2.0  $\mu\text{m}$  (P1) and 3.5  $\mu\text{m}$  (P2) and a n-i-p diode with a 2.0- $\mu\text{m}$  intrinsic region width (N1). The reverse leakage characteristics were strongly influenced by the fabrication process and in some cases revealed the presence of a significant surface component; however, the total leakage remained sufficiently low for the noise to be measured on P1, P2, and N1 diodes of 50- and 100- $\mu\text{m}$  diameters and P2 diodes of 200- $\mu\text{m}$  diameter. An example of the leakage current and photocurrent measured on a 50- $\mu\text{m}$  diameter P2 diode is shown in Fig. 1. The noise power was measured using the custom built setup described by Lau *et al.* [7] capable of employing phase sensitive detection (PSD) to differentiate both the photocurrent from the leakage current and the photocurrent noise from the leakage current noise. The use of PSD enabled the measurement of multiplied photocurrent noise at room temperature, removing the complication of cooling to reduce the leakage current. This setup has been used successfully in the measurement of excess noise in other III–V materials in the presence of high leakage currents [8].

A primary aim of this work was to measure the excess noise in InAs under the two extreme conditions of pure electron initiated multiplication and pure hole initiated multiplication,  $F_e$  and  $F_h$ , respectively. For large area diodes, these conditions can be achieved using focused p-side illumination of p-i-n diodes and n-side illumination of n-i-p diodes, respectively, as described earlier [5]. In this work, laser wavelengths of 633 and 1150 nm

Manuscript received January 09, 2009. First published April 10, 2009; current version published June 10, 2009. This work was supported by the EMRS DTC.

The authors are with the Department of Electronic and Electrical Engineering, The University of Sheffield, Sheffield S1 3JD, U.K. (e-mail: andy.marshall@sheffield.ac.uk; c.h.tan@sheffield.ac.uk; m.j.steer@elec.gla.ac.uk; j.p.david@sheffield.ac.uk).

Digital Object Identifier 10.1109/LPT.2009.2019625

were used to ensure absorption was, confined to the top p- and n-type cladding layers with the shorter wavelength being required where the cladding layer was thinner. However, to maintain the expected transimpedance amplifier gain response from the measurement circuit a lower limit was imposed on the acceptable dynamic resistance of the device under test, restricting the maximum size of diode which could be measured. This in turn made it difficult to constrain the laser spot on the mesa top, resulting in some illumination of the mesa sidewall and base.

The minority electron diffusion length in the p-type cladding layer of the p-i-n diodes characterized is expected to be greater than the cladding thickness [9]. Hence, when the p-i-n diodes were illuminated by a wavelength of light almost entirely absorbed in the p-type cladding layer, a substantial fraction of the photogenerated electrons would have diffused to the depletion region, giving the desired pure electron injection primary photocurrent. Indeed this was confirmed by the measurement of unity gain external quantum efficiencies (QEs)  $\sim 15\%$  under such conditions on large area P1 and P2 diodes, with the spot well focused on the mesa top. During the noise measurements on small area p-i-n diodes, some contamination of the intended electron injection will have occurred from carriers generated by light falling on the mesa sidewall and around the base. However, in relation to the substantial electron injection from the p-type cladding layer, the level of contamination was not thought to be significant. This expectation is supported by the similarity between the multiplication measured during noise measurements on small area p-i-n diodes and the multiplication due to pure electron injection ( $M_e$ ) measured previously on large area diodes [5], as shown in Fig. 1.

In contrast to these results on p-i-n diodes, when large area n-i-p diodes were illuminated by a wavelength of light almost entirely absorbed in the n-type cladding layer the measured unity gain external QE was only  $\sim 1\%$ . This is thought to be due to the photogenerated minority holes having a short diffusion length in relation to the cladding thickness, so that only a small fraction reached the depletion region. In relation to this low level pure hole injection the contamination resulting from light falling on the mesa sidewall would have been more significant than for p-i-n diodes. Furthermore, minority electrons generated by light falling around the mesa base would have been collected through diffusion from a much larger volume than the corresponding holes were in p-i-n diodes. Hence a relatively higher degree of electron contamination, of the desired hole injection, was expected during the measurements on n-i-p diodes. This is confirmed by comparing the multiplication measured during noise measurements on small area n-i-p diodes with the multiplication due to pure hole injection ( $M_h$ ) measured previously on large area diodes [5], as shown in Fig. 2. The higher multiplication factors measured at any given bias on the smaller diodes are regarded as evidence of contaminated injection, since with  $\beta \sim 0$  there must have been significant levels of electron injection to initiate this multiplication.

The excess noise measured on InAs p-i-n diodes is shown in Fig. 3. The results are near, or a little below, the level predicted by the local model for  $k = 0$ . This confirms that avalanche multiplication in InAs is dominated by electron impact ionization, in the electric field range exercised, as was predicted

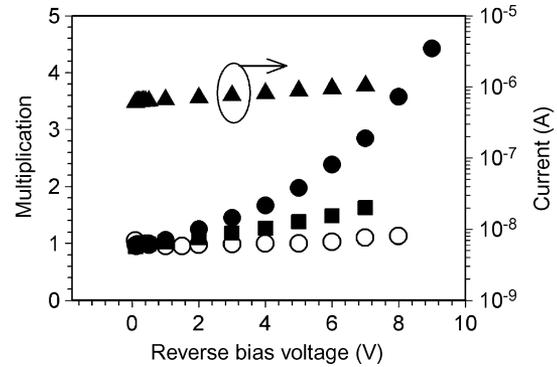


Fig. 2. Multiplication measured on 100- $\mu\text{m}$  (■) and 50- $\mu\text{m}$  (●) diameter N1 diodes and photocurrent (▲) measured on a 100- $\mu\text{m}$  diameter N1 diode, under top illumination during noise measurement. Reported  $M_h$  (○) [5].

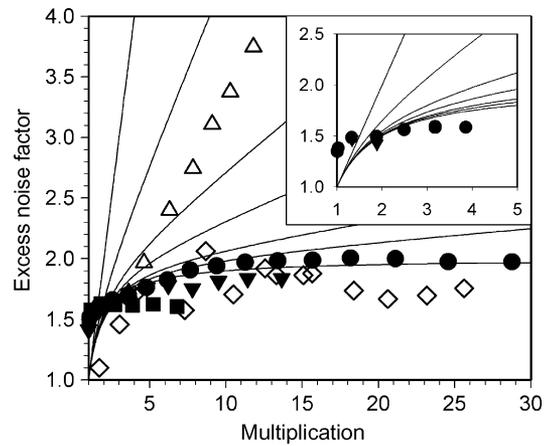


Fig. 3. Excess noise measured on P1 (inset) and P2 diodes with 50- $\mu\text{m}$  (●), 100- $\mu\text{m}$  (▼), and 200- $\mu\text{m}$  (■) diameters under top illumination.  $F_e$  measured on an  $\text{Al}_{0.48}\text{In}_{0.52}\text{As}$  p-i-n APD with a 0.29- $\mu\text{m}$  intrinsic width ( $\Delta$ ) and  $F_e$  reported by Beck [4] for a  $\text{Hg}_{1-x}\text{Cd}_x\text{Te}$  APD ( $\diamond$ ). Reference lines from the local model [1] with  $k = 0, 0.01, 0.02, 0.05, 0.1, 0.3$ , and 1.

from photomultiplication measurements [5]. Such an optimal excess noise characteristic has previously only been reported for  $\text{Hg}_{1-x}\text{Cd}_x\text{Te}$  APDs and Fig. 3 includes one such result [4], also measured at room temperature. In contrast, the  $F_e$  characteristic for an  $\text{Al}_{0.48}\text{In}_{0.52}\text{As}$  APD, also shown in Fig. 3 and broadly representative of state of the art telecommunications APDs, continues to rise with increasing gain in common with other reported III-V APDs.

Due to the significant electron contamination to the hole injection obtained in small diameter N1 diodes, the excess noise shown in Fig. 4 is not that due to pure hole injection, but rather that due to device diameter-dependent levels of mixed electron and hole injection. This is evident from the excess noise being significantly lower for the 50- $\mu\text{m}$  diameter diodes than for the 100- $\mu\text{m}$  diameter diodes. Were the purity of the hole injection to be improved, it would be expected that the excess noise would increase towards the local model prediction for  $k = \infty$  in complement to the  $k \sim 0$  characteristic for electron initiated multiplication. However, with pure hole injection unachievable, the  $k \sim 0$  characteristics measured on the p-i-n diodes are most clearly corroborated by measurements on N1 diodes when the laser spot was positioned on the mesa sidewall and at its base,

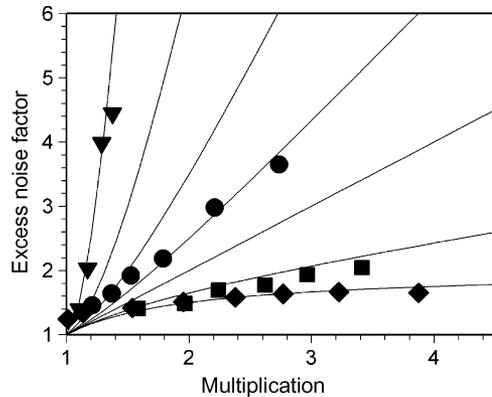


Fig. 4. Excess noise measured on N1 diodes with 50- $\mu\text{m}$  ( $\bullet$ ) and 100- $\mu\text{m}$  ( $\blacktriangledown$ ) diameters under top illumination and on 50- $\mu\text{m}$  diameter diodes for illumination on the mesa sidewall ( $\blacksquare$ ) and on the substrate next to it ( $\blacklozenge$ ). Reference lines from the local model [1] with  $k = 0, 0.3, 1, 2, 4, 10,$  and  $40$ .

the excess noise results for which are also shown in Fig. 4. Under these conditions the photocurrent injected into the depletion region contained a high proportion of electrons and the excess noise was again very low.

### III. DISCUSSION

It is evident that the excess noise results for InAs p-i-n diodes depend to a degree on the diameter of the measured diode with smaller diodes showing higher noise. This suggests that truly pure electron injection was not achieved on all diodes. Since the noise power is proportional to the square of the gain, the effects of low level contamination to the injected photocurrent are more pronounced in the excess noise than the photomultiplication. It follows that the excess noise  $F_e$ , to be expected with truly pure electron injection, is equal to or less than that measured on the largest diodes. In this case, it is necessary to offer an explanation as to how  $F_e$  for InAs APDs can fall below the lower boundary condition for excess noise in the local model, usually considered to be approximately accurate for APDs like P2 with thick multiplication regions. It has been shown [10] that where  $\alpha$  and  $\beta$  have similar magnitudes, the local model is reasonable because the deadspace ( $d$ ), not taken into account by the local model and equal to the distance traveled by a carrier while it obtains the threshold energy required before it can potentially undergo impact ionization, is relatively small compared with the mean ionization path length  $\alpha^{-1}$ . Since there is less than one ionization event on average per carrier transit of the multiplication region

in such cases below breakdown, the deadspace introduces little determinism into the ionization probability distribution within the multiplication width and hence has little effect on the excess noise. However, as  $k$  approaches zero, the injected carriers of the ionizing type must, for gains greater than two, traverse a deadspace and undergo impact ionization multiple times in a single transit of the multiplication region. In this case,  $\alpha^{-1}$  must reduce to a fraction of the total multiplication region width and it is proposed that  $d$  can become sufficiently significant with respect to it in order to account for the sublocal model noise measured. An indication of the  $d$  to  $\alpha^{-1}$  ratio required to reduce the excess noise when  $k = 0$  can be found in the work of Saleh *et al.* [11], who showed that  $F$  asymptotically approaches 1.7 if  $\alpha d = 0.1$ .

### REFERENCES

- [1] R. J. McIntyre, "Multiplication noise in uniform avalanche diodes," *IEEE Trans. Electron Devices*, vol. ED-13, no. 1, pp. 164–168, Jan. 1966.
- [2] J. P. R. David and C. H. Tan, "Material considerations for avalanche photodiodes," *IEEE J. Sel. Topics Quantum Electron.*, vol. 14, no. 4, pp. 998–1009, Jul. 2008.
- [3] J. C. Campbell, "Recent advances in avalanche photodiodes," *IEEE J. Sel. Topics Quantum Electron.*, vol. 10, no. 4, pp. 777–787, Jul. 2004.
- [4] J. D. Beck, C. Wan, M. Kinch, J. Robinson, P. Mitra, R. Scritchfield, F. Ma, and J. Campbell, "The HgCdTe electron avalanche photodiode," in *Proc. SPIE, Infrared detector materials and devices*, 2004, vol. 5564, pp. 44–53.
- [5] A. R. J. Marshall, C. H. Tan, M. J. Steer, and J. P. R. David, "Electron dominated impact ionization and avalanche gain in InAs photodiodes," *Appl. Phys. Lett.*, vol. 93, no. 11, p. 111107, Sep. 2008.
- [6] A. R. J. Marshall, C. H. Tan, J. P. R. David, J. S. Ng, and M. Hopkinson, "Fabrication of InAs photodiodes with reduced surface leakage current," in *Proc. SPIE, Optical materials in defence systems technology*, 2007, vol. 6740, p. 67400H.
- [7] K. S. Lau, C. H. Tan, B. K. Ng, K. F. Li, R. C. Tozer, J. P. R. David, and G. J. Rees, "Excess noise measurement in avalanche photodiodes using a transimpedance amplifier front-end," *Meas. Sci. Technol.*, vol. 17, pp. 1941–1946, Jun. 2006.
- [8] Y. L. Goh, J. S. Ng, C. H. Tan, W. K. Ng, and J. P. R. David, "Excess noise measurement in  $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$ ," *IEEE Photon. Technol. Lett.*, vol. 17, no. 11, pp. 2412–2414, Nov. 2005.
- [9] M. J. Kane, G. Braithwaite, M. T. Emeny, D. Lee, T. Martin, and D. R. Wright, "Bulk and surface recombination in InAs/AlAs<sub>0.16</sub>Sb<sub>0.84</sub> 3.45  $\mu\text{m}$  light emitting diodes," *Appl. Phys. Lett.*, vol. 76, no. 8, pp. 943–945, Feb. 2000.
- [10] S. A. Plimmer, J. P. R. David, and D. S. Ong, "The merits and limitations of local impact ionisation theory," *IEEE Trans. Electron Devices*, vol. 47, no. 5, pp. 1080–1088, May 2000.
- [11] B. E. A. Saleh, M. M. Hayat, and M. C. Teich, "Effect of dead space on the excess noise factor and time response of avalanche photodiodes," *IEEE Trans. Electron Devices*, vol. 37, no. 9, pp. 1976–1984, Sep. 1990.