3D-CE5.h: Additional merge candidates derived from shifted disparity candidate predictors
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Title: 3D-CE5.h: Additional merge candidates derived from shifted disparity candidate predictors

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Purpose: Proposal

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Abstract

The coding performance of the HTM 5.0.1 can be improved by inserting in the candidate merge list two new candidates, the maximal length of list still being equal to 6. These candidates are derived from the first disparity compensated candidate found in the list, which is then horizontally shifted by +4 and -4. Bit rate gains are respectively 0.4% and 0.5% for the side views 1 and 2. The overall bit rate gain is 0.2%.

1 Introduction

In 3D-HEVC, up to six merging candidates are made available for selection when inter-view motion prediction is enabled. The efficiency of this approach relies on how relevant vectors present in the list are. In HTM 5.0.1, several adoptions improved the candidate merge list in pruning duplicated candidates and in inserting, when IMPV is available, disparity motion vector derived from disparity vector [1][2].

It is still possible to improve the coding performance by adding two new candidates derived from the first disparity compensated candidate found in the candidate merge list in adding a horizontal shift of -4 or +4 to it. That is equivalent to a horizontal shift of -1 or +1 pixel.

The method described below proposes to replace existing candidates of the candidate merge list described in the text specification draft with two candidates pointing to the base view and horizontally shifted by the offset -4 and +4 respectively.

This proposal is the follow-up of JCT3V-B0080.

2 Proposed method

First, the merge candidate list is constructed as described in the section “G.8.5.2.1.1 Derivation process for luma motion vectors for merge mode” [3]. Nothing is changed with respect to the way it is done in HTM 5.0.1 A possible candidate merge list could be as depicted in Figure 1:
In this list, each motion vector candidate is defined with:

- The luma motion vectors \( \text{mvL}^0 \) and \( \text{mvL}^1 \),
- The reference indices \( \text{refIdx}^0 \) and \( \text{refIdx}^1 \),
- The prediction list utilization flags \( \text{predFlag}^0 \) and \( \text{predFlag}^1 \).

Once the candidate merge list is constructed, the refinement of the list is a three step process:

1. Search of the 1\(^{st}\) disparity compensated predictor,
2. Creation of the two new candidates,
3. Insertion of the new candidates into the candidate merge list.

These three steps are detailed below.

### 2.1 Search of the 1\(^{st}\) disparity compensated predictor

The candidate merge list is scanned from the first position (index 0) to the third one (index 2). The first candidate found in the list during this search which has either its L0 vector or L1 vector or both pointing to another view is marked as selected candidate (a vector \( \text{mvL}^X \) is pointing to another view if the viewId of current coding unit is not equal to the viewId of reference \( \text{refIdx}^X \)).

If no such candidate exists, the process ends.

### 2.2 Creation of the two new candidates

Let \( m \) be the index of the selected candidate. This index \( m \) is higher or equal to 0 and lower or equal to 2. The selected candidate is defined by \( \text{mvL}^0_m \), \( \text{mvL}^1_m \), \( \text{refIdx}^0_m \), \( \text{refIdx}^1_m \), \( \text{predFlag}^0_m \), and \( \text{predFlag}^1_m \). Two refined candidates \( \text{candDCPa} \) and \( \text{candDCPb} \) are constructed as follows:

```plaintext
If \( \text{mvL}^0_m \) is pointing to another view:

\[
\begin{align*}
\text{mvL}^0_{\text{candDCPa}} &= \text{mvL}^0_m + (4, 0) \\
\text{mvL}^1_{\text{candDCPa}} &= \text{mvL}^1_m \\
\text{refIdx}^0_{\text{candDCPa}} &= \text{refIdx}^0_m \\
\text{refIdx}^1_{\text{candDCPa}} &= \text{refIdx}^1_m \\
\text{predFlag}^0_{\text{candDCPa}} &= \text{predFlag}^0_m \\
\text{predFlag}^1_{\text{candDCPa}} &= \text{predFlag}^1_m \\
\text{mvL}^0_{\text{candDCPb}} &= \text{mvL}^0_m + (-4, 0) \\
\text{mvL}^1_{\text{candDCPb}} &= \text{mvL}^1_m
\end{align*}
\]
```
refIdxL0candDCPb = refIdxL0
refIdxL1candDCPb = refIdxL1
predFlagL0candDCPb = predFlagL0
predFlagL1candDCPb = predFlagL1

Else (mvL1 vector is pointing to another view):

mvL0candDCPa = mvL0
mvL1candDCPa = mvL1 + (4, 0)
refIdxL0candDCPa = refIdxL0
refIdxL1candDCPa = refIdxL1
predFlagL0candDCPa = predFlagL0
predFlagL1candDCPa = predFlagL1

mvL0candDCPb = mvL0
mvL1candDCPb = mvL1 + (-4, 0)
refIdxL0candDCPb = refIdxL0
refIdxL1candDCPb = refIdxL1
predFlagL0candDCPb = predFlagL0
predFlagL1candDCPb = predFlagL1

2.3 Insertion of the new candidates into the candidate merge list

The two new candidates will be added at the 4th position (index 3) and 5th position (index 4). The previous 4th and 5th candidates might be overwritten. However, the previous 4th candidate will be moved to the 6th position (index 5) first. These two operations are depicted in the following figures and the pseudo code listed after.

![Figure 2: Moving of the fourth candidate to the last position in the candidate list.](image)

![Figure 3: Overwriting of fourth and fifth candidates by newly generated candidates.](image)

// insertion of the two new candidates
Experimental results

This section provides results of the proposed method in comparison with the 3D-HTM anchor. The 3D-HTM 5.0.1 [4] is utilized and the method is implemented on top of it. All tests are run based on the common tests conditions [5].

Only the method TComDataCU::getInterMergeCandidates in TComDataCU.cpp is modified (and also TypeDef.h to activate the macro INRIA_NEW_MERGE_CAND).

Results are provided in table 1. The overall average bitrate reduction is 0.2%, 0.1%, 0.2% for decoded texture views, synthesized views, coded and synthesized views, respectively.

No added complexity is reported.
Table 1: Coding gains with respect to anchor for 3-view case

<table>
<thead>
<tr>
<th></th>
<th>video 0</th>
<th>video 1</th>
<th>video 2</th>
<th>video only</th>
<th>synthesized only</th>
<th>coded &amp; synthesized</th>
<th>enc time</th>
<th>dec time</th>
<th>ren time</th>
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<tbody>
<tr>
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<td>0,2%</td>
<td>0,0%</td>
<td>0,0%</td>
<td>0,0%</td>
<td>0,0%</td>
<td>95,5%</td>
<td>102,0%</td>
<td>106,3%</td>
</tr>
<tr>
<td>Kendo</td>
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<td>-0,7%</td>
<td>-0,3%</td>
<td>-0,1%</td>
<td>-0,1%</td>
<td>97,4%</td>
<td>101,9%</td>
<td>110,7%</td>
</tr>
<tr>
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<td>-0,7%</td>
<td>-0,6%</td>
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<td>-0,2%</td>
<td>-0,2%</td>
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<td>98,2%</td>
<td>113,8%</td>
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<td>-0,2%</td>
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<td>102,4%</td>
<td>106,0%</td>
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<td>-0,2%</td>
<td>-0,1%</td>
<td>-0,1%</td>
<td>-0,2%</td>
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<td>101,1%</td>
<td>106,7%</td>
</tr>
<tr>
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<td>-0,2%</td>
<td>-0,2%</td>
<td>-0,2%</td>
<td>102,6%</td>
<td>101,5%</td>
<td>104,8%</td>
</tr>
<tr>
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<td>-0,4%</td>
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<td>100,7%</td>
<td>110,2%</td>
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<td>-0,4%</td>
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<tr>
<td>1920x1088</td>
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<td>-0,4%</td>
<td>-0,5%</td>
<td>-0,2%</td>
<td>-0,2%</td>
<td>-0,2%</td>
<td>102,6%</td>
<td>101,5%</td>
<td>104,8%</td>
</tr>
<tr>
<td><strong>average</strong></td>
<td>0,0%</td>
<td>-0,4%</td>
<td>-0,5%</td>
<td>-0,2%</td>
<td>-0,1%</td>
<td>-0,2%</td>
<td><strong>100,2%</strong></td>
<td><strong>100,9%</strong></td>
<td><strong>107,5%</strong></td>
</tr>
</tbody>
</table>

4 Conclusion

This proposal provides an overall bit rate gain of 0.2% with no added complexity. It is proposed to adopt it.

5 References


6 Patent rights declaration(s)

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