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TECHNICAL MEMORANDUM (NASA) 77

BI-DIRECTIONAL COMMUNICATION INTERFACE
FOR MICROPROCESSOR-TO-SYSTEM/370

Described is a hardware and software interface
to allow two-way communication between a
microprocessor system and the IBM System/370

(NASA-CR-163940) BI-DIRECTIONAL
COMMUNICATION INTERFACE FOR
MICROPROCESSOR-TO-SYSTEM/370 (Ohio Univ.)
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I. INTRODUCTION

This paper documents the design and operation of a bi-directional communication interface between a microcomputer and the IBM System/370. The hardware unit inter-connects a modem to interface to the S/370, the microcomputer with an EIA I/O port, and a terminal for sending and receiving data from either the microcomputer or the S/370. Also described is the software necessary for the two-way interface. This interface has been designed so that no modifications need to be made to the terminal, modem, or microcomputer. This unit is designed to upgrade an uni-directional interface already in use [1].

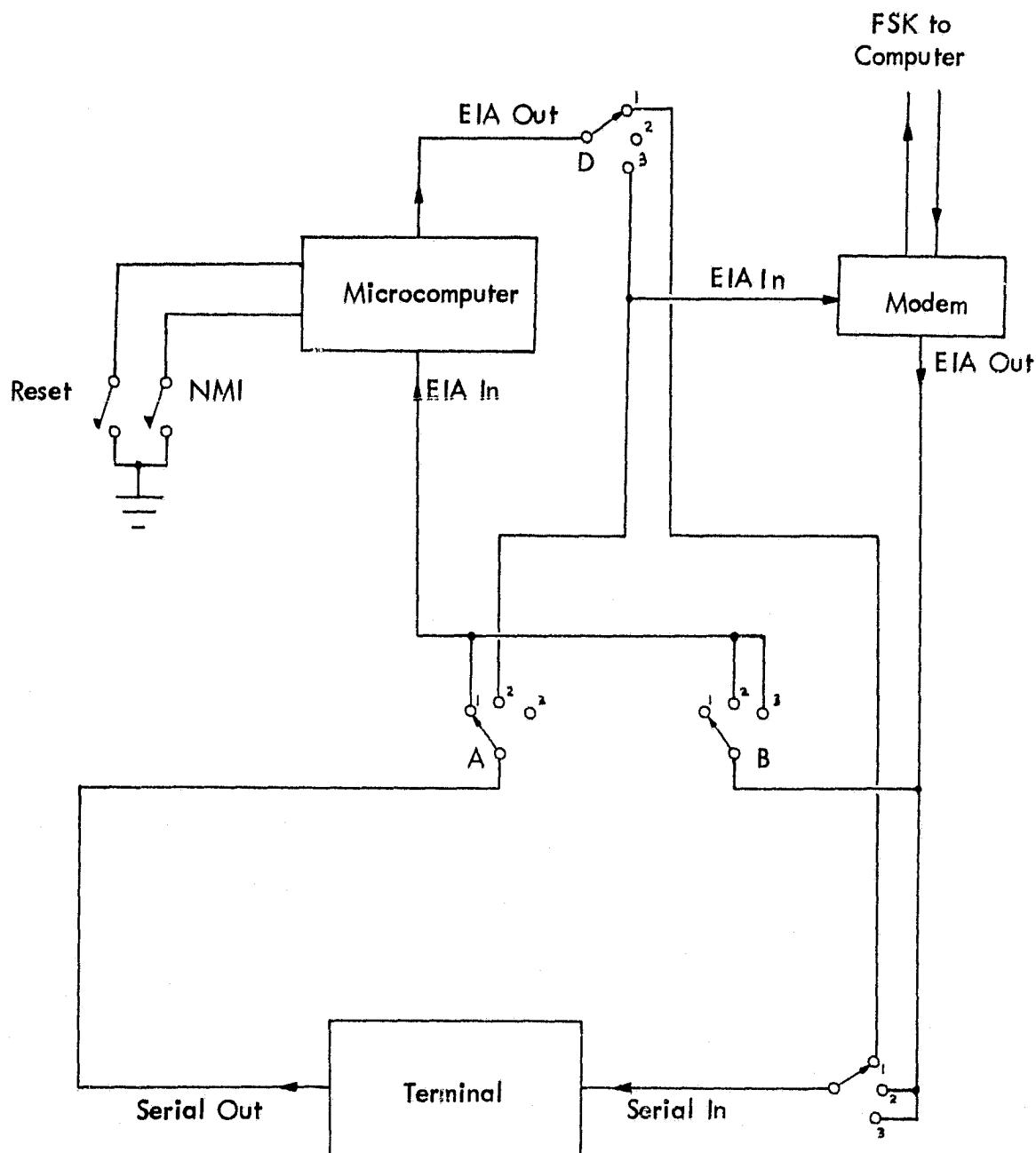
II. INTERFACE DESCRIPTION: HARDWARE

Figure 1 shows the paths of signals between the microcomputer, the modem, and the terminal. The hardware interface consists of a four-pole, three-position switch and cables and plugs to connect the switch box to the other devices. All signals are assumed to be RS-232C (EIA standard).

In switch position 1, the microcomputer is connected directly to the terminal; all communications are between these two only. The modem is isolated in this position and it is not necessary to have it connected if no communication to the S/370 is desired. In position 2, the serial out from the keyboard is routed to the modem for communicating to the S/370. The serial out from the modem goes to the terminal and the serial in of the microcomputer. In this position, it is possible to send commands and receive responses from the S/370, while the microcomputer reads the data sent by the S/370. Thus it is possible to load a program into the microcomputer by displaying the object file on terminal. It is necessary to switch to position 1 and issue the microcomputer load command prior to typing the file. Position 3 on the switch box connects the serial out from the modem to the terminal and to the serial in on the microcomputer. In addition, the serial out from the microcomputer is sent to the modem. Here, the microcomputer communicates directly with the S/370, the terminal always displays the response sent by the S/370. With proper positioning of the half-duplex/full-duplex switches on the terminal and modem, the responses from the microcomputer may also be displayed. Note that the serial-out from the terminal is isolated, thus it may be necessary to start a program on the microcomputer by pressing the NMI (non-maskable interrupt) switch on the switch box.

Table 1 lists the connection used on the terminal and modem. Connections for RS-232C are made through 25-pin D-connectors. Data terminal equipment (DTE) devices are supplied with a male (DB-25P) connector while data communication equipment (DCE) devices are supplied with a female (DB-25S) connector. Figure 2 shows the detailed routing of connections from the connectors on the terminal and modem through the switch box.

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Note: Switches Shown in Position 1.

Figure 1. Signal Routing For Bi-Directional Interface.

	DTE	DCE	
P GND	1	1	Protective Ground
S OUT	2	2	Serial Output
S IN	3	3	Serial Input
RTS	4	4	Request to Send
CTS	5	5	Clear To Send
DSR	6	6	Data Set Ready
S GND	7	7	Signal Ground
RLSD	8	8	Received Line Signal Detect
DTR	20	20	Data Terminal Ready

Table 1. RS-232C Connections.

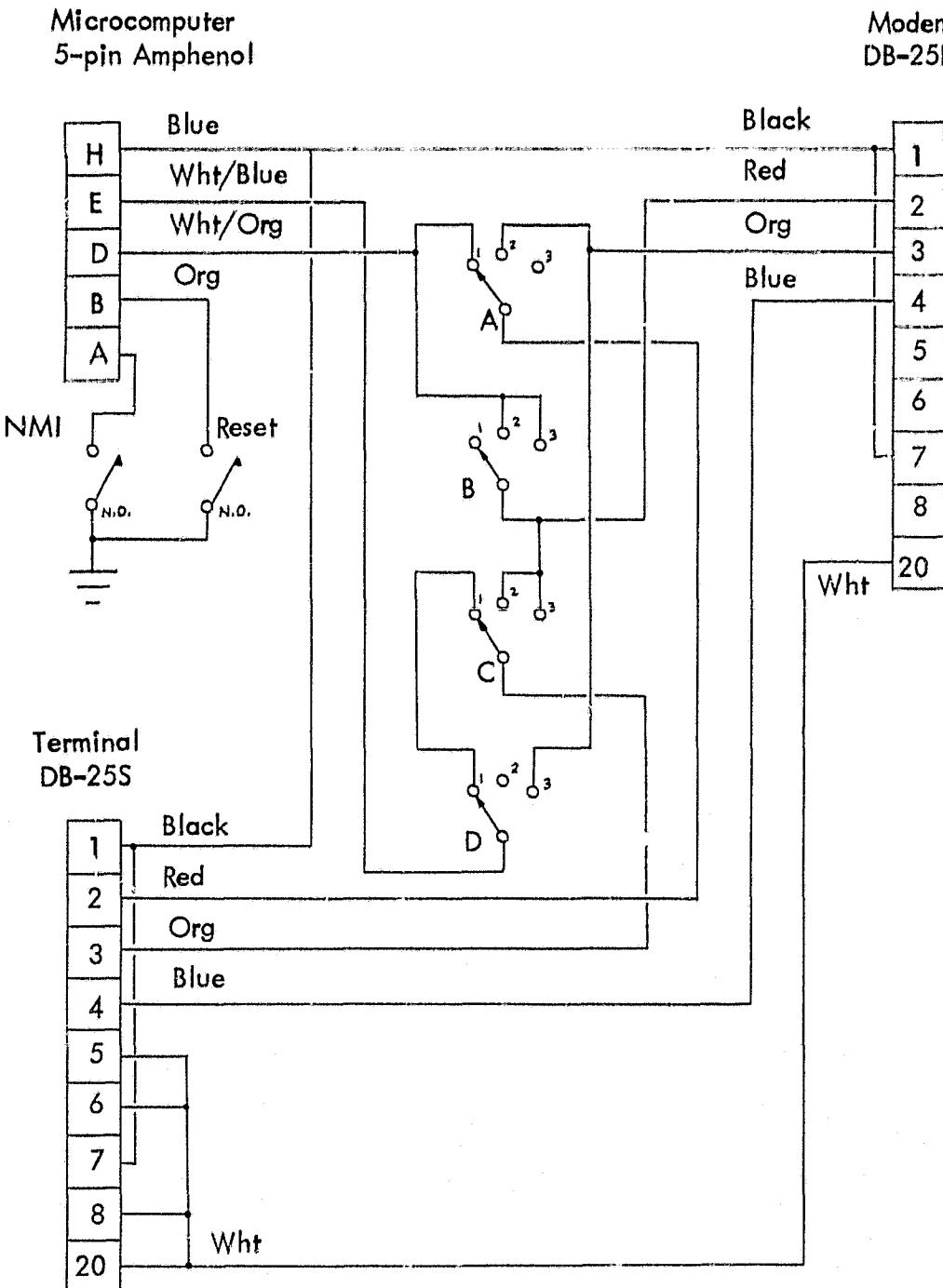


Figure 2. Interface Wiring Diagram.

III. INTERFACE DESCRIPTION: SOFTWARE

Full utilization of the bi-directional interface requires a set of programs to be run simultaneously on the microcomputer and the S/370. Figure 3 shows a block diagram of how the programs would operate for a typical application. Some points to be considered in writing the interface software are:

- a. Most microcomputers store character data internally as ASCII.
- b. Serial communications between devices are generally in ASCII format.
- c. The I/O routines for the S/370 expect to receive ASCII which is then converted to EBCDIC, which the S/370 uses for internal storage of character data.
- d. The Conversational Monitor System (CMS) portion of the VM/370 operating system is line-oriented, i.e., no system action is taken until a carriage return (hex OD) is received.
- e. The S/370 issues a prompt when ready for another line.

A typical application for which this interface has been used is transmitting data collected by the microcomputer on a cassette tape to the S/370, where it is stored on a disk file for further processing. The sequence of events is as follows: the data to be transmitted is stored in a buffer in the microcomputer's memory. Generally, 80 characters comprise one line. Note that one byte consists of two four-bit hexadecimal numbers, each of which is converted to ASCII. Thus if 80 characters are to be sent, the buffer is 40 bytes long. After 80 characters are sent, a carriage return (hex OD) is sent. The S/370 does the ASCII-to-EBCDIC conversion and places the EBCDIC characters in a user buffer in the S/370 memory. When the S/370 is ready to receive another line, it sends a series of control characters. The microcomputer reads and recognizes these control characters as the prompt signal to send another line. The sequence of control characters currently sent by S/370 is shown in Figure 4.

Appendix A gives a listing of a MOS Technology 6502 microcomputer program (intended to be run using the 'Super-Jolt' micro unit) for reading 40 bytes of data from a Memodyne digital cassette tape unit and sending these to the S/370. The data to be sent are packed BCD numbers; i.e., one BCD digit occupies four bits, two BCD numbers are contained in one byte. Each BCD digit is sent as ASCII by the 'output byte' routine in the microcomputer monitor program (at address 72B1 (hex) in the Super-Jolt (TM) monitor). A carriage return is sent at the end of the line with a call to the WRT routine at address 72C6 (hex).

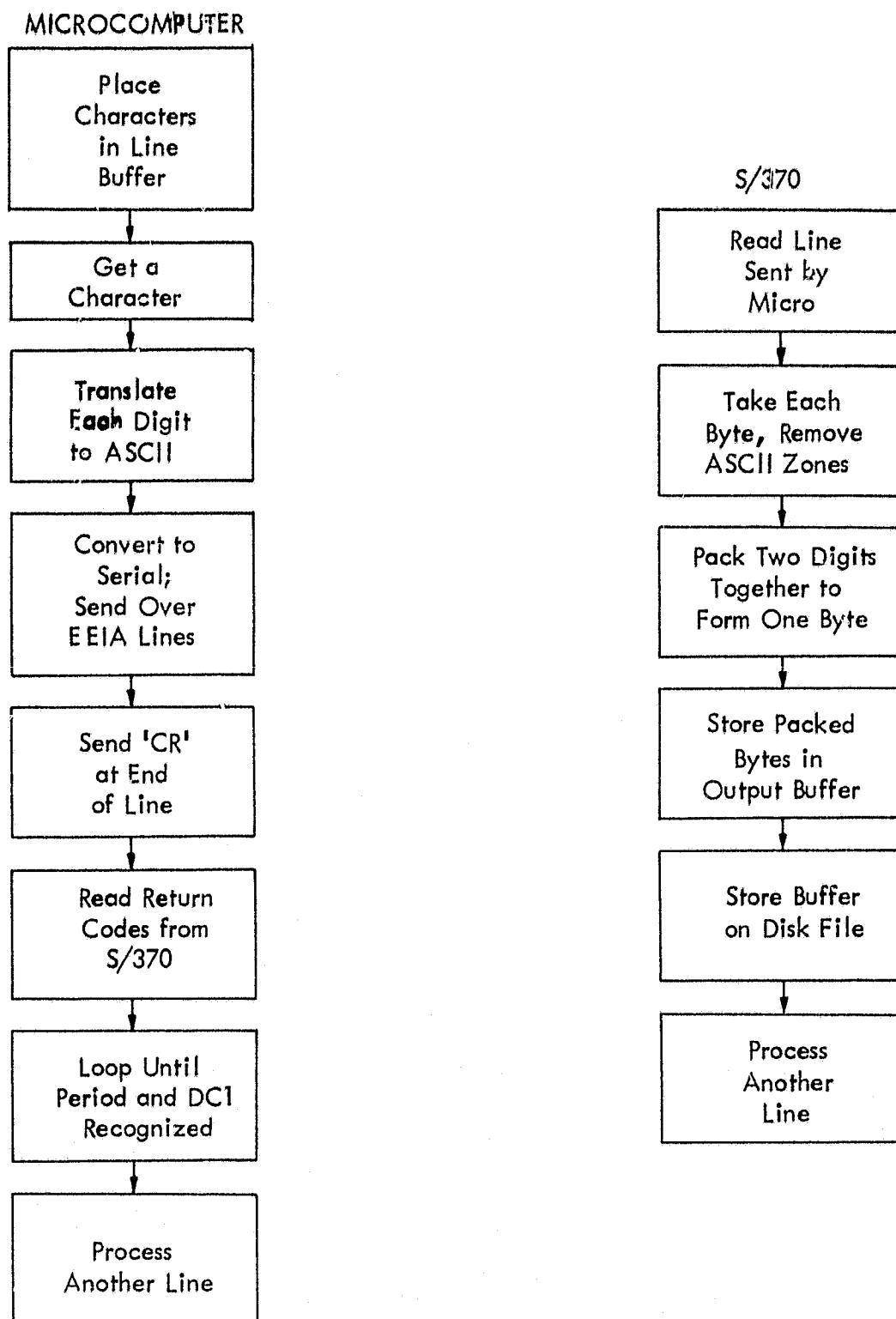


Figure 3. Control Program Flow Charts.

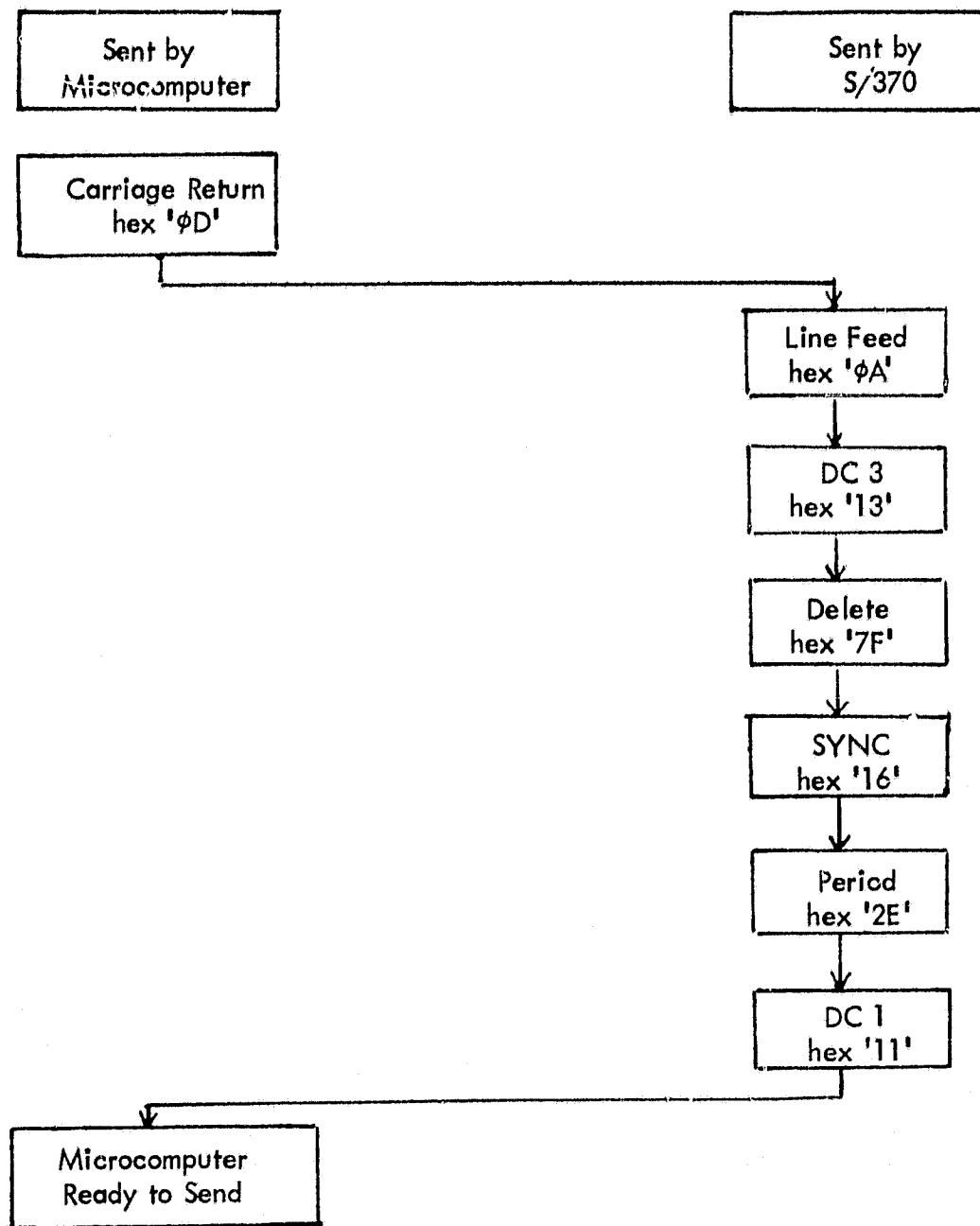


Figure 4. Control Characters Sent by S/370 After Receiving Carriage Return.

Program lines 1 to 78 are initialization steps used for the Memodyne interface hardware and to position the tape properly. Lines 79 to 91 constitute the main part of the program which builds up the 40-byte buffer then sends the buffer to the S/370. This part loops continuously whether or not any data is received. The operator should monitor the operation to stop the program when all the data has been transmitted. Subroutine READ is called to read a byte from the tape unit. Subroutine W370 sends the 40-character buffer to the S/370, sends a carriage return, then looks for a period (hex 2E) followed by a DC-1 (hex 11). If this sequence is not done, the S/370 issues a read-error message. When these two characters are received, control is passed back to the main program sequence.

Subroutine RDT is a modification of the RDT routine at address 72E9 in the Jolt monitor. Most serial-read routines on microcomputers are full-duplex; as each bit is received, it is echoed back out to the sending device. However, the S/370 can receive half-duplex only. Thus it is necessary to change the interface method through the modem or to re-write the read routine so that the received bits are not echoed by the microcomputer. This is the purpose of having a separate read routine. If this is not done, read-errors result. The program presented here is shown to illustrate one application of the bi-directional interface. Other uses on other microcomputers would still use the same basic philosophy.

The companion program that is run on the S/370 is shown in Appendix B. This program is written in IBM 360/370 assembler language [2] using standard CMS I/O routines. Again this program illustrates the application of sending data to the S/370 for storage on a disk file.

The data is read 80-bytes at a time, each BCD character in its ASCII format. Each character read is stripped of the upper four-digit mask and is repacked. This is done by the translate instruction at line 88 and the PACK instruction at line 90. Since one record produces only 40 packed BCD digits, two lines are read before one 80-byte record is written to the file. A blank line or an incomplete line is filled to the end with zeros. Each time a record is written, a counter is incremented which is printed at the end of program execution.

IV. INTERFACE OPERATION

The example of transmitting data from the microcomputer to the S/370 will be continued here to show how the interface may be operated. After the interface is properly connected, power should be applied to all units. At this point it is usually necessary to load the microcomputer with a program stored on a disk file. Thus the switch box should be set to position 2 and the appropriate CMS LOGON procedure performed. When the microprocessor object code is ready for transmittal (through editting, assembling, simulating, etc.) the switch box should be set to position 1, the microcomputer reset button pushed, and a carriage return or other appropriate

key to reset the microcomputer typed. Then issue the proper command to set the microcomputer for loading hexadecimal data over its serial lines. The switch box is then set back to position 2 and the appropriate command is issued to the S/370 to load the microcomputer with the object file. Next, the unit is switched back to position 1 to verify correct loading, initialize any memory locations and set up the NMI vector address to the start of the program. Now the switch box is placed in position 2 and the program to receive the data is started and then the unit is set to position 3 and the NMI button pressed.

As operation commences, the prompting period and any other responses from the S/370 will be displayed on the terminal. Depending on the setting of the half-duplex/full-duplex switches on the terminal and modem, data sent by the microcomputer will also be displayed on the terminal.

When the operation is finished, the unit may be set to position 2 to stop the S/370 program then position 1 to stop the microcomputer program.

V. SUMMARY

A discussion was presented here of an interface unit and software procedures to allow two-way communication between a microcomputer and a central computer. This can be used for two-way data transmission, control and other applications where bi-directional communications are necessary. As an aid to setting up the software for other computer systems, ASCII [3] and EBCDIC [4] tables are given in Tables 2 and 3.

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OF PCOR CHARTS

	000	001	010	011	100	101	110	111
0000	NULL	① DC ₀	!	0	Q	P		
0001	SOM	DC ₁	!	1	A	Q		
0010	EOA	DC ₂	"	2	B	R		
0011	EOM	DC ₃	#	3	C	S		
0100	EOT	DC ₄ (stop)	\$	4	D	T		
0101	WRU	ERR	%	5	E	U		
0110	RU	SYNC	&	6	F	V		
0111	BELL	LEM	'	7	G	W		
1000	FE ₀	S ₀	(8	H	X		
1001	HT	SK)	9	I	Y		
1010	LF	S ₂	*	:	J	Z		
1011	V/TAB	S ₃	+	;	K	I		
1100	FF	S ₄ (comma)	<		L	\		
1101	CR	S ₅	-	=	M	J		
1110	SO	S ₆	.	>	N	↑		
1111	SI	S ₇	/	?	O	↔		
			b ₇	-----b ₁				
							ACK	
							②	
							ESC	
							DEL	

Example:

100	0001
-----	------

 = A
b₇-----b₁

The abbreviations used in the figure mean:

NULL	Null Idle	CR	Carriage return
SOM	Start of message	SO	Shift out
EOA	End of address	SI	Shift in
EOM	End of message	DC ₀	Device control ①
			Reserved for data
			Link escape
EOT	End of transmission	DC ₁ - DC ₃	Device control
WRU	"Who are you?"	ERR	Error
RU	"Are you . . . ?"	SYNC	Synchronous idle
BELL	Audible signal	LEM	Logical end of media
FE	Format effector	SO ₀ - SO ₇	Separator (information)
HT	Horizontal tabulation		Word separator (blank, normally non-printing)
SK	Skip (punched card)	ACK	Acknowledge
LF	Line feed	②	Unassigned control
V/TAB	Vertical tabulation	ESC	Escape
FF	Form feed	DEL	Delete Idle

Table 2. ASCII Table.

Bit Positions 0, 1, 2, 3, 4, 5, 6, 7

Second Hexadecimal Digit

Digit Punches

Bit Positions 8, 9, 10, 11

First Hexadecimal Digit

Zone Punches

	00	01	10	11	00	01	10	11	00	01	10	11	00	01	10	11
0000 0	(1) NUL	(2) DLE	(3) DS	(4)	(5) SP	(6)	(7) G	(8) T					(9) S	(10) H	(11) U	(12) 0
0001 1	SOH	DC1	SOS				(13)		a	i	~		x	j	(14)	1
0010 2	STX	DC2	FS	SYN					b	k	t		s	x	k	2
0011 3	ETX	TM							c	l	r		c	l	t	3
0100 4	PT	RES	BYP	PN					d	m	u		d	m	u	4
0101 5	HT	NL	LF	RS					e	n	v		f	n	v	5
0110 6	LC	BS	ETB	UC					f	p	w		f	o	w	6
0111 7	DEL	IL	ESC	EOT					g	q	x		g	p	x	7
1000 8	GE	CAN							h	q	y		h	q	y	8
1001 9	BLF	EM							i	r	z		i	r	z	9
1010 A	SMM	CC	SM		c	i	(15)									
1011 B	VT	CU1	CU2	CU3	-	\$,	#								
1100 C	FF	IFS		DC4	<	*	%	@					J	H		
1101 D	CR	IQS	ENQ	NAK	()	-	-	-								
1110 E	SO	IRS	ACK		+	;	>	*					Y			
1111 F	SI	IUS	BEL	SUB	!	-	?	*								EO

Serial Hole Patterns

- | | |
|-----|---------------|
| (1) | 12-0-9-8-1 |
| (2) | 12-11-9-8-1 |
| (3) | 11-0-9-8-1 |
| (4) | 12-11-0-9-4-1 |
| (5) | No Punches |
| (6) | 12 |
| (7) | 11 |
| (8) | 12-11-0 |

- | | |
|------|-------|
| (9) | 12-0 |
| (10) | 11-0 |
| (11) | 0-8-2 |
| (12) | 0 |

Control Character Representations

ACK	Acknowledge
BEL	Bell
BS	Backspace
BYP	Bypass
CAN	Cancel
CC	Cursor Control
CR	Carriage Return
CUI	Customer Use 1
CU2	Customer Use 2
CUD	Customer Use 3
DC1	Device Control 1
DC2	Device Control 2
DC4	Device Control 4
DEL	Delete
DLE	Data Link Escape
DS	Digit Select
EM	End of Medium
ENQ	Enquiry
EO	Eight Ones
EOT	End of Transmission
ESC	Escape
ETB	End of Transmission Block
RES	Restore
RLF	Reverse Line Feed
RS	Render Stop
SM	Set Mode
SMM	Start of Manual Message
SO	Shift Out
SH	Shift In
SOS	Start of Heading
SP	Space
STX	Start of Text
SUB	Substitute
SYN	Synchronous Idle
TM	Tape Mark
UC	Upper Case
VT	Vertical Tab

Special Graphic Characters	
€	Cent Sign
,	Period, Decimal Point
<	Less-than Sign
(Left Parenthesis
+	Plus Sign
)	Right Parenthesis
&	Ampersand
!	Exclamation Point
\$	Dollar Sign
*	Asterisk
^	Right Arrow
:	Semicolon
=	Logical NOT
-	Minus Sign, Hyphen
/	Slash
:	Vertical Line
,	Comma
%	Percent
_	Underscore

Table 3. EBCDIC Table.

VI. REFERENCES

- [1] Microprocessor-to-System/370 Interface, Robert W. Lilley, NASA TM-55 (Revised), Avionics Engineering Center, Department of Electrical Engineering, Ohio University, April, 1978.
- [2] OS/VS - DOS/VS - VM/370 Assembler Language, GC33-4010-4, International Business Machines Corporation, February, 1975.
- [3] Deen, Bill R., Kenneth Muchow, and Anthony Zeppa, "Digital Computer Circuits and Concepts", Reston Publishing Company, Inc., Reston Virginia, 1974, pg. 56.
- [4] IBM System/370 Principles of Operation, GA22-7000-5, International Business Machines Corporation, August 1976, pg. 288.

VII. APPENDICES

A. Program Listing for Microcomputer Control Program.

```
***** THIS PROGRAM IS DESIGNED FOR RUNNING ON THE JOLT/MEMODYNE ***** UNL00010
* SYSTEM FOR RECOVERING DATA STORED ON THE DIGITAL TAPE. * UNL00020
* THE DATA IS READ IN 40 BYTES AT A TIME AND STORED IN A * UNL00030
* BUFFER, THEN THE BUFFER IS SENT TO THE S/370 OVER THE JOLT'S * UNL00040
* SERIAL LINES. WITH ASCII CONVERSION, 80 BYTES ARE ACTUALLY * UNL00050
* SENT OVER THE SERIAL LINES. * UNL00060
* * UNL00070
* * UNL00080
* * UNL00090
* * UNL00100
* * UNL00110
***** J.P. FISCHER 08/1980 ***** UNL00120
* * UNL00130
* * UNL00140
PIAA EQU $4000 ADDRESS OF PIA SIDE A UNL00150
PIAB EQU $4002 ADDRESS OF PIA SIDE B UNL00160
WRT EQU $72C6 JOLT WRITE DATA TO SERIAL OUT LINE UNL00170
WROB EQU $72B1 UNL00180
MPB EQU $6E02 PIA B FOR SERIAL I/O WORK UNL00190
MCLK1T EQU $6E04 PIA TIMER UNL00200
MCLKRD EQU $6E04 SAME AS ABOVE UNL00210
MCLK1F EQU $6E05 SOME MORE TIMER STUFF UNL00220
MAJCRT EQU $EA UPPER 8 BITS OF BAUD RATE UNL00230
HINCRT EQU $EB LOWER 8 BITS OF BAUD RATE UNL00240
TAPESY EQU %00000010 PATTERN FOR TAPE SYNC CHECK UNL00250
BOT EQU %000000100 PATTERN FOR BOT/EOT CHECK UNL00260
LF EQU %00010000 PATTERN FOR LOAD FORWARD FUNCTION UNL00270
REW2 EQU %00100000 PATTERN FOR REWIND OPERATION UNL00280
START EQU %10000000 PATTERN FOR INITIATING START UNL00290
*
ORG 0 UNL00300
XTEMP BSS 1 TEMPORARY FOR X UNL00310
YTEMP BSS 1 TEMPORARY FOR Y UNL00320
BUFFER BSS 40 UNL00330
UNL00340
UNL00350
UNL00360
*
ORG $200 UNL00370
JSR INIT SET UP PIA FOR MEMODYNE UNL00380
LDA PIAB PREPARE TO CHECK BOT UNL00390
AND =BOT SEE IF ON LEADER UNL00400
BNE NOTBOT IF NOT, THEN OK UNL00410
LDA PIAB GET SIDE B UNL00420
EOR =LF CLEAR LOAD FORWARD BIT UNL00430
ORA =START SET START BIT HIGH UNL00440
STA PIAB AND STORE TO LOAD FORWARD UNL00450
BTLOOP LDA PIAB GET STATUS UNL00460
AND =BOT SEE IF STILL ON LEADER UNL00470
BEQ BTLOOP CONTINUE TESTING UNTIL OFF UNL00480
LDA PIAB UNL00490
ORA =LF SET LOAD FORWARD HIGH UNL00500
STA PIAB AND REPLACE UNL00510
IDLOOP LDA PIAB UNL00520
AND =BOT NOW LOOP UNTIL AT READY POINT UNL00530
BNE LDLOOP KEEP GOING UNTIL ON HOLE UNL00540
LDA PIAB UNL00550
```

	EOR =LF	SET LOAD FOWARD LOW TO MOVE	UNL00560
	STA PIAB	OFF OF HOLE	UNL00570
TLOOP	LDA PIAB		UNL00580
	AND =BOT		UNL00590
	BEQ TLOOP		UNL00600
	LDX =\$80	TIMER ROUTINE	UNL00610
TIMXT	DEX		UNL00620
	BNE TIMXT	KEEP LOOPING UNTIL OUT	UNL00630
	ORA =LF	NOW RETURN LOAD FOWARD	UNL00640
	STA PIAB	HIGH, SHOULD BE OFF OF HOLE	UNL00650
NOTBOT	LDA =0	CLEAR ACCUM. AND SET	UNL00660
	STA PIAA+1		UNL00670
	STA PIAB+1		UNL00680
	LDA =\$P8		UNL00690
	STA PIAB		UNL00700
	LDA =0		UNL00710
	STA PIAA		UNL00720
	LDA =\$FF		UNL00730
	STA PIAB+1		UNL00740
	STA PIAA+1		UNL00750
	LDA PIAB		UNL00760
	AND =%11110111		UNL00770
	STA PIAB		UNL00780
*			UNL00790
*		NOW INITIALIZE THE 370 AND START	UNL00800
*		SENDING DATA.	UNL00810
*			UNL00820
LPLDS	LDX =0	READ 40 CHARACTERS FROM TAPE	UNL00830
L80	JSR READ	GET A BYTE FROM RECORDER	UNL00840
	STA BUFFER,X	SAVE IN OUTPUT BUFFER	UNL00850
	INX	DO ANOTHER ONE	UNL00860
	CPX =40	DONE 40 BYTES YET?	UNL00870
	BNE L80	IF NOT, DO AGAIN	UNL00880
	LDY =40	SEND THESE 40	UNL00890
	JSR W370	SEND TO SYSTEM	UNL00900
	JMP LFLDS		UNL00910
*			UNL00920
*		INITIALIZATION FOR PIA	UNL00930
*			UNL00940
INIT	LDX =0		UNL00950
	STX PIAA+1		UNL00960
	STX PIAB+1		UNL00970
	STX PIAA		UNL00980
	LDA =\$B8		UNL00990
	STA PIAB		UNL01000
	LDA =\$FF		UNL01010
	STA PIAA+1		UNL01020
	STA PIAB+1		UNL01030
	LDA =0		UNL01040
	ORA =LF		UNL01050
	ORA =REW2		UNL01060
	STA PIAB		UNL01070
	RTS		UNL01080
*			UNL01090
*			UNL01100

```
*****
* THIS IS THE READING PORTION OF THE PROGRAM TO RECOVER * UNL01110
* DATA FROM THE RECORDER AND PLACE IN THE MICROCOMPUTER'S * UNL01120
* MEMORY. * UNL01130
* UNL01140
* UNL01150
* UNL01160
***** UNL01170
* UNL01180
* UNL01190
READ LDA PIAB UNL01200
    ORA =START SET START HIGH UNL01210
    STA PIAB UNL01220
RDLP LDA PIAB UNL01230
    AND =TAPESY WAIT UNTIL SYNC IS HIGH UNL01240
    BEQ RDLP UNL01250
    LDA PIAB UNL01260
    EOR =START SET START LOW AGAIN UNL01270
    STA PIAB UNL01280
INLP1 LDA PIAB UNL01290
    AND =TAPESY WAIT UNTIL SYNC IS LOW UNL01300
    BNE INLP1 UNL01310
    LDA PIAA GET THE DATA FROM RECORDER UNL01320
    RTS UNL01330
*
* UNL01340
* UNL01350
***** UNL01360
* UNL01370
* THIS SUBROUTINE OUTPUTS A LINE OF CHARACTERS TO THE S/370. * UNL01380
* THE ADDRESS OF THE BUFFER IS IN PAGE ZERO AND IS * UNL01390
* INDEXED BY THE X-REGESTER. THE LENGTH OF THE BUFFER * UNL01400
* TO BE SENT IS CONTAINED IN THE Y-REGESTER. AFTER THE * UNL01410
* BUFFER IS SENT, A 'CR' IS SENT THEN THE PROGRAM WAITS * UNL01420
* FOR THE CONTROL CHARACTERS BETWEEN THE 'CR' AND PERIOD * UNL01430
* TO BE SENT BACK, THEN WAITS FOR THE CONTROL * UNL01440
* CHARACTER AFTER THE PERIOD INDICATING THE S/370 * UNL01450
* IS IN THE READ STATE. * UNL01460
* UNL01470
***** UNL01480
* UNL01490
W370 LDX =0 POINT TO FIRST CHARACTER UNL01500
    STX XTEMP ZERO X-TEMP SPACE UNL01510
    STY YTEMP SAVE LENGTH UNL01520
WLOOP LDX XTEMP GET POINTER UNL01530
    LDA BUFFER,X GET A CHARACTER UNL01540
    JSR WROB SEND IT UNL01550
    INC XTEMP X+1 UNL01560
    DEC YTEMP LESS ONE CHARACTER UNL01570
    BNE WLOOP GO AGAIN IF NOT DONE UNL01580
    LDA =$D CARRIAGE RETURN UNL01590
    JSR WRT TELL 370 THIS IS END-OF-LINE UNL01600
SCANP JSR RDT READ JUNK FROM SYSTEM UNL01610
    CMP =$2E PERIOD UNL01620
    BNE SCANP UNL01630
    JSR RDT LOOK FOR UNL01640
    CMP =$11 DC1 UNL01650
```

BNE SCANP	UNL01660	
RTS	UNL01670	
*	UNL01680	
*	UNL01690	
*****	UNL01700	
*	* UNL01710	
*	* UNL01720	
HIGH SPEED REWIND.	* UNL01730	
*****	UNL01740	
*	UNL01750	
JSR INIT	UNL01760	
LDA =\$B8	UNL01770	
STA PIAB	UNL01780	
LDA =LF	UNL01790	
STA PIAB	UNL01800	
ORA =REW2	UNL01810	
STA PIAB	UNL01820	
BRK	UNL01830	
*	UNL01840	
*	UNL01850	
*****	UNL01860	
*	* UNL01870	
*	* UNL01880	
MODIFIED JOLT READ ROUTINE.	* UNL01890	
THIS ROUTINE IS IDENTICAL TO THE ORIGINAL 'RDT' ROUTINE	* UNL01900	
AT ADDRESS \$72E9, BUT THIS ROUTINE OPERATES IN HALF-	* UNL01910	
DUPLEX RATHER THAN FULL-DUPLEX MODE.	* UNL01920	
*****	UNL01930	
*	UNL01940	
RDT LDX =8	UNL01950	
*	UNL01960	
RDT1 LDA MPB	WAIT FOR START BIT	UNL01970
LSR A		UNL01980
BCC RDT1		UNL01990
*	UNL02000	
JSR DLY1		UNL02010
*	UNL02020	
RDT2 JSR DLY2		UNL02030
LDA MPB	CY = NEXT BIT	UNL02040
LSR A		UNL02050
*	UNL02060	
PHP	SAVE BIT	UNL02070
TYA	Y CONTAINS CHAR BEING FORMED.	UNL02080
LSR A		UNL02090
PLP	RECALL BIT	UNL02100
BCC RDT4		UNL02110
ORA =\$80	ADD IN NEXT BIT	UNL02120
RDT4 TAY		UNL02130
DEX		UNL02140
BNE RDT2	LOOP FOR 8 BITS	UNL02150
EOR =\$FF	COMPLEMENT DATA	UNL02160
AND =\$7F	CLEAR PARITY	UNL02170
JSR DLY2		UNL02180
CLC		UNL02190
*	UNL02200	

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DLY2	JSR DLY1	UNL02210
*		UNL02220
DLY1	PHA SAVE FLAGS AND A	UNL02230
	PHP	UNL02240
	TXA	UNL02250
	PHA SAVE X	UNL02260
	LDX MAJCRT	UNL02270
	LDA MINCRT	UNL02280
*		UNL02290
DL3	STA MCLK1T	UNL02300
	LDA MCLK1F	UNL02310
	BPL DL3	UNL02320
	DEX	UNL02330
	PHP	UNL02340
	LDA MCLKRD RESET TIMER INT FLAG	UNL02350
	PLP	UNL02360
	BPL DL3	UNL02370
*		UNL02380
	PLA	UNL02390
	TAX	UNL02400
	PLP	UNL02410
	PLA	UNL02420
	RTS	UNL02430
*		UNL02440
*		UNL02450
	ORG \$FFFF	UNL02460
	HEX 00,02	UNL02470
*		UNL02480
	END	UNL02490

B. Program Listing for S/370 Control Program.

TITLE 'UNLOAD\$S: READS RECORDS FROM MEMODYNE/MICROCOMPUTER INTERFACE AND STORES ON DISK.' UNL00010
PRINT NOGEN UNL00020
SPACE UNL00030

***** UNL00040
***** UNL00050

* * UNL00060
* THIS PROGRAM IS DESIGNED TO BE RUN ON THE S/370 IN CON- * UNL00070
* JUNCTION WITH THE MICRO 'UNLOAD' PROGRAM AND THE MEMODYNE/ * UNL00080
* MICROCOMPUTER HARDWARE INTERFACE. RECORDS READ FROM * UNL00090
* TAPE BY THE MICRO ARE SENT TO THE 370 IN ASCII, CP THEN * UNL00100
* TRANSLATES THESE TO EBCDIC WHICH MUST BE TRANSLATED * UNL00110
* BACK TO HEX BY THIS PROGRAM. 80 BYTES ARE SENT AT A * UNL00120
* TIME (40 EQUIVALENT HEX CHARACTERS) AND 80 HEX CHARACTERS * UNL00130
* ARE STORED ON THE DISK FILE. * UNL00140
* * UNL00150
* * UNL00160
* * UNL00170

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***** UNL00180
SPACE 2 UNL00190
UNLOAD\$S START X'E000' UNL00200
USING UNLOAD\$S,12 UNL00210
MVI FLAGS,0 CLEAR ALL FLAG BITS UNL00220
LA 1,8(,1) POINT TO FILE NAME FIELD UNL00230
LR 2,1 SAVE PLIST ADDRESS UNL00240
CLI 0(1),X'FF' BLANK ? UNL00250
BE NOID IF SG, ERROR UNL00260
LA 1,8(,1) UNL00270
CLI 0(1),X'FF' NO FILETYPE? UNL00280
BE NOID IF NCT, ERROR UNL00290
MVC FILEID+8(16),0(2) MOVE PARTIAL ID UNL00300
LA 1,8(,1) UNL00310
CLI 0(1),X'FF' NO FILEMODE UNL00320
BE NOMODE IF NCT SUBSTITUTE 'A' UNL00330
MVC FILEID+24(2),16(2) MOVE IN NEW MODE UNL00340
B CHECK CONTINUE UNL00350
NOMODE MVI FILEID+24,C'A' MOVE IN 'A' UNL00360
MVI FILEID+25,C' ' UNL00370
SPACE UNL00380
CHECK LA 1,8(,1) MOVE POINTER UP SOME MORE UNL00390
CLI 0(1),X'FF' SEE IF ANYTHING THERE UNL00400
BE CHECK1 IF NOT, CONTINUE UNL00410
CLI 0(1),C'(' SEE IF OPTION UNL00420
BNE PARMERR IF NOT, BAD PARM UNL00430
LA 1,8(,1) NEXT FIELD UNL00440
CLI 0(1),X'FF' SEE IF BLANK UNL00450
BE CHECK1 UNL00460
CLC 0(8,1),OPTREP SEE IF REPLACE OPTION UNL00470
BNE BADOPT IF NOT, CONTINUE UNL00480
OI FLAGS,1 SET REPLACE BIT UNL00490
SPACE UNL00500
CHECK1 TM FLAGS,1 SEE IF REPLACE IN EFFECT UNL00510
BZ OPENF IF NOT, GO ON UNL00520
OPENF FSERASE FSCB=FILEID UNL00530
FSOPEN FSCB=FILEID UNL00540
CL 15,F36 SEE IF INVALID DISK UNL00550

BE	INVDISK	UNL0056J	
SPACE	2	UNL0057J	

*		UNL0058J	
*	THIS PART OF THE PROGRAM CAUSES A TERMINAL	* UNL0059J	
*	READ TO GET THE ASCII CHARACTERS, THEN TRANSLATES	* UNL0060J	
*	THEM TO HEX AND STORES ON DISK.	* UNL0061J	
*		* UNL0062J	
*		* UNL0063J	

SPACE		UNL0064J	
SLR	4,4	UNL0065J	
SLR	7,7	UNL0066J	
LA	2,WBUF	UNL0067J	
LA	4,8	UNL0068J	
L	5,IBUF80	UNL0069J	
RDLOOP	LA 3,IBUF	UNL0070J	
	RDTERM IBUF	UNL0071J	
	LTR 0,0	UNL0072J	
	BZ DONE	UNL0073J	
	WAITT	UNL0074J	
	TR IBUF(80),TRTBL	UNL0075J	
STRIPZ	MVC TEMP(8),0(3)	UNL0076J	
	PACK TEMP1(5),TEMP(9)	UNL0077J	
	MVC 0(4,2),TEMP1	UNL0078J	
	LA 2,4(2)	UNL0079J	
	BXLE 3,4,STRIPZ	UNL0080J	
	SPACE	UNL0081J	
	RDTERM IBUF	UNL0082J	
	LTR 0,0	UNL0083J	
	BZ DONE1	UNL0084J	
	WAITT	UNL0085J	
	LA 3,IBUF	UNL0086J	
	TR IBUF(80),TRTBL	UNL0087J	
Z1	MVC TEMP(8),0(3)	UNL0088J	
	PACK TEMP1(5),TEMP(9)	UNL0089J	
	MVC 0(4,2),TEMP1	UNL0090J	
	LA 2,4(2)	UNL0091J	
	BXLE 3,4,Z1	UNL0092J	
	LA 2,WBUF	UNL0093J	
	FSWRITE FSCB=FILEID	UNL0094J	
	LTR 15,15	UNL0095J	
	BNZ WRTERR	UNL0096J	
	LA 7,1(7)	UNL0097J	
	B RDLOOP	UNL0098J	
	SPACE	UNL0099J	
DONE1	MVI WBUF+40,0	UNL0100J	
	MVC WBUF+41(39),WBUF+40	PREPARE TO CLEAR	UNL0101J
	FSWRITE FSCB=FILEID	UNL0102J	
	LTR 15,15	UNL0103J	
	BNZ WRTERR	UNL0104J	
	LA 7,1(7)	UNL0105J	
	SPACE	UNL0106J	
*****			UNL0107J
*	NOW CLOSE THE FILE.	* UNL0108J	
*		* UNL0109J	
*		* UNL0110J	

FILE: UNLOADSS ASSEMBLE A

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FILE: UNLOAD\$S ASSEMBLE A

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TRTBL	DC XL16'F0F1F2F3F4F5F6F7F8F9F0F0F0F0F0F0'	UNL0166J
	EQU TBL-X'40'	UNL0167J
	END UNLOAD\$S	UNL0168J