NASA CASE NO. NPO-16,584-1CUPRINT FIG. 2b

## NOTICE

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NRO-JPL

(NASA-Case-NPC-16584-1-CU) METHOD OF MEASURING FIELD FUNNELING AND BANGE STRAGGLING IN SEMICONDUCTOR CHARGE-COLLECTING JUNCTIONS Patent Application (NASA) 19 p HC A02/MF A0

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JPL Case No. C-16584 CIT Case No. 1846 Docket No. 85/150

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# Origin of Invention

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The invention described herein was made in the performance of work under a NASA contract and is subject to the provisions of Public Law 96-517 (35 USC 202) in which the contractor has elected to retain title.

## Background of the Invention

This invention relates to a method of measuring electric-field funneling length created by irradiating a semiconductor charge-collecting junction with an electron-hole-pair generating charged particle, and more particularly to where the charged particle penetrates beyond the depletion region in the semiconductor layer and yet all the charge generated at the selected energy level is collected by drift rather than diffusion.

One form of semiconductor charge-collecting junction, a silicon surface (Schottky) barrier detector, is used extensively for both counting and energy measurement of charged particles. For heavy ions with limited range in silicon, a partially depleted detector can knowingly be used to accurately measure the ion kinetic energy, provided that the ion range does not exceed the width of the depletion layer. However, it is not generally known that the range of the ion may actually exceed the depletion width and still have all of the charge generated by the ion collected at the Schottky barrier. This is due to an extension of the electric field outside the depletion region along the end of the ion track, an occurrence

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which has been labeled "field funneling," and is recognized in an article by C.M. Hsieh, P.C. Murley, and R.R. O'Brien, "Collection of Charge from Alpha-Particle Tracks in Silicon Devices," IEEE Transactions on Electron Devices, Vol. ED-30, No. 6, pp. 686-693, June 1983.

In that article, the authors disclose collection time measurement apparatus in which a under test is biased and irradiated with an alphaparticle source. The charge collected is then transmitted through an FET probe to a wide band amplifier and displayed on a high frequency oscilloscope. The collection time was measured through the FET probe. When an alpha-particle struck the collecting structure, the voltage began to drop rapidly as electrons were collected. The waveform was a negative voltage amplitude proportional to the collected charge. waveform was amplified in a wide band amplifier and displayed on a high frequency real time oscilloscope. The pulse-heights were analyzed in a multichannel analyzer in a manner different from that employed in the present invention.

### Objects and Summary of the Invention

It is an object of this invention to provide a 25 method of measuring electric-field funneling length in a semiconductor charge-collecting junction at a junction bias voltage level at which the field funneling length is at a maximum.

An object of another aspect of the invention is to measure range straggling at a bias voltage level at which effects due to diffusion are minimized.

In accordance with the invention electric-field funneling length is measured while irradiating

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a semiconductor charge-collecting junction with electron-hole-pair generating charged particles first junction bias voltage level which is selected such that the ion penetration range is shorter than the depletion region so that all the charge generated at a selected energy level is collected by drift. The collected charge is measured in an energy band of a multichannel analyzer. The bias voltage is then reduced to a second level in order to reduce the depth of the depletion region such that the total charge can no longer be collected by drift and measured in the energy band previously displayed in the multichannel analyzer. This is done to obtain the point at which the charge is no longer fully collected which is representative of the maximum electric field funneling length which may be calculated by measuring the difference at the second bias voltage level of the depletion width and the ion penetration range.

In accordance with another aspect of the invention the bias voltage is further lowered to a third level at which the particles are collected over a spread of energy levels in the multichannel analyzer, while at least some of the particles are still collected at the selected energy level. From this the different depths of penetration of the particles are determined while additional effects due to diffusion are minimized.

The novel features that are considered characteristic of the invention are set forth with particularity in the appended claims. The invention will best be understood from the following description when read in connection with the accompanying drawings.

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# Brief Description of the Drawings

FIG. 1 is a schematic cross section for a partially depleted heavy ion silicon-surface-barrier detector showing electron-hole plasma generated along an ion track.

FIG. 2a is a one dimensional energy band diagram for a Schottky barrier in a silicon-surface-barrier detector with reverse bias and no ion track, and FIG. 2b illustrates an energy band diagram with an ion track of range R.

FIG. 3 is a plot of capacitance of a siliconsurface-barrier detector as a function of reverse bias voltage.

FIG. 4 is a range versus energy curve for alpha particles in silicon showing energies for Th-228 emission.

FIG. 5 is a block diagram of the electronic system used for measurement of charge collection from alpha-particle tracts in a silicon surface barrier detector.

FIGs. 6a, b, c, and d are photographs of oscilloscope traces displaying charge collection data for Th-228 alpha-particles in a silicon-surface-barrier detector at four different bias-voltage levels.

FIG. 7 is a table of silicon-surface-barrier detector charge-collection data in which runs 1, 4, 5, and 6 illustrating the data in FIGs. 6a, b, c, and d.

## Detailed Description of the Invention

The method of this invention measures electric field funneling length when irradiating a semiconductor charge-collecting junction generally with electron-hole-pair generating charged particles. A preferred embodiment involves the use of a silicon

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detector for the surface barrier semiconductor charge-collecting junction. One can observe the funneling effect experimentally in surface barrier detectors (hereafter SSBDs) simply by measuring the charge collected from an ion of tain energy and range as a function of detector bias potential. Differential capacitance measurements yield the depletion width as a function of bias voltage. By knowing the range of a monoenergetic ion, it can be observed that the plasma charge generated along an ion track is fully collected even when the depletion width is less than the ion range. as the depletion width is diminished by lowering the detector bias beyond a certain point, the narrow collected charge energy peak observed for a monoenergetic ion begins both to decrease in amplitude and to be dispersed in energy. This observation is indicative of the fact that the end-of-range charge is now being collected by diffusion rather than by an elec-20 tric field (or drift). Since diffusion is isotropic some of the charge is therefore lost and a decrease in energy amplitude occurs, and the amount of charge collected from individual ions is dispersed by range straggling of the particles, i.e., energy dispersion occurs. The latter manifestation is not observed if only individual ions are analyzed, i.e., if only individual waveforms from single ions are examined. Data have been obtained from ionized-charge collection in a partially depleted SSBD as a function of bias voltage. The purpose of these measurements is to observe the extent to which the electric field is extended outside the depletion region along an ion track, i.e., the electric field funnel length, and to observe the dispersion of a charge collection

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energy deposit due to range straggling of ion tracks in SSBDs.

SSBD 10 formed with a gold layer 11 on N-type silicon 12 , with a reverse or negative bias of magnitude V applied to the device 10 from terminal 13 to ground 14. A voltage-dependent depletion region of width  $W_{\rm SB}$  15 is created adjacent to the gold layer 11. An ion is illustrated penetrating the SSBD 10 with a range R along the line 16 creating an electron-hole plasma along the ion track as shown.

FIGs. 2a and 2b show a one-dimensional energyband diagram for the Schottky barrier. FIG. 2a represents the case for no ion track. The electric field created by the stationary ionized crystal lattice (space charge) is confined to the depletion When an ion penetrates the device the elecregion. tric field is reconfigured as shown in FIG. 2b. Note that the mobile electron-hole pairs or plasma created by the ion track cancel out the electric field originally contained in the depleted region illustrated in FIG. 2a. An electric field is set up at the end of the ion track such that the total potential drop remains constant  $(V_R + V)$ . This electric field  $\xi_{\text{FUNNEL}}(t)$  defines the spatial extent of the funnel and is highly time dependent. As time progresses after the penetration by the ion, the field retreats back to the original configuration of FIG. 2a. Charge is therefore collected by the total field which extends to the end of the track outside the depletion region. The dependence of the Schottky barrier depletion width on bias potential  $W_{SR}(V)$  plus the substrate doping  $N_s$  may be obtained from capacitance measurements by plotting  $1/C^2$  vs. V, where C is the differential capacitance of the SSBD. The results of

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capacitance measurements obtained from the SSBD used for the analysis of this embodiment are shown in FIG. 3 where N $_{\rm S}$  is the N-type substrate donor concentration.

In order to study the effects of field funneling and range straggling in a SSBD, a Th-228 radiation source was selected which emits alpha-particles with well-defined energies in the range of 5 MeV to 9 MeV and which exhibits reasonably long ranges in silicon (25 $\mu$ m to 60 $\mu$ m). These ranges are illustrated in FIG. 4. By choosing these relatively low mass ions, the effects of plasma-time delay and pulseheight defect are minimized.

Data had been obtained using the electronic system shown schematically in FIG. 5. 15 The charge collected from individual ions of the Th-228 alphaparticle emission source 20 which irradiates the SSBD is amplified in a pre-amplifier 22, biased by a detector-bias supply 23, and amplified further with pulse shaping in an amplifier and pulse-shaper 24. 20 The output of amplifier and pulse-shaper 24 is then analyzed in a multichannel pulse-height analyzer 25, the output of which is displayed on a display 26 in the form shown in the photographs of FIG. 6a through 6d illustrating the number of alpha particles counted versus the alpha-particle energy or collected charge. As the particles are counted the number of counts within each energy (collected charge) interval is recorded and displayed as shown in the photographs of FIGs. 6a, b, c, and d. The photograph in FIG. 6a shows the complete alpha-particle emission spectrum obtained from the Th-228 source as illustrated in The two largest energy peaks are 6.78 MeV and 8.78 MeV. Both photographs of FIGs. 6a and b show virtually identical spectra. The average range of an

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8.78 MeV alpha-particle in silicon is  $59\mu m$ . The depletion width  $W_{SB}$  for photographs of FIGs. 6a and 6b are  $61\mu m$  and  $44\mu m$ , respectively, as obtained directly from differential capacitance measurements as a function of bias voltage from FIG. 3. In photograph A the depletion width  $(61\mu m)$  is greater than the range  $(59\mu m)$  of the most penetrating 8.78 MeV alpha-particle. Therefore, all of the ionization charge is collected, i.e.:

$$Q_{C} = 8.78 \text{ MeV} = 0.390 \text{ pC}$$
 $22.5 \text{ MeV/pC}$ 

In the photograph of FIG. 6b, the depletion width  $44\mu m$  is  $15\mu m$  less than the longest range  $(59\mu m)$ . Since no charge is lost from the last  $15\mu m$  of the ion track outside the original depletion region, the electric field must be extended by that distance. This in effect represents a funnel length of  $59\mu m$  -  $44\mu m$  =  $15\mu m$  for 8.78 MeV alphas at this bias voltage (30 volts).

The table of FIG. 7 contains a complete set of data obtained for a gold N-type silicon SSBD. Observe that as the bias voltage and depletion width are further decreased as shown in the photograph of FIG. 6c, the 8.78 MeV peak decreases in energy at the peak by an amount  $\delta E_{MAX} = 0.21$  MeV. Furthermore, the energy spread becomes pronounced, i.e.,  $\Delta E = 0.4$  MeV. The value of  $\delta E_{MAX}$  represents the amount of charge lost at the end of the ion track owing to diffusion away from the Schottky barrier. The energy dispersion,  $\Delta E$ , reflects range-straggling at the end of the ion track, i.e., there is a variation in the distance traveled by individual ions due to straggling. The value of  $\Delta E = 0.4$  MeV represents a range variation of  $\Delta R = 2.0\mu m$  (see FIG. 4). The photograph of FIG. 6d

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illustrates the further diminuation of the charge collected from the 8.78 MeV alphas by a further reduction in the bias voltage to 11 volts. Photograph of FIG. 6d also shows that the 6.78 MeV alphas whose range equals  $40\mu m$  are experiencing charge loss due to the decreased depletion width of  $27\mu m$ , indicating a funnel length of approximately  $13\mu m$ .

The type of data obtained from SSBDs using the electronic system such as that illustrated in FIG. 5, and exemplified in FIGs. 6a through 6d and the table of FIG. 7, clearly indicates the role of field funneling and range straggling in the charge collected from ion tracks in silicon solid state devices. technique has an advantage over that of observing individual particle response; i.e., output voltage as a function of time, since the individual tracks exhibit different charge losses by diffusion due to range straggling. A disadvantage of this technique is that it does not directly measure the charge-collection time. However, charge-collection time measurements for times less than 1 ns are plagued by experimental difficulties. The data clearly show the effect of electric-field-enhanced charge collection, or field funneling. It should be emphasized that, in principle, the electric field along an ion track extends to the end of the track. Since the field diminishes with distance from the surface, charge loss due to diffusion becomes more pronounced as the Therefore, the concept of a distance increases. funnel length is somewhat nebulous, since it varies with particle mass, energy, detector bias, doping level, etc.

The method described herein may be used to measure charge collection in semiconductor junction devices ( $Q_{\mathbb{C}}$  in the table of FIG. 7) with both

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Schottky barriers (SSBDs) and P-N junctions (discrete devices and ICs). These measurements can be obtained with the commercially available electronic system illustrated in FIG. 5. The maximum field funnel length can be determined by detecting the point at which a portion of the charge is no longer collected by drift, i.e., the point at which the collected charge starts to move to the left in the photographs of FIG. 6, such is illustrated in photograph 6b. The electric-field funneling length is then calculated by measuring the difference, at the second bias voltage level at which this happens, of the depletion width and the ion penetration range. Range straggling may be measured by further lowering the bias voltage to a level at which the particles are collected over a spread of energy while at least some of them are still collected at the selected energy as shown in FIG. 6c, where the charge collected for the 8.78 MeV alpha-particles ranges downward but up to the 8.78 MeV energy level. In the photograph of FIG. 6d the 20 charge collected from the 8.78 MeV alphas is further dispersed and contains the effect due to diffusion and other effects which tend to make this data less useful.

The measurements of charge collected from ion tracks in semiconductor devices is of utility in the assessment of single event upset in IC chips. conclusion, the technique of this invention may be used to measure both prompt and delayed charge collection from ion tracks in semiconductor devices by varying the device bias potential, the ion energy, and the ion species. The device or SSBD in which the charge collection was observed is constructed from low-doped N-type ( $N_S = 2 \times 10^{13}$  cm) silicon and is operated at large reverse-bias potential up to 60

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volts. For more highly doped silicon materials and lower bias voltages, the junction depletion width will be smaller. Therefore, for junction devices contained in integrated circuits, the differentiation between field-aided (drift) charge collection and that which occurs by diffusion will not be as clear as for the SSBD. Devices made from materials other than silicon (e.g., GaAs) will behave differently from either the SSBD or the silicon integrated circuits. Charge collection from ions heavier than alpha-particles will exhibit plasma-time and pulseheight effects not seen with the light He nuclei.

Since the principles of the invention have now been made clear, modifications which are particularly adapted for specific situations without departing from those principles will be apparent to those skilled in the art. Consequently, the appended claims are intended to cover such modifications as well as the subject matter described, and to be limited only to the true spirit of the invention.

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# METHOD OF MEASURING FIELD FUNNELING AND RANGE STRAGGLING IN SEMICONDUCTOR CHARGE-COLLECTING JUNCTIONS

### ABSTRACT OF THE DISCLOSURE

Electric-field funneling length is measured while irradiating a semiconductor charge-collecting junction with electron-hole-pair generating charged particles at a first junction bias voltage. The bias voltage is then reduced to a second level in order to reduce the depth of the depletion region such that the total charge can no longer be collected by drift and measured in the energy band previously displayed in the multichannel analyzer. This is representative of the maximum electric field funneling length which may be calculated by measuring the difference at the second bias voltage level of the depletion width and the ion penetration range. The bias voltage is further lowered to a third level at which the particles are collected over a spread of energy levels while at least some of the particles are still collected at the selected energy level. From this the different depths of penetration of the particles are determined while additional effects due to diffusion are minimized.

#### AWARDS ABSTRACT

Inventor: John A. Zoutendyk

JPL Case No. 16584

NASA Case No. NPO-16584-1-CU

Contractor: JET PROPULSION

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November 13, 1985

METHOD OF MEASURING FIELD FUNNELING AND RANGE STRAGGLING IN SEMICONDUCTOR CHARGE-COLLECTING JUNCTION

This invention relates to a method of measuring electric field funneling length created by irradiating a semiconductor charge-collecting junction with an electron-hole pair generating charged particle, particularly where the charged particle penetrates beyond the depletion region in the semiconductor layer and yet all the charge generated at the selected energy level is collected by drift rather than diffusion.

FIG. 1 illustrates a schematic cross section for a partially depleted heavy ion silicon surface barrier detector showing electron-hole plasma generated along an ion FIGs. 2a and 2b show a one dimensional energy band diagram for the Schottky barrier and a silicon surface barrier detector, such as in FIG. 1, having reverse bias; FIG. 2a illustrating no ion track and FIG. 2b an ion track of range R. FIG. 3 illustrates a plot of capacitance of a SSBD as a function of reverse bias voltage from which  $\mathbf{W}_{\text{SB}}$  of FIG. 2b may be calculated as a function of capacitance. illustrates a typical range versus energy curve for alphaparticles in silicon showing energies for Th-228 emission. FIG. 5 illustrates the apparatus employed in the method of the present invention in schematic diagram form. The output of the multichannel analyzer 25 is displayed on a CRT display 26 in the form shown in FIG. 6 for different bias voltillustrating the charge-collection data for Th-228 alpha-particles in a SSBD. FIG. 7 is a table of SSBD charge-collection data for the four bias voltage levels of FIG. 6 and two additional bias voltage levels. field funneling length is measured while irradiating a semiconductor charge-collecting junction 21 of FIG. 5, electron-hole pair generating charged particles from Th-228 source 20 at a first junction bias voltage level, which is selected such that the ion penetration range is shorter than the depletion region such is illustrated in FIG. 6a so that all the charge generated at a selected energy level is collected by drift. The collected charge is measured in a band of a multichannel analyzer 25 of FIG. 5. The bias voltage is then reduced to a second level in order to reduce the depth of the depletion region such that total charge can no longer be collected by drift and measured in the band previously employed in multichannel analyzer 25. This is done to obtain the point at which the charge is no longer fully collected, which is representative of the maximum electric field funneling length that which may be calculated by measuring the difference at the second bias voltage level, such as illustrated in FIG. 6b of the depletion width and the ion

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\_\_91109 (Zip) penetration range. Another aspect of the invention involves reducing the bias voltage further to a third level such as illustrated in FIG. 6c at which the particles are collected over a spread of energy levels in the multichannel analyzer 25 while at least some of the particles are still collected at the selected energy level. From this the different depths of penetration of the particles are determined while additional effects due to diffusion are minimized.

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This technique may be used to measure both prompt and delayed charge collection from ion tracks and semiconductor devices by varying the device bias potential, the ion energy, and the ion species. The measurement of charge collected from ion tracks and semiconductor devices is of great utility in the assessment of single event upset in IC chips.

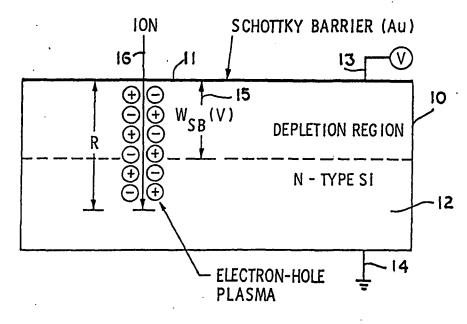
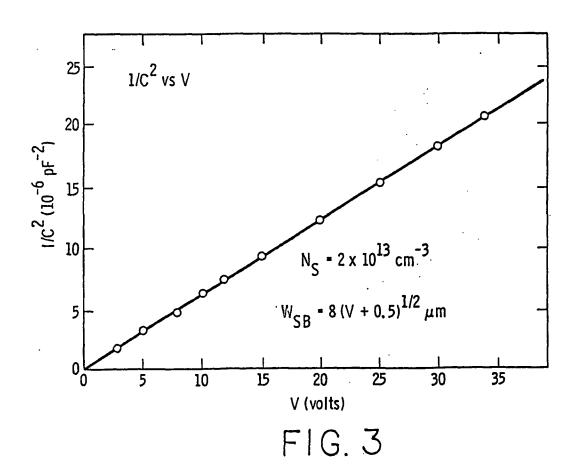
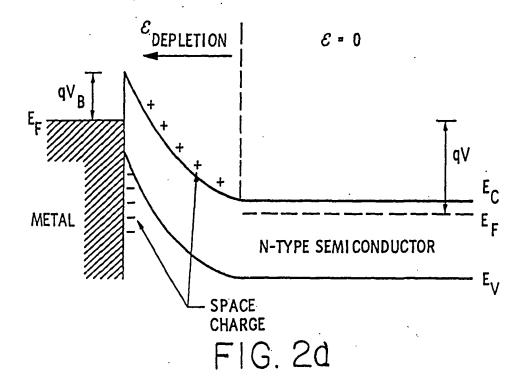


FIG. 1





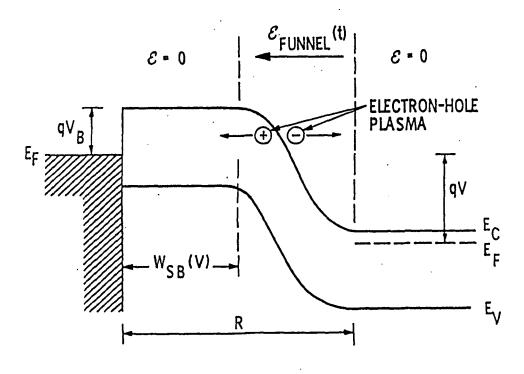
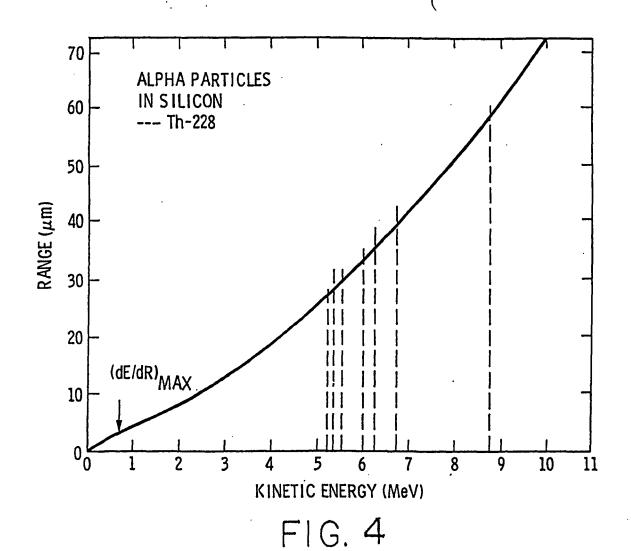
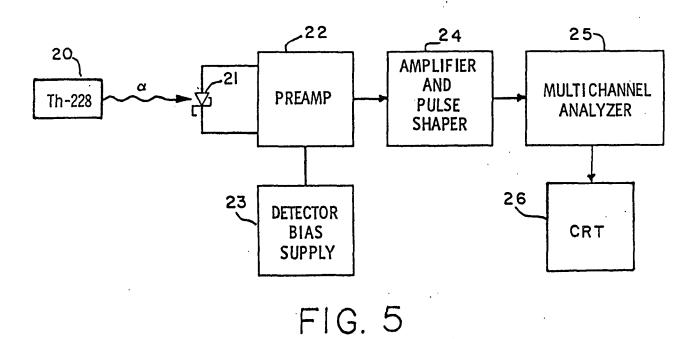
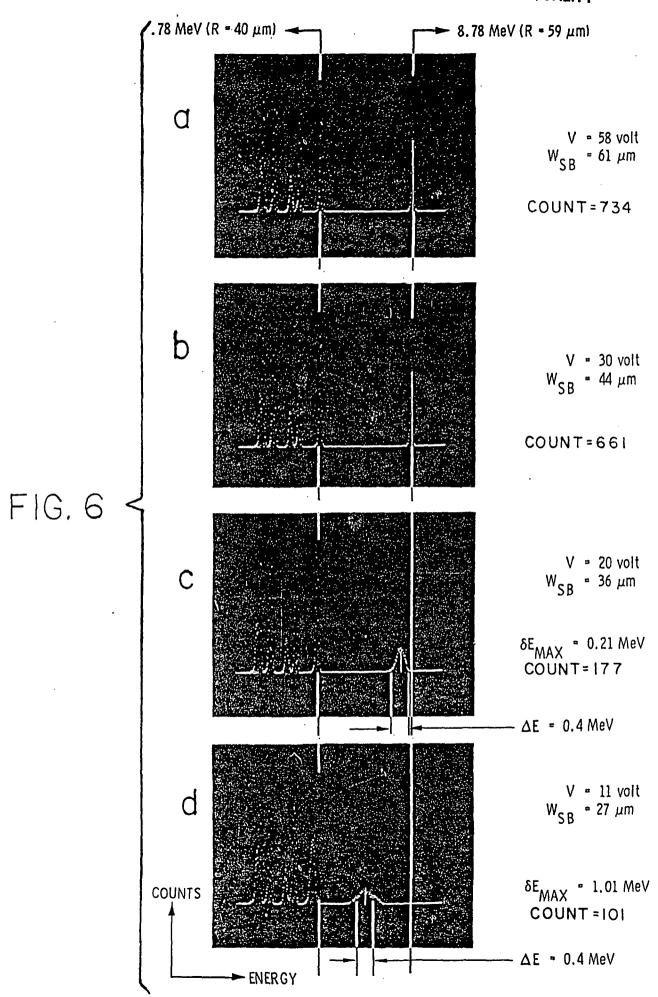


FIG. 2b







RUN	RUN PHOTO		W <sub>SB</sub> (μm) <sup>(2)</sup>	BIAS(V) $W_{SB}(\mu m)^{(2)} R^{(3)} - W_{SB}(\mu m) \delta E_{MAX}(MeV) \Delta E(MeV) Q_C(pC)^{(4)}$	SE <sub>MAX</sub> (MeV)	ΔE (MeV)	Q <sub>C</sub> (pC) <sup>(4)</sup>
~-	Ą	58	19	0>	< 0.01	< 0.1	0.390
2	ı	48	. 55	4	< 0.01	< 0.1	0.390
	ı	39	20	6	< 0.01	<0.1	0.390
4	മ	30	44	15	0.03	<0.1	0.389
2	ပ	20	36	ន	0.21	0.4	0.381
9	Q	11	12	32	1.01	0.4	0.345
	(1) (2) (3) (4)	Au/N-TYPE Si; $W_{SB} = 7.99 (V)$ FOR 8.78 MeV $C_{C}(PC) = E(Me)$		n-3 i, R = 5	FROM CAPACITANCE MEASUREMENTS	MEA SUREME	NTS

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