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Electro-Mechanical Actuator DC Resonant Link Controller

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National Aeronauticsand Space Administration

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Electro-Mechanical Actuator Final Report

Contract NAS3-25799

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INTRODUCTION 1.

1.1 Scope

This report summarizes the work **performed** on the 68 HP **electro-mechanical** actuator (EMA) system developed on NASA contract NAS3-25799. The 70 HP system **consists** of a motor **controller** system and a linear actuator **capable** of up to 32,000 lbs loading. The system is designed to demonstrate the **capability** of large, **high** power linear actuators for applications such as Thrust Vector Control (TVC) on rocket engines. The baseline design is the Advanced Launch System (ALS) L vehicle preliminary **concept.** This system utilizes a resonant power **converter** that operates at a nominal frequency, of 55 kHz.

. SYSTEM DESCRIPTION

The electromechanical actuator system, shown in figure 1, consists of an input power source, a motor controller, a high efficiency induction motor, and an linear actuator. The EMA system is capable of forces up to 32,000 lb. at velocities up to 7.4 in/sec. The three phase induction motor is capable of up to 68 HP peak to accelerate the motor rotor and associated actuator and load inertias. The peak actuator output is 32.8 HP to the load.

Figure 1. Electromechanical actuator

2.1 Actuator

The actuator was **originally** designed by Moog Aerospace under a Moog internal R&D program to meet the Space Shuttle Main **Engine** Thrust Vector Control (TVC) requirements. The actuator **converts** the rotary motion of the induction motor to linear motion and is **capable** of up to 48,000 lbs. of stall loading. Impulse loads of up to 100,000 Ibs will not damage the unit. The design was modified to increase the maximum linear rate as required by the **ALS** system from 5.2 in/sec to 7.4 in/sec and likewise the **maximum** force required is reduced from 48,000 Ibs. to 32,000 lbs. The 14,700 rpm top speed of the **motor** translates to the maximum 7.4 inches per second of linear motion. The range of motion is \pm 5.5 inches with a rated acceleration of 60 in/sec/sec. **An** LVDT (Linear Variable Displacement Transducer) is used to measure the linear motion of the actuator. The LVDT is **mounted** in **the** housing of the actuator and is **connected** to the motor **controller** by a dual twisted pair **cable.** The LVDT accepts a sinusoidal reference signal and returns a sinusoidal drive signal that varies in phase and amplitude, dependent on the linear position of the actuator. These signals are processed by the motor controller electronics. A load cell is also installed that will measure actuator **compressive** and tensile force.

2.2 Motor

The motor is a Sundstrand designed, AC induction motor **capable** of 68 peak HP. It was designed to provide a **high** power to weight ratio and attains about 3.5 peak HP per pound with a weight of 19.6 lbs. The motor is a six pole, 3 phase design with a maximum synchronous speed of 15,000 rpm. Cooling is achieved by **conduction** through the motor bearings and housing to the actuator body. The motor is designed to require no additional **cooling** when operating in its TVC environment for a typical 10 **minute ALS** mission. Forced air cooling may be used during extended laboratory testing by **connecting** two air fittings located on the top of the motor to filtered shop air. The motor's magnetics are constructed from Hyperco-50. A Harowe resolver (#21BRCX-335-512) is mounted on the motor to provide motor angular position. The resolver is a **six** terminal device with a reference frequency input and a sine and cosine output. The angular position of the rotor is determined by **comparing** the input reference with the output signals. There is **also a** thermocouple **embedded** in the **motor's stator which may be used** to **measure motor temperature. The rotor's** temperature **may be measured through an access hole located on the motor's rear housing. A power output of 35 HP at** 750 Hz **is specified with an input current of 106 A/phase. The peak power output of 68** HP **at** 750 Hz **is predicted with an input current of 210 A/phase.**

2.3 Controller Hardware Description

The hardware configuration of the motor controller is shown in Figure 2. The hardware is divided into two subsections. The first section is a VME based card rack containing the computer processing elements. The second section is the power stage which contains the power components and the measurement and link control functions. Communication between the two sections is via two fiber optic lines.

Figure 2. Controller hardware configuration

The software development and user interface board is a VME based **'386** PC **manufactured by Radysis. All 320C30 DSP software is developed on** this PC **and then downloaded** to the **DSP board over** the **VME bus. The** PC **also serves as the user interface** that **allows** the **user** to **issue commands** and **to vary controller gains and parameters. The** PC then issues these **commands** to the **DSP controller over** the **VME bus.**

The DSP board is a stacked board set **consisting** of **a motherboard** and two daughter-boards. The motherboard **contains** a single 320C30 processor, the VME interface drcuitry, high speed SRAM for the 320C30, and the VME dual ported memory. The first daughter-board **contains** another 320C30 processor and its associated SRAM while the second daughterboard contains a high speed digital I/O interface. The DSP board has been set up to be the VME bus **controller** and arbitrator with the PC being a slave. All motor controller functions, excluding resonant **link control,** are performed by the DSP board. The **control** provided by the DSP on the motherboard includes current regulation and **command** execution while the control provided by the DSP on the daughterboard includes position, rate, or torque regulation, and **command** function **generation.**

The DSP interface board contains the fiber optic **interface** to the **power stage and** the **digital interface** to **the DSP board. The board** takes the **data from** the **fiber optic** receiver **and converts it** to **32 bit data** (the **upper 16 bits are zero), which is** then **transferred** to the **DSP I/O board. The board also** takes **the switch output commands from** the **DSP I/O board and writes** them **to the fiber optic transmitter.**

The VME I/O board contains an external analog command input **and 4 analog output ports. The external command input** is **a +6V signal** that **can be used for position, rate, or torque commands. The analog outputs are used for monitoring and testing** the **controller and can be set** to **look at a number of points in the system.**

The measurement board in the **power stage, along with the DSP board interface,** is **shown in figure 5. The measurement board contains** two **parallel A/D channels and associated signal conditioning,** resonant **link control circuitry, a switch output command** interface, **and resolver and LVDT converter circuits. At a fixed** time **before each zero voltage transition during** the **resonant cycle, motor, controller, and actuator sensor data is sampled and digitally converted,** then **is** transmitted **serially via a fiber optic cable. As soon as the data** is **received by** the **DSP board, the current** regulator **code** is **run on the primary DSP and** the new **switch commands are send back** to **the power stage via a second fiber optic cable. The switch commands are** then **sent to the switch drive board which provides** the **proper signal levels for** the **IGBT's.** The **current regulation** function takes **approximately 6.5** usec from the time that the **currents** are sampled to the time the new switch **commands** are issued.

Figure 3. Measurement board and DSP interface

° SYSTEM SOFTWARE

3.1 PC Interface Software

> **All** software for the Radysis PC has been developed using Microsoft C version 6.0. The program is a menu driven text based program that is loaded into memory from the hard disk at run time. The modules that make up the program are listed below:

The **directories for** the various files are as follows:

To modify the program (i.e. **control.exe) simply modify the appropriate module and/or header file and type** *make control* **at the command line while in the source code directory. This will** run **the Microsoft make utility NMAKE using the make file named control.mak.**

3.2 DSP Software

Software for the 320C30 processors on the Pentek **DSP board has been developed using TIC and assembly language version 4.1.** *C* **is used where timing** is **noncritical. The DSP control program is created in** two **separate modules for each of the two processors. The 4283 board is the motherboard and** the **4244 is** the **coprocessor daughterboard. The modules** that **make up** the **program are listed below.**

The directories for the various **files are as** follows:

To modify the **DSP program simply modify** the **appropriate module and/or header file. To compile and link all the modules** type **either** *make83* **or** make44 **depending on which processor code is being modified. If you compile the file directly,** then **adding a -l to** the *make*** **command will only link the .obj files and will** not **compile** (e.g. *make83* **-l). The** make83 **and** *make44* **batch commands must be executed while** in **the source code directory.**

Since there is no access to the 4244 program memory, the 4244 code must first be loaded into the 4283 memory and then the 4283 code will load the 4244 code. For this reason, the initialization address and size of the 4244 code must be determined from the **code44.map** file and then placed in the **move83.h** header file every time the 4244 code is modified. Since the **move83.h** header file is modified, *make83* -l needs to be run which will automatically recompile **move83.c.** If only the 4283 code is modified, there is no need to run *make44.*

After compiling and linking the DSP code it must then **be loaded into program memory. This is done by** typing *load_dsp* **at** the **command line. It does** not **matter which directory you do** this **from. This batch file prompts** the **user** to **reset** the **DSP board,** then **loads the 4244 into memory, then prompts the user to reset** the **DSP board again, then loads the 4283 code, and finally runs the** PC **interface code. At this point** the **controller should be ready** to run.

3.3 PC to **DSP Communication**

The PC **communicates** to the DSP board through VME shared memory. To issue a **command** to the DSP controller, the PC will write commands to the shared memory. To indicate that a **command** is available, the PC will write to a mailbox register in the shared memory. The DSP monitors this mailbox register to determine if there is a message from the PC. When a message is available, the DSP **controller** will get the **commands** from the shared memory, **execute** the **command,** and then clear the mailbox register to indicate that the **command has** been executed.

The DSP **controller** also **communicates** to the PC board through the shared memory. While the controller is running, data is continuously written to the shared memory. This data includes operating conditions and internal and **external** variables. The DSP **controller** only responds to **commands** from the PC and does not initiate **commands.**

4. THEORY OF OPERATION

4.1 Resonant DC Link Operation

The actively clamped resonant dc link converter circuit is depicted in Figure 4. Vdc is the power source voltage. Lres and Cres form the resonant tank in the **circuit.** The voltage clamp, which is **comprised** of Scl and associated **flyback** diode and the **clamping capacitor** Cclamp, prevents the link voltage from exceeding a preset value. The three phase bridge consists of switches \$1- S6 with the associated flyback diodes.

Figure 4. Actively clamped resonant dc link power processor

At time tl, as shown in figure 4, the voltage across the resonant capacitor Cres is 0. All bridge switches(S1-S6) are turned on, causing the current in the resonant inductor Lres to increase. At the end of the fixed shorting time, one switch in each of the three bridge legs is turned off according to the switch pattern selected by the current regulator. The switch pattern is selected in such a way to reduce the error between the commanded and actual motor currents. When the selected switches are opened in the bridge, the link short is removed and the current in the resonant inductor now flows in the resonant capacitor and increases the link voltage Vlink, shown as time t2. As

Figure 5. Resonant link operation

the voltage across **Cres** increases, **at** time t3 the Scl flyback **diode becomes forward biased and Cclamp begins charging. At** the **time** that the **Scl flyback diode conducts, Scl is also** tuned **on. Scl is left on until** the **current** into **the clamp capacitor** through the **diode is approximately equal to** the **current flowing back through Scl, shown as** time t4, thereby **maintaining the clamp voltage constant. Opening the clamp switch causes the current flowing back** through the **resonant** inductor to **discharge the resonant capacitor,** time tS, **and** returns **the link voltage** to **zero. The bridge diodes start conducting at time** t6 **when** the **link voltage is less** than **0,** thereby restarting the **cycle again.** Parameters for the actively clamped resonant link controller are shown in table 1.

Table 1. Actively clamped resonant dc link parameters

The **torque,** rate, **or** position **control algorithm, depending on which mode** is **commanded,** is run on a second DSP processor on the DSP board. Data transfer to the second DSP processor is **coordinated** by the primary DSP after **completing** the **current** regulation function. The primary DSP also handles the interface to the VME bus where **command** generation is initiated. The secondary DSP additionally handles all internal command function generation. Internal command functions include sine wave, square wave, triangle wave, sine sweep, and arbitrary waveforms. There is also a provision for an external **command** input via a VME interface card.

4.2 Motor Current Regulation

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The **control** of the motor **currents** is performed with a discrete control algorithm **called** a modified adjacent state regulator. A discrete algorithm is required due to the discrete nature of the switching instants in the resonant voltage waveform. A switch state selector, as shown in figure 6, has seven possible **switching** states of the ordered triplet {Sa,Sb,Sc} where Sa =1 when \$1 is on and 52 is off and $Sa=0$ when 51 is off and 52 is on in the bridge circuit.

Possible voltage vectors

Figure 6. Possible switching vectors in adjacent state regulator **Selection of one of the possible states is accomplished as** follows. **At** the

instant that the currents are sampled and then sent to the DSP board, the phase errors are found by:

> i_{as} err = i_{as}^* - i_{as} $i_{bs}err = i[*]_{bs} - i_{bs}$ i_{CS} err = - i_{AS} err - i_{BS} err $(* = reference)$ (wye connected machine)

If the magnitude of all the current errors is less than the hysteresis band (10A) then the zero state is chosen (000 or 111). If the current error magnitude of any phase is greater than the hysteresis band then the desired state for each phase is found by:

$$
Sx = 1 for i x s err >= 0
$$
\nor

\n
$$
Sx = 0 for i x s err < 0
$$

When the desired state is found, then that state is selected if the desired state is adjacent to the current state. If the desired state is not adjacent to the current state then a zero state is chosen.

The chose between which zero state to select, state 0 or state 7, is made depending on the neutral count. The neutral count is found as follows:

Count new =
$$
2(Sa + Sb + Sc) - 3 + Count_old
$$

If the zero state is selected then the (111) state is chosen when the new count is less than zero and the (000) state is chosen when the new count is greater than zero.

The reference currents for the current regulator are generated from the desired flux current, ie^* _{ds} and the torque output of the position regulator, i^{e^*} _{GS} as follows:

> $i^*_{as} = i^{e^*}_{qs} \cos\varnothing_e + i^{e^*}_{ds} \sin\varnothing_e$ $i_{bs} = i_{cs} \cos(\omega_e - 2\pi/3) + i_{cs} \cos(\omega_e - 2\pi/3)$ where \mathcal{O}_{ρ} is the sum of the rotor angle and the slip angle

4.3 **Position Control**

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The control of the actuator position as shown in figure 7. The output of the position controller is a torque command that is used to generate the phase current reference commands. Motor rate is limited to ± 14700 rpm with rate feedback obtained from the difference between rotor angle samples.

Figure 7. Position regulator block diagram

4.4 Rate Control

The control of the motor **rate as** shown **in** figure **8. The** output of the **rate controller is a torque command that is used** to **generate** the **phase current reference commands. Motor** rate **is limited to +14700** rpm **with** rate **feedback obtained from** the **difference between** rotor **angle samples.**

Figure 8. Rate regulator block diagram

4.5 Torque Control

The **control of the motor** torque **as shown in figure 9. The output of** the **position controller** is **a limited** torque **command that is used** to **generate the phase current reference commands. Motor rate is unlimited so special care is needed to make sure motor** rate **does not exceed** the **maximum allowed.**

Figure 9. Torque regulator block diagram

SYSTEM OPERATION $5.$

System Setup 5.1

Connect the system as shown in figure 8.

Communication between the DSP controller and the power stage is via two fiber optic cables. These cables should be connected with Tx from the I/O board going to Rx on the power stage and with Rx on the I/O board going to Tx on the power stage. Control power is provided to the power stage by a separate box containing three power supplies, with +5V and ±15V going to the logic board through a 'D' connector and +15V going to the drive board through a circular connector.

Feedback from the **actuator** and **motor** comes through a *"D'* **connector into** the **control** board in the power stage. This **cable** branches out to the LVDT, resolver, and force **connectors.** The connections to the motor and the DC power supply are made through circular connectors on the front of the power stage.

Figure 10. System connections

5.2 System **Startup**

After **connecting** the system as shown in figure 8, turn on power to the power

supply **box and** the **card** rack. Be **sure** the **controller** is powered up **before** applying power from the high-voltage DC power source. The Vltn LED may be lit after powering up the system. This LED indicates a communications error between the power stage and the DSP board and may normally occur during power turn on. Reset the LED with the reset button next to the LED. If the LED is still lit then a problem is indicated, probably the fiber optic cables being **crossed.**

5.3 Loading Control and Interface Software

> **After powering** up the **controller, the software needs** to **be** loaded. Type *Ioad_dsp* **at the command prompt. This command will load the code** into **the DSP and then start** the PC **interface code. You will be prompted to push** the **reset button on the DSP board during program loading. This button is located in the lower left hand corner of** the **DSP board. If** the **code successfully loads the message 'Controller Responded OK'** will **appear, otherwise an error message will appear.**

5.4 Capacitor Precharging

> **Before starting the power stage,** the **clamp capacitor** needs to **be charged to approximately .4 times the input voltage. This can be done with an external lab power supply through a 100 ohm** resistor. **Be sure to disconnect** the **power supply** before **starting the controller.**

5.5 **Starting** the **Controller**

> **Before starting** the **controller, make sure** the **controller gains and parameters are set to** the **appropriate values. The controller can be started through the** *run* **command in the main menu. If you** try **to start the controller when the clamp capacitor** is not **charged or is partially charged,** the **controller will** not **start and the software will need to** be **reloaded and the capacitor charged up** to **the correct value before trying to restart.**

. **TEST CONFIGURATION**

6.1 Test Article

The system under test includes the **EMA motor controller,** the **Sundstrand AC induction motor, and the Moog actuator.**

6.2 Test Equipment

6.3 **Test** Plan

Testing **was** performed in accordance with the "Task Order #14 System Test Plan" REV 4 dated *7-29-93.*

Measurement Points

Figure 11. Test Measurement Configuration

7. **TEST** PROCEDURES AND **MEASUREMENT TECHNIQUES**

7.1 **General**

There **are eight sets of** tests that were performed **on** the system.

- 1) **Motor** Parameters
- **2) Motor Characteristic** Curves
- 3) Steady State Power Loss and Efficiency
- 4) Motor Rate Step Response
- 5) No Load **Actuator** Tests
- 6) Inertial Load Test on SSME Test Stand at Moog
- 7) Force-Velocity Test on SSME Static Load Stand at Moog
- 8) Force-Velocity Test on SSME Static Load Stand at Marshall

Each of these areas required different test procedures and measurement techniques. The specifics are outlined in the test plan and summarized in the following sections.

7.2 **Motor** Parameters

The motor parameter tests were done under the 40 HP testing and the results are reproduced here. Two sets of tests were performed that **determined** the Sundstrand motor parameters. The first test is the blocked rotor test that determines leakage inductance and rotor and stator resistances. The second test is the unloaded motor test that determines the mutual **inductance** and resistance associated with magnetic core loss.

7.3 Motor Characteristic Curves

> The motor characteristic curve measurements generate a set of torque/speed **curves** that are used to verify the Sundstrand motor simulation results and **verify** the **controller** operation. **The** test procedure requires that ids=constant for **constant** flux. Two values of ids were chosen: ids=85A, **corresponding** to full rated flux, and ids=42A, **corresponding** to 50% rated flux. The dynamometer is commanded to a set of speeds on the motor torque/speed **curve** and the stator frequency is maintained constant by increasing the torque output and slip frequency.

> **A** Fluke 8600A **digital multimeter** was used to **measure** input **dc** voltage. The input dc **current** was obtained from a coaxial current shunt and the output of the shunt was read by a Fluke 8600A. A Yokagawa 2533 digital power meter was used to determine motor voltage, motor phase current, and power factor. **A** single phase was used to derive the total motor input power after verifying that all three phases were producing **similar** data. Motor output torque was read by the torque transducer and resolver associated with the dynamometer. The data points for each curve were taken at approximately the same stator

temperature to minimize errors.

7.4 Steady State Power Loss / Efficiency Measurements

The steady state power loss/efficiency measurements are similar to the motor curve data with the exception that the controller is run in the rate regulation mode and the dynamometer is run in the torque mode. An additional set of points corresponding to 25% rated flux is also included in this set of data.

7.5 **Motor Rate** Step **Response**

The motor rate was commanded to run at 7500 rpm CCW and then a step to 7500 rpm CW was issued and the response was recorded. The motor is unloaded during this test and ids is set for full flux (85A).

7.6 No Load Actuator Test

> The unloaded actuator test verifies the system operation and characterizes the system in preparation for the subsequent loaded testing at Moog. For the purpose of conducting the no load actuator tests, the motor was attached to the actuator which in turn was mounted on a table. The testing consisted of two sets of tests. The first test used a sine wave input signal to measure the system frequency response and phase shift. This was performed for amplitudes ranging from ± 0.1 " to ± 5.5 ". The frequency was varied from 0.05 Hz to 8 Hz. A frequency sweep was also performed at ± 0.1 " and ± 0.25 ". The step response of the system was measured in the second set of tests. A square wave input signal to the system was adjusted from $\pm 0.25''$ to $\pm 5.5''$ at 0.3 Hz and the actuator's output position was recorded on a strip **chart** recorder.

7.7 **Inertial** Load **Test** on SSME **Test** Stand

> The inertial load actuator tests were performed on the SSME inertial test stand at the Moog facility in West Aurora, New York. The actuator was installed in the inertial test stand with one end of the actuator attached to the equivalent of the SSME inertial load and the other end of the actuator attached to the equivalent of the structure mounting attach point stiffness, thereby simulating actual load dynamics. The first test used a swept sine wave input from 0.1 Hz to 10 Hz at $\pm 0.1''$ and $\pm 0.5''$ peak. The gain and phase of the system was plotted for the two input **commands** at various values of **controller** gains. The step response of the system was measured in the second set of tests. A square wave input signal to the system was adjusted from +0.25" to ±5.5" at 0.3 Hz and the actuator's output position was recorded on a strip chart recorder.

7.8 Force-Velocity Test on SSME Test Stand

> The force-velocity actuator tests were performed on the SSME static test stand at the Moog facility in West Aurora, New York. The test actuator was **installed** in the force velocity test stand and actuator rate and load force were

measured. Load force **is** provided **by** a regulated **hydraulic** piston that can be controlled in the extension and retraction mode. The hydraulic pressure regulator that controls the load is relatively slow and exhibits ringing at the application of the step command. This ringing dies down and the force and velocity **measurements can** then **be made.**

The test actuator is commanded to -5.5 inches before the **test** is **to start. A command is** then **made** to **+5.5 inches with a given force commanded and then the** position is **commanded back** to **-5.5** inches. **Actual force and linear rate are recorded** in **both directions. The commanded force is varied from no load** (actually **a** test **stand minimum of approx. 5000 lbs.) to full** rated **load. This test verifies the loading capability of the actuator and controller.**

8. **TESTS AND RESULTS**

8.1 General

The test data gathered to satisfy the test **plan is** the **culmination of many tests and verifications. In most cases** the **data was** re-measured to **confirm** the **results. This** thorough **measurement approach was** necessary **due** to the **complex nature of the test article and because of** the **opportunities for equipment error.**

It is important to note that all of the motor testing at Sundstrand was performed with a sine wave motor drive, quite different from our PDM controlled motor voltage waveform. We have no error information from Sundstrand in regard to their **measurements. References are also made to Sundstrand's** "predicted **performance" of the motor. These figures were generated by Sundstrand during** the **design phase of** the **motor and are included in** the "ALS **ACTUATOR INDUCTION MOTOR** *CRITICAL* **DESIGN REVIEW" dated April 4, 1991.**

8.2 Motor Curves Test **Results**

The motor curves test data **was gathered at** two **different** values of **flux:** ids* *=* **50% rated flux(42A) and** ids* *=* **100%** rated **flux(85A).** The **current, and** thereby **the slip, was selected to give representative data points on each curve with phase currents up to 120Arms. The motor curves are restricted to the area within the operational limits of the controller. For the condition of full rated flux, this limits the torque to well below the maximum** torque **output since this would imply operation at 100's of amps and approx. 150 hp. The area of interest is mainly** the **maximum** torque **per amp and maximum efficiency both of which occur at significantly less than the peak torque.**

Figure 12. Typical motor curve test data (700Hz/Ids=85A)

The motor curves information is **represented in tabular form as well as graphical form** in **Appendix D. The graphs or charts skow** the **variations of torque, efficiency, and power factor** relative to dyno **torque. The following graphs are a summary of** the **data** in **Appendix D.**

Controller Efficiency - Ids=42A

Figure 13. Controller Efficiency **-** *(I/2 flux)*

Controller Efficiency - Ids=85A

Figure 14. Controller Efficiency - *(full flux)*

The controller efficiency reaches **a maximum of about 96% at 115 Amps per phase and** 700 Hz. **The controller** reaches **maximum efficiency at fairly low loads and then levels off. The controller shows higher efficiency at full-rated flux than at half-rated for any load. This is due** to the **fact that** the **higher rated flux means higher currents. As would be expected, the efficiency of the controller** is **a generally a function of current out of** the **controller and** not **as strong a function of power factor.**

Motor Efficiency Ids=42A

Figure 15. Motor Efficiency **-** *(1/2 flux)*

Motor Efficiency Ids=85A

Figure 16. Motor Efficiency **-** *(full flux)*

The motor's efficiency reaches **a maximum** of **about 86%** at 115 **Amps per** phase and 14,000 rpm (700 Hz). The motor's top rated speed is 14,700 rpm, the point at which Sundstrand predicted 89.9% efficiency. We were unable to test the motor beyond 14,000 rpm due to limitations of the gearbox and dynamometer. The motor efficiency measurement error of $\pm 10\%$ is significant in this **case,** since the predicted motor efficiency falls within this window.

Overall Efficiency Ids=42A

Figure 17. End-to-End Efficiency - *(1/2 flux)*

Overall Efficiency Ids=85A

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Figure 18. End-to-End Efficiency **-** *(full flux)*

The overall system efficiency reaches **a** maximum **of** over 80% for **rated** *speed* and rated flux. In general, the efficiency for one-half the rated flux is less than **the** efficiency for full-rated flux. However, the efficiency does show some improvement when operated at **half-rated** flux when the load torque is less than about 25 in-lbs. The strategy of lowering the flux for improved **efficiency** would only be advantageous if the motor spent long periods at low loads.

Sundstrand performed a simulation of the motor's capabilities as part of their design procedure. This data is used as a **check** on the motor data gathered. The simulation indicated the following operational maximums:

The measured efficiency does not reach the Sundstrand simulation data. **A** peak efficiency of 85.7% was measured at 125 in-lbs, 13,660 rpm, a phase **current** of 113Arms, and a power factor of .628, yielding 27.0 hp. The motor rate was limited to 14000 rpm due to the limitation of the dyno.

Motor power factor and efficiency measured lower in practice than the simulated data predicted. The maximum power factor achieved under test was 0.63 ±0.5%. Typical simulated values for the power factor fell into the 0.75 to 0.8 range. Some of the discrepancy in the predicted and measured power factor and efficiency may be explained by the low motor inductance. The low motor inductance causes **higher current** ripple in the motor **currents** which results in higher motor losses. Also the present **current** regulator is a "bang bang" regulator which depends on the motor inductance to perform the integration of the motor current. The circuitry is designed to respond to polarity of the error signal. A simulation performed by Krause and Associates for NASA LeRC indicates that with a low inductance, resulting in a large **current** ripple, a significant limitation in the torque output of the motor at higher speeds **can** be expected since the regulator is not able to limit the lower frequency harmonics, showing up also as poorer power factor.

8.3 Steady State Power Loss */* Efficiency Test Results

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For the purpose of these tests the slip constant (Ks) was adjusted to 8 Hz/A/A, the **average** value that produces the most efficient operation under typical operating conditions. The data sheets and associated charts in Appendix G illustrate **this** fact. The system **efficiency** scale (on the right of the **chart)** represents the % system efficiency. ie^* _{ds} and ie^* _{qs} are scaled on the left side of the **chart.** The lower torque values for a given motor speed provide a better picture of the efficiency vs. ids/iqs because there are more data points. The higher torgue levels prevent the system from maintaining the **commanded** torque without higher **currents** and **consequently** fewer data points.

Table 2. System test data (Ids = *85A)*

Vdc	Idc	Motor Current	HP	Motor Speed	Motor Eff	Cntir Eff	Overall Eff
	$(Volts)$ $(Amps)$	(Arms)		RPM			
301.5	11.8	66.8	2.30	2992	56.5	85.3	48.2
301.5	18.8	84.6	4.18	2992	63.8	86.2	55.0
301.3	25.8	103.1	5.86	2992	64.3	87.5	56.2
301.2	18.2	67.2	4.68	5992	70.3	90.6	63.7
301.0	29.4	85.1	8.23	5996	75.2	92.2	69.4
300.8	41.0	105.7	11.58	5992	76.0	92.1	70.0
300.9	25.2	67.7	7.33	8984	76.9	93.7	72.1
300.7	41.6	87.1	12.79	8988	81.3	93.8	76.3
300.1	56.8	107.7	17.56	8964	81.0	94.9	76.9
300.8	31.2	66.4	9.50	11984	80.1	94.3	75.5
300.5	52.0	87.6	16.54	11984	83.1	95.0	79.0
299.9	72.8	111.9	23.33	11980	84.1	94.8	79.7
300.6	36.0	66.5	11.35	13980	82.3	95.1	78.2
300.1	58.4	88.5	18.93	13996	84.4	95.5	80.6
299.6	83.0	115.4	27.72	13952	85.7	97.0	83.2
299.6	90.0	121.4	29.44	13904	84.8	96.1	81.5

Total system efficiency exceeds 80% for the full flux commands at the higher motor speeds. The controller and motor also peak in efficiency at around the same point. As the current is increased beyond this point, saturation of the current regulator due to the series inductor begins and the efficiency starts decreasing. As seen from the **data in Appendix E,** the **area where an efficiency advantage is gained for reducing** the **flux occurs only at** the **very small loads probably for loads less** than **10% of** the **full load torque.**

8.4 Motor Dynamic Response

> **The motor** responded to **a** 7500 rpm to **-7500** rpm **step** in **110 msec. This corresponds to an acceleration of 148,920** rpm/sec **or 2482** rev/sec **2.**

8.5 No Load Actuator */* **System Test Results**

> **As specified** in the **No Load Actuator Test** Procedure (Appendix **I), the frequency response was measured using a sine wave input and a sine sweep** input. **The step** response **was measured using a square wave input.**

> **The frequency** response **of the system** is **best determined by identifying the frequency at which the phase delay is equal to 90 degrees. The command/position phase shift changes quickly approaching the pole** in **this second order system. The sine sweep data shows that the 90 degrees phase shift occurs at 5.0** Hz **and +0.1" amplitude with an amplitude gain of 0.6.** The **response of the system falls off** rapidly **at frequencies above 5.0** Hz. **At larger amplitudes and lower phase currents** the **cutoff frequency** is **lower.**

> **The step response for the unloaded actuator is 1.56 sec for a step of 11** inches(-

5.5 in to 5.5 in). This is mostly a rate limited response as it takes just over 100 msec to go from 0 to 15000 rpm.

The control loop constants may be adjusted to achieve optimum system response for differing conditions. For a second order system, which the unloaded actuator closely approximates, the classic solutions can be used (i.e. a larger value for K_p improves the small signal response by increasing w_n and decreasing zeta, thereby decreasing damping, while an increase in Kr increases zeta, thereby increasing damping). The values selected give an Wn of 20 radians/sec and a zeta of .95.

8.6 Force-Velocity Stand */* System Test Results

> The horsepower requirements for this system were 32.8 HP at 32,000 lbs. The maximum output power that was demonstrated was 31 HP at 32,000 lbs of force. The torque output of the motor is reduced at the higher stator frequencies due to the effects of the motor inductance as mentioned in Section 8.2. Also to reach the 31 hp, a higher value of motor current was required. This results in lower efficiency due to the decreased torque per amp output.

8.7 Inertial Test Stand */* System Test Results

> The control loop constants may be adjusted to achieve optimum system response for a given set of requirements. For a second order system, a larger value for K_p increases wn and decreases zeta, thereby decreasing damping and improving rise time, while an increase in K_r increases zeta, thereby increasing damping. However, the response of the system on the inertial test stand is more complicated. The addition of the load inertia and the structure spring makes calculation of the correct gains extremely difficult. methods that are normally used are computer simulation and trial and error. The gains we had initially selected during the simulation did not give us the desired results on the test stand, so we had to adjust the gains while running the system on the stand. These adjusted gains, while giving adequate response, are not the optimum values.

> The test data on the inertial test stand at MOOG showed some interesting results. The simple second order system of the unloaded actuator and the dyno loaded motor was no longer valid. The load inertia and the effective spring of the attach point structure increased the complexity of the overall system. The gains which directly controlled the natural frequency and damping of the system were now indirectly controlling the system. This meant that finding the proper control gains was a matter of trial-and-error. The data presented is the result of this process and did not necessarily result in the optimal gains for this system. As can be seen from the gain-phase plots of the system, there is a load resonance around 8 Hz. This load resonance resulted in a gain that was too high at the 180 degree crossover point of the phase, thereby resulting in a potentially unstable system. At the larger command input(5%), the load resonance was amplified to a point that

resulted in overspeed operation of the **motor,** and **large amounts** of regenerated power. To remedy this, additional compensation needs to be added to increase the gain and phase margin.

. MEASUREMENT ERROR

9.1 Principle **Sources Of Error**

Torque Measurement Error (at **motor) +_3 in-lbs**

9.2 Error Assessment

The assessment of error in **determining system efficiency** values is **complex.** The **magnitude of the errors depends on several factors. The measurement error changes with the operating point of the system because the Yokagawa power meter for example, specifies accuracy as a percentage of the range. The range changes during the measurements depending on the magnitude of the voltage and current. But for a given** range, the **measurements made on the low end of the** range **have a greater error** associated **with** them. Power **is particularly difficult** to **measure with a high level of accuracy. The available test equipment** is **capable of making** real **power measurements** to **an accuracy of between 2% and 20% in our test application. This is due to both the range in which the voltage and current magnitudes fall and** the **poor power factor environment in which many of the measurements were made. At the low power factors, power factor measurement error increases to about 4% on the Yokagawa 2533.**

Some of the measurements have errors specified to **a percentage of the**

measured value as well. The digital multi meter's errors are specified in this way. This requires a multi-step process when determining the total measurement error.

Another important consideration is the number of arithmetic calculations that are performed using the data. Each successive calculation accumulates the errors from the preceding computations. The very nature of the motor parameter determination requires numerous calculations to arrive at the end values, which consequently have large error uncertainties.

There are two types of error sources; Random Errors and Guarantee Errors. Random errors are the result of unknown or unpredictable forces that result in measurements varying from reading to reading on a given instrument. These types of errors may be analyzed using statistical methods. Guarantee errors are errors attributed to the capabilities of a piece of test equipment. Providing that the equipment is properly calibrated, all measurements will be within a specified error range. In determining the error associated with each measurement or calculation, these errors are treated differently. Random errors may be added using the root-sum-square techniques available in **statistical** theory while **guarantee** errors **must** be **added algebraically.**

The analysis of our data has revealed a potentially high variance of the values due to errors. It is important to keep in mind that these error numbers are a worst **case calculation** dictated by the standards of error analysis. This is not the likely error as it is improbable that all error components will be in their worst **case condition** at the same instant. In fact the actual error is a probably a fraction of the worst case scenario. Examples of the worst case error analysis are included in **Appendix** H.

9.3 Torque Transducer

> The Eaton model 1105 torque transducer and it's associated Daytronic 3178 signal **conditioner** are integrated into the dynamometer assembly. They measure and transmit the motor's torque output (after a 4:1 gear reduction) to the dyno control terminal. There are several sources of error in this equipment. The torque transducer error is made up of nonlinearity, **equipment.** The torque transducer error is made up of σ of ϵ in a hysteresis, and repeatability errors of $\pm 0.1\%$, $\pm 0.1\%$, and $\pm 0.05\%$ of $\pm 0.05\%$ respectively. The full scale measurement **capability** of the torque transducer is 5000 in-lbs. The Daytronic signal conditioner has an error of $\pm 0.05\%$ of full scale. The torque transducer **errors** are **considered** random errors and **consequently** the total **error** is determined from their root sum square (RSS= 0.15). Including the signal conditioner error, the sum of these errors is $\pm 0.2\%$, corresponding to ± 10 in-lbs accuracy in the dyno torque measurement. The motor torque measurement error is ± 2.5 in-lbs (The 4:1 gearbox separating the dyno from the motor results **in** a corresponding reduction in motor torque measurement error). Additional error is introduced in measuring the motor torque due to the gearbox nonlinearities. The total accuracy in measuring

motor torque is +3 in-lbs including these gearbox variations.

9.4 **Power Measurement** Meter **Accuracy**

The Yokagawa model 2533 digital power **meter** has a voltage, **current** and power measurement accuracy of $\pm 2\%$ of the range up to frequencies of 20 kHz, as specified by Yokagawa. The manufacturer has not measured the accuracy above this frequency, so we **measured** the voltage and **current** frequency response of the unit up to 400 kHz. Those measurements were within $\pm 2\%$ of our calibrated reference signal. The power factor accuracy is specified as $\pm 0.5\%$ at a 0.5 PF and 60 Hz. The power factor measurements were verified at **several** frequencies ranging from 100 Hz to 700 Hz on the Tektronix DSA 602 oscilloscope. **At** low power factors, however, Yokagawa relaxes this specification to $\pm 4\%$ (PF=0.1). The power measurement accuracy is based on the selected range of the measurement instrument. For our tests the **current** range was always 155 **A,** dictated by a **current** transformer used with the test **set** up. The voltage range changed depending on **conditions.** The following is a summary of the voltage, **current** and power measurement errors attributed to the Yokagawa 2533 voltage range setting.

The accuracy of the power measurements included in the data are on average good to about 10%. The error is higher than 10% on low power factor measurements and those power measurements made on the low end of the range of the instrument. The error is heavily dependent on the voltage range selection because the power error numbers given above are fixed relative to a given range. For example, if power is measured as 3000 watts with the voltage range set to 600 volts, the error is 3000 watts ±1860 W or ±62%. Due to this **limitation** all measurements are performed with the instrument's range adjusted to minimize this error. In **certain** low power, high voltage situations, however, this result is unavoidable.

9.5 Digital **Meter** Accuracy

The **digital** voltmeters used in the DC **measurements** were **highly accurate** relative to the AC power meter **capabilities.** In practice the DC input power to the inverter was measured using a Fluke 8600A DVM in the DC voltage mode. Input **current** was measured as a voltage on the 200 mV range by using a precision 0.0004949 ohm shunt resistor. The inverter input voltage was measured on the 1200 volt DC scale. These measurements are accurate to
+0.02% of input +0.008% of the range.

9.6 No **Load Actuator Test Error**

The measurements involved in performing the No Load Actuator Tests are relatively simple and not subject to large errors. Two signals are measured; the **command** amplitude, phase and frequency, and the actuator position amplitude, phase and frequency. These measurements are performed and documented on the strip **chart** recorder, and verified on the DSA602 scope. The DSA602 is also used to plot the position */* **command,** X-Y plots. The source of the signals are identical D/A **converters** located in the motor **controller card cage.** These D/A **converters monitor** both the input **command** voltage and the actuator position. The D/A converters were calibrated to one another prior to the testing to an accuracy of ± 5 mV. Most of the another prior to the testing to an accuracy of +5 inV. Most of the measurements were in the ± 1.0 v to ± 10.0 v range so the D/A accuracy is negligible. The small signal measurements in the 150 mV range were more susceptible to random noise present in the environment. This is noticeable on the X-Y plots as random data points. The strip chart recorder was particularly adept at reducing environmental noise. Its differential inputs and noise filters effectively eliminated this noise from the plots. The steps that are noticeable in the small signal position traces are caused by the 5 m/Sec update time of the D/A **converters.** The total error associated with the no load actuator tests is about $\pm 3\%$.

9.7 Cumulative Calculation Errors

Appendix H, Worst Case Error Analysis, contains examples of the error analysis applied to the experimental data. These **calculations** demonstrate the large errors that result from the **cumulative** affect of errors in successive calculations. These error margins should be considered as a worst case probability only, when evaluating the test data. The realistic value for error margins is significantly less than the worst case error analysis indicates. Measurements made at low powers, high voltages relative to range and low power factors have greater maximum errors. In general, most of the **calculated** values are accurate to within 10% of their stated values.

10. SUMMARY OF TEST DATA

The Sundstrand induction motor for this **project was designed for minimum losses and low inertia. A byproduct of these general design goals is low leakage inductance** in **the motor. Low inductance** is **a common characteristic of high horsepower motors where a current source converter would typically be used instead of a voltage source converter** to drive the **low inductance motors. Since** the **controller used here** is **a voltage source, as are most PWM type controllers,** the **current** rate **of** rise in the **windings is equal** to the **applied** input voltage **minus** the back **EMF** voltage divided by the leakage inductance. When the motor inductance is low, the rate of rise of **current** is high, and therefore the **current** ripple is large. To reduce the ripple, an inductor can be added to the motor windings, or the switching frequency could be increased, or an alternate **converter** topology **could** be used. Since the second two **solutions** would mean **considerable** redesign, the motor inductance was increased by the addition of an **inductor** in series with the motor windings. Some actual phase **current** waveforms are shown in Appendix I.

The fuU scale system has been verified **for operation** up to **about 31** HP. **At motor rates below 8000** rpm, **full** torque **capability has been demonstrated. At motor rates above 8000 rpm** the **controller starts saturating and the actual current does not follow the current reference. Testing revealed** that **the system is not producing the motor torque** it **should, based on predictions from the motor's manufacturer. Simulations performed by Krause and Associates for NASA, identified this as a potential problem due to limitations in** the **current regulator caused by insufficient motor voltage. Since an inductor has been placed in series with the motor phase windings to decrease the large ripple currents by increasing** the **controller output impedance, the voltage** is **also lowered across** the **motor windings by** the **voltage drop in the series inductors. The current regulator** therefore **starts saturating at the higher commanded motor voltages. The applied motor voltage is proportional to motor input frequency, resulting in an increase in current error and in an accompanying increase** in **lower frequency harmonics at the higher motor rates, and** therefore reduced power output. **A** value of **50uH is** a **compromise** between higher ripple **currents** and output voltage saturation.

During preliminary testing **in** San Diego, **a power** switch in the **motor** bridge failed. The failure occurred at 200 Arms/phase and is the result of exceeding the maximum allowed junction temperature. The **current** reached the **commanded** value but failed shortly afterward. To eliminate this failure **mode** the heat load or the plate temperature must be reduced. To reduce the heat load, the present power switches **could** be replaced by the lower vsat **switches.** Replacing the switches would reduce the average vsat from 3.5 volts to 2.7 volts, resulting in a 23% decrease in the conduction losses, the **main contributor** to overall losses. To lower the base plate temperature, the **cold** plate temperature **could** be lowered or the interface **could** be redesigned. The power **stage** base plate, whose base is a flat plate that measures 12" by **18",** is attached to a **cold** plate with machine screws. Thermal compound is spread between the surfaces to eliminate any voids that may result. It is difficult to get **even** pressure at the metal to metal **contact** between the two large flat plates, resulting in higher thermal impedance. The thermal **calculations** originally done on the box interface were based on ideal interface conditions, which **could** not be achieved with the present design. The **cold** plate temperature was therefore lowered from around 27 degrees C **to** around 15 degrees C. This temperature is just above the **condensation** point here, though this needs to be watched for varying **climatic conditions.** No other similar failures have been observed since decreasing the cold plate temperature.

The overall system efficiency was tested at a maximum of about 83%. The motor efficiency peaked at 86%. The controller power stage efficiencies measured as high as 97% respectively. In general, the efficiency calculations are good to approximately +5%, although worst case analysis indicates about +10%. All of these efficiencies are in the range anticipated for the system. Power loss analysis of the controller power stage predicted > 90% efficiencies.

The step response of the loaded actuator is 770mSec for a 10 inch step. The large signal step is not really indicative of the controller bandwidth, as the motor spends most of the transition time with the motor revs limited.

The inertial load actuator tests indicated a system frequency response of approximately 4 Hz for the 2% input command $(\pm 0.1'')$ at 200 A/phase with a strong resonance showing up at around 9 Hz. As can be seen from the gainphase plots, the controller gain is not sufficiently down by the 180 degree crossover point of the phase, thereby presenting stability problems for the controller. The frequency response of the system is less at higher amplitudes or lower phase currents. The frequency response of the system may be adjusted by changing the control constants or the phase currents as demonstrated by the test data. Adjustment of these parameters is available via the computer terminal interface. The actuator test data is accurate to, at worst, about $\pm 3\%$.

The worst case error analysis revealed potentially large deviations in the measured motor data. The errors may be attributed to two areas. The first is the relative inaccuracy of the AC power measurements. The available test equipment (Yokagawa 2533) is specified as 2% accurate at full scale and high power factors. Our system often operates at lower power factors and fractions of the full scale meter readings. This results in potential errors of about 10% of the measured values.

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APPENDIX A

DRAWINGS

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70 HP ELECTROMECHANICAL ACTUATOR SYSTEM DIAGRAM

MOTOR CONTROLLER SYSTEM CONFIGURATION

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APPENDIX B

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Motor Parameters Test Data; GDSS and Sundstrand

The tables containing the measured data have column headings defined below:

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Stator Resistance vs Temperature

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Blocked Rotor Impedance

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APPENDIX C

MOTOR **PARAMETER TEST PROCEDURE** AND TEST **DATA**

Reproduced from 40 HP **Electro-Mechanical** Actuator **Test Report** NASA Contract: NAS325799 - August 1993

 $\label{eq:2} \mathcal{L}(\mathcal{L}^{\text{max}}_{\mathcal{L}}(\mathcal{L}^{\text{max}}_{\mathcal{L}})) \leq \mathcal{L}(\mathcal{L}^{\text{max}}_{\mathcal{L}}(\mathcal{L}^{\text{max}}_{\mathcal{L}}))$

Measuring Induction Motor Parameters:

Measuring induction motor parameters with a Pulse Density Modulated (PDM) motor controller requires specialized test Rs), and Core Loss Resistance (Rm). The high harmonic content of the drive voltage and to a lesser extent, the drive Inductance (Ls), Rotor Leakage Inductance (Lr), Magnetizing Inductance (Lm), Rotor and Stator resistances (Rr̃ and procedures. The procedures outlined here are required to achieve accurate measurements of the Stator Leakage current that are characteristic of a PDM drive make the following test procedure necessary.

Blocked Rotor Testing:

With the rotor blocked (held motionless) the single phase induction motor model reduces to only equivalent resistances The goal of Blocked Rotor Testing is to identify the leakage inductances of the motor and the rotor/stator resistances. resistance and inductance are effectively shorted out by the small values of rotor resistance and leakage inductance. and leakage inductances. This occurs because when the slip goes to 1 (with the rotor blocked), the magnetizing

Blocked Rotor Measurements:

- 1) Measure the stator resistance (Rs) with a resistance bridge or milliohm meter. The measurement should be performed on all three phases of the induction motor from Line to Neutral, at different temperatures.
- equivalent resistance (Req = Rr + Rs) may be calculated. This measurement should be made with test equipment 2) Measure the Real or Effective Power and the current into the motor on a per phase basis. These measurements should be made over a range of input currents and drive frequencies. Using the data gathered in this test the capable of reading the entire harmonic spectrum produced by the PDM controller.

 $\text{Rr} = \text{Req} - \text{Rs}$ Then to calculate Rr:

Req = Real Power

motor's fundamental test frequency. This is necessary because the leakage inductance is measured indirectly using 3) Leakage Inductance: This measurement requires that the per phase voltage and current be determined at the

the blocked rotor impedance and then computed from the following formulas:

 $Zbr = V$ A) Calculate the Blocked Rotor Impedance Zbr.

B) Calculate the inductive reactance (X L) from Zbr.

 $X_L = \sqrt{2b^2 - \text{Req}^2}$

 $2 \pi F$

 $\mathbf{R} = \mathbf{P}$

C) Calculate the equivalent leakage inductance Leq = Lr + Ls:

controller the fundamental frequency component of the voltage is difficult to measure because the voltage is a series of Note: The voltage and current measured in step (A) must be the motor drive fundamental frequency. With a PDM pulses. Signal filtering or signal processing is necessary to identify the magnitude of the fundamental voltage component.

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Appendix , Motor rest rioceum

No Load Testing:

The goal of No Load Testing is to identify the magnetizing inductance and the core loss resistance of the motor. When resistance are predominant in the motor model under these conditions because they are magnitudes larger than the the motor is running with no load the slip becomes very close to zero. The magnetizing inductance and core loss

leakage inductances and rotor/stator resistances.

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Appendix A; Motor Test Procedure

No Load Measurements:

this test the core loss resistance (Rm) may be calculated. This measurement should be made with test equipment These measurements should be made over a range of voltages and drive frequencies. Using the data gathered in 1) Measure the Real or Effective Power and the voltage at the motor Line to Neutral terminals on a per phase basis. capable of reading the entire harmonic spectrum produced by the PDM controller.

$$
Rm = \frac{V^2}{Power_R}
$$

2) Magnetizing Inductance: This measurement requires that the per phase voltage and current be determined at the motor's fundamental test frequency. This is necessary because the magnetizing inductance is measured indirectly using the no load impedance and then computed from the following formulas:

74

Rm

controller the fundamental frequency component of the voltage is difficult to measure because the voltage is a series of Note: The voltage and current measured in step (A) must be the motor drive fundamental frequency. With a PDM pulses. Signal filtering or signal processing is necessary to identify the magnitude of the fundamental voltage component.

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APPENDIX D

MOTOR AND **UNLOADED** ACTUATOR TEST **PROCEDURE**

 \mathcal{L}_{eff}

NASA CONTRACT: NAS3-25799 DC RESONANT LINK CONTROLLER SYSTEM TEST PLAN

Principal Engineer:

Ken Schreiner

Program Manager:

Pat Klement

TABLE OF CONTENTS

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 \mathcal{A}

*** Commanded Values**

 $\sim 10^7$

1.0 SYSTEM DESCRIPTION

This Electro-Mechanical Actuation system utilizes a DC resonant motor controller and an induction motor powered linear actuator capable of up to 70 HP output. The system's power source is either a 300 VDC power supply, used for laboratory testing, or a 300 VDC battery. Control of the system is accomplished via a computer terminal which is an integral part of the system. The EMA Monitor Program controls all aspects of the systems operation. System testing is performed on a dynamometer when determining maximum power output, or on an actuator test stand for determination of frequency and phase response.

1.1 CONTROLLER

This controller utilizes field oriented control to determine the phase current values necessary to drive the induction machine to a commanded speed value. The controller also requires two position feedback sensors when used with a linear actuator. The first is a rotary position sensor which is mounted on the motor shaft and provides rotor angle and speed feedback. The second is a linear position sensor which determines the amount of actuator extension or retraction.

Field-oriented control relies on equations generated from a d-q (directquadrature) axis model of the induction machine. Figure 1-1 illustrates the equivalent circuit of the induction machine. When the rotor flux is aligned with the d-axis, the daxis component of the current, i^{e} _{ds}, is decoupled and becomes the flux producing current. At this point the q-axis component, ie_{qs}, may be used to control torque or speed as illustrated by the following equations:

$$
T = \frac{3}{4} \frac{p \text{ Lm}^{\text{e}}_{\text{O}} \text{ Lr}}{\text{ Lr}}^{\text{e}} \text{ ds} + \omega_{\text{S}} = \frac{R_r \text{ Lm}}{\text{ Lr}^{\text{e}} \text{ Lr}}^{\text{e}} \text{ ds} + \lambda_{\text{O}}^{\text{e}} \text{ Lr}^{\text{e}} \text{ Lm}^{\text{e}} \text{ ds} : \text{ assumes i}^{\text{e}}_{\text{O}} \text{ constant.}
$$

The **user** definable variable needed **to** calculate these formulas are:

 R_r = Rotor Resistance

 L_m = Mutual Inductance

 L_r = Rotor Inductance

 \overrightarrow{P} = Number Of Poles

 $ie_{ds}^* = d$ -Axis Current In Synchronous Frame

Figure 1-2 shows the block diagram of the system. A comparison of the commanded actuator position to the actual actuator position produces an error signal. This error signal is used by the software algorithms to generate the required field oriented control motor **currents** that move the motor/actuator and eliminate the position error. The rotor angle feedback and the actuator force feedback signals are used by the algorithms to determine the appropriate command signals. Motor current regulation is performed using computer software by comparing the actual motor currents from phase A and B feedback currents to the commanded phase currents. The resulting current error is used to determine the power switching sequence which ultimately controls the motors phase currents.

Figure 1-1: Induction Machine Equivalent Circuit For Field Orientation Operation

Figure 1-2: Motor Controller Block Diagram

1.2 MOTOR

The motor being **tested** is **the** 70 **Hp** induction machine developed by Sundstrand for NASA contract number NAS3-25799, task order 1 and EMA ADP 2402.

1.3 DYNAMOMETER TEST STAND

Motor testing under load shall be performed on the dynamometer test stand procured under the EMA ADP 2402. The motor shall be coupled to the dynamometer through a 4:1 gear box which reduces the rotational rate of the shaft to a maximum of 3500 RPM.

2.0 **TEST PLAN DESCRIPTION**

2.1 OBJECTIVE

This test plan was developed to provide a method of testing the DC link resonant motor controller and the motor/actuator system. The objective of the testing is to determine system capabilities including the motor operating parameters, motor performance, motor efficiency, controller efficiency, system efficiency, system frequency and phase response, optimum operating point information, and maximum output power.

2.2 MEASUREMENTS

The testing requires measurement of several parameters including **currents,** voltages, power, motor speed, torque, temperature and actuator position. The following paragraphs indicate proposed measurement methods for all quantities of interest as well as proposed equipment.

2.2.1 VOLTAGE, CURRENT, AND POWER

Input voltage is measured with a digital voltmeter and input current is measured with a digital voltmeter on the output of a coaxial current shunt. The measurement points for motor current, voltage, and power determination are shown in Figure 2-1. The Yokogawa 2533 Digital Power Meter is capable of measuring the voltage, current and power on the complex pulse density modulated waveforms that are present in this system. The Yokogawa 2533 Digital Power Meter contains circuitry to analyze the voltage and current waveforms and calculate the apparent and real powers.

2.2.2 TORQUE

Torque shall be measured using the in-line torque transducer on the dynamometer. The torque transducer's signal is processed by a signal conditioner which generates a DC voltage proportional to the torque output of the torque transducer. This voltage is interpreted by the dynamometer's computer terminal and displayed on the terminal monitor.

2.2.3 SPEED

When **the** dynamometer **is in use, the shaft speed of** the motor may be calculated from the dynamometer speed (displayed on the terminal monitor) and the gear box ratio. The actual motor speed is displayed on the motor controller's computer monitor.

2.2.4 TEMPERATURE

There are **two temperature measurement** points **within** the **system.** One is part of the heat exchanger system and indicates the temperature of the coolant in the cold plate. The final temperature point is a thermocouple embedded within the stator. This temperature is monitored using a temperature meter.

2.2.5 ACTUATOR POSITION

The actuator position is measured by the LVDT (Linear Variable Displacement Transducer) mounted in the actuator's housing. The signal from this device is processed by the Controllers computer software and is available as an output to the strip chart recorder.

2.3 TESTS

The testing is comprised of two major test areas: static load testing and open loop actuator testing.

2.3.1 STATIC LOAD TESTING

The dynamometer is to be used during these tests. Two static load tests are to be carried out. They are the motor characteristic curves and steady-state power loss/efficiency tests. The test set-up for the static load testing is illustrated in figure 2- 1.

2.3.1.1 MOTOR CHARACTERISTIC CURVES

The purpose of these tests is to determine the torque and associated **efficiencies** for **both the motor and the system over the operating speed range of the motor** for **various constant stator** frequencies **and currents. To establish a set stator current, ieds* and ieqs* are set to specific values and therefore the normal operating mode of calculating ieqs* is bypassed. In order to maintain a constant stator frequency, it is necessary to use the dynamometer to drive the induction motor to a set speed and adjust the rotor slip until the stator** frequency **is the desired value.**

Figure 2-1: The Static Load Test Set-up

2.3.1.2 STEADY-STATE POWER **LOSS/EFFICIENCY** DETERMINATION

The purpose of the steady state power loss measurements is primarily to establish the operating efficiencies for a given speed/torque requirement. Due to the characteristics of the controller and the dynamometer, the only static loading capability of the test system is to operate the motor at a commanded speed against a torque supplied by the dynamometer. For the purpose of this test, the commanded rate can be anywhere within the range of 0, stall, to about 14000 RPM under a load of up to 23.5 ft-lbs. The top speed is limited by the dynamometer's maximum speed.

The i^eds^{*} shall be varied to command from 25% of rated flux to 100% of rated flux. By measuring the power into the controller, power into the motor, and mechanical power out of the motor, the power loss and total system efficiency can be determined.

2.3.2 MOTOR DYNAMIC NO LOAD TESTING

Dynamic no load testing is performed without the dynamometer attached **and only motor's** rotor **inertia** acting **to oppose the** motor movement. **The** test set-up is shown in Figure 2-1. A strip chart recorder is used to record the data. For this test the step response of the motor shall be measured. From the step response, the no load frequency response may be mathematically calculated.

There are two individual step response tests to be carried out. The first is a step from zero speed to 7500 RPM. The second is a step from 7500 RPM in one direction to 7500 the opposite direction. This second test will demonstrate the ability of the system to operate in a regenerative mode since it will enter a regenerative power phase as it dynamically brakes the inertial load.

2.3.3 ACTUATOR NO LOAD TESTING

This **series of** two tests measures **the** frequency response, phase response and step response of the entire system, with the actuator operating in a no-load condition. For the purpose of these tests the motor is attached to the actuator and the actuator is mounted on a table, with a coupling that allows free movement of the actuator end fitting.

- 3.0 TEST PLAN PROCEDURES
- 3.1 STATIC LOAD TEST PROCEDURES
- 3.1.1 MOTOR CHARACTERISTIC CURVES TEST PROCEDURE
	- 1. Power **up** motor **controller.**
	- **2.** Initialize controller.
	- 3. **Power** up the DC power supply and adjust the output voltage to 300 VDC.
	- **4. Power** up dynamometer in speed mode.
	- . Command dynamometer speed to test value. Set phase voltage to frequency ratio to 120Vrms L-N / 750Hz. Keep this ratio constant for all data points in this test.
	- . Adjust slip frequency until stator frequency is at desired test value. **NOTE:** Under no circumstance is the output dyno torque to exceed 1200 in-lb. **This** will result in the truncation of some of the motor **curves.**
	- **°** Record **stator** current **and** frequency, dynamometer **shaft speed,** dynamometer torque, calculated motor torque and shaft speed through gear box, power into the controller, VI-n, la, power, and power factor into motor.
	- **8.** Reduce dynamometer **speed** for next point **on curve.**
	- 9. Repeat steps 6 thru 8 until sufficient points are generated to establish a speed-torque curve.
	- 10. Repeat **6** thru 9 for **each of** the frequencies in **the** table.
	- **11. Calculate and** plot **.vs.** motor **shaft speed** the system efficiency, motor efficiency, and motor torque value.

* May result in truncated motor curves **due** to torque limits.

3.1.2 STEADY-STATE POWER LOSS/EFFICIENCY TEST PROCEDURE

1. Power up the DC power supply and adjust the output voltage to 300 VDC.

Set ie_{ds}^* for 25% flux.

- 2. Set dynamometer to test point torque values, as given in the table.
- 3. Command test point motor shaft speed and allow system to reach steady state condition.
- 4. Record ieds*, commanded motor shaft speed, actual dynamometer shaft speed, actual dynamometer torque, calculated motor torque through gear box, power into the controller, V_{1-n} , I_a , power, and power factor into motor.
- 5. Command next test point speed value at same load. Go to 2.
- 6. Repeat steps 2 thru 5 until all tests have been completed.

3.2 MOTOR DYNAMIC NO LOAD TEST PROCEDURE

- 1. **Power** up motor controller.
- 2. **Initialize controller.**
- 3. **Power** up the DC power supply and adjust the output voltage to 300 VDC.
- Command a motor speed of 7500 RPM and allow to reach steady-state

condition.

- 5, Command a motor speed of 7500 RPM in the opposite direction and allow to reach steady-state condition. Record the step response of the motor.
- 6. Power **down** the **DC** power source.
- 7. Power down or reset motor controller.
- 8. Plot and evaluate the step response and estimate frequency response from the data.
- 3.3 ACTUATOR FREQUENCY RESPONSE TEST
	- 1. Power up motor controller.
	- 2. **Initialize** controller for rated flux.
	- 3 Apply the specified SINUSOIDAL command (see table) for a ten cycle minimum.
	- 4. Plot the position vs. command on an X-Y plot.
	- 5. Record the position and command signals on a strip chart recorder.
	- 5. Repeat for all test points.

3.4 ACTUATOR STEP RESPONSE TEST

- 1. Power up motor controller.
- 2. Initialize controller to rated flux.
- 3 Apply the specified STEP command (see table) for a five cycle minimum.

4. Record the position and **command signals** on a **strip** chart recorder.

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5. Repeat for all test points.

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APPENDIX E

MOTOR AND UNLOADED ACTUATOR TEST **DATA**

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SECTION 1- MOTOR **CURVES** TEST **DATA**

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Controller Efficiency - Ids=42A

Controller Efficiency - Ids=85A

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ا ╬ ♦ □ 25 $\overleftrightarrow{\eta}$ ٰל ╠ Slip(Hz) Ŷ P 20 Ŷ ₿ $\frac{6}{1}$ þ ♦ ₿ ۸ $\frac{1}{1}$ ১ မာ Ω □ \bullet ΣF $0.\overline{0}$ $\frac{8}{10}$ 0.7 $0.\overline{6}$ $0.\overline{5}$ $\ddot{0}$. $0.\overline{2}$ 0.4 $\overline{0}$ \bullet Efficiency(%)

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SECTION 2 - SYSTEM TEST **DATA**

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SECTION 3 - MOTOR STEP RESPONSE

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SECTION ⁴ ACTUATOR No-LOAD **STEP** RESPONSE

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The no load test data **was** recorded **under** the following **conditions:**

- $Kp = 8.0$, $Ki = 0.1$, and $Kr = 35.0$ unless otherwise noted.
- Command and Position scales are equal on the strip **charts** unless otherwise noted. \bullet

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SECTION 5 - ACTUATOR NO-LOAD SINUSOIDAL RESPONSE

The **no load** test **data was** recorded **under** the **following conditions:**

- **k Kp** = 8.0, Ki = 0.1, and Kr = 35.0 unless otherwise noted.
- **Command and** Position scales **are equal** on the strip **charts unless** otherwise noted.

SECTION 6 - ACTUATOR NO-LOAD SINE SWEEP

The **no** load test data was recorded under the following conditions:

Kp = **8.0,** Ki *=* **0.1,** and Kr *=* 35.0 unless **otherwise noted.** \bullet

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Command and Position scales are equal on the strip **charts** unless otherwise noted. \bullet

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 $\label{eq:2.1} \frac{1}{\sqrt{2}}\int_{\mathbb{R}^3}\frac{1}{\sqrt{2}}\left(\frac{1}{\sqrt{2}}\right)^2\frac{1}{\sqrt{2}}\left(\frac{1}{\sqrt{2}}\right)^2\frac{1}{\sqrt{2}}\left(\frac{1}{\sqrt{2}}\right)^2\frac{1}{\sqrt{2}}\left(\frac{1}{\sqrt{2}}\right)^2.$

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SECTION 7- ACTUATOR No-LOAD X-Y SINUSOIDAL PLOTS

The **no load** test data was recorded under the **following conditions:**

- **Kp** *=* **8.0, Ki** *=* **0.1,** and Kr *=* **35.0** unless otherwise noted. ϵ
- Command and Position scales are equal on the strip charts unless otherwise noted. \bullet

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 $±$, S _{in} $7Hz$
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APPENDIX **F**

MOOG FACILITY TEST PROCEDURE

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NASA CONTRACT: NAS3-25799

DC MOTOR CONTROLLER SYSTEM TEST PLAN; MOOG FACILITY

Prepared by:

Chris Fulmer

Principal Engineer:

Ken Schreiner

Program Lead:

Pat Klement

TABLE OF CONTENTS

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1.0 SYSTEM DESCRIPTION

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1.1 CONTROLLER

This controller utilizes field oriented control to determine the phase current values necessary to drive the induction machine to a commanded speed value. The controller also requires two position feedback sensors when used with a linear actuator. The first is a rotary position sensor which is mounted on the motor shaft and provides rotor angle and speed feedback. The second is a linear position sensor which determines the amount of actuator extension or retraction.

Field-oriented control relies on equations generated from a d-q (directquadrature) axis model of the induction machine. Figure 1-1 illustrates the equivalent circuit of the induction machine. When the rotor flux is aligned with the d-axis, the daxis component of the current, ie_{ds} , is decoupled and becomes the flux producing current. At this point the q-axis component, ie_{qs} , may be used to control torque or speed as illustrated by the following equations:

$$
T = \frac{3}{4} \frac{p \text{ Lm}^e}{\text{ Lr}} \int_{qS}^{*} \text{ ds} \qquad \omega_S = \frac{Rr \text{ Lm}}{\text{ Lr} \lambda_{\text{dr}}^e} \int_{qS}^{*} \text{ ds} \qquad \lambda_{\text{dr}}^e = \text{ Lm}^e \text{ ds} \qquad \text{assumes i d}^e \text{ constant.}
$$

The user definable variable needed to calculate these formulas are:

 R_r = Rotor Resistance

 L_m = Mutual Inductance

 L_r = Rotor Inductance

 $P =$ Number Of Poles

 $ie_{ds}^* = d$ -Axis Current In Synchronous Frame

Figure 1-2 shows the block diagram of the system. A comparison of the commanded actuator position to the actual actuator position produces an error signal. This error signal is used by the software algorithms to generate the required field oriented control motor currents that move the motor/actuator and eliminate the position error. The rotor angle feedback and the actuator force feedback signals are used by the algorithms to determine the appropriate command signals. Motor current regulation is performed using computer software by comparing the actual motor currents from phase A and B feedback currents to the commanded phase currents. The resulting current error is used to determine the power switching sequence which ultimately controls the motors phase currents.

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Figure 1-1: Induction Machine Equivalent Circuit For Field Orientation Operation

1.2 MOTOR

The motor that is integrated into the MOOG built actuator is a 70 Hp induction machine developed by Sundstrand for NASA contract number NAS3- 25799, task order I and EMA ADP 2402.

1.3 ACTUATOR TEST STAND

The actuator test stands used for this sequence of tests are of two types. The first provides an inertial load to the actuator system that represents the load typical of the Space Shuttle's Main engine. The second test stand provides a load to the actuator system that may be adjusted to values between zero and 55,000 pounds. The actuator may thus be tested at varying velocities and loads up to the maximum rated load of 55,000 pounds.

2.0 TEST PLAN DESCRIPTION

2.1 OBJECTIVE

This test plan was developed to provide a method of testing the DC link resonant motor controller and the motor/actuator system. The objective of the testing is to determine system capabilities including the system frequency and phase response, and maximum output power.

2.2 MEASUREMENTS

The testing requires measurement of several parameters including actuator commanded position, actuator actual position, actuator toad and actuator velocity. The following paragraphs indicate proposed measurement methods for all quantities of interest as well as proposed equipment.

2.2.1 FREQUENCY AND PHASE RESPONSE

The frequency and phase response shall be measured on the inertial test stand by comparing the commanded signal to the actuator output. The commanded and actual actuator positions are available as signal outputs from the motor controller. The inertial test stand also provides the actual position information. A strip chart recorder shall be used to record the measurement data.

2.2.2 FORCE

Force shall be measured using the in-line force transducer on the MOOG force-velocity test stand.

2.2.3 VELOCITY

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Velocity **shall** be measured **using the instrumentation on the** MOOG forcevelocity test **stand.** The velocity is also displayed on the motor controller's computer monitor.

2.2.4 TEMPERATURE

There **are** two temperature measurement points within the system. One is part of the heat exchanger system and indicates the temperature of the coolant in the cold plate. The second temperature measurement point is a thermocouple embedded within the motor stator. This temperature is monitored using a temperature meter to ensure that the temperature does not exceed the maximum operating parameters during testing.

2.2.5 ACTUATOR POSITION

The actuator position is measured by the LVDT (Linear Variable Displacement Transducer) mounted in the actuator's housing. The signal from this device is processed by the Controllers computer software and is available as an output to the strip chart recorder.

2.3 TESTS

The testing is performed on two different test stands:

2.3.1 ACTUATOR INERTIAL TESTING

This series of tests measures the frequency response, phase response and step response of the entire system, with the actuator operating on an inertial test stand.

2.3.2 ACTUATOR VELOCITY/FORCE TESTING

This series of tests measures the maximum power output of the system, with the actuator operating in a hydraulically loaded test stand.

3.0 TEST PLAN PROCEDURES

3.1 ACTUATOR INERTIAL FREQUENCY/PHASE RESPONSE TEST

- 1. Install the actuator in the inertial test stand and power up the motor controller.
- 2. Initialize the controller.
- 3 Apply the specified SINUSOIDAL command (see table) for a ten cycle minimum.
- 4. Plot the position vs. command on an X-Y plot.
- 5. Record the position and command signals on a strip chart recorder.
- 6. Power down the DC power source.
- 7. Power down or reset motor controller.

3.2 ACTUATOR STEP RESPONSE TEST

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- 1. Install the actuator in the inertial test stand and power up the motor controller.
- 2. Initialize controller.
- 3 Apply the specified STEP command (see table) for a five cycle minimum.
- 4. Record the position and command signals on a strip chart recorder.
- 5. Power down the DC power source.
- 6. Power down or reset motor controller.

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3.3 ACTUATOR FORCE/VELOCITY TEST

1. Install the actuator in the Force/Velocity test stand and power up motor controller.

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2. Initialize controller.

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- 3 Apply the specified command (see table) for a five cycle minimum at the specified load.
- 4. Record the commanded and actual actuator position, and velocity on a strip chart recorde
- 5. Power down the DC power source.
- 6. Power down or reset motor controller.

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APPENDIX G

Mooc FACILITY TEST **DATA**

The **no load** test data was recorded **under** the **following conditions:**

- Kp *=* 13.0, Ki = 0.1, **and** Kr *=* **50.0** unless otherwise noted. \bullet
- Command and Position scales are equal on the strip charts unless otherwise noted. \bullet

SECTION 1 - FORCE-VELOCITY TEST DATA

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SECTION 2- STEP RESPONSE TEST DATA

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SECTION 3 – FREQUENCY RESPONSE TES **DATA**

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SECTION 4- STIFFNESS TEST DATA

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APPENDIX H

WORST CASE ERROR ANALYSIS

Reproduced from 40 HP Electro-Mechanical Actuator Test Repo NASA Contract: NAS325799 - August 1993

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 $\label{eq:2.1} \frac{1}{\sqrt{2}}\int_{\mathbb{R}^3}\frac{1}{\sqrt{2}}\left(\frac{1}{\sqrt{2}}\right)^2\frac{1}{\sqrt{2}}\left(\frac{1}{\sqrt{2}}\right)^2\frac{1}{\sqrt{2}}\left(\frac{1}{\sqrt{2}}\right)^2\frac{1}{\sqrt{2}}\left(\frac{1}{\sqrt{2}}\right)^2.$

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Worst Case Error Analysis

Motor Parameters Determine errors in Coreloss (Rm) and Magnetizing $\hat{\eta}$ Inductance (L_m) Data Point: Frequency = 730HZ No Load motor Test Harmonic Voltage per $d = 92.2V$ エスレ Fundamental Voltage por $\phi = 85.1V$ \pm 5% Current per phase = 54.74 ± 3.14 $Error$ in power measurement = \pm 3/0 VA Error in P.F. measurement = \pm 4% $PT = 0.7$ Core Loss $R_{m_{max}} = \frac{(92.2 + 2)^2}{(92.2 \times 54.9 - 310)^{(6.14 \times 0.96)}}$ $R_m = \frac{V^2}{\rho_{\text{real}}}$ ultre $$ R_M max = 13.9 \in $=- (90.2)^2$ (70.2)
 $(5063 + 3/0)$ (714×1.04) سيستعلم R_{wmin} (12.22) 13.4 2 max (+13.9%)
1055 10.42 min (-14.8%) 78Z W (112.62) POWER Accuracy measured = $645W$ $-9.17)$ 352
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3 Determine Rm and Lm at low trequency $\label{eq:1} \mathcal{L}(\mathbf{x}) = \mathcal{L}(\mathbf{x}) \mathcal{L}(\mathbf{x})$ and low voltage (High error data point) Data Point. Frequency = 150 Hz No Load Motor Test Harmonic Voltage per $4 = 23V + 0.6V$ Fundamental Voltage per d = $9.6V$ \pm 5% Current por ϕ = 28A ± 3.1A Error in power measurement = $93VH$ $PF = 0.15$ Error in P.F. measure mont $4 - 1$ \int Core Loss $R_{m \text{ max}} = (\text{z} \text{ s.6})^2$ $= 7.01$ $(644 - 93)(0.15 \times 0.96)$ $=$ $(22.4)^2$. $2n$ min $= 4.4ⁿ$ $(644 + 93)(0.5 \times 1.04)$ 7 7.0 R max (+31.1%) $> -4.4r$ -min- (-17.67) 115 W max $(+16.2)$ P_{real} = 99 W power accuracy ≥ 79.3 W min (-19.7)

B) magnetizing Inductance $(9.6)(1.05)$ Veund $Zn1$ max Z_{n} Znl m4 $Zn \mid min = \frac{(9.6)(0.95)}{(28+3.1)}$ $0.29J$ $X_{M_{max}} = \left[\frac{1}{0.29} - \frac{1}{4.4}\right]^{-1} = 0.31$ $\left[\frac{1}{0.40} - \frac{1}{4.4}\right]^{-1} = 0.44$ \times n \cdot nīvi \Rightarrow $.67E-4$ (+20.1%) L_{m} (3.89 E-4) -4 $(-15.496$ -3.29 ~ 100 $\sim 10^{-10}$ \mathcal{L}_{max} and \mathcal{L}_{max} and \mathcal{L}_{max}

3 Rotor Resistance (Pir) and Leakage Inductance (Leg)

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 \odot Inductance Leakage (B) $\frac{V$ emdamental Z_{br} = $Z_{\rm br}$ max. -0.281 $\frac{19.95}{72.2}$ max 0.23 μ min $\frac{18.05}{78.4}$ $Z_{\rm br}$ min $X_1 = \sqrt{Z_{5r}^2 - \beta_{eq}^2}$ $\sqrt{(0.28)^2 - (6.9E^{-2})^2}$ $= 0.27$ $X_{L_{max}}$ $\sqrt{(0.23)^2 - (9.63E^{-2})^2} = 0.21$ $\cdot \chi_{L}$ Leq= $\frac{\chi_{L}}{2\pi f}$ 5.89 E-S Henries Leq_{max} \equiv $4.58E-5$ Henrics Leg -min $7.5.89E-5(113.14)$ - Leg (5.21E-5) -5 (-12.1%) $4.58E$

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\frac{1}{2}\int (continued \text{D}x \text{ power min } = \frac{(\frac{17.8}{1})(\frac{40.93}{40.3})}{0.000 + 149} = \frac{147.23 \text{ W}}{0.000 + 149} = \frac{(\frac{17.8}{100})(\frac{40.93}{40.3})}{\frac{5250 - 12}{5250 - 12}} = \frac{147.23 \text{ W}}{300 \text{ N} \cdot 140} = \frac{(\frac{53.75 \text{ in.8b} + 3\frac{3.3b}{3}}{5252 - 12})}{\frac{5252 - 12}{5252 - 12}}
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m\text{ by } \frac{10}{100} = \frac{(\frac{53.75 \text{ in.8b} + 3\frac{3.3b}{3}}{5252 - 12})}{\frac{5252 - 12}{5252 - 12}}
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m\text{ by } \frac{10}{100} = \frac{(\frac{53.75 \text{ in.8b}}{5252 - 12})}{(\frac{12.43 \text{ H}P)(\frac{746}{40} \text{ W/HP})}{(\frac{147250 \text{ W})}{\frac{147250 \text{ W}}{1472}} \times 100} = \frac{63.75}{\frac{63.75 \text{ in.8b}}{1477 \text{ U}} \times 100} = \frac{63.75}{\frac{63.75 \text{ in.8b}}{1477 \text{ U}} \times 100} = \frac{63.75}{\frac{63.6 \text{ m}^2}{\frac{1477 \text{ U}}{339}}}
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Power Stage Efficiency Error

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Determine Error in Power Stage Efficiency Error Data Point: M_0 +or $PF = 0.63$ $=$ 700 HZ $Frequency$ $~t$ o.5% 3450 RPM Dyno Speed Current per phaze = $68.4A$ $\pm 3.1A$ Apparont
Power = 6036VH Voltage per phase = $87.6V$ = 2.0 V $±$ 310 VA Real Power Out = 11930 W \pm (1860 VH $x PF$)
(inverter) Powe Factor $\alpha t = 0.54 \pm 0.5\%$ $(n \vee e \vee f \vee f)$ 20 KHZ Voltage = 350 V ± 1.9V Apparent Power out = 21770 VA ± 1860VA $(mwrtw)$ 20 KItz current out = 62.2A \pm 3.1 A $(invertex + ev)$ (Apparent power + 1860M) (PF + 0.52) Real Power
(inverter MAX) $=$ $(21770 - 11860)(-51 \times 1.005)$ = $12824W (+7.5\%)$ as rower
(inverter min) = (Apparent Power -1860) (PF -0.5%) $Real$ Power $\bigcap_{i=1}^{\infty}$ = $10.698 \text{ W} \left(-10.3 \text{ K}\right)$

 $\widehat{5}$ (continued) (Apparent Power + error) (PF + error). Power Stage Out (Par phase) $(6036 + 310)(0.63 \times 1.005)$ Power stage Out
(por phase) may = $4018 \frac{w}{phase} (+6.3\frac{u}{b})$ $(6036 - 310)$ (0.63 x 0.995) Power Stage out (per phase) min $3589 \text{ W}/phase (-5.1\%)$ Power Out Motor \bullet Power Stage Ett. $t_0 =$ $X100$ Power in P.S. 3 phaxs 4018 x3 Stage Eft max Power $X100$ 10698 $1/3$ % _____35.89 x3 Power $5 + aqe$ -Eft $-mn$ $X100$ $12^{4}824$ $84%$ \equiv 100 % max Power Stage Eff. (87%). 64% win 361

Motor Efficiency Errors (6)

Determine the Error in the motor Efficiency calculations Data Point: $700Ht$ Freguency \equiv 3460 RPM j motor speed = 13,600 Dyno speed 4845 V # \pm 3/0 V # Apparent Power fer phase) $0.61 \pm 0.5\%$ Power Factor $=$ 44.5 in-lbs \pm 3 in-lbs Motor Torgue \equiv [Apparent Power ± Erior] [PF ferror] Single Phase Motor Input Power Motor Power = $(4895 + 310)(0.61 \times 1.005)$ $= 3191 \frac{v}{d}$ $\frac{1}{2}$ motor $\frac{1}{2}$ fower = $(4845 - 310)(0.61x0.995)$ min $= 2783$ ν/ϕ $=$ (motor tirque t torged error) (RPM) Motor Power det act $(12) (5252)$ $=$ 10.25 HP motor power max $motor$ power min = 8.96 HP

 $(c$ *ntinued*) motor power outman x 100 Maximum efficiency motor power in min mo $\frac{(10.25HP)(746W/HP)}{2783W/428}$ $92₀$ $\frac{(8.96 \text{ H}P)(746 \text{ W}/\text{H}P)}{(3191 \text{ W}/p)(30)}$ minimum efficiency $modo$ 70% 92% max motor efficiency (80 %) 70.6 min $\bar{1}$, $\bar{1}$

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APPENDIX I

PHASE CURRENT WAVEFORMS

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