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Topography-free sample for thermal spatial response measurement of scanning thermal microscopy

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A novel fabrication technique is described for the production of multimaterial, lithographically defined, topography-free samples for use in experiments to investigate the nature of contrast in scanning probe microscopy (SPM). The approach uses a flat sacrificial substrate as the base for fabrication, which is deleted in the final step. This leaves an exposed, flat surface with patterns of materials contrast defined during the lithography stages. In the example application presented, these are designed to challenge the detection ability of a scanning thermal microscopy (S_{Th}M) probe, although many other applications can be envisioned. There are many instances in SPM where images can exhibit topographically induced artifacts. In S_{Th}M, these can result in a change of the thermal signal which can easily be misinterpreted as changes in the sample thermal conductivity or temperature. The elimination of these artifacts through postprocessing requires a knowledge of how the probe responds thermal features of differing sizes. The complete sample fabrication process, followed by successful topographic/thermal scanning is demonstrated, showing sub-1.5 nm topography with a clear artifact-free thermal signal from sub-100 nm gold wires. The thermal spatial resolution is determined for the sample materials and probe used in this study to be in the range of 35–75 nm. © 2015 American Vacuum Society. [<http://dx.doi.org/10.1116/1.4933172>]

I. INTRODUCTION

As research and manufacturing processes continue to push electronic and optical devices further into the nanoscale, the measurement of such small structures has become ever more challenging for conventional metrology techniques. Scanning probe microscopy (SPM), with its ability to study surfaces in various environments with nanoscale resolution, has become a popular tool. The ability to integrate various sensors directly onto the SPM tip enables the mapping of many physical characteristics beyond topography, such as electrical conductivity,¹ magnetic domain structures,² and temperature.³ However, complex tip-sample interactions can make it difficult to obtain truly quantitative results from either contact or noncontact SPM. A general problem is that artifacts caused by topographic variations of the sample can easily obscure contrast resulting from changes in the properties of a material. Many SPM techniques have been demonstrated to suffer from severe topographically induced artifacts, including conductive AFM (Ref. 4) and scanning near-field optical microscopy.^{5–7} In order to fully investigate the interactions between tip and sample, it is essential to obtain SPM/sensor data from samples without significant topography. Here, we present a reliable method for the production of lithographically patterned samples, exhibiting strong material contrast but with minimal surface topography. The utility of these samples is then demonstrated using scanning thermal microscopy (S_{Th}M) in active mode. This mode employs a sensor, located at the AFM tip, which has been heated above ambient temperatures through Joule heating. As the tip is scanned

across a surface, changes in tip-sample thermal interaction result in variations in tip temperature, which are detected electrically.

Thermal measurement at the nanoscale is a subject of significant practical importance. Temperature control has become perhaps the most crucial issue in nanoscale devices due to Joule heating caused by very high power densities.⁸ This issue may be further exacerbated in advanced devices such as nanowire-based semiconductor transistors as a result of their low thermal conductivity.^{9–11} A number of techniques, such as Raman spectroscopy,¹² infrared imaging,¹³ and near-field optical thermography,¹⁴ have already been developed to measure the temperature in microdevices. However, the resolution of these techniques is limited to the micrometer-scale and is not suitable for obtaining temperature distributions in sub-100 nm devices. S_{Th}M^{15–17} makes the achievement of high spatial resolution temperature measurement a reality. Besides temperature measurement, the nanoscale study of material thermal properties such as thermal conductivity can be achieved using active mode S_{Th}M. This has been an area of focus in recent years, offering many applications in microelectronics where functional materials such as strained layers may not be available as bulk samples for measurement.^{18–20} However, as with many other SPM techniques, S_{Th}M suffers from topographically induced artifacts in the data it generates. To thoroughly interpret the results obtained using S_{Th}M, these artifacts have to be either eliminated or be comprehensively understood.^{21–23}

S_{Th}M thermal data distortion, induced by heat transfer between the cantilever and the sample through the air, is considered to be one of the major artifacts. However, advanced scanning methods such as the “double-scan technique”²⁴ and “topography-correction technique”²³ have been demonstrated

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to effectively diminish this artifact. Another major artifact, induced by the topography of the sample, is hard to eliminate due to its complexity^{25–28} and it is this example that will be considered here. The heat transfer between the probe tip and the sample has been proven to occur through a combination of pathways, including thermal conduction through the solid–solid contact, water meniscus, and air and thermal radiation (in near and far fields) when performing measurement under ambient conditions. Gotsmann and Lantz²⁹ have studied the effect of surface roughness on the thermal interface conductance under vacuum conditions. By constructing a model that describes the thermal transport across individual contact points, they defined the conductance per atom–atom contact as

$$G_{\text{atom}} = G_Q N \tau, \quad (1)$$

where G_Q is the universal thermal conductance of a quantum point contact, N is the number of different phonon modes, and τ is the corresponding transmission coefficient. As the number of contact points varied with changes in surface roughness or topography, the thermal signal was seen to vary due to the change in the number of atom–atom contacts. Several groups have observed this phenomenon in their thermal scans, for example, Soudi *et al.*²¹ reported a “double-line-like” thermal profile when scanning 40–60 nm heated nanowires in both air and vacuum in passive mode. In the presence of these topographically induced artifacts, the local temperature of the nanowire could only be approximated by averaging the whole region, which is undoubtedly inaccurate.

Efforts have been made to obtain real thermal images without topographically induced artifacts by employing an algorithm to postprocess the thermal signal.³⁰ However, this approach is not straightforward for samples which exhibit both material and topographic variation. In summary, knowledge of the thermal spatial resolution of any given SThM probe type will benefit the quantitative study of heat transfer mechanisms between the tip and sample, which is still a controversial subject.³¹ Recent work in this area has focused on ways to estimate the real resolution while taking into account the meniscus radius and issues relating to the mechanical contact.^{24,32–34}

In contrast to other SThM probes, such as the Wollaston probe which exhibits poor lateral resolution, $\sim 1.5 \mu\text{m}$, due to a blunt tip,^{31,35} microfabricated probes have a nanoscale tip radius allowing them to image smaller thermal features.²⁵ SThM probes are used in two modes—active mode in which the self-heated probe is cooled by transferring heat to the sample and passive mode in which the probe receives heat flowing from the heated sample. Zhou *et al.*³⁶ observed a thermal spatial resolution of hundreds of nanometers by scanning a 300–1500 nm thick palladium acetate thin film with a probe operating 20–40 °C above room temperature. In addition, Sadat *et al.*³⁷ used a thermocouple tip with ~ 20 nm radius to scan heated features on a metallic surface, successfully achieving sub-100 nm thermal–spatial resolution. Shi *et al.*³⁸ scanned a heated 14 nm diameter multiwall carbon

nanotube and observed a resolution of around 50 nm, consistent with the tip radius of the SThM probe used. Hinz *et al.*³⁹ observed 25 nm thermal–spatial resolution under high vacuum conditions in experiments aimed at determining the thermal conductivity of a 3 nm thick HfO_2 film. However, few researchers have considered the effect of topography on the measurement other than to note its presence.

In this paper, we present a fabrication method for the production of a topography-free sample consisting of two materials, gold and Si_3N_4 , with a large difference in thermal conductivity. Thin-film narrow gold wires have been patterned and then scanned using a microfabricated SThM probe working in DC active mode in air. By analyzing the results, the thermal spatial resolution of the probe in the absence of topographic artifacts can be clearly observed.

II. DESIGN AND FABRICATION

When considering sample planarization, chemical mechanical planarization (CMP), which is widely used in the integrated circuit industry is an obvious choice. However, this technique has drawbacks such as dishing and erosion, where some regions and materials polish faster than others limiting the range of different materials within can be used. This is combined with the high complexity and cost of developing a process and the potential for significant surface damage, making CMP unattractive for scanning technology.⁴⁰ An alternative approach, employing beam exit Argon ion cross-section polishing has been proposed by Kolosov *et al.*⁴¹ This provides a method to produce a sample surface with topography down to 1 nm, which is suitable for SPM scanning. However, this approach offers limited flexibility in the definition of material patterns, as well as limited control of the feature thickness.

One possible approach for eliminating surface topography of a lithographic pattern is through filling an etched cavity using a second material. This would require two aligned levels of lithography for a single pattern: one for etching and the other for deposition. However, there will be inevitable misalignment between the levels, even if the most accurate alignment methods were employed.⁴² The consequence of any misalignment would be inconsistent thickness resulting from a step or trench between the cavity and the deposited material, resulting in an overall increase in peak-to-peak topographic variation.

The sample fabricated in this work employed a different approach. The process is based around common, generic resist-based pattern transfer techniques compatible with a wide range of materials: lithographically defined metal patterns were fabricated on a sacrificial substrate before being embedded into an epoxy-supported backing. Finally, the initial “sacrificial” substrate was deleted. This method takes advantage of the high quality surfaces obtainable using CMP polished wafers, while simultaneously avoiding the chemical and mechanical damage generated by polishing the pattern itself.

The complete fabrication process flow is shown in Fig. 1. The desired pattern is deposited onto a sacrificial GaAs substrate as shown in step (a). This process employs a traditional bilayer PMMA resist patterned using 100 kV electron beam lithography.⁴³ The resist stack consisted of 38 nm of PMMA having a molecular weight of 85 kDa and 38 nm of PMMA having a molecular weight of 350 kDa. Twenty nanometers gold was evaporated, followed by “lift-off” in 50 °C acetone for 12 h. The pattern consisted of wires located in a 1 μm gap between two large gold pads, permitting easy pattern location within the AFM/SThM using an optical microscope. It should be noted that the process is compatible with thicker layers of material if the highest resolution is not required and if thicker resist is used. Structures of thickness up to 150 nm have been successfully patterned by the authors using this technique.

The fabrication of the backing substrate is a multilayer process as shown in the following steps from Fig. 1: 400 nm low stress inductively coupled plasma chemical vapor deposition (ICP-CVD) SiN_x was deposited⁴⁴ onto the sample which had been dehydrated on 180 °C hot plate for 5 min to improve adhesion (b); then a 100 μm thick layer of SU-8 3050 was spun onto the processed face of the sample (c); and finally, the sample was heated and a piece of quartz was placed onto the hot SU-8 to provide a rigid mechanical handle for the released sample (d).

Once the full material stack was complete, the GaAs substrate was deleted using a citric acid/ H_2O_2 wet etch, uncovering the Au pattern and SiN_x background. The finished sample is as shown in step (e).

GaAs was chosen as the substrate due to its availability as a polished wafer, electrical conductivity (useful when using electron-beam lithography) and relative ease of wet etching.⁴⁵ Although silicon would appear to be a more economic choice, the available wet⁴⁶ and dry etches⁴⁷ would potentially damage the Au/ SiN_x pattern resulting in a roughening of the surface. Wet etching of GaAs is possible using various solutions such as citric acid, H_2SO_4 , HCl, and HF with H_2O_2 , but it was decided to use 50% citric acid with H_2O_2 to minimize pattern damage.⁴⁸ The GaAs etch rate is strongly dependent on the temperature and ratio of citric acid and H_2O_2 , with 5:1 citric acid: H_2O_2 giving the highest etch rate (~ 250 nm/min) at room temperature.

Gold was chosen as an evaporated pattern material due to its ease of fabrication, chemical resistance to etching,⁴⁸ and high thermal conductivity. The thermal spatial resolution of SThM probes is commonly claimed to be sub-100 nm, with the heat conduction area considered to be the same as the tip curvature radius.^{33,49} To address these length scales, narrow gold wires with width ranging from 35 to 75 nm were fabricated as patterns.

The fabricated backing substrate is a sandwich structure: a conformal layer of SiN_x around the gold structure, a thick layer of SU-8 cured epoxy and a rigid quartz handle. The SiN_x offers three important properties: good adhesion to the gold patterns, large thermal contrast with gold, and resistance to the GaAs wet etch. The stack adopted replaces an initial design for the backing substrate which was simply to employ a double-layer of SU-8 3050. However, after release etching, few patterns were successfully transferred onto the

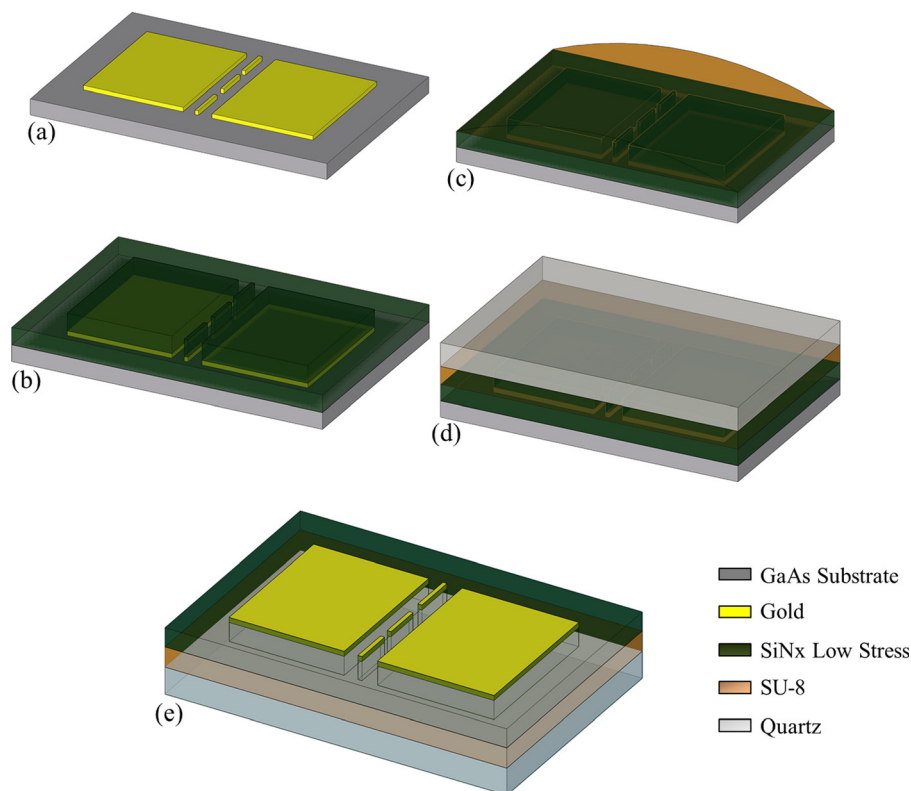


FIG. 1. (Color online) Fabrication process of the topography-free sample.

SU-8 substrate due to the poor adhesion of gold to SU-8. ICP CVD was chosen due to its ability to deposit SiN_x at room temperature, maximizing the possible number of compatible materials for the lithographic pattern and eliminating any topography associated with differential thermal expansion during fabrication. Two types of SiN_x , compressive low stress (~ 100 MPa) and compressive high stress (~ 3 GPa)⁴⁴ were tested. The deposition gas used for both is SiH_4/N_2 with flow rate of 7/6 sccm under 4 mTorr pressure. Both high stress and low stress SiN_x provide an obvious improvement in the pattern adhesion, however, high stress SiN_x induced small wrinkles in the Au pattern. Figure 2 shows optical and topographic AFM images of the sample after release etching. The wrinkles caused by the high stress SiN_x are clearly visible, particularly in the AFM image [Fig. 2(b)]. As a consequence further samples were prepared using low stress SiN_x only.

The SU8 layer alone does not provide a flat backing material due to its tendency to bow after the GaAs substrate has been dissolved. Therefore, a piece of quartz, which is transparent to UV light, was used to cover the SU-8 at the end of the softbake. In the final structure, the SU8 is utilized more as an adhesive with low thermal conductivity than a substrate.

Following removal of the GaAs, the freshly exposed surface topography was checked using AFM. During this

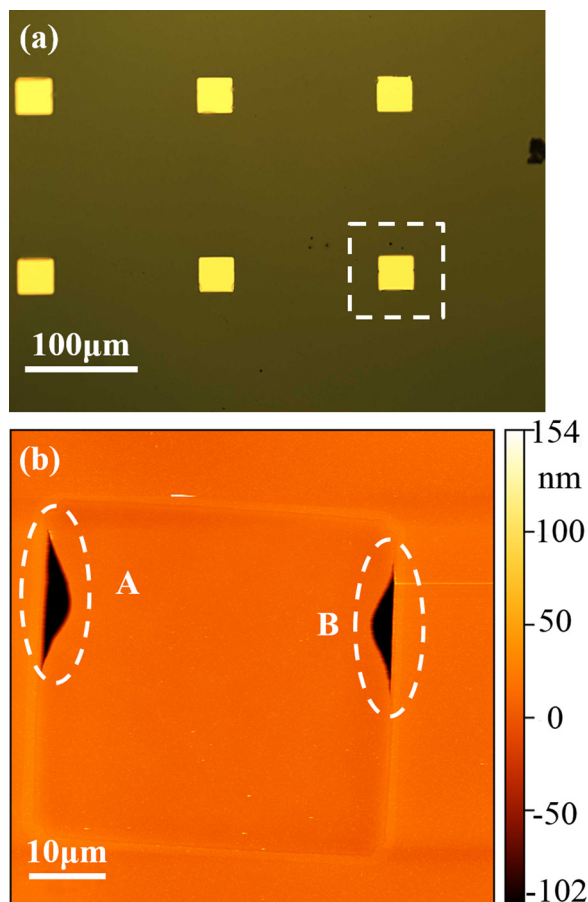


Fig. 2. (Color online) (a) Optical microscope image of the topography-free sample with high stress SiN_x adhesion enhancement layer and (b) AFM height image of the same sample, the scan region is indicated by the dashed square in (a). Wrinkles are highlighted in regions A and B.

inspection, a dependence of the residual topography on the thickness of SiN_x was observed. Repeated experiments were used to optimize the SiN_x thickness. Samples with 100–800 nm thick low stress SiN_x , deposited under the conditions previously described, were studied. For each sample, the average step height between the gold pattern and the SiN_x was obtained by averaging the data from eight to ten patterns on each sample, as shown in Fig. 3. It can be seen that topography reduces with the increasing thickness of SiN_x , until no further improvement is observed (400 nm and above). This is tentatively ascribed to the increased rigidity of the SiN_x film with increasing thickness.

Figure 4 shows SEM and AFM images of the same region on the sample after completion. The images show 60 nm gold narrow wires fabricated at the center of the gap formed by two gold pads.

III. EXPERIMENTAL SETUP

The topography-free sample was scanned using microfabricated, thermally compensated cantilever probes developed in Glasgow.⁵⁰ The experimental setup is shown in Fig. 5 with the inset showing an SEM micrograph of the probe. The probe is Joule-heated by a 0–2.0 mA DC current source controlled by a variable voltage reference, V . The probe forms part of a Wheatstone bridge with R_1 , R_3 , and R_4 , enabling its resistance to be continuously monitored. The differential voltage from the bridge is amplified by an instrumentation amplifier with a voltage gain of 2000. Previous testing has shown that the probe can survive a maximum 2.5 mA DC current without a permanent change in its physical character.

The probe was calibrated using the experimental setup shown in Fig. 6(a). The SThM probe was immersed in an inert fluorocarbon liquid (flutec pp3, F2 Chemicals Ltd.) together with a PT100 thermal sensing element attached. The beaker was coupled to a Peltier thermoelectric module using thermal paste. The temperature of the Peltier was

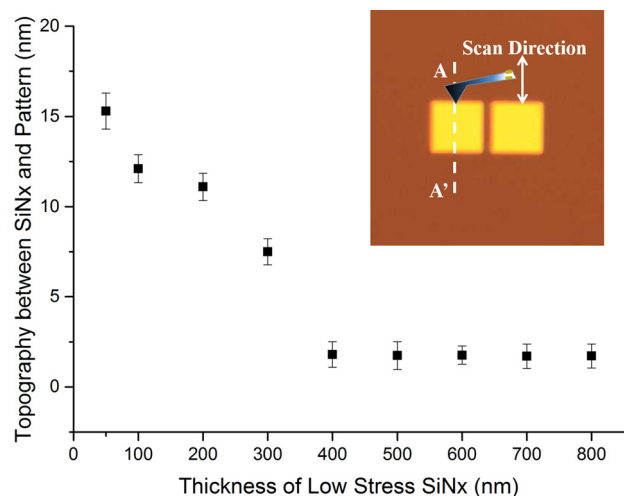


Fig. 3. (Color online) Step heights between gold and SiN_x on the sample surface after release etch vs the thickness of SiN_x . The inset image shows the AFM scan used for obtaining the step height.

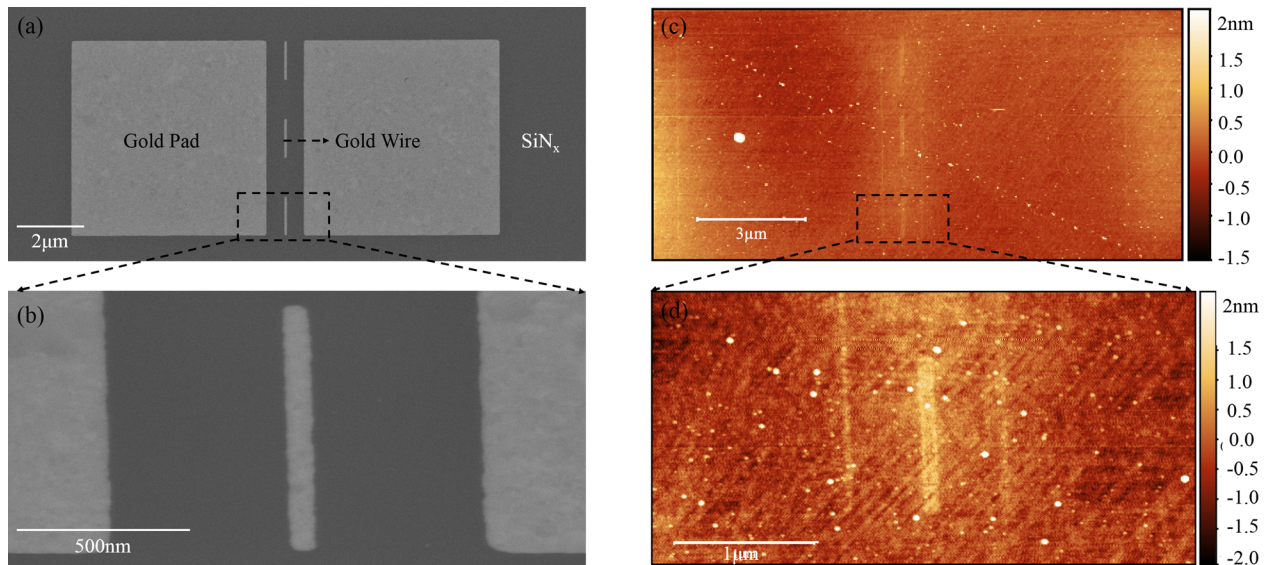


FIG. 4. (Color online) (a) SEM image of the pattern after release etching, (b) detail of the narrow wire zoomed in from (a), (c) tapping mode AFM topographic image showing the same area as (a), and (d) detail of the narrow wire zoomed in from (c) showing a residual topographic variation of less than 2 nm.

varied, and the four terminal resistance of the probe was measured using a multimeter. The temperature of the flutec pp3 was measured using the attached Pt100 resistance element, and it was assumed that this was the same temperature as the SThM probe. Ten values of the probe resistance and flutec temperature were recorded and are shown in Fig. 6(b). A least squares method was used to fit the data to a straight line, giving a sensitivity for the whole probe, consisting gold wires, pads, and the platinum tip of 3.14 ± 0.02948 K/ Ω . The cross sectional area of the platinum tip resistor is much smaller than that of the gold wires or pads on the probe. Therefore, $\sim 99\%$ resistance of the whole probe is

contributed by the platinum, ensuring a high sensitivity to the temperature change at the tip.

In all experiments, 0.8 mA was chosen as the bias current, resulting an out-of-contact probe effective temperature of $\sim 55^\circ\text{C}$. This provided an appropriate compromise between a high thermal signal-to-noise ratio (SNR) with acceptable residual cantilever thermal bending.

IV. RESULTS AND DISCUSSION

Figure 7 shows the SEM, AFM, and in-air SThM scans obtained from the same narrow wire. The width of the gold

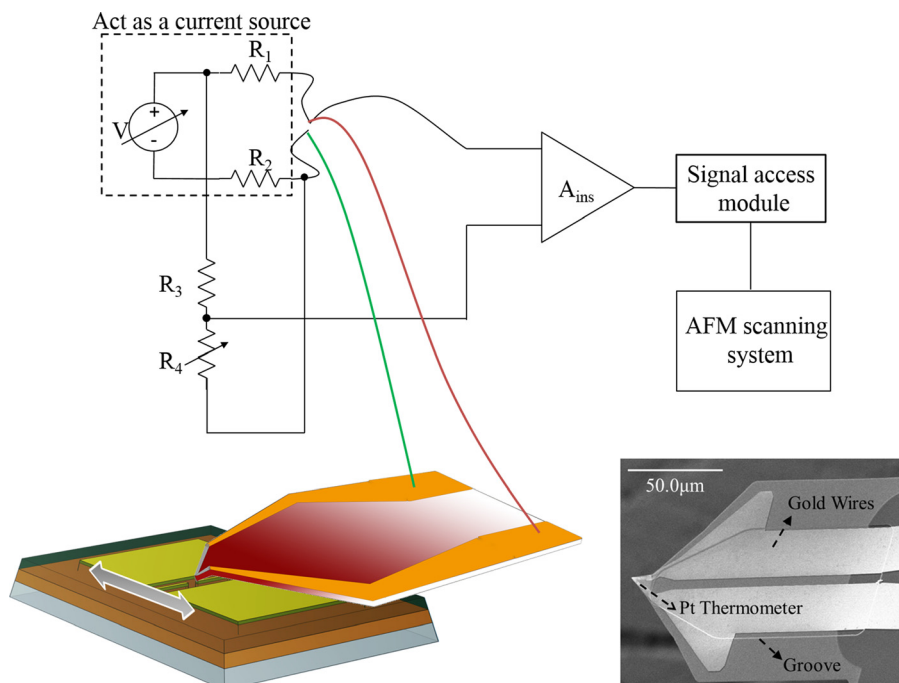


FIG. 5. (Color online) Schematic diagrams showing the electrical arrangement used to bias the probe and to detect the probe electrical resistance change. Inset image shows the SThM probe used with tip, Pt resistor, gold connecting wires and thermal compensation groove are all clearly visible.

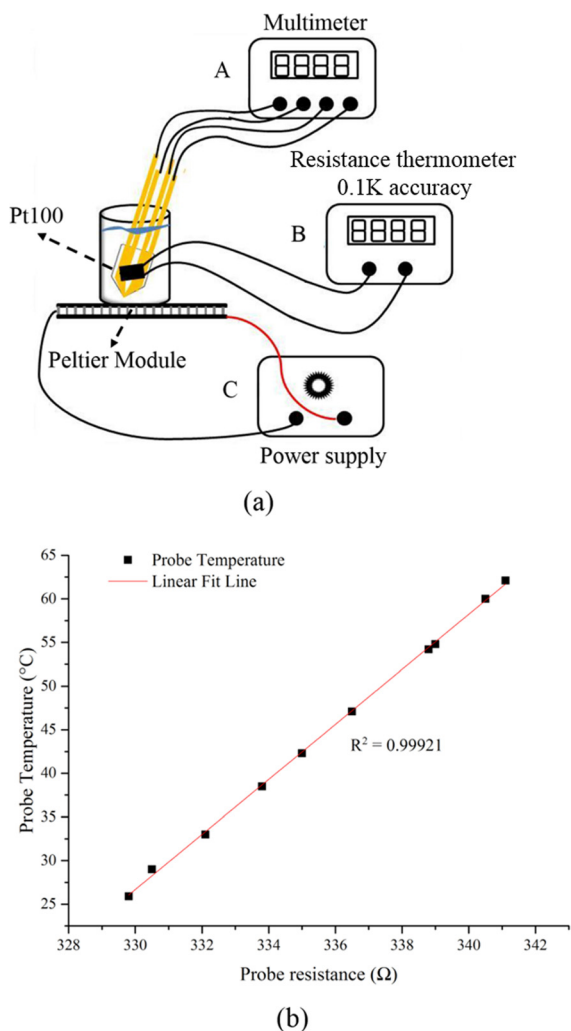


FIG. 6. (Color online) (a) Experimental setup for measuring the temperature coefficient of resistance of the probe, and (b) plot of the probe temperature vs the probe resistance. R^2 , ranging from 0 to 1, is the coefficient of determination that indicates the linearity of the fitted line.

wire is ~ 35 nm as determined by low vacuum backscatter SEM, avoiding the well-known errors associated with secondary electron scattering for linewidth measurement⁵¹ [Fig. 7(a)]. The topographic AFM image [Fig. 7(b)] and the thermal image [Fig. 7(c)] were obtained simultaneously during the thermal scan, and the 35 nm gold wire can be clearly seen. Data extracted from line traces at the same position on all images are plotted in Figs. 7(d) and 7(e). The step height associated with the 35 nm Au wire is ~ 1.5 nm, and the feature is clearly visible on the thermal image.

A comparison of the thermal signal intensity from the 35 nm wire with that of the gold pads shows that the magnitude of the thermal signal from the wire is lower than that of the gold pads, suggesting that it is “incompletely resolved.”

This incomplete resolution can be attributed to the complex heat transfer mechanisms at the tip-sample contact. One powerful approach to aid understanding of the underlying heat transfer mechanisms between the tip and the sample is to use a thermal resistance network, as suggested by Luo *et al.*⁵² Figure 8(a) summarizes the thermal resistance

network for interactions between the probe and the sample. R_t and R_s represent the thermal resistance of the tip and the sample, respectively. The contact thermal resistance, R_{int} , represents thermal interaction between the tip and the sample and is a combination of many factors; however, solid to solid contact (R_{ss}), water meniscus (R_w), and air (R_{air}) conduction are generally believed to dominate over heat transfer through radiation (R_{rad}).^{28,33,53} Gorbunov *et al.*⁵⁴ proved that heat dissipation to the sample from a heated tip is a function of the tip-sample contact radius as well as the sample materials being scanned. This has been investigated by Gotsmann²⁹ who indicated that the contact thermal conductance is dependent on the pressure when the contact radius is constant. Hence, the tip-sample force was kept 20 nN during our experiment. These thermal interactions have been recently discussed in detail by King *et al.*⁵⁵ and Gomès *et al.*²⁸ As the geometry and materials comprising the Si_3N_4 tip will not change significantly, the thermal resistance of tip, R_t , is regarded as a constant value.

In order to obtain a fuller understanding of the thermal profile generated by scanning the SiN_x and gold wire, the thermal resistance of each tip-sample interaction can be estimated. As both the sample and probe have features with dimensions comparable to the mean free path of heat carriers (λ), the effect of heat transfer in the ballistic regime must be considered. Thermal spreading resistance including both diffusive and ballistic heat transport can be calculated using⁵⁶

$$R_{sp} = \frac{1}{4kb_c} \left(1 + \frac{8}{3\pi} K_n \right), \quad (2)$$

where k is the thermal conductivity of the material, b_c is the contact radius, and K_n is the Knudsen number defined as λ/b_c . Sultan *et al.*⁵⁷ have shown that the mean free path of carriers in SiN_x is smaller than 2 nm at room temperature. It has been demonstrated that the contact radius between probes of the same tip geometry as those used in this work and a sample is ~ 50 nm.³⁴ Therefore, thermal transport can be regarded, in this instance, as entirely diffusive due to the small Knudsen number. Furthermore, the SiN_x film thickness of 400 nm makes it unnecessary to consider boundary scattering effects. Thus, the thermal spreading resistance of the SiN_x can be given by^{28,34}

$$R_{\text{SiN}} = \frac{1}{4k_{\text{SiN}} b_c}, \quad (3)$$

where k_{SiN} is the thermal conductivity of the SiN_x [~ 3 W/m K (Ref. 57)]. This gives a value for R_{SiN} in the order of 10^7 K/W. For the gold, the mean free path at room temperature is 10–30 nm, dependent on whether the gold is single crystal or polycrystalline.⁵⁸ Therefore, both diffusive and ballistic transport must be considered. Moreover, as the thickness of the gold is 20 nm, substrate (SiN_x) effects need to be considered when calculating the thermal spreading resistance. Accounting for all of these factors, thermal spreading resistance can be calculated using^{56,59}

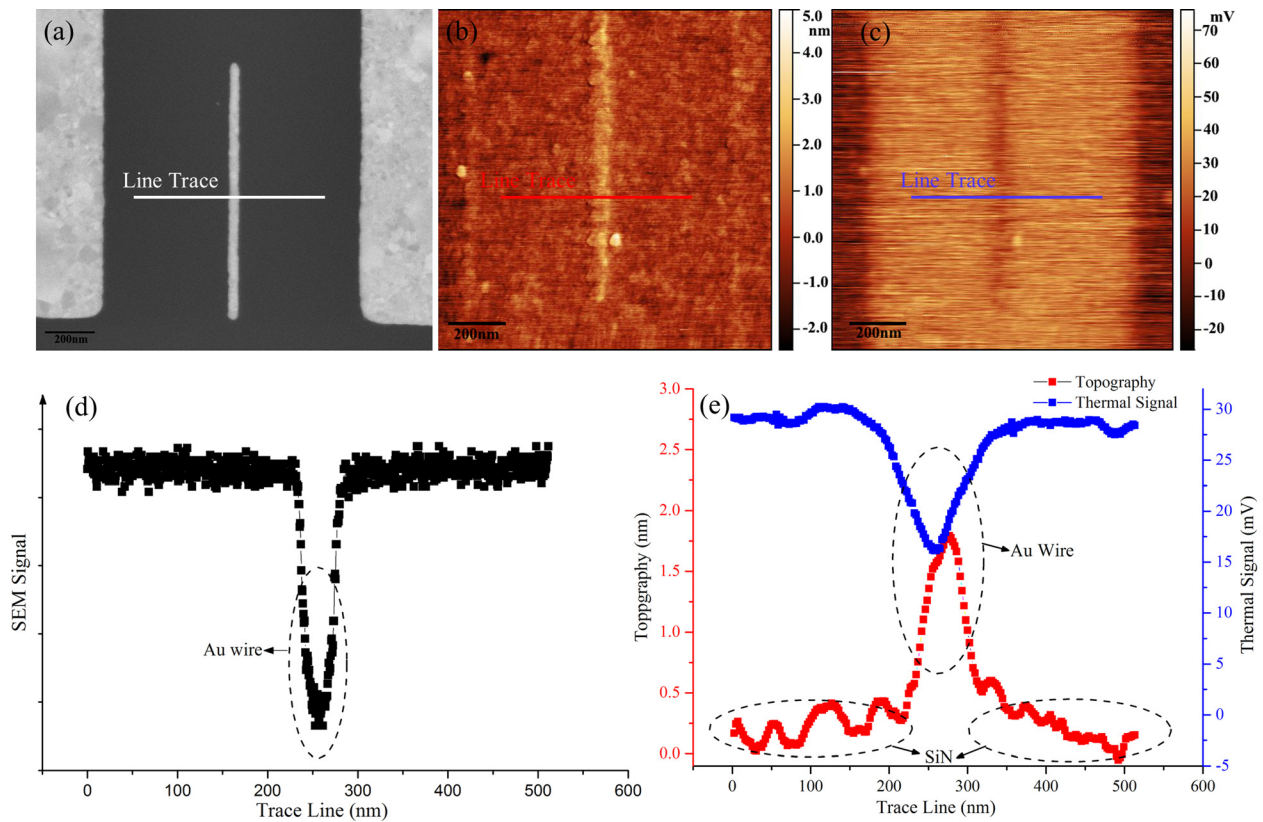


FIG. 7. (Color online) (a) Backscatter SEM image shows the 35 nm narrow gold wire, (b) AFM topographic image shows the same wire as (a), (c) thermal image of the same feature scanned by SThM probe, (d) plot of the line trace for SEM averaged over 16 lines, and (e) topography and thermal signals averaged over 16 lines. Dotted circles in (d) and (e) indicate the signal of the gold wire and SiN_x.

$$R_{Au} = \frac{1}{4k_{Au}bcK_n} - \frac{1}{2\pi k_{Au}t} \log_{10} \left(\frac{2}{1 + \frac{k_{Au}}{k_{SiN}}} \right), \quad (4)$$

where k_{Au} and t are thermal conductivity and thickness of the gold. This gives a thermal resistance for the thin film

gold as being around 10^4 K/W, which is 3 orders of magnitude smaller than that of SiN_x. We can infer from this that a dominant factor affecting the probe temperature is variation in sample spreading resistance. However, it is important to also consider other mechanisms.

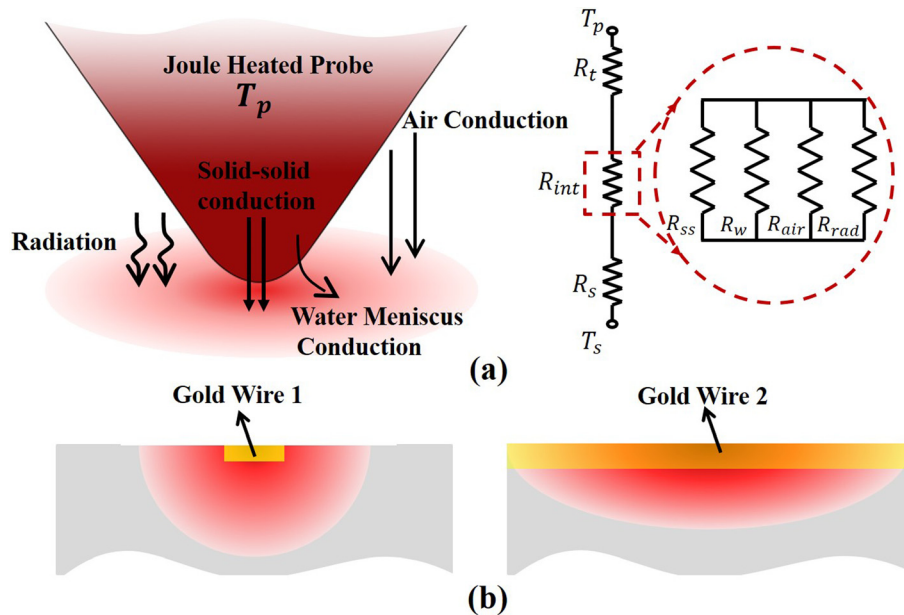


FIG. 8. (Color online) (a) Heat transfer interactions between the probe and the sample, together with the thermal resistance network showing the relationship between each interaction, and (b) different thermal diffusion as a result of variation in the width of the gold wires. Gold wire 1 and gold wire 2 indicate the narrow and wide wire, respectively. The heat spreading is represented by the hemispherical areas surrounding the gold wires.

It has been recognized that the thermal resistance associated with heat transfer through the water meniscus is at least an order larger than that through the solid–solid contact and air conduction.^{27,31} This is in agreement with Assy and Gomès³² who showed that the water meniscus radius for probes of the same tip geometry as that in this work at a similar temperature is smaller than 9 nm, representing just 1%–6% of the total contact thermal conductance. This strongly suggests that the temperature of the tip as it scans across the sample is not dominated by the water meniscus.

Furthermore, the thermal interface resistance between the probe and the sample can be described as^{60,61}

$$R_{ss} = \frac{R_B}{\pi b_c^2}, \quad (5)$$

where R_B is the thermal boundary resistance, having the same unit as the bulk thermal contact resistance. It has been recognized that, under ambient conditions, R_B does not change significantly with sample materials and has a typical value of 1×10^{-9} to 5×10^{-8} m² K/W.²⁸ For the hard materials (i.e., those that do not show plastic damage on scanning) used in this work, solid–solid contact radii (b_c) do not vary greatly, and have been proved to be approximately 50 nm.^{33,34} Therefore, it can be expected that the solid to solid interface thermal resistance, R_{ss} , will lie in the range of 10^5 – 10^6 K/W.

Heat conduction through the air is another interaction that will affect the probe temperature. As the heat coefficient

defined for the air gap between the tip and sample is independent of the sample material,^{56,62} the thermal resistance of the air gap will not vary with SiN_x or gold and is considered to be on the order of 10^5 K/W.⁴⁹ Therefore, when comparing all of these interactions between the tip and sample, the spatial variation of thermal spreading resistance due to different sample materials is the most likely candidate for causing the variations observed in the SThM thermal image.

As shown in Fig. 8(b), for the gold wire with small width (“Gold Wire 1”), heat spread into the sample will be largely controlled by the SiN_x, resulting in approximately cylindrical thermal diffusion. With an increase in the gold wire’s width (“Gold Wire 2”), there is more heat spreading within the gold wire, due to the relatively high thermal conductivity of the gold, resulting in a lower probe temperature. At the limit of large wire width, the thermal signal measured from the gold wires is expected to tend toward the same value observed when in contact with the gold pads, which may be considered to constitute an infinite in-plane feature.

The scan of a 75 nm-width gold wire (Fig. 9) is also consistent with this explanation. The thermal signal shown in Fig. 9 is obviously larger when compared to that of the 35 nm wire (Fig. 7). Specifically, the difference in the thermal signal between the gold wire and the SiN_x are 11.3 mV for the 35 nm wire and 19.8 mV for the 75 nm wire. The large effect of topographic artefacts is also seen in Figs. 9(b) and 9(c) where particles (indicated by the circles A/A' and B/B') are observed to give rise to large thermal contrast.

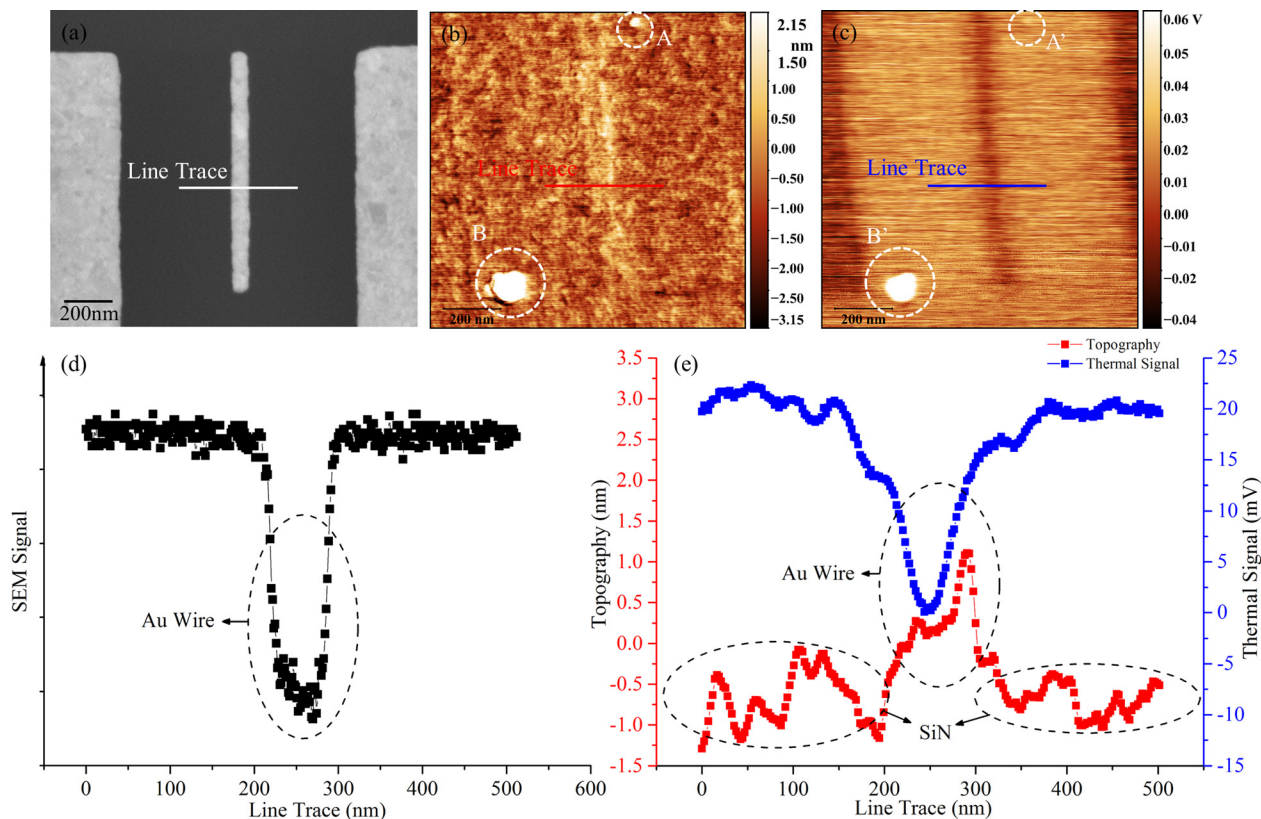


Fig. 9. (Color online) (a) SEM image shows the 75 nm narrow gold wire, (b) AFM topographic image, (c) thermal image shows the same feature scanned by SThM probe, and plots of the line traces from (d) SEM averaged over 16 lines and (e) topography and thermal signals averaged over 16 lines. Dotted circles in (d) and (e) indicate the signal of the gold wire and SiN_x.

It should be noted that there is a small amount of topography associated with the gold features. However, this is of the same magnitude as the general roughness of the background SiN_x and gold (~1 nm). Therefore, any topographically induced artifacts in the thermal image are expected to be of the same size as the background noise present in the thermal signal. Any effect will certainly be much smaller than that present in an equivalent sample produced without using the method described in this work.

In order to easily visualize the change in thermal signal intensity, wires with widths ranging from 35 to 75 nm were scanned using the same experimental setup and the thermal signal from different pattern regions analyzed using a simple ratio, defined as

$$\text{Ratio} = \frac{V_{\text{wire}} - V_{\text{SiN}}}{V_{\text{pad}} - V_{\text{SiN}}}, \quad (6)$$

where V_{wire} , V_{pad} , and V_{SiN} represent the thermal signal obtained on the gold wires, gold pads, and SiN_x, respectively. This ratio can be used to easily assess the relative signal magnitude as observed on wires of differing width scaled to that observed on gold pads. As the thermal signal on the wire approaches that of the background SiN_x, the ratio will approach 0, whereas a ratio of 1 indicates the same signal as that observed on the large Au pads. It can be said that the closer the ratio is to 1, the more “resolved” the Au wire is, that is, the signal becomes increasingly characteristic of the material, not the geometry. Figure 10 shows a plot of the ratio versus the width of the gold wires from 35 to 75 nm. Values for V_{wire} , V_{pad} , and V_{SiN} were obtained from the SThM images by averaging a rectangular 5×50 -pixel region on top of each feature as shown in Fig. 10 inset. The error bars represent ± 1 standard deviation of the averaged values. As expected, the ratio observed on the gold wire slowly increases with an increase in the wire’s width, simultaneously the standard deviation decreases due to the improved SNR associated with a larger signal. This shows that by employing a microfabricated SThM probe to scan a sample consisting two materials having a large difference in thermal

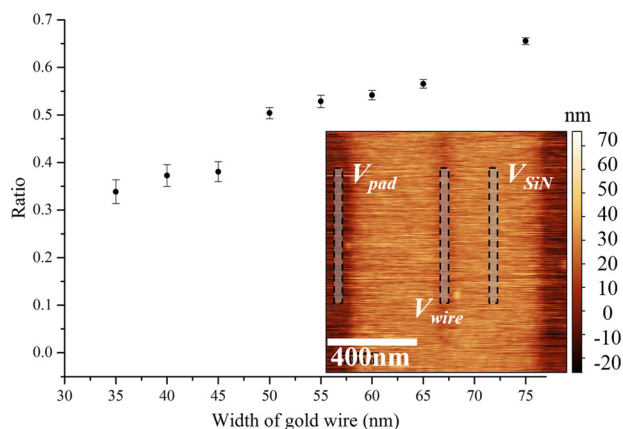


Fig. 10. (Color online) Plot of the thermal intensity ratio vs the width of the gold wire. The inset image shows the 5×50 pixels rectangular regions used to obtain the averaged signal from wire, gold pad and SiN_x.

conductivity (SiN_x and Au in this case), the variation of pure thermal materials contrast can be quantified as a function of the size of the object being imaged. Hence, when scanning an object which is large, the contrast will have a limiting value, which is characteristic of the materials imaged. In contrast, for small objects, the contrast diminishes in a way which is in some sense characteristic of the material and the effective size of the SThM probe. By measuring this variation as a function of sample size, a robust and reproducible measure of the characteristic length scale which determines a SThM measurement of thermal conductivity may be obtained. In the present case we observe a smooth degradation of contrast for length scales down to 35 nm, such that the observed signal at 75 nm scale has a contrast level which is 70% of that measured for large area films. This number is consistent with published claims of resolution for SThM using microfabricated probes.

V. CONCLUSION

We have demonstrated a general procedure to achieve lithographically defined, topography-free samples with significant materials contrast. The fabrication of the sample uses the commonly available fabrication methods of ebeam lithography, metal evaporation and liftoff, dielectric layer deposition, and wet etching. The utilization of multiple layers provides a stable and robust substrate. The method may be readily extended to allow the production of more complex samples with multiple materials and higher resolution patterns. It is also compatible with photolithographic pattern definition, allowing the low cost production of calibration samples.

The utility of these samples has been illustrated using SThM to generate high-resolution images, free of topographically induced artifacts. This data allowed the in-air thermal-spatial response of a micromachined SThM probe to pure materials contrast to be unambiguously determined at length scales in the range of 35–70 nm.

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