

Multilevel Inverters: Literature Survey – Topologies, Control Techniques & Applications of Renewable Energy Sources - Grid Integration

Er. Mamatha Sandhu *, Dr.Tilak Thakur **

*(Department of Electrical Engineering, Chitkara University, Punjab Campus, Rajpura-01, India)

** (Department of Electrical Engineering, PEC University of Technology, Chandigarh-12, India)

ABSTRACT

Multilevel inverters are in favor of academia as well as industry in the recent decade for high-power and medium-voltage applications. In addition, they can synthesize switched waveforms with lower levels of harmonic distortion compared to a two-level converter. Multilevel converters have received increased interest recently as a result of their ability to generate high quality output waveforms with a low switching frequency; the multilevel concept is used to decrease the harmonic distortion in the output waveform without decreasing the inverter power output. This paper presents a review on most important topologies, control techniques of multilevel inverters and also the applications powered by multilevel inverters which are becoming an enabling technology in many industrial and research areas.

Keywords - Multilevel inverter, Neutral point clamped, CHB, FACTS and Renewable energy sources.

I. INTRODUCTION

In recent years, multilevel inverters have gained popularity with medium and high power ratings. Renewable energy sources such as photovoltaic, wind, and fuel cells can be interfaced to a multilevel converter system [1]. Many multilevel converter topologies have been proposed during the last two decades. Research has engaged novel converter topologies and unique modulation schemes. The three types of multilevel converter structures reported in the literature are: cascaded H-bridge converter with separate dc sources, diode clamped (neutral-clamped), and flying capacitors (capacitor clamped). Modulation techniques and control paradigms have been developed for multilevel converters such as sinusoidal pulse width modulation (SPWM), selective harmonic elimination (SHE-PWM), space vector modulation (SVM), and others. Many, multilevel converter applications include industrial medium-voltage motor drives, renewable energy systems with utility interface, flexible AC transmission system (FACTS), and traction drive systems. The common multilevel converter topologies are the neutral-point-clamped converter (NPC), flying capacitor converter (FC) and Cascade H-Bridge (CHB) have developed from last two decades. Multilevel inverters are best for medium and high power applications [2].

II. MULTILEVEL INVERTER-TOPOLOGIES AND CONTROL TECHNIQUES

2.1 DIODE CLAMPED INVERTER

Effective control technique for medium-voltage high-power induction motor fed by cascaded neutral-point-clamped inverter [3]. [4], five/nine-level twelve-switch inverter for three-phase high-speed electric machines having a low per-unit leakage reactance is described. [5], used a new single-inductor multi-output dc/dc converter that can control the dc-link voltages of a single-phase diode-clamped inverter asymmetrically to achieve voltage quality enhancement. [6], comparative analysis between the classical structure of Neutral Point Clamped (NPC) and the emerging Active NPC converters. [7], DC bus short circuit protection is usually done using the sensed voltage across collector and emitter (i.e., *VCE* sensing), of all the devices in a leg. [8], introduced a new nine-level active neutral point-clamped (9L ANPC) converter for the grid connection of large wind turbines (WTs) to improve the waveform quality of the converter output voltage and current. [9], investigations of dc-link voltages balance with the use of a passive RLC circuit in a single-phase diode clamped inverter composed of two three-level legs. [10], three-level active neutral point-clamped zero-current-transition (3L-ANPC ZCT) converter for the sustainable energy power conversion systems. [11], new modulation techniques for three-phase transformer less neutral point clamped inverters to eliminate leakage currents in

photovoltaic systems without requiring any modification on the multilevel inverter.[12], comparison between three level diode neutral-point-clamped zero-current transition (DNPC-3L ZCT) inverter and three-level active neutral-point clamped zero-current-transition (ANPC-3L ZCT) inverter, with respect to switching energy, volume, as well as parasitic inductance influence, the topologies are compared.

2.2 CASCADED MULTILEVEL INVERTER

[13], Cascaded H-bridge multilevel inverter can be implemented using only a single dc power source and capacitors.[14], cascaded H-bridge multilevel boost inverter for electric vehicle (EV) and hybrid EV (HEV) applications implemented without the use of inductors. [15], new feedback control strategy for balancing individual dc capacitor voltages in a three-phase cascade multilevel inverter-based static synchronous compensator. [16], single-phase cascaded H-bridge converter for a grid-connected photovoltaic (PV) application. The independent control of each dc link voltage, for the tracking of the maximum power point of each string of PV panels is carried out. [17], direct torque control (DTC) scheme for electric vehicles (EVs) or hybrid EVs using hybrid cascaded H-bridge multilevel motor drive based on DTC operating principles is implemented.[18], generalized multiband hysteresis modulation and its characterization have been proposed for the sliding-mode control of cascaded H-bridge multilevel-inverter (CHBMLI)-controlled systems. [19], symmetric hybrid multilevel topologies are introduced for both single- and three-phase medium-voltage high power systems. [20], the impacts of the connected load to the cascaded H-bridge converter as well as the switching angles on the voltage regulation of the capacitors are studied. [21], used a new topology of a cascaded multilevel converter based on a cascaded connection of single-phase sub multilevel converter units and full-bridge converters then, the structure is optimized.

2.3 FLYING CAPACITOR MULTILEVEL INVERTER

[22], mathematical analyses of the balancing process in boost and buck-boost converters and investigations of voltage sharing stabilization with the use of passive *RLC* circuit in switch-mode flying capacitor dc-dc converters are presented. [23], topology of flying capacitor multilevel converter which has several terminals of different dc voltage and an ac voltage terminal proposed to utilize the topology as an integrated power conversion module.[24], control strategy of flying capacitors multilevel inverters. The analysis of the permissible switching states, especially the possibility of the multiple commutations is carried out. [25], the

stabilization of the input DC voltages of five level flying capacitors (FLFC) voltage source inverters (VSI). A feedback control algorithm of the rectifier is proposed. [26], an experimental photovoltaic (PV) power conditioning system with line connection in which the conditioner consists of a flying capacitors multi-cell inverter fed by a dc-dc boost converter is carried out. [27], two active capacitor voltage balancing schemes are proposed for single-phase (H-bridge) flying-capacitor multilevel converters, based on equations of flying capacitor converters. The methods are effective on capacitor voltage regulation in flying-capacitor multilevel converters.

2.4 SINUSOIDAL PWM

[28], multicarrier sub-harmonic pulse width modulation (PWM), called disposition band carrier and phase-shifted carrier PWM (DBC-PSC-PWM), method is developed to produce output voltage levels of $(n \times m + 1)$ and to improve the output voltage harmonic spectrum with a wide output frequency range. [29], carrier-based closed-loop control technique has been developed to reduce the switching losses based on insertion of 'no switching' zone within each half cycle of fundamental wave. [30], five-level pulse width modulation inverter configuration, including chopper circuits as DC current-power source circuits using small smoothing inductors, is verified through computer simulations and experimental tests. [31], designed a seven-level flying capacitor multilevel inverter by using sinusoidal pulse width modulation technique. [32], addressed a modified Sinusoidal Pulse Width Modulation (SPWM) modulator with phase disposition that increases output waveform up to 7-level while reducing output harmonics.[33], pulse-width modulation (PWM) for single-phase five-level inverter via field-programmable gate array (FPGA) is carried out.

2.5. SPACE VECTOR PWM

[34], two discontinuous multilevel space vector modulation (SVM) techniques are implemented for DVR control to reduce inverter switching losses maintaining virtually the same harmonic performance as the conventional multilevel SVM for high number of levels. [35], two carrier-based modulation techniques for a dual two-level inverter with power sharing capability and proper multilevel voltage waveforms were introduced. Their main advantage is a simpler implementation compared to SVM. [36], focused a novel 3-D space modulation technique with voltage balancing capability for a cascaded seven-level rectifier stage of SST.

2.6 SHE-PWM

A method to obtain initial values for the SHE-PWM equations according to the reference modulation index M and the initial phase angle of output fundamental voltage is investigated thoroughly. [37], the elimination of harmonics in a cascade multilevel inverter by considering the non-equality of separated dc sources by using particle swarm optimization is presented. [38], new formulation of selective harmonic elimination pulse width modulation (SHE-PWM) technique suitable for cascaded multilevel inverters with optimized DC voltage levels. [39], neutral point voltage control strategy for the three-level active neutral point clamped (ANPC) converter using selective harmonic elimination pulse width modulation (SHE-PWM). [40], a control strategy is proposed to regulate the voltage across the FCs at their respective reference voltage levels by swapping the switching patterns of the switches based on the polarity of the output current, the polarity of the FC voltage, and the polarity of the fundamental line-to-neutral voltage under selective harmonic elimination pulse width modulation. [43], suggested a novel space vector modulation (SVM) technique for a three-level five-phase inverter, based on an optimized five vectors concept.

2.7 SPACE VECTOR CONTROL

[41], switching strategy for multilevel cascade inverters, based on the space-vector theory. The proposed switching strategy generates a voltage vector with very low harmonic distortion and reduced switching frequency. [42], PWM technique for induction motor drives involving six concentric dodecagonal space vector structures is proposed. [43], novel space vector modulation (SVM) technique for a three-level five-phase inverter is based on an optimized five vectors concept. [44], switching strategy for multilevel cascade inverters, based on the space-vector theory. The proposed high-performance strategy generates a voltage vector across the load with minimum error with respect to the sinusoidal reference.

III.APPLICATION-RES-GRID INTEGRATION

A permanent-magnet synchronous generator wind turbine as shown in Fig.1 is a simplified diagram of a 3L-NPC back-to-back configuration. The gearbox can be avoided by electromechanically achieving the speed conversion between the low-speed rotor shaft (about 15 r/min) and the grid frequency (usually 50 or 60 Hz), for multi-pole generators. The hybrid 5L-ANPC been proposed in back-to-back configuration in [45 - 46]. The use of a 3L-NPC at the grid side and a three-phase diode full-

bridge rectifier with a boost converter dc-dc stage at the generator side is proposed [47].

In this, boost converter performs the MPPT of the generator side, whereas the NPC regulates active and reactive power. The boost naturally elevates the voltage, which is suitable for Medium Voltage operation of the NPC. The main advantage over the back-to-back NPC is a simple, low-cost, and reliable front end with the expense of current harmonics at the generator side and lower dynamic performance.

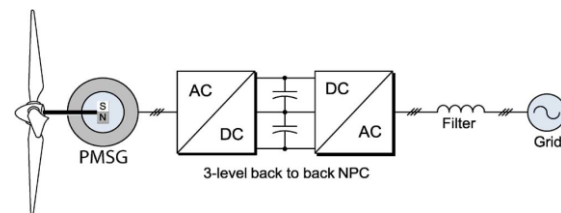


Fig 1 Back-to-back NPC power converter for PMSG based variable - speed wind turbine.

The Cascaded H Bridge on the other side requires multiple isolated dc sources, and therefore, its application is not straight forward. But, some interesting concepts based on rectifiers fed from independent generator stator windings of a permanent-magnet synchronous generator have been proposed, with each one rectified and used to provide the dc source for each H-bridge of the CHB converter [48]. With the increase in the number of levels of the converter will allow lower switching frequency operation, improving grid side power control performance and grid code compliance without filters. However, due to the reduction in the cost of photovoltaic modules (among other factors); grid-connected photovoltaic power plants are consistently increasing in power rating mainly now, hundreds of large photovoltaic-based power plants over 10 MW [49], are operating, and even more are under development. In addition, one of the fastest developing renewable energy sources in the last years is photovoltaic grid-connected systems [50].

Centralized and multistring configurations are used with a central dc-ac converter that interfaces the power to the grid, for large photovoltaic power plants. With more demand of grid code requirements for photovoltaic systems, multilevel topologies become more attractive. In [51], has been proposed a dc-ac converter stage in a multistring photovoltaic configuration, in which a five-level converter is formed by a three-level H-bridge with a bidirectional switch arrangement that can clamp two additional levels to the output. In [52]-[53], Cascaded H Bridge based grid interfaces are proposed with their respective control scheme.

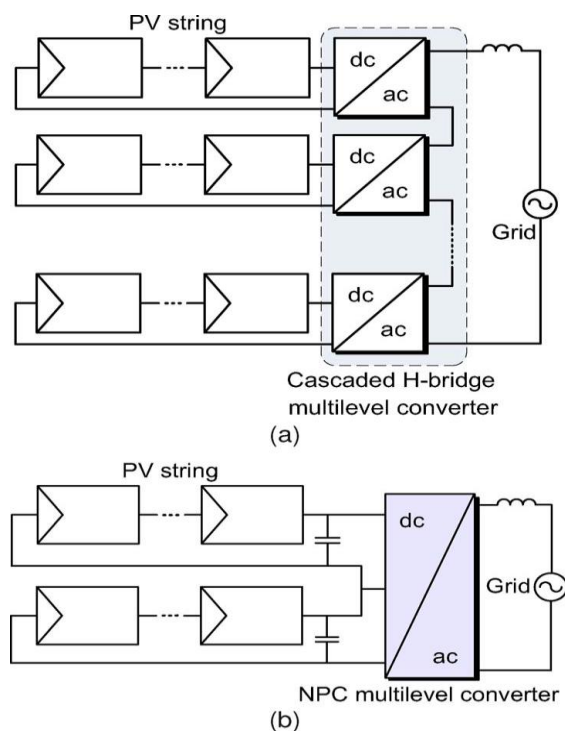


Fig 2. Multilevel-converter-based photovoltaic grid-connected systems. (a) CHB based. (b) NPC based.

Fig.2 (a) and (b) shows a CHB-based and an NPC-based multilevel multistring photovoltaic topology respectively. The series connection of the H-bridge makes CHB very impressive, as the strings naturally elevate the voltage, eliminating the need for a boost stage or step-up transformer. Enabling a reduction in the average device switching frequency, it increases the apparent switching frequency of the total converter waveform. The higher amount of voltage levels produces an intrinsic reduction of all the harmonics, which reduces the need for grid side filters, along with the efficiency improvement. A trend clearly shows an exponential growth of grid tied photovoltaic systems, compared with the evolution of the stand-alone technology due to improved efficiency (no losses in energy storage and additional converter stages). A recent application of multilevel converters is hydro-pumped energy storage. A large-scale energy storage system in which water is pumped from a lake, river, or even ocean to a higher located reservoir; at the time of requirement and the same water is used for hydropower generation. Hydro-pumped storage is especially useful for nuclear power plants since the reactor operation level cannot abruptly be changed, and during low power demand, the excess energy can be used to pump water to the reservoir. It can also be applied for wind power plants, when the wind energy surpasses the consumption demand.

In the high-power-demand period (peak hours), water from the reservoir is used to generate the additional required energy also.

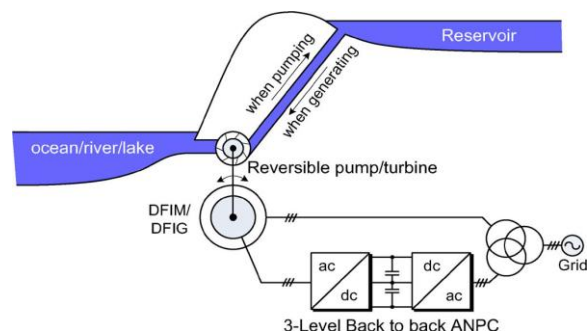
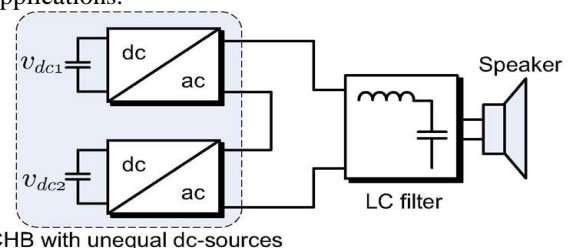


Fig 3. Back-to-back ANPC doubly fed induction generator/motor for hydro pumped energy storage application

A simplified overview of a hydro-pumped energy storage system is illustrated in Fig. 3. The system uses a reversible Francis hydro-pump or turbine that can be used for either water pumping or generation. These systems are usually operated at fixed speed with synchronous motor or generator due to their high power rating. In a doubly fed induction generator, with a partially rated converter interconnecting the rotor to the grid a small percent of variable-speed operation above or below synchronous speed can improve efficiency at different load and operating conditions; thus, can provide a percentage of variable speed for a much higher power rated pump or turbine [54]. To enhance the controllability and the power transfer capability of the network, Flexible AC Transmission Systems (FACTS) have been introduced as the solution. Different technologies that are considered as FACTS are AFs, static compensators (STATCOM), dynamic voltage restorers (DVRs), unified power flow controllers (UPFCs), and unified power quality conditioners. All these systems can, in one way or another, provide instantaneous and variable reactive power compensation in response to grid voltage transients (voltage sag, swell, harmonics, etc.), enhancing the grid voltage stability [55]. These devices (AF, STATCOMs, DVRs, and UPFCs) are currently gaining importance due to more demanding grid codes [56], which even require low voltage-ride-through capability during voltage sags. Several multilevel converter applications for these systems have been proposed, the CHB and NPC topologies seem to be the most suited for STATCOM applications.



CHB with unequal dc-sources

Fig 4. Nine-level asymmetric fed CHB class-D digital audio Amplifier.

3.1. OTHER APPLICATIONS

Class-D digital audio power amplifiers are not in the high-power and medium-voltage range, the improved power quality (mainly reduced THD) and the possibility of reaching higher apparent switching frequencies, without increasing the average device switching frequency, have led to the research lines to apply multilevel technology in this field [57]–[59]. Here, the cascaded topologies, particularly the Cascaded H Bridge fed with unequal dc sources, seem more attractive since they can easily reach a high number of levels (less THD), improving audio quality while facilitating high-frequency filtering. Fig.4 shows a nine-level class-D digital audio amplifier proposed in [58]. Large conveyor systems are one of the standard applications of multilevel converters, not necessarily demanding high performance and high dynamic control. In Grid-connected PV applications [60], the primary issue of solar PV energy is its generation variability where large-scale PV systems are considered, which presents a great challenge to the power system operator. Energy storage is a key component in improving energy efficiency, security and reliability [61]. As the power level of the PV systems increases and the grid connections takes place at medium voltages, the system can benefit from the utilization of multilevel converters due to the lower EMI, reduced switching frequency, increased efficiency and improved waveform quality [62-63]. The multilevel concept is used to decrease the harmonic distortion in the output waveform without decreasing the inverter power output using renewable energy sources like Wind, PV etc. [64-65]. For hybrid photovoltaic and wind energy system connected to the grid, the method allows the renewable energy sources to deliver the load together or independently depending upon their availability. The usage of five-level inverter reduces Total Harmonic Distortion (THD) in output voltage and helps in eliminating bulk filters required at the output side [66].

IV. CONCLUSIONS

In this paper many topologies and control techniques have been reviewed, which helps the researchers to use proper techniques to control multilevel converters for renewable energy sources grid integration. The elimination of the transformer implies significant cost, volume, and weight reduction, and it also would reduce system complexity and losses. There are now several commercial products available for wind power converters, centralized photovoltaic converters, STATCOMs hydro pumped storage, etc. It can be seen that a clear trend in the diversification of multilevel powered applications are increasing. It is expected that this trend will continue, and more

applications will be enabled by this technology, due to more grid codes, continued increase in power demand of the applications, increasing development of power semiconductors, and the benefits of multilevel technology. In this respect, the application of multilevel converters to FACTS systems is very promising. For, a distributed generation system involving multiple energy sources and networks of different voltage levels, multilevel voltage source converters can effectively be used as a power management system. ng system may be installed instead of storage system.

REFERENCES

- [1] J. Rodriguez, J. S. Lai and F. Z. Peng, "Multilevel Inverters: Survey of Topologies, Controls, and Applications," *IEEE Transactions on Industry Applications*, vol. 49, no. 4, Aug. 2002, pp. 724-738.
- [2] A. Bendre and G. Venkataramanan, "Neutral current ripple minimization in a three-level rectifier," *IEEE Trans. Ind. Applicat.* , vol. 42, no. 2, pp. 582–590, Mar. 2006.
- [3] Baoming Ge, Fang Zheng Peng, Aníbal T. de Almeida, and Haitham Abu-Rub, "An Effective Control Technique for Medium-Voltage High-Power Induction Motor Fed by Cascaded Neutral-Point- Clamped Inverter", *IEEE Trans On Industrial Electronics*, Vol. 57, No. 8, pp. 2659-2668, Aug 2010.
- [5] Arash A. Boora, Alireza Nami, Firuz Zare, Arindam Ghosh, and Frede Blaabjerg, "Voltage-Sharing Converter to Supply Single-Phase Asymmetrical Four-Level Diode-Clamped Inverter With High Power Factor Loads", *IEEE Trans On Power Electronics*, Vol. 25, No. 10, Pp. 2507-2520, Oct 2010.
- [6] C. Attaianese, M. Di Monaco, and G. Tomasso, "Three-Phase Three- Level Active NPC Converters for High Power System", *International Symposium on Power Electronics, Electrical Drives, Automation and Motion*, pp. 204-209, Speedam 2010.
- [7] Amit Kumar Jain, and V. T. Ranganathan, "VCE Sensing for IGBT Protection in NPC Three Level Converters—Causes For Spurious Trippings and Their Elimination", *IEEE Trans On Power Electronics*, Vol. 26, No. 1, pp. 298-307, Jan 2011.
- [8] Jun Li, Subhashish Bhattacharya, and Alex Q. Huang, "A New Nine- Level Active NPC (ANPC) Converter for Grid Connection of Large Wind Turbines for Distributed Generation", *IEEE Trans On Power*

- Electronics*, Vol. 26, No. 3, pp. 961-972, March 2011.
- [9] Robert Stala, "Application of Balancing Circuit for DC-Link Voltages Balance in a Single-Phase Diode-Clamped Inverter With Two Three-Level Legs", *IEEE Trans On Industrial Electronics*, Vol. 58, No. 9, pp. 4185-4195, Sept 2011.
- [10] Jin Li, Jinjun Liu, Dushan Boroyevich, Paolo Mattavelli, and Yaosuo Xue, "Three-level Active Neutral-Point-Clamped Zero-Current-Transition Converter for Sustainable Energy Systems", *IEEE Trans. On Power Electronics*, Vol. 26, No. 12, pp. 3680-3693, Dec 2011.
- [11] Marcelo C. Cavalcanti, Alexandre M. Farias, Kleber C. Oliveira, Francisco A. S. Neves, and João L. Afonso, "Eliminating Leakage Currents in Neutral Point Clamped Inverters for Photovoltaic Systems", *IEEE Trans. On Industrial Electronics*, Vol. 59, No. 1, pp.435-443, Jan 2012.
- [12] Jin Li, Jinjun Liu, Dushan Boroyevich, Paolo Mattavelli, and Yaosuo Xue, "Comparative Analysis of Three-Level Diode Neural-Point-Clamped and Active Neural-Point-Clamped Zero-Current-Transition Inverters", *8th International Conference on Power Electronics*, pp. 2290-2295, May 30-June 3, 2011.
- [13] Zhong Du, Leon M. Tolbert, Burak Ozpineci, and John N. Chiasson, "Fundamental Frequency Switching Strategies of a Seven-Level Hybrid Cascaded H-Bridge Multilevel Inverter", *IEEE Trans. On Power Electronics*, Vol. 24, No. 1, pp. 25- 33, Jan 2009.
- [14] Zhong Du, Burak Ozpineci, Leon M. Tolbert, and John N. Chiasson, "DC-AC Cascaded H-Bridge Multilevel Boost Inverter With No Inductors for Electric/Hybrid Electric Vehicle Applications", *IEEE Trans. On Industry Applications*, Vol. 45, No. 3, Pp. 963-970, May/June 2009.
- [15] Yu Liu, Alex Q. Huang, Wenchao Song, Subhashish Bhattacharya, and Guojun Tan, "Small-Signal Model-Based Control Strategy for Balancing Individual DC Capacitor Voltages in Cascade Multilevel Inverter-Based STATCOM", *IEEE Trans. On Industrial Electronics*, Vol. 56, No. 6, pp. 2259-2269, June 2009.
- [16] Elena Villanueva, Pablo Correa, José Rodríguez, and Mario Pacas, "Control of a Single-Phase Cascaded H-Bridge Multilevel Inverter for Grid-Connected Photovoltaic Systems", *IEEE Trans. On Industrial Electronics*, Vol. 56, No. 11, pp. 4399-4406, Nov. 2009.
- [17] Farid Khoucha, Soumia Mouna Lagoun, Khoudir Marouani, Abdelaziz Kheloui, and Mohamed El Hachemi Benbouzid, "Hybrid Cascaded H-Bridge Multilevel-Inverter Induction-Motor-Drive Direct Torque Control for Automotive Applications", *IEEE Trans. On Industrial Electronics*, Vol. 57, No. 3, pp. 892-899, March 2010.
- [18] Rajesh Gupta, Arindam Ghosh, and Avinash Joshi, "Multiband Hysteresis Modulation and Switching Characterization for Sliding-Mode-Controlled Cascaded Multilevel Inverter", *IEEE Trans. On Industrial Electronics*, Vol. 57, No. 7, pp. 2344-2353, July 2010.
- [19] Domingo A. Ruiz-Caballero, Reynaldo M. Ramos-Astudillo, Samir Ahmad Mussa, and Marcelo Lobo Heldwein, "Symmetrical Hybrid Multilevel DC-AC Converters With Reduced Number of Insulated DC Supplies", *IEEE Trans. On Industrial Electronics*, Vol. 57, No. 7, pp. 2307-2314, July 2010.
- [20] Hossein Sepahvand, Jingsheng Liao, and Mehdi Ferdowsi, "Investigation on Capacitor Voltage Regulation in Cascaded H Bridge Multilevel Converters With Fundamental Frequency Switching", *IEEE Trans. On Industrial Electronics*, Vol. 58, No. 11, pp. 5102-5111, Nov. 2011.
- [21] Javad Ebrahimi, Ebrahim Babaei, and Goverg B. Gharehpetian, "A New Topology of Cascaded Multilevel Converters With Reduced Number of Components for High-Voltage Applications", *IEEE Trans. On Power Electronics*, Vol. 26, No. 11, pp. 3109-3118, Nov. 2011.
- [22] Robert Stala, "The Switch-Mode Flying-Capacitor DC-DC Converters With Improved Natural Balancing", *IEEE Trans. On Industrial Electronics*, Vol. 57, No. 4, pp. 1369-1382, April 2010.
- [23] M. Hojo, and K. Minato, "Integrated Power Conversion for DC Power System by Flying Capacitor Multi-Level Converter", *The 2010 International Power Electronics Conference*, pp. 337-342, 2010.
- [24] Pavel Kobrle, Jiří Pavelka, "Analysis of Permissible State of Flying Capacitors Multilevel Inverter Switch", *14th International Power Electronics and Motion Control Conference*, pp. T3-42 – T3-45, oct. 2010.
- [25] Z.Oudjebour and E.M.Berkouk, M.O.Mahmoudi, "Modelling, Control and Feedback Control of the Multilevel Flying Capacitors Rectifier. Application to Double Star Induction Machine", *IEEE*

- International Energy Conference*, pp. 507-512, 2010.
- [26] M. Trabelsi, and L. Ben-Brahim, "Development Of A Grid Connected Photovoltaic Power Conditioning System Based On Flying Capacitors Inverter", *8th International Multi-Conference on Systems, Signals & Devices*, pp. 1-6, May 2011.
- [27] Mostafa Khazraei, Hossein Sepahvand, Keith A. Corzine, and Mehdi Ferdowsi, "Active Capacitor Voltage Balancing in Single-Phase Flying-Capacitor Multilevel Power Converters", *IEEE Trans. On Industrial Electronics*, Vol. 59, No. 2, pp. 769-778, Feb. 2012.
- [28] Moncef Ben Smida and Faouzi Ben Ammar, "Modeling and DBCPSC- PWM Control of a Three-Phase Flying-Capacitor Stacked Multilevel Voltage Source Inverter", *IEEE Trans. On Industrial Electronics*, Vol. 57, No. 7, pp. 2231-2239, July 2010.
- [29] P.K. Chaturvedi, S. Jain, P. Agarwal, "Reduced switching loss pulse width modulation technique for three-level diode clamped inverter", published in *IET Power Electronics*, Vol. 4, Iss. 4, pp. 393-399, 2011.
- [30] Suroso, and T. Noguchi, "Common-emitter topology of multilevel current-source pulse width modulation inverter with chopper-based DC current sources", *IET Power Electronics*, Vol. 4, Iss. 7, pp. 759-766, 2010.
- [31] K.Ramani and Dr.A.Krishnan, "High Performance of Sinusoidal Pulse Width Modulation Based Flying Capacitor Multilevel Inverter fed induction Motor Drive", *International Journal of Computer Applications (0975 - 8887) Volume 1 - No. 24*, pp. 98-103, 2010.
- [32] Ilhami Colak, Ramazan Bayindir, Ersan Kabalci, "Design and Analysis of a 7-Level Cascaded Multilevel Inverter with Dual SDCSs", *International Symposium on Power Electronics, Electrical Drives, Automation and Motion*, pp.180-185, 2010.
- [33] Wahidah Abd. Halim and Nasrudin Abd. Rahim, "FPGA-Based Pulse-Width Modulation Control For Single-Phase Multilevel Inverter", *IEEE First Conference on Clean Energy and Technology, CET*, pp.57-62, 201
- [34] Ahmed M. Massoud, Shehab Ahmed, Prasad N. Enjeti, and Barry W. Williams, "Evaluation of a Multilevel Cascaded-Type Dynamic Voltage Restorer Employing Discontinuous Space Vector Modulation", *IEEE Trans. On Industrial Electronics*, Vol. 57, No. 7, pp. 2398- 2410 July 2010.
- [35] Gabriele Grandi, and Darko Ostojic, "Carrier-Based Discontinuous Modulation for Dual Three-Phase Two-Level Inverters", *International Symposium on Power Electronics, Electrical Drives, Automation and Motion*, pp. 839-844, Speedam 2010.
- [36] Xu She, Alex Q. Huang, and Gangyao Wang, "3-D Space Modulation With Voltage Balancing Capability for a Cascaded Seven-Level Converter in a Solid-State Transformer", *IEEE TRANS. On Power Electronics*, Vol. 26, No. 12, pp. 3778-3789, Dec. 2011.
- [37] H. Taghizadeh and M. Tarafdar Hagh, "Harmonic Elimination of Cascade Multilevel Inverters with Non equal DC Sources Using Particle Swarm Optimization", *IEEE Trans. On Industrial Electronics*, Vol. 57, No. 11, pp. 3678-3684, Nov. 2010.
- [38] Mohamed S. A. Dahidah, Georgios Konstantinou, and Vassilios G. Agelidis, "SHE-PWM and Optimized DC Voltage Levels for Cascaded Multilevel Inverters Control", *IEEE Symposium On Industrial Electronics And Applications (ISIEA 2010)*, pp. 143- 148, Oct 2010.
- [39] Sridhar R. Pulikanti, Mohamed S. A. Dahidah, and Vassilios G. Agelidis, "Voltage Balancing Control of Three-Level Active NPC Converter Using SHE-PWM", *IEEE Trans. On Power Delivery*, Vol.26, No. 1, pp. 258-267, Jan. 2011.
- [40] Sridhar R. Pulikanti, and Vassilios G. Agelidis, "Hybrid Flying- Capacitor-Based Active-Neutral-Point-Clamped Five-Level Converter Operated With SHE-PWM", *IEEE Trans. On Industrial Electronics*, Vol. 58, No. 10, pp. 4643-4653, Oct. 2011.
- [41] José Rodríguez, Luis Morán, Pablo Correa, and Cesar Silva, "A Vector Control Technique for Medium-Voltage Multilevel Inverters", *IEEE Trans. On Industrial Electronics*, Vol. 49, No. 4, pp. 882-888, Aug. 2002.
- [42] Anandarup D, K. Sivakumar, Rijil Ramchand, Chintan Patel, and K Gopakumar, "A High Resolution Pulse Width Modulation Technique Using Concentric Multilevel Dodecagonal Voltage Space Vector Structures", *IEEE International Symposium on Industrial Electronics (ISIE 2009)*, pp. 477-482, July 2009.
- [43] Liliang Gao and John E. Fletcher, "A Space Vector Switching Strategy for Three-Level

- Five-Phase Inverter Drives”, *IEEE Trans. On Industrial Electronics*, Vol. 57, No. 7, pp. 2332-2343, July 2010.
- [44] José Rodríguez, Luis Morán, Jorge Pontt, Pablo Correa, and Cesar Silva, “A High-Performance Vector Control of an 11-Level Inverter”, *IEEE Trans. On Industrial Electronics*, Vol. 50, No. 1, pp. 80-85, Feb. 2003.
- [45] M. Winkelkemper, F. Wildner, and P. K. Steimer, “6 MVA five-level hybrid converter for windpower,” in *Proc. IEEE Power Electron. Spec. Conf.*, Jun. 15–19, 2008, pp. 4532–4538.
- [46] M. Winkelkemper, F. Wildner, and P. Steimer, “Control of a 6 MVA hybrid converter for a permanent magnet synchronous generator for wind power,” in *Proc. 18th ICEM*, Sep. 6–9, 2008, pp. 1–6.
- [47] M. Malinowski, S. Stynski, W. Kolomyjski, and M. P. Kazmierkowski, “Control of three-level PWM converter applied to variable-speed-type turbines,” *IEEE Trans. Ind. Electron.*, vol. 56, no. 1, pp. 69–77, Jan. 2009.
- [48] C. H. Ng, M. A. Parker, L. Ran, P. J. Tavner, J. R. Bumby, and E. Spooner, “A multilevel modular converter for a large, light weight wind turbine generator,” *IEEE Trans. Power Electron.*, vol. 23, no. 3, pp. 1062–1074, May 2008.
- [49] Pvsources.com, *Large-Scale Photovoltaic Power Plants: Ranking*. [Online]. Available: <http://www.pvsources.com/en/top50pv.php>
- [50] “Renewable Energy Policy Network for the 1st Century,” Renewables Global Status Report-2009 Update, 2009. [Online]. Available: <http://www.ren21.net/publications>.
- [51] N. A. Rahim and J. Selvaraj, “Multistring five-level inverter with novel PWM control scheme for PV application,” *IEEE Trans. Ind. Electron.*, vol. 57, no. 6, pp. 2111–2123, Jun. 2010.
- [52] E. Villanueva, P. Correa, J. Rodriguez, and M. Pacas, “Control of a single-phase cascaded H-bridge multilevel inverter for grid-connected photovoltaic systems,” *IEEE Trans. Ind. Electron.*, vol. 56, no. 11, pp. 4399–4406, Nov. 2009.
- [53] S. Kouro, A. Moya, E. Villanueva, P. Correa, B. Wu, and J. Rodriguez, “Control of a cascaded H-bridge multilevel converter for grid connection of photovoltaic systems,” in *Proc. 35th IEEE IECON*, Porto, Portugal, Nov. 2009, pp. 1–7.
- [54] J. A. Suul, K. Uhlen, and T. Undeland, “Variable speed pumped storage hydropower for integration of wind energy in isolated grids-case description and control strategies,” in *Proc. NORPIE*, Jun. 9–11, 2008, pp. 1–8.
- [55] D. Soto and T. C. Green, “A comparison of high-power converter topologies for the implementation of FACTS controllers,” *IEEE Trans. Ind. Electron.*, vol. 49, no. 5, pp. 1072–1080, Oct. 2002.
- [56] *Grid code—High and Extra High Voltage*, E.ON Netz GmbH, Zurich, Switzerland, Apr. 2006.
- [57] V. M. E. Antunes, V. F. Pires, and J. F. Silva, “Digital multilevel audio power amplifier with a MASH sigma-delta modulator to reduce harmonic distortion,” in *Proc. IEEE Int. Symp. Ind. Electron.*, Jun. 20–23, 2005, vol. 2, pp. 525–528.
- [58] V. M. E. Antunes, V. F. Pires, and J. F. A. Silva, “Narrow pulse elimination PWM for multilevel digital audio power amplifiers using two cascaded H-bridges as a nine-level converter,” *IEEE Trans. Power Electron.*, vol. 22, no. 2, pp. 425–434, Mar. 2007.
- [59] C. W. Lin, B.-S. Hsieh, and Y. C. Lin, “Enhanced design of filter less class-d audio amplifier,” in *Proc. DATE Conf. Exhib.*, Apr. 20–24, 2009, pp. 1397–1402.
- [60] Bailu Xiao ; Ke Shen ; Jun Mei ; Filho, F. ; Tolbert, L.M. ,”Control of cascaded H-bridge multilevel inverter with individual MPPT for grid-connected photovoltaic generators” *IEEE Energy Conversion Congress and Exposition (ECCE)*, 2012, Publication Year: 2012 , Page(s): 3715 – 3721.
- [61] Guohui Yuan, “Improving grid reliability through integration of distributed PV and energy storage”, *IEEE PES Innovative Smart Grid Technologies (ISGT)*, 2012, Publication Year: 2012 , Page(s): 1 – 2.
- [62] Huiying Zheng; Shuhui Li ; Chuanzhi Zang ; Weijian Zheng Coordinated control for grid integration of PV array, battery storage and super capacitor” *IEEE Trans. On Power and Energy Society General Meeting (PES)*, Publication Year: 2013 , Page(s): 1 – 5, 2013.
- [63] S. Rivera, S. Kouro, B. Wu, J. I. Leon, J. Rodriguez, and L. G. Franquelo, “Cascaded H-bridge multilevel converter multistring topology for large scale photovoltaic systems,” *IEEE ISIE 2011*, pp. 1837-1844.

- [64] Sayed, Mahmoud A.; Elsheikh, Maha G.; Ahmed, Emad M.; Orabi, Mohamed; Abdelghani, Afef Ben; Belkhodja, Ilhem Slama, "Low-Cost Single-Phase Multi-Level Inverter for Grid-Tie PV System Applications", *35th International Telecommunications Energy Conference 'Smart Power and Efficiency' (INTELEC), Proceedings of 2013*, Publication Year: 2013, Page(s): 1 – 6.
- [65] Morya, A.K.; Shukla, A., "Space vector modulated cascaded H-bridge multilevel converter for grid integration of large scale photovoltaic power plants", *Fourth International Conference on Power Engineering, Energy and Electrical Drives (POWERENG), 2013*, Publication Year: 2013, Page(s): 181 – 186.
- [66] Biju, K.; Ramchand, R., "Modeling and simulation of single phase five level inverter fed from renewable energy sources", *Annual International Conference on Emerging Research Areas, 2013 and International Conference on Microelectronics, Communications and Renewable Energy (AICERA/ICMiCR), 2013*, Publication Year: 2013, Page(s): 1 – 5