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DESIGN AND CHARACTERIZATION OF MULTIPLE COUPLED MICRORING BASED WAVELENGTH DEMULTIPLEXER IN SILICON-ON-INSULATOR (SOI)

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We report in this paper, an optimized design and characterization of SOI based single mode, four channels wavelength demultiplexer using microrings. The usage of siliconon-insulator (SOI) allows a wide free spectral range (FSR) for the device that is crucial in developing ultra-compact integrations of planar lightwave circuits (PLCs). The characterizations are done using Finite-Difference Time-Domain (FDTD) mode simulations from RSOFT. Serially cascaded microring arrays up to the third order are presented to study the design trade-off among the FSR, Q-factor and optical losses of the laterally coupled wavelength demultiplexer. The demultiplexer is expected to be working at C-band region of Wavelength Division Multiplexing (WDM) for a wavelength around 1550 nm. Our proposed demultiplexer has low insertion loss (< 0.5 dB) and a crosstalk around $12 \sim 19 \, \text{dB}$.

Keywords: Microring resonator; silicon photonic; silicon-on-insulator; wavelength demultiplexer.

1. Introduction

In the last few years, silicon photonics has emerged as one of the important technologies which cover a wide area of applications, such as bio-chemical sensing and laser manufacturing. Silicon-based photonic devices are expected to be one of the most important basic blocks for photonic integrated circuits (PICs) in the near future. Silicon Microring Resonators (MRRs) based devices have gained increasing attention for a number of reasons: compact size, easy design, relatively cheap material compared to other III-V semiconductor materials and suitability for integrated photonic system.^{1,2} Microring resonators have been employed in various applications including wavelength filtering, sensing, multiplexing and time delay device.^{3,4} Selecting or combining the desired wavelengths in wavelength division multiplexing (WDM) system can be done with the application of wavelength multiplexer/demultiplexer. The basic structure of the MRRs based wavelength multi/demultiplexer consists of two input/output waveguides and a ring. Previous researchers have been working on various types of materials for device fabrication such as silica, silica-on-silicon, polymers, lithium niobate and so forth.^{5,6} Even so, in our work, emphasis is put on silicon-on-insulator (SOI) owing to its numerous advantages such as high-index contrast, low-cost and its compatibility with the available fabrication facility.^{7–9}

In this paper, Finite-Difference Time-Domain (FDTD) numerical simulations are utilized to analyze the design and characterization of 1×4 SOI-based wavelength demultiplexer. The tools not only ease the design process but reduce the working time and cost as well. First, key design parameters such as waveguide size and gap width between waveguides are optimized. Then the device performance in terms of FSR, Q-factor, as well as insertion loss and crosstalk are investigated.

2. Device Description and Theory

Figure 1 shows the schematic diagram of the proposed configuration of the first order wavelength demultiplexer. The demultiplexer consists of a straight input bus waveguide, four output waveguides and four laterally cascaded microring waveguides. The light input is coupled into the bus waveguide and the microring waveguides that serve as resonant optical cavities. Four wavelengths: λ_1 , λ_2 , λ_3 and λ_4 from input port are separated into four distinctive output channels according to wavelength in resonance with the corresponding ring waveguides which have four different ring radii. Unequal ring radius will resonate with signals of different carrier wavelengths. Hence, the four input wavelengths can be extracted into four different output channels, functioning as drop demultiplexer. The bending loss of the output channels is assumed to be negligible.

The rib waveguide width for the ring waveguide, W_R and the bus waveguide, W_B modeled in this work are 400 nm with the slab height of 400 nm, fabricated



Fig. 1. Schematic diagram of the proposed wavelength demultiplexer.

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on top of 700 nm buried oxide layer. The gap distances between the input/output waveguides and the ring waveguide (W_g) are 300 nm, same as the gap size between serially cascaded ring waveguides. We assume that all the silicon waveguides have the same refractive index of 3.5 while the refractive indexes for SiO₂ and air are 1.45 and 1.0, respectively.

The power emitted at the through port and the drop port for single microring can be estimated according to the formula¹⁰:

$$T_{\rm through} = \frac{(\lambda - \lambda_0)^2 + \left(\frac{FSR}{4\pi}\right)^2 (\kappa_p^2)^2}{(\lambda - \lambda_0)^2 + \left(\frac{FSR}{4\pi}\right)^2 (2\kappa^2 + \kappa_p^2)^2}$$
(2.1)

$$T_{\rm drop} = \frac{4x \left(\frac{FSR}{4\pi}\right)^2 (\kappa^4)}{(\lambda - \lambda_0)^2 + \left(\frac{FSR}{4\pi}\right)^2 (2\kappa^2 + \kappa_p^2)^2}$$
(2.2)

where λ_0 is the resonant wavelength, κ^2 is the power coupling coefficient between the bus waveguide and the resonator, and κ_p^2 is the propagation power loss coefficient per round trip in the microring resonator.

The important characteristics of the MRRs based wavelength demultiplexer are the Free Spectral Range (FSR), Quality Factor (Q-factor), insertion loss and crosstalk. A wide FSR is demanded in Wavelength Division Multiplexing (WDM) system. Larger FSR means more signal can be accommodated into a channel at the same time. Its value is determined by¹¹:

$$FSR \approx \frac{\lambda_0^2}{n_g(\lambda)L_{\text{eff}}}$$
 (2.3)

where n_q is the group index and L_{eff} is the effective length of the device.

The Q-factor as a measure of wavelength selectivity, can be defined as the ratio of the power stored to the power dissipated in the ring.¹² It portrays the sharpness of resonant peak, and it can be calculated according to the following formula.

$$Q \approx \frac{\lambda_0}{(\Delta\lambda)_{3_{dB}}} = \frac{2\pi n_{gr} L_{\text{eff}}}{2\lambda_0 \arccos\left[\frac{1+|t|^2 \tau^2 - 4|t|\tau}{-2|t|\tau}\right]}$$
(2.4)

Here, n_{gr} is the group velocity, L_{eff} the effective length, τ is field attenuation and t transmission coefficient.

The microring efficiency can be determined by crosstalk; the difference between the output power at the through port and the drop port, while insertion loss can be measured by comparing the output power of the device to a reference input power.¹³ Insertion loss is one of the most important specifications in determining the maximum power loss.¹⁴

3. Results and Discussions

In this section, we will first present the optimized values of the system parameters for the proposed wavelength demultiplexer by analyzing the transmission characteristics obtained by 2D numerical simulations. Then, the performance of the optimized design will be discussed in details in terms of the crosstalk and the insertion loss.

3.1. Design optimization

We have selected the values $6 \,\mu\text{m}$, $8 \,\mu\text{m}$, $10 \,\mu\text{m}$ and $12 \,\mu\text{m}$ for R_1 , R_2 , R_3 and R_4 respectively, of the ring radii for the proposed device. A large increment in ring radii is due to the fact that a very small difference in ring radii is hard to fabricate. The resonance behavior can be explained by Fig. 2 where it shows the on-resonance and off-resonance state for the third order microring resonators with $R = 6 \,\mu\text{m}$, $Wg = 30 \,\text{nm}$, $W_B = 400 \,\text{nm}$ and $W_R = 400 \,\text{nm}$. In general, resonances in a ring are expected to occur at λ_0 and its multiples if the resonant condition as given below¹⁵ is satisfied,

$$n_{\rm eff}L = m\lambda_0 \tag{3.1}$$

where n_{eff} is the effective index of the circular waveguide, L is the length of the ring, and m is an integer. In this condition, the input signal with wavelength λ_0 will be coupled into the ring waveguide and all others will be transmitted to the through port [Fig. 2(a)]. At off-resonance state, input signal will bypass the ring and emitted at the through port [Fig. 2(b)]. L is $2\pi R$, therefore it can be clearly seen that the resonance condition is dependable on the ring radii.

Three different gap sizes, Wg have been studied and the output power of the single microring device with $R = 8 \mu m$, $W_R = 400 nm$, $W_B = 400 nm$ are plotted in Fig. 3. It is seen that the best result is obtained for gap size of 30 nm. The coupling strength reduced by 80% when the distance between the channel waveguide and ring or distance between two serially cascaded rings increased to 100 nm. By narrowing Wg, the cross coupling between the straight input waveguide and the ring waveguide increases while the self coupling decreases. As the self coupling is



Fig. 2. (a) On-resonance state (b) Off-resonance state.



Fig. 3. Spectral responses of MRRs based device with various gap sizes and waveguide channel widths.

reduced, more power is coupled to the drop port whereas the nonresonant output power at the through port is reduced. Figure 3 also shows the effect of varying the bus waveguide width, W_B on the device performance (with $R = 8 \mu m$, Wg = 30 nmand $W_R = 400 nm$). It is obvious that the output power dropped as the channel waveguide width is increased. Channel waveguide width of 600 nm produced the lowest output transmission where less than 50% of the input signal is coupled to the output waveguide. Yet, thin waveguides are hard to fabricate by conventional lithography and E-beam lithography is required.

Figure 4 presents the plots of the transmission response of the device with single ring and those of doubly and triply cascaded microring with $R = 8 \mu m$, Wg = 30 nm, $W_R = 400 nm$ and $W_B = 400 nm$. It can be observed that regardless the number of seriated rings, FSR remains 12 nm but the output power was decreased. It is worthwhile to understand that higher seriated microrings will suffer more attenuation due to longer transmission length. To enhance the device performance,



Fig. 4. Spectral responses of MRRs based devices with first, second and third order MR configurations.



Fig. 5. Spectral responses of the MRRs device with various radii.

new device designs are expected; hence, intensive studies in the device modeling are demanded in the future research.

In Fig. 5, we observed the effect of varying ring radii on MRR. The MRR resonated at different wavelengths, giving different FSR values. Here, we use the optimized value of waveguide channel width and gap width ($W_B = 400 \text{ nm}$ and Wg = 30 nm). It is interesting to notice that, the FSR was proportional to the ring radii. This result agrees well with theoretical calculations predicting that shrinking the ring radii will increase the FSR.¹⁶ In addition, the Q-value is raised as the ring radii are reduced. For instance, the Q-value calculated from the graph is 486 for $8 \mu \text{m}$ and 321 for $12 \mu \text{m}$.

3.2. Demultiplexer performance

From Fig. 5, it is clearly seen that, the design is suitable for multiplexing device working at 1549 nm, 1544 nm, 1541 nm and 1554 nm wavelength. Non-uniform wavelength spacing occur due to huge ring radii different. Table 1 summarizes the performance of the proposed device. The insertion loss of all channels is less than 0.5 dB for single seriated microring and less than 2.22 dB for triple seriated microrings. To validate the data, we compare the results with the theoretical value calculated using

Radius, μm	Resonant wavelength, nm	Microring order	Crosstalk, dB	Insertion loss, dB
6	1549	1^{st}	-19.77	0.222
8	1544	1^{st}	-12.55	0.457
10	1541	1^{st}	-16.53	0.457
12	1554	1^{st}	-19.54	0.457
6	1549	3^{rd}	-13.80	0.177
8	1544	3^{rd}	-10.79	2.212
10	1541	3^{rd}	-10.00	2.212
12	1554	3^{rd}	-10.00	2.212

Table 1. Summary of the demultiplexer performances.

formula of transfer functions.¹⁶ Our design has a better insertion loss, thereby proving that a low loss can be obtained in the proposed device. The crosstalk is about $12-20 \,\mathrm{dB}$ for single microring and becomes higher for the third order microrings for all ring radii.

4. Conclusion

We proposed a demultiplexer design composed of microring resonator with four output channels, investigated by FDTD software. The designed device has a good performance with low insertion loss, and low crosstalk. To improve the performance of the proposed device, further optimization of design parameters such as ring radii, microring orders, and ring width must be considered. This work will be continued with fabrication of the device and its characterization. For this purpose, the present analyses will provide useful guidance before laboratory works are carried out.

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