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# A DIGITAL INSTRUMENTATION SYSTEM FOR ELECTRIC VEHICLE DATA ACQUISITION

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by David M. Embree

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## A Thesis

Presented to the Graduate Committee of Lehigh University in Candidacy for the Degree of Master of Science

in

Electrical Engineering

Lehigh University 1978

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This thesis is accepted and approved in partial fulfillment of the requirements for the degree of <sup>.</sup> Master of Science.

SEPTEMBER 21, 1978

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Professor in Charge

Chairman of Department

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#### ABS TRACT

A microprocessor controlled data acquisition system has been developed for electric vehicle use on a Battronics Minivan at Lehigh University. The system acquires data from numerous sensors, records it on digital cassette tapes, and the same hardware is reconfigured to play back the data to the Lehigh University CDC 6400 computer for data reduction and storage. Sensed quantities include battery voltage, current, and electrolyte temperatures, motor armature and field voltages, controller duty cycle, drive line torque, distance travelled, speed, acceleration, and vehicle inclination. Data taken on various vehicle runs has been successfully processed and used by The Pennsylvania State University for vehicle model synthesis and verification.

# I. Introduction

#### Electric Vehicle Fundamentals

Over the past few years, battery powered Electric Vehicles (EV's) have come under extensive research and development as an alternative to conventional internal combustion engine vehicles. This is due to the significant advantages which electric vehicles can offer [1]: reduction of present dependence on fossil fuels, specifically oil, essential elimination of exhaust pollutants from the vehicle, lower primary energy costs, higher reliability through use of solid state components replacing mechanical ones, reduction of noise generated by the vehicle, reduced engine vibration, and lower overall maintenance costs.

However, the present state of electric vehicle technology does not allow the EV to compete favorably with the internal combustion engine vehicle in terms of speed, range, rate of refuelling and overall vehicle flexibility [2]. These limitations restrict the type of service the electric vehicle can effectively provide. Morris [2] and others have studied the possible classes of service and determined that the "programmed vehicle",

i.e., the vehicle making the same type of relatively short journey each day, offers the most promise with present EV technology. The electric vehicle is not new to this type of operation, for over 35,000 electric vans have been used for milk delivery in England for many years [2] and the U.S. Postal service has had electric postal delivery trucks in service for over three years with cost effective results [4] and good driver acceptance [5].

Since the van configuration offers a great deal of promise with available EV technology, several firms researched and produced electric vans, among them Lucas [6], [7] and Copper Development [8] of England, Volkswagen and Daimler-Benz [9] of Germany, Fiat [10] of Italy and Battronics [3] of the United States. While these electric vans perform satisfactorily, the limitations noted previously become apparent when the electric vans' performance is compared with conventionally powered vehicles. Present EV technology must be examined to improve vehicle performance limitations and user acceptance.

#### Vehicle Limitations

The electric vehicles now in use have five interrelated areas which limit their overall performance:

1) Vehicle range is restricted. The vans discussed in [3], [6]-[10] all have a range under 80 miles when loaded, restricting them to use on in-city or short suburban delivery routes. This is directly related to battery energy storage capacity which, if increased, extends vehicle range. 2) The "refuelling time", i.e. time needed to recharge the vehicle batteries, is long (typically 6-16 hours for the lead-acid batteries presently used in most electric vehicles) compared with the time to refuel a gasoline powered vehicle. This refuelling restriction coupled with the range restriction means vehicles must stand idle for long periods during recharging, or extra sets of batteries must be purchased to have fully charged batteries available whenever and wherever they are needed. This in turn requires garage facilities at several places, as battery pack weight necessitates use of a fork lift or special jack for removal and replacement. Due to high battery cost, this is not usually feasible unless a large fleet operation is maintained. 3) Present lead-acid batteries have fairly short cycle lives. According to battery manufacturers, available batteries for propulsion systems yield between 250 and 800

charge-discharge cycles before the end of their useful life [11]. Using the higher figure and considering an 80 mile range, this means that under ideal conditions batteries must be replaced at 64,000 mile intervals. More typically, an electric van travels 40 miles between recharges and lead-acid batteries realistically have 400 charge-discharge cycle lives, yielding only 16,000 miles per battery set. The expense of replacing batteries can be compared with the cost of rebuilding an internal combusion engine, since the electric propulsion system components rarely require major maintenance [4]. Internal combusion engines usually require major overhaul at intervals between 50,000 and 100,000 miles and this typically costs \$1000, but battery packs cost \$2000-\$4000 and must be replaced more frequently as previously indicated. Battery improvements are needed to increase cycle life, thereby making EV's more economically attractive. 4) Available electric vans are quite heavy. The vehicles described in [3], [6]-[10] weigh between 4000 and 7000 pounds. A large portion of this weight is due to the lead-acid battery pack, which is typically 2500 pounds and leads to slower acceleration and shorter vehicle range. As new materials and technologies are incorporated in vehicle

construction, overall weight will be reduced and performance improved. 5) Vehicle cost is high. Due to limited production of EV's and their components, currently available electric vans cost from \$6000 for small passenger vans [12] to \$15,000 for larger delivery vans [13]. These costs could be reduced significantly by mass production, which will occur thru larger demand by vehicle users. This will most likely be triggered by better performance and improved range resulting from further electric vehicle research and development.

#### Need for Data Acquisition and Vehicle Simulation

EV improvements can be accomplished in many interrelated ways, three of which are advanced technology subsystems, vehicle simulation, and data acquisition on present vehicles. Advanced technology improves the efficiency of each vehicle component, e.g. battery improvements could immediately improve vehicle performance in terms of range, speed, and battery cycle life. Other components, such as motor, controller, charger, and auxiliary systems also offer possible improvement. However, development of advanced subsystems requires considerable time and capital before production, thus

it is desirable to know what improvements can be expected from a specific subsystem before developing it.

Estimates of performance improvements can be made using computer vehicle simulation models. These models, once synthesized and verified, are used to calculate performance gains resulting from new components without actually constructing and testing them. This type of simulation can result in great savings, through development of only the most promising new components, and in reduced road testing [14]. However, synthesis and verification of a vehicle model requires a data base from an existing vehicle in order to show that model predictions follow test results under varying vehicle operating conditions.

Collection of data from an existing electric vehicle requires development of a data acquisition system. The work here, supported by a contract from the Pennsylvania Science and Engineering Foundation (PSEF) [15] to Lehigh University, describes the development of a data acquisition system and data collecting method used on a Battronics Minivan. This data was used as an aid in synthesis and verification of a vehicle simulation model being developed by The Pennsylvania State University, under subcontract to Lehigh University.

#### II. EV Data Acquisition System

#### Design Constraints and System Selection

The data acquisition system for the Battronics electric vehicle was chosen subject to physical, electrical and budgetary constraints. For example, all the vehicle borne instrumentation must be extremely rugged for reliable service, and since an electric vehicle is an electrically noisy environment, suppression of ever present large voltage spikes and transient signals as well as proper signal conditioning of the many simultaneous inputs is essential.

A microprocessor based digital data acquisition system was selected, since it is relatively inexpensive (less than \$4000) and offers other advantages compared to an analog recording or telemetry system. The analog recording system used by Menga [17] involves an expensive multi-channel analog tape recorder with subsequent data digitization for computer reduction. The telemetry system used by NASA [13] is also relatively expensive and must be operated near a base location if good communications are to be achieved. The microprocessor controlled digital data system selected here can service many data channels quickly, store the data in memory, and then record it on an inexpensive digital

cassette recorder. Later, this data is played back to a central data processing computer using the same hardware system with different software. This scheme avoids hardware duplication, allows software changes in data acquisition and playback methods, consumes little power compared with analog recording systems, and if desired allows simple expansion of the number of data input channels. The system block diagram, cf. Fig. 1, illustrates the microprocessor's central role in both data collection and output processes.

#### System Operating Modes - Data Acquisition and Recording

The microprocessor shown in Fig. 1 is the system controller which determines the timing of data acquisition and recording, as illustrated in greater detail in Fig. 2. Signals from various sensors installed on the vehicle are continuously processed and conditioned to be acceptable to the input multiplexer and analog to digital converter (ADC). When a measurement is desired, as determined by the microprocessor software, the microprocessor selects the proper input channel using the input multiplexer and signals the ADC to begin conversion. Upon completion, the microprocessor collects the digitized data and

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stores it in memory. After all desired data for a given measurement set has been taken, the microprocessor transfers them and other pertinent data items (e.g. data set, time, checksum) to the tape unit through appropriate communications hardware. The tape unit records the measurement set on a Philips digital cassette. This process is continued until all the desired measurement sets have been recorded for a given vehicle data run.

#### Data Playback and Processing

The data stored on the cassette is recovered using the equipment of Fig. 3. The microprocessor begins the operation by starting the tape unit and examining the data for a start word. Once this is found, the microprocessor transfers an entire measurement set from the tape unit through the communications interface into memory. The tape unit is stopped after all data words in the set have been transferred and the microprocessor checks the data (parity, checksum, start and stop words) to insure that valid data has been received. After verification, the microprocessor transfers each data word using a second communications link to a computer terminal and onto a phone line link to a

central computing facility. After transmission of all data words, the microprocessor starts the tape recorder again and repeats the entire procedure until all desired data has been transferred and stored in the main computer. This data is then reduced, printed out and stored on magnetic tape for future studies and model verification by The Pennsylvania State University personnel.

III. Digital Hardware and Power Supplies

#### Design Concepts

The digital hardware, cf. Figs. 2, 3, consisting of the microprocessor system, cassette tape unit, analog to digital converter, input multiplexer, and associated interfaces, forms the heart of the data logging system. These components were selected with overall system automation, component modularity and availability, and total system expense in mind. Based on these criteria, the data logging system was constructed using three plug-in circuit cards: microprocessor system, analog input and communications. These cards are installed in a rack mounted chassis along with part of the associated signal conditioning electronics, cf. Fig. 4.

#### Microprocessor System

The KIM-1 microprocessor system, manufactured by MOS Technology, Norristown, Pa. [18], cf. Fig. 5, consists of an 8 bit microprocessor (6502 MPU) with a 16 line address bus and an 8 line data bus connected to two interface devices, the 6530-002 and 6530-003. The 6530-002 and associated components interface the system to various peripheral units: a teletype (TTY), and audio tape unit for program storage and a small keyboard and display. The 6530-003 has 15 bidirectional input/output lines under software control, thru which the microprocessor controls external devices by toggling the states of these lines. Finally, 1024 (1K) 8 bit bytes of random access memory (RAM) are used for program and data storage. Hardware details and complete equipment schematics are provided in [18], [19].

Connections to the unit are made through two 44 pin edge connectors: the application connector, which provides routing to software development and storage devices and to the 15 bidirectional input/output lines used to interface microprocessor and communications

hardware, and the expansion connector, which provides access to unused memory locations through the address and data buses, cf. Fig. 5. The expansion connector is used to interface the analog input system to the microprocessor.

The 15 bidirectional input/output lines control various devices in data acquisition and data playback modes. These lines are split into the A data register, consisting of lines 0 through 7, and the B data register, consisting of lines 0 through 5 and 7 (line 6 is used by the microprocessor and is not externally available). Line assignments in both operating modes are shown in Fig. 6. Register A is used to output or input 8 bits of data from the digital tape unit, while register B is used for communication and tape unit control, cf. Section III. Difficulties encountered with register B, when line 3 failed due to excessive loading by the tape recorder start/stop lines, were eliminated by buffering the signal, cf. Fig. A1(b)\*, and moving it to line 4 on the B data register.

A Perkin-Elmer Model 1100 video computer terminal, used to develop the microprocessor software, allowed

<sup>\*</sup>Fig. Al(b) indicates Fig. 1(b) of App. A, etc.

easy entry, display and control for quick debugging. Required interfacing circuitry for the terminal (RS-232 interface standard) to the KIM-1 teletype outputs (TTL compatible) uses MC1488 quad TTL to RS-232 line drivers and MC1489 quad RS-232 to TTL line receivers, cf. Fig. Al(a).

Programs developed were stored on tapes for later use with an inexpensive audio cassette recorder, using the interconnections shown in Fig. A2 and standard KIM-1 procedures for recording and reading data on tape [18]. This technique eliminates the need for a programmable read only memory (PROM).

#### Tape Recording Unit

A Philips Compact Digital Cassette Recorder, whose specifications are given in Fig. 7, was chosen for storing data. The associated control box of the rack mounted recorder unit, cf. Fig. 8, has status lights for beginning or end of tape, A/B side of cassette, cassette in position, unit ready to proceed and cassette write enable plug. Switches are provided for manual control of fast or slow tape speed, reverse or forward direction, lock cassette in place, enable cassette read, and enable cassette write. Control box

details are shown in Fig. A3. Note the forward control line is also under microprocessor control through the tape unit connector (pin 4) and buffer, cf. Fig. Al(b).

#### Communications Interfacing

#### Microprocessor to Tape Unit

A conversion must be made from the microprocessor's parallel data format to a serial format before the data stored in its memory can be transferred to the digital tape unit. This is accomplished using a <u>Universal Asynchronous Receiver/Transmitter (UART)</u>, the Western Digital TR1863B [20], chosen due to its single power supply requirement (+5 Vdc), cf. Fig. A4.

The UART's transmitter portion, used during data acquisition, converts the 8 parallel data bits provided by the microprocessor at data register A, cf. Fig. 6, to serial data with a start bit, two stop bits and a parity bit, cf. Fig. 9. The data transfer rate was experimentally chosen at 1200 bits per second for maximum recording reliability consistent with acceptable data measurement intervals. To transmit each character, the microprocessor pauses until the 'transmitter

register empty' line, cf. Figs. 6 and A4, has gone from the low (OV) to high (+5V) state, transfers a valid data word to data register A, loads the UART data word register by momentarily bringing the 'transmitter register load' line to a high state, and again pauses until the 'transmitter register empty' line indicates that character transmission is complete. This process is repeated until all data words are transferred to the tape unit.

The receiver portion of the UART, pins 4 through 20, cf. Fig. A4, is used during data playback. Serial data in the format shown in Fig. 9 is transferred from the tape unit to pin 20 of the UART. When a complete character has been received and the UART has verified that a proper word has been received by checking parity and stop bits, the converted parallel data is placed in a holding register and the 'character received' line, cf. Figs. 6 and A4, is set to a low The microprocessor then transfers the data values value. from the receiver holding register through data port A and stores the data word in memory. The UART is reset by a low level signal applied to the 'data received reset' line by the microprocessor through data register B, and the process continues for each word sent to the UART.

If bad parity or stop bits are detected, a visual indication is provided by the parity and framing error light, cf. Fig. A5. The SN7402 NOR gate is configured as a latch to hold the light emitting diode on when a bad bit is detected. The SN7400 NAND gate is used as an inverter for light reset once the error condition has been acknowledged by the operator. Errors of this type are rare, since the cassette tapes are certified by the manufacturer to be free of "bit drops", i.e., areas of tape which do not permit proper recording.

Since the UART does not have an internal clock to provide bit rate information, an external clock at a frequency 16 times that of the bit transfer rate is required. Data transfer rate is 1200 bits per second which corresponds to a clock rate of 19.2 kHz, generated by the circuit shown in Fig. A6. The voltage controlled oscillator (VCO) portion of a Western Electric phase locked loop, the 502EP (R3221), is used to provide an extremely stable 76.8 kHz square wave signal [21]. [22], which is divided by 4 with a SN7493 binary counter to obtain the 19.2 kHz square wave (Clock A) for the tape-microprocessor communication UART. Further division by 4 generates a 4.8 kHz square wave (Clock B). which is used in the microprocessor-main computer interface.

#### Microprocessor to Main Computer

Data stored on cassette tape is transferred to the Control Data Corporation (CDC) 6400 main computer facility at Lehigh University via a 300 bit per second phone line link. Since the phone line is a serial data channel, the microprocessor's parallel data format is again converted using a Western Digital TR1863B UART. Connections to the transmitter portion of the UART (the receiver is not used) are shown in Fig. A7. The CDC 6400 computer requires even parity, generation of two stop bits, standard ASCII characters and hence 7 bit UART operation, which in turn necessitates breaking the 8 bit data word into two hexadecimal characters before transmission, cf. Section V.

Microprocessor operation of the UART for character transmission is identical in form to the tape communication UART, cf. Section III. The 16 times clock frequency, 4.8 kHz, is generated by the circuit previously described, cf. Fig. A6. Serial output data is shifted from the UART, pin 25, through a TTL to RS-232 level converting integrated circuit (MC1488), cf. Fig. A1(a), directly into the modulator/demodulator (MODEM) portion of the Perkin-Elmer computer terminal for transmission to the CDC 6400 main computer.

#### Analog Input System

Interfacing the microprocessor to several inputs requires an input multiplexer, analog to digital converter and appropriate control circuitry. The Burr-Brown MP-21 analog input system [23], cf. Fig. 10, was chosen to fulfil these requirements on the basis of modularity, compatability with the KIM-1 microprocessor system, and low cost.

The MP-21 system inputs enter a 16 to 1 line multiplexer controlled by the microprocessor's address bus, lines AO thru A15. Each input channel corresponds to a location in memory of the base address,  $1COO_{16}$ , plus the input channel number in hexadecimal, e.g. IN-6 corresponds to  $1CO6_{16}$ , IN-10 corresponds to  $1COA_{16}$ , etc. The analog input signal selected enters an instrumentation amplifier, which is set so that the analog to digital converter (ADC) spans an input range of -5V to +5V. After appropriate delay for amplifier setting and analog to digital conversion, the 8 bits of data are placed into the tri-state output latch.

In operation, signal conversion begins as the microprocessor places the MP-21 address corresponding to the desired input channel (IN-0 thru IN-15) on to the address

bus (data is ignored at this time). The microprocessor delays 40  $\mu$ s (35  $\mu$ s settling time and 5  $\mu$ s ADC time), then places the same address on the address bus. The MP-21 enables data bus drivers and transfers the digitized value over the data bus for storage in the microprocessor's memory.

Detailed connections to the MP-21 module; which is mounted on the analog interface board, cf. Fig. 4, are shown in Fig. A8.

#### Power Supplies

Regulated dc voltages and currents at +5V at 2.0A, +15V at 0.7A, +24V at 0.6A, +28V at 0.1A, -5V at 1.0A, and -15V at 0.7A are required to operate the data logging apparatus, signal conditioning equipment, and sensors. The scheme adopted which avoids power supply duplication and provides operation both on the vehicle (data acquisition) and in the laboratory (system testing and data playback) is shown in Fig. 11. A 115 Vac operated power supply producing the voltages and currents listed above is operated from the 115 Vac laboratory mains; during vehicle operation the 115 Vac is derived from an EICO Model 1080 Inverter, which provides 60 Hz (nominal) square waves. This inverter,

also used to operate other 115 Vac instrumentation, e.g. oscilloscopes, is powered by a 90Ah, 12V storage battery, which requires recharging after continuous data logging of approximately 7 hours. Two other small battery packs are used for sensors, cf. Section IV.

A detailed schematic of the 115 Vac input power supply system is shown in Fig. A9. The +24 Vdc and +28 Vdc supplies are commerically available units, whereas the ±5V and ±15V power supplies were specially designed to operate with either sine or square wave inputs. Since the chokes limit the filter capacitor's charge to the average sine wave value, their voltage rating can be low. The solid state regulators for the ±15V supplies are located on the power supply chassis, but the ±5V regulators are in the main equipment rack chassis. Airflow from a small fan in the power supply cools it as well as the main equipment rack.

IV. Vehicle Specifications, Sensors, and Analog Signal Conditioning

#### Battronics Minivan Specifications

A 1974 Battronics Minivan EV, presently leased to Lehigh University by the Pennsylvania Power and Light Co. (PP&L), was modified from its standard driver,

passenger, and cargo format to carry a driver, five passengers and instrumentation for data acquisition while providing a student bus service from campus to a local dormitory. Entrance to the van is through sliding doors on either side, cf. Fig. 12, and a large rear door, cf. Fig. 13. Vehicle propulsion is provided by a 42 hp series wound, direct current (dc) motor, a silicon control rectifier (SCR) chopper type controller with bypass and field weakening contactors, cf. Fig. 14, and two series connected battery modules which yield 112V, cf. Fig. 15. A simplified schematic of this portion of the system is shown in Fig. 16. The main batteries are recharged through an on-board charger which operates from either 115 or 230 Vac: the auxiliary equipment battery is recharged with a dc-to-dc converter.

Mechanically, the vehicle, weighing approximately 5000 pounds, has a solid axle front and rear suspension, with a conventional front engine/rear drive format. Although a transfer case with two speeds is installed in the van, only the low gear ratio (1.96:1) is used over the routes selected. Complete vehicle details are listed in App. B.

#### Sensor Design Constraints and Signal Conditioning

The following sensor and signal conditioning constraints are due to the Minivan specifications and data acquisition system design:

ζ.

- 1. Since measurements are made on the 112V system, which is isolated from the vehicle chassis, the sensors must also be insulated from the vehicle chassis because available differential amplifiers have limited common mode ranges. This in turn implies that the instrumentation system ground must be insulated from the vehicle chassis.
- 2. Low power consumption by sensors and signal conditioning circuits.
- 3. Properly shielded cables and connectors must be used to avoid coupling of controller transients onto the signal lines.
- Where possible measurements must be made differentially to avoid common mode signal difficulties.
- The sensor signal range must be adjusted to match the analog input system's range, i.e.
  -5V to +5V, for greatest signal resolution. This implies that gain or attenuating stages are required.
- 6. Filtering of high frequency components is essential so that the signal's "running average" is recorded.

Points 5 and 6 embody the required signal conditioning. While the reasons for amplifying low or attenuating high level signals is clear, the need for signal filtering is not obvious. For example, during operation, a silicon control rectifier (Main SCR or "5 REC", cf. Fig. 16) and associated circuitry adjusts the average motor current by periodically chopping it to produce a varying

duty cycle = 
$$\frac{t_{on}}{t_{on} + t_{off}} = \frac{t_{on}}{T}$$
,

where  $t_{on}$  = amount of time current is allowed to flow  $t_{off}$  = amount of time current does not flow T = period of waveform =  $\frac{1}{f}$ f = waveform frequency

which in turn controls the motor speed. The controller is designed so that <u>both</u> duty cycle and waveform frequency vary with throttle opening for smooth acceleration and best performance. The chopping frequency vs. duty cycle graph, cf. Fig. 17, shows that the lowest frequency component generated during controller operation is approximately 65 Hz. Since the data logging system samples the sensor signals only once per second, a signal average must be created to properly represent motor-controller system performance. It is shown in App. C that a one stage low pass filter with a time constant  $\tau = 0.2$  seconds provides a reasonable running average while retaining good transient performance.

Similar filters are used on other parameters, e.g. vehicle inclination, acceleration, etc., for recording average parameter behavior rather than momentary fluctuations.

#### Sensed Quantities and Sensors

A collection of measured variables, sensors and pertinent figures is found in Table 1. These sensed quantities are divided into three groups: electrical (battery-motor-controller parameters), mechanical (drive line torque and vehicle motion parameters), and other (time interval between measurements and battery electrolyte temperatures). Details of sensor functions and circuits involved follow.

#### Electrical Variables

There are seven different measured electrical quantities: battery voltage and current, armature and field voltage, controller duty cycle, controller bypass, and field weakening, cf. Table I. Proper conditioning of the signals involved in data logging is achieved through individually designed and constructed circuits.

The battery voltage conditioning circuit, cf. Fig. AlO, consists of operational amplifier OA-1 and an input resistive divider which attenuates and level

shifts the battery voltage so that zero and 150V corresponds to an output voltage of  $\pm$ 5V respectively. Battery voltage fluctuations occurring when the controller interrupts the current are smoothed by the .single stage low pass filter consisting of OA-2, 2  $\mu$ F capacitor, and the 50K and 100K resistors, cf. App. C.

Battery current sensing apparatus, cf. Fig. All, consists of a 200 mV at 600A current shunt (installed in the negative lead to the 112V battery pack, cf. Fig. 16), an input stage (LM725) with a gain of 25, and a unity gain network with differential resistor ladder and OA-1. The unity gain network is included because it was originally thought that the current shunt was installed in the positive lead of the 112V battery Since the battery pack negative side is also system. instrumentation ground, a current shunt installed in the positive lead will cause an excessive common mode voltage to appear at the first stage operational amplifier's input. Thus, the first stage is powered from a separate battery to isolate it from the rest of the system, and its output is delivered to the resistive divider network, which reduces the common mode voltage to a reasonable value for the second stage (OA-1) to
reject. Amplifier OA-1 also compensates for the signal attenuation by the divider, and OA-2 provides the required signal conditioning.

The circuit sensing the armature and field voltages is shown in Fig. A12. The armature voltage is sensed at the motor armature terminals through an input resistor/capacitor network which filters high voltage spikes, caused by motor inductance, and reduces the common mode voltage to a level which OA-1 can handle. Stage OA-1 level shifts and reduces the  $\pm 100V$ input to  $\pm 10V$  at its output. Low pass filtering and further attenuation of the signal by a factor of 2, to produce an output voltage range of  $\pm 5V$ , occurs through OA-2.

The field voltage is sensed at the motor field terminals, using essentially the same circuit as the armature voltage sensing circuitry, cf. Fig. A12, except that the second operational amplifier stage provides a gain of 2, which shifts the field voltage range  $V_{in} = \pm 25V$  to  $V_{out} = \pm 5V$ .

The duty cycle is measured using the circuit shown in Fig. Al3. The main SCR ("5 REC", cf. Fig. 16) trigger signal, which is turned on and off as the battery current is pulsed, is applied to the input of

this unit consisting of a comparator (LM301 operational amplifier), OA-1, OA-2, the voltage references and their associated resistors and diodes. The output of this precision comparator (pin #6 of LM301) is high (+5V)/low (-5V) if the input signal is greater/less than the threshold (set at pin 2 of LM301). The average value (i.e., the dc level) of this waveform represents the duty cycle of the input waveform and is obtained using two identical cascaded low pass filter stages, cf. App. C, having overall time constant 0.2 The input threshold level, approximately seconds. 0.25V, is set to yield representative duty cycles based on comparison of trigger and battery current waveforms.

In addition to duty cycle variation, the controller has both bypass and field weakening features for higher vehicle speed operation. In the bypass mode the motor is connected directly to the battery through the 1A contactor, cf. Fig. 16. The output voltage of the bypass monitor circuit, cf. Fig. Al4, is at a low level (OV) during controller bypass (microswitch closed) and high level (+5V) during normal operation. If higher vehicle speeds are desired during the bypass mode, field weakening is introduced using the field

weakening (FW) contactor, cf. Fig. 16, by placing a 0.008Ω resistance in shunt with the motor field which decreases its current and increases motor speed. The field weakening monitor circuit is identical to the bypass monitor circuit and is in the high state (+5V) only during the field weakening mode.

#### Mechanical Quantities

To properly characterize the motion of the van and its driveline, the propeller shaft torque, distance travelled, vehicle speed, acceleration and inclination are sensed using ancilliary transducer units, cf. Table 1.

Propeller shaft torque is measured with a Lebow model 1228H-10K strain gauge shaft torque sensor consisting of a strain gauge bridge which produces an output linearly related to the torque transmitted through the propeller shaft. The low level bridge output signal (77.75 mV at 10,000 in-1b) is applied to a low drift, low level amplifier, cf. Fig. A15. The amplifier is designed to account for the 350.7 $\Omega$ (nominal) bridge impedance at signal nodes (B and D) and has first stage gain 64.57 to provide full scale output range  $\pm 10,000$  in-1b over the  $\pm 5V$ output range. The first stage output voltage is conditioned by a single stage low pass filter with

time constant 0.2 seconds and sent to the analog input system.

The distance travelled and vehicle speed are sensed with a Nucleus Corporation Model NC-7 fifth wheel and associated apparatus, cf. Fig. 18, whose speed output from a precision tachometer generator is 88.6 mV per mile per hour of vehicle speed. This signal appears at IN-1 of the analog input system after conditioning in the low pass filter, cf. Fig. A16. A Nucleus Model RSA retentive speedometer is installed to assist the driver in observing vehicle speed.

Distance information is available in visual and digital forms at the Nucleus model PTA pulse totalizer outputs, cf. Fig. 18. Digital pulses (one pulse per foot travelled) produce an analog output signal proportional to distance travelled from an 8 bit up counter consisting of two 74193 synchronous 4 bit counters and a Hybrid Systems digital to analog converter, cf. Fig. Al7. The counter's sequence 0,1,2,...,254,255,0,1,2,... combined with vehicle top speed of about 70 ft/s is used to determine how far the vehicle has gone in the past several seconds: each time the counter system resets to zero, 256 ft are added to the distance travelled using system software.

Vehicle acceleration is measured using a Kistler Instrument Model 303T102 servo accelerometer. The sensor, mounted in the foreward center of the van in a one inch thick aluminum plate and steel frame bolted to the vehicle floor, has its sensitive axis pointed to the front of the van and level with the horizon. Foreward acceleration is calculated, cf. Section V, by eliminating the component due to gravity using terrain data from the gyroscope (discussed below).

The accelerometer unit requires regulated 28 Vdc, 25.69K accelerometer range and 120K accelerometer gain resistors to yield an output range of  $\pm 1g$  (g = acceleration due to gravity), cf. Fig. Al8. The 25.69K resistor in conjunction with an 8  $\mu$ F capacitor and 500 $\Omega$  resistor form a lead-lag low pass signal conditioning filter while maintaining reasonable servo stability.

Inclination is measured using an Electronic Specialty Co. Model N3200 vertical gyroscope which has an internal motor and blower for gyro erection, instead of requiring an external compressed air source, and is powered by two 12V motorcycle batteries which supply the heavy initial current (3A) on gyro

start up. Inclination information is derived from a 2K potentiometer having a  $\pm$ 5V range over its  $\pm$ 55 degree angular span with the connections arranged for positive/negative output corresponding to uphill/down-hill respectively. This output, which has a maximum error  $\pm$ 1.5 degrees, appears at IN-4 of the analog input system, cf. Fig. A19.

#### Other Variables

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Important measured quantities which do not fall into mechanical or electrical categories directly are the time interval between measurement sets and battery electrolyte temperatures. The time between measurement sets is generated by the crystal controlled (1 MHz) microprocessor reference. The microprocessor requires approximately 160 milliseconds to acquire, store and transfer data to the cassette tape. It then enters a timing loop, whose length was adjusted by trial and error, and exits this loop one second after initial start of data acquisition thereby generating measurement sets spaced at one second intervals.

Battery efficiency and cycle life both depend upon temperature [24]. Thus, two battery electrolyte temperatures are monitored to determine the extremes

which occur during operation. Since the initial thermocouple system was unreliable, due to the low level signal (2 mV) and high common mode voltages, an Analog Devices AD590L two terminal temperature transducer was This device produces an output current substituted. directly proportional to absolute temperature, with  $1\mu A/^{O}K$  output and overall accuracy of  $+1^{O}K$ . The circuit shown in Fig. A20 provides a constant voltage reference source for these transducers and the operational amplifiers, which level shift the output voltage generated by the AD590L and 10K load resistor over the temperature range -5 to  $55^{\circ}$ C. The 1µF capacitor in conjunction with the 10K resistor act as a low pass filter to reduce system noise.

Physical construction of the battery temperature probe, cf. Fig. 19, consists of a 4 inch length of 0.25 inch diameter glass tubing cut and sealed at one end. After the temperature sensor is wired and placed in the tube, mineral oil is added to the halfway point, and the tube is placed in a battery cap and held in place with rubber 0-rings. The assembly is then placed in the battery, the wires taped to the top of the glass tube, and the tube pressed down until it touches the battery plate protection sheet.

#### V. Computer Programming

#### Computers and Tasks Involved

The large quantity of data generated during acquisition is processed, stored, and printed out by computers, which readily provide flexibility and necessary capabilities. As previously indicated, two types of computers are involved: a KIM-1 microprocessor, which forms the heart of the data acquisition system, and a large computing facility, the Control Data Corporation (CDC) 6400 at Lehigh University. Thus, KIM-1 programs were developed for data acquisition and recording with subsequent recovery and transmission to the CDC 6400 facility, where the raw data is reduced to useful results, printed out, and stored on magnetic tape. The tapes are then supplied to The Pennsylvania State University personnel who synthesize and verify the vehicle model being developed. Each of these tasks, which have different programming requirements, are discussed in detail in the following sections.

# Microprocessor Programming

# Data Acquisition

During data acquisition, the KIM-1 controls both the analog input system and the digital tape unit,

cf. Figs. 2, 20. After initializing, the microprocessor starts the ADC on the first information channel, pauses 40 microseconds, directs the data value into memory and adds it to the checksum. This process is repeated until all 16 channels have been sampled and the data stored in memory. The tape unit is then started foreward (in record mode) and the start word, which is the time increment counter, is sent to the UART, followed by the sixteen data samples, the checksum and the stop word, which is the data run number assigned by the operator, cf. Fig. 21(a). Between each item sent to the UART, the microprocessor waits one character space to avoid framing and overrun errors on data playback. The microprocessor stops the tape unit, waits approximately 840 milliseconds, initiates data taking again, then repeats this entire process until a data run is completed. Each side of a 282 foot digital tape cassette can record 14 minutes of data in this manner.

#### Data Playback

On playback, the microprocessor must transfer data from the tape unit to memory and output characters representing this data to the CDC 6400 main computer. The transmission format is two hexadecimal ASCII

characters representing each 8 bits of data, cf. Fig. 21(b). The program flow chart, cf. Fig. 22, lists the steps followed by the microprocessor for data transfer. After initialization, the tape unit is started foreward (in playback mode), data is examined for a start word which is then stored and added to the The eighteen remaining data items (checksum, checksum. 16 data words, and stop word) are transferred from tape to memory, the tape unit is stopped while the checksum, start and stop words are each tested and if any of these are incorrect, the microverified: processor transmits a "00" and selects the next measurement set on the tape, otherwise it transmits the verified set to the CDC 6400. Each data word is split into upper and lower four bits, and these bits are translated into ASCII characters and transmitted, high order bits followed by low order bits. After all 19 fields (38 characters), cf. Fig. 21, and a line feed and carriage return have been sent, the tape unit is restarted and the entire process repeated until all the data from the tape cassette has been transferred.

# Main Computer Programs

## Data Receiving and Storage

The interactive text editing program SENATOR [25]

is used to receive the ASCII characters, sent during data playback, and build a new data file. Automatic line number generation and data file structuring are provided by the input mode, and the paper tape directive prevents the retransmission of characters from the CDC 6400 while the microprocessor is transferring data to it. To transfer the data and build a file, the user prepares the microprocessor system, cf. Fig. 23, types SENATOR, specifies the data file EVDATA and system DATA, then INPUT and %T. Two data transfer switches on the terminal/microprocessor control box are moved from the terminal to microprocessor position, and data playback is initiated by pressing "GO" on the microprocessor keyboard. After data transfer is complete, the playback system is halted by pressing "ST", the data transfer switches are returned to the terminal position, CRTL and S are typed simultaneously followed by a The SENATOR holding file containing the semicolon. entire data file is now ready for use or storage.

Temporary file storage on system disc space is available using the command CATALOG, EVDATA, ID=EV [26]. The file is retained under the name EVDATA and identifier EV for recall by the data reduction and processing program.

#### Data Reduction and Processing

The raw data is reduced using a CDC 6400 FORTRAN program of approximately 100 executable statements. The program selects the EVDATA file (previously constructed), checks that it is the desired data set, then converts each two character hexadecimal field, cf. Fig. 21(b), into its decimal equivalent and stores them in an array having a range 0 to 255. These values are converted by the program into final form representing each sensed variable, cf. Table 2. When all the data has been converted and reduced, the program prints out several copies, cf. App. D, and stores the file EVOUT using the CATALOG utility [26], for use in magnetic tape generation.

# Magnetic Tape Generation

Two magnetic tapes are generated for use by EV personnel: a permanent file copy and a data transfer tape. Permanent file copies containing all reduced data are maintained on tapes EV1 and EV2, stored in the Lehigh University Computing Center tape library. These are created using the COPYBF utility [26] and standard tape handling procedures.

The tape for data transfer to The Pennsylvania State University's IBM computer is created with the CDC utility package FORM in SCOPE stranger'writing format, blocked with 80 characters per record and 20 records per block, and a coded tape format. Details on the complete procedure are found in [27].

VI. Results

#### Vehicle Data Runs

Several types of vehicle runs can be made using the data acquisition system. Each vehicle test requires installing the data logging system on board the van, activating the system power supplies, transcribing the microprocessor program from tape, inserting a digital cassette in the tape unit, resetting various system components, and entering the data set number, cf. Fig. 24. The test is started by pressing \_"GO" on the microprocessor keyboard, and the data run proceeds automatically until "ST" is pressed. Any number of runs can be made by repeating this procedure.

After preliminary calibrating and debugging runs, vehicle data acquisition commenced over several routes and driving cycles, cf. Fig. 25. Constant speeds,

wide open throttle (WOT) accelerations, and coast downs from high speed were conducted on Riverside Drive, a 1.5 mile flat course. Variable terrain is encountered on a 1.2 mile hilly course on the Lehigh University campus, along the 2.5 mile loop to the Bishopthorpe student dormitory, and over the 6 mile South Mountain run to and from SMAGS, a second student dormitory. Data from the campus run and level course tests are presented next.

## Campus Run Data

Vehicle data from run number 50, a single loop on the campus route, is presented in App. D. A sketch of the route showing the roads and slopes involved is shown in Fig. 26; the vertically directed streets (North-South) have slopes  $2^{\circ}$  to  $7^{\circ}$  whereas the horizontal ones are essentially flat  $(\pm 1^{\circ})$ . Maximum observed drive line torque (686 N·m) occurred 194 seconds into the run, after stop number 3, with a correspondingly large battery current (323 A). Total route distance travelled was 2059 m, and maximum speed was 58.8 km/hr, after running with both bypass and field weakening in operation. After field weakening was engaged, battery voltage dropped to 89.6V, 254 seconds after start, and increased as the current

drawn decreased (from 254 to 266 seconds). Electrolyte temperature rose 1.3°C and 1.0°C in the outer and center cells respectively over the total run time of 412 seconds.

#### Level Course Data

Average vehicle velocity versus time for wide open throttle acceleration is presented in Fig. 27. The averaged acceleration data yields a O-30 km/hr time of 5.8 seconds and a O-50 km/hr time of 16.5 seconds, in good agreement with the results obtained by Dustin, <u>et al</u> [13]. Top speed of approximately 67 km/hr was reached after 60 seconds.

Coast down data was taken by accelerating the vehicle to approximately 65 km/hr, releasing the throttle and allowing the vehicle to roll freely to a stop, cf. Fig. 28. An almost constant deceleration occurred during the 92 seconds required for the vehicle to stop.

Reduced data from constant speed data runs 30-33 are presented in Table 3. As speed increases, duty cycle also increases, cf. Fig. 29. While the drive shaft torque and battery voltage do not change much, power drawn from the battery increases: battery current rises by a factor of 3 as speed changes from

8.2 to 47.7 km/hr. Correspondingly, battery energy expended per kilometer drops as vehicle speed increases: at 40 km/hr 1.31 megajoules is required to travel one kilometer. This agrees with the results of Dustin, et al [13] (1.5 MJ/km) when the charger efficiency of about 0.9 is accounted for.

Vehicle range at each speed was calculated using battery ampere-hour capacity, measured speed and battery current, and assuming an 80% battery discharge. These results, cf. Fig. 30, along with the NASA experimental results [13], indicate that vehicle range increases up to 40-50 km/hr, where 80-100 km ranges may be expected.

#### Preliminary Model Results

Although model synthesis and parameter adjustment is still ongoing, preliminary results from The Pennsylvania State University vehicle simulation are shown in Figs. 31-34 for a wide open throttle and coasting run. The simulation models the battery performance, motor-controller interactions, and vehicle dynamics using separate FORTRAN subroutines, and both printed and plotted results are generated. Each plot shows both data and simulation results as functions of time, with the experimental results generally somewhat

larger than the simulation results. The velocity, cf. Fig. 31, and battery current, cf. Fig. 32, show that the largest discrepancies occur at high speeds. Both drive shaft torque, cf. Fig. 33, and motor armature voltage, cf. Fig. 34, follow the experimental results closely. Further simulation results for these and other parameters are expected as model synthesis proceeds.

# VII. Summary

A digital data acquisition system was designed and constructed for data collection on a 1974 Battronics Minivan electric vehicle. This system is microprocessor based and interfaced with various sensors on board the van to measure battery voltage, current, and electrolyte temperatures, motor armature and field voltages, controller duty cycle, bypass, and field weakening, drive line torque, distance travelled, vehicle speed, acceleration, and inclina-Raw data taken over several different routes. tion. with both varied and level terrain, was recorded by the system on digital cassette tapes. These tapes were played back using the same data acquisition hardware and different software to the CDC 6400 main computer at Lehigh University, where the raw data was

processed and both hard copy and magnetic tapes generated. The data was then sent to The Pennsylvania State University, where it was used to aid in synthesis and verification of the vehicle model being developed. Experimental and preliminary model results agree to first order, and further model optimization is expected to yield closer correlation.

	Pertinent Figure(s)	Fig. Alo	Fig. All	Fig. Al2	Fig. Al3	Fig. Al4	Fig. Al5	Fig. 18 Fig. A16 Fig. A17	Fig. Al8	Fig. Al9		{F18. 19 {F18. A20
and Pertinent Figures	Sensor	Circuit	Shunt	Circuit) Circuit	Circuit	Microswitch) Microswitch	Strain Gauge Torque Meter	Fifth Wheel System	Servo Accelerometer	Gyroscope	Microprocessor	Semiconductor Temperature
Sensed, Sensors,	Type	Electrical	Electrical	Electrical Electrical	Electrical	Electrical Electrical	Mechanical	Mechanical Mechanical	Mechanical	Mechanical	Other	Other
Table 1 Variables	Variable	Battery Voltage	Battery Current	Armature Voltage Field Voltage	Duty Cycle	Controller Bypass Field Weakening	Drive Line Torque	Distance Travelled Speed	Accleration	Vehicle Inclination	Time	<b>Battery Electrolyte</b> Temperature

	Re Conversion Procedure	$\sim \sim (CHV^{T}) + (No. of Resets) + 256$	1 (CHV)/256; If in bypass, duty cycl	(((CHV-128)*0.0391)+0.1231)*11.08	90 (CHV)*0.7097	$((CHV-128)*7.483\times10^{-3}) - sin(\theta)$ where $\theta = inclination angle$	∞ ((CHV)+(No. of Resets)*256)*0.3048	0 (((CHV-128)*0.0391)+0.02)*226.53	150 (CHV)*(150/256)	(CHV-128)*4.6806	(CHV-128)*(100/128)	(CHV-128)*(25/128)	r OUT If CHV > 251, field weakening is otherwise, OUT.	o 55 ((76.7-((CHV)-128)*0.4398)-32)*(5/9
	Nan	0 to	0 to	<del>+</del> 55	0 to	<b>7</b> 1	0 to	<del>-</del> 113(	0 to	<del>1</del> 600	) ±100	+25	IN O	-5 tí
Variable (te)	ATTADIE (UILL)	Time (seconds)	Duty Cycle (unitless)	Inclination (degrees)	Velocity (km/hr)	Acceleration (g)	Distance Travelled (m)	Torque (N•m)	Battery Voltage (volts)	Battery Current (amps)	Armature Voltage (volts	Field Voltage (volts)	Field Weakening (unitless)	Battery Electrolyte Temperatures ( <sup>O</sup> C)

CHV means Converted Hexadecimal Value, Range O to 255, cf. Section V. ÷

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Sensor Variable Conversions

Table 2

Calculated Vehicle Range d ( (km)	45	61	69	73	78	80
Battery Energy Expended EBatt (MJ/km)	2.37	1.78	1.53	1,41	1.31	1.25
Battery Power out Pout (kW)	5.4	7.7	10.0	12.5	14.5	16.5
Battery Current IBatt (A)	48	68	16	115	135	157
Battery Voltage VBatt (V)	114	112	110	109	107	105
Torque T (N·m)	63	71	66	11	74	83
Duty Cycle (unitless)	0.12	0.23	0.35	0.47	0.61	0.77
Speed v (km/hr)	8.2	15.6	23.5	31.9	39.7	47.7
Run #	30	30	31	32	32/33	33

③ d(km) = v(km/hr)×330A-H×0.8 1Batt(A)

$$(2) E_{Batt}(MJ/km) = \frac{3.6 \times P_{out}(kW)}{v(km/hr)}$$

Notes: (1)  $P_{out}(W) = V_{Batt}(V) \times I_{Batt}(A)$ 

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Averaged Constant Speed Results for Runs 30-33

Table 3



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Fig. 1. Block Diagram of Digital Data Acquisition System







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Fig. 5. KIM-1 Microprocessor System Block Diagram

| line<br>∦         | Allocation during:<br>Data Acquisition Data Playback                          |
|-------------------|-------------------------------------------------------------------------------|
| <u>_</u>          |                                                                               |
| <u>A Register</u> |                                                                               |
| 0                 | (Output) LSB, tape unit data (Input)                                          |
| 1                 | (Output) 2 <sup>1</sup> , tape unit data (Input)                              |
| 2                 | (Output) 2 <sup>2</sup> , tape unit data (Input)                              |
| 3                 | (Output) 2 <sup>3</sup> , tape unit data (Input)                              |
| 4                 | (Output) 2 <sup>4</sup> , tape unit data (Input)                              |
| 5                 | (Output) 2 <sup>5</sup> , tape unit data (Input)                              |
| 6                 | (Output) 2 <sup>6</sup> , tape unit data (Input)                              |
| 7                 | (Output) MSB, tape unit data (Input)                                          |
| <u>B Register</u> |                                                                               |
| 0                 | Disconnect Receiver data<br>register, Tape (Output)                           |
| 1                 | Load Xmit Register,<br>Tape (Output) Load Xmit Register,<br>CDC 6400 (Output) |
| 2                 | Data Received<br>Reset (Output)                                               |
| 3                 | Unavailable                                                                   |
| 4                 | Tape Foreward Start/Stop<br>Control (Output)                                  |
| 5                 | Transmitter Holding Register<br>Empty, CDC 6400 (Input)                       |
| 6                 | Unavailable                                                                   |
| 7                 | Xmit Register Empty, Character Received,<br>Tape (Input) Tape (Input)         |
| Note: Outp        | ut means output from microprocessor to device,                                |
| Inpu              | t means input to microprocessor from device.                                  |

Fig. 6 Data Register Line Allocations

- Type: Philips 40461 digital cassette recorder.
- Drive: Uses Philips type cassettes. Three motors (two hub, one capstan), foreward/reverse at 7.5 ips, fast foreward/reverse at 75 ips. Start time 40 ms, start path 7 mm; stop time 30 ms, stop path 2 mm.
- Physical: 5" × 5" × 12", 8.3 lb. Metal cased, unit is rack mounted, cf. Fig. 8.
- Signals: Control (TTL levels) fast/slow, foreward, reverse, lock, unit select, read enable,
  - Status (TTL levels) begin/end of tape, A/B side, cassette in position, unit ready, cassette write enable plug
    - Data read data (TTL level), write data (TTL level), analog read data (for testing).
  - Power: +5V at 0.5A, +24V at 0.7A

Fig. 7 Digital Cassette Tape Recorder Specifications



# Fig. 8 Rack Mounted Digital Cassette Tape Unit and Control Box

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| Parameters | Selected: | Odd Parity             |
|------------|-----------|------------------------|
|            |           | Two Stop Bits          |
|            |           | 8 Bit Data Word Length |
|            |           | 1200 Baud Data Rate    |

| Fig. | 9. | Digit | tal Ca | Cassette |  |  |  |
|------|----|-------|--------|----------|--|--|--|
|      |    | Tape  | Data   | Format   |  |  |  |

.



Fig. 10. Block Diagram of Analog Input System



(a) In Laboratory: Testing and Data Playback



(b) On Board Vehicle: Data Acquisition





# Fig. 12 Battronics Minivan - Front Quarter View



# Fig. 13 Battronics Minivan - Rear and Interior View



# Fig. 14 Underhood Electricals - Battronics Minivan



# Fig. 15 Right Hand Side Battery Compartment -Battronics Minivan






Fig. 17. Measured Controller Chopping Frequency vs. Duty Cycle



Fig. 18. Nucleus Fifth Wheel Apparatus

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Fig. 19 Cross Section of Battery Temperature Sensor





|                 | S TART<br>WORD | IN-15        | 11-14        | IN-13             | IN-12             | <b>IN-2</b>      | I-NI          | 0-NI       | CHECK<br>SUM | STOP<br>WORD   |
|-----------------|----------------|--------------|--------------|-------------------|-------------------|------------------|---------------|------------|--------------|----------------|
|                 | ¥<br>₩         | rward        |              |                   |                   |                  | ¥             | top W      | ord ∎ D      | ata Set Mumber |
|                 |                |              |              |                   |                   |                  | *St           | art W      | ord = T      | ime Counter    |
|                 |                |              | (a)          | Digita            | l Tape            | Data Fo          | ormat         |            |              |                |
|                 | DATA<br>SET    | CHECK<br>SUM | IN-15        | 14-14             | IN-13             | IN-12            | 11-11         | н<br>:     | N-1 IN       | -0 TIME        |
| Data:<br>Field: | 10             | 38<br>2      | 80<br>13     | 80                | A9<br>5           | B7<br>6          | 62<br>7       | •          | 80 0<br>17 1 | 8<br>00<br>00  |
|                 | I              | -<br>(Each   | (<br>fleld r | b) Dat<br>epresen | a Outpu<br>ts two | t Form<br>hexide | at<br>cimal c | <br>tharac | ters)        |                |
|                 |                |              | Na 21        | 6.01<br>6.01      | ירן<br>ער דיים    |                  |               |            |              |                |

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Fig. 22 Data Playback Programming





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- Install three rack mounted components (power supply, tape unit, electronics), analog tape unit and digital tape control box.
- 2) Connect: Digital tape unit plug to electronics Analog tape unit to electronics cable Power line from electronics to power supply 110V power line (power supply) to mains Data output cable to electronics rack.

Ü

- 3) Turn on power supply.
- 4) To load program, enter on microprocessor keyboard: "RS"

| "AD", | 00 <b>F</b> 1, | "DA",  | 00         |
|-------|----------------|--------|------------|
| "AD", | 1703,          | "DA",  | 1 <b>F</b> |
| "AD", | 17F9,          | "DA",  | 01         |
| "AD", | 17FA,          | "DA",  | 00         |
| +,    | 1C             | ·      |            |
| "AD", | 1873,          | "GO" . | •          |

- 5) Microprocessor program loading is complete when "0000 xx" appears (x = don't care). If "0000 xx" does not appear, rewind analog tape cassette and repeat step 4. Rewind analog tape cassette after "0000 xx" appears.
- 6) Set electronics rack switches to "data playback" and "KBD"; reset UART and parity light.
- 7) Load, lock and rewind desired digital cassette on correct side.
- 8) Enable data playback (digital tape control box).
- 9) Login on CDC 6400.
- 10) Follow procedure of p. 37, main text, for data playback of one or several data sets.

## Fig. 23. Data Playback Checklist

- 1) Reset vehicle trip odometer before starting.
- 2) Install three rack mounted components (power supply, tape unit, electronics), analog tape unit and digital tape control box.
- 3) Connect 10 sensor input lines and 4 sensor power lines to electronics rack.
- 4) Connect: Power line from electronics to power supply Main battery ground lead (green) to electronics Digital tape unit plug to electronics Analog tape unit to electronics cable 110V power line (power supply) to inverter.
- 5) Turn on inverter, power supply, and Nucleus equipment power switches.
- 6) To load program, enter on microprocessor keyboard: "RS"

"AD", OOF1, "DA", OO "AD", 1703, "DA", 1F "AD", 17F9, "DA", 01 "AD", 17FA, "DA", OO +, 1C "AD", 1873, "GO".

5.

- 7) Microprocessor program loading is complete when "0000 xx" appears (x = don't care). If "0000 xx" does not appear, rewind analog tape cassette and repeat step 6. Rewind analog tape cassette after "0000 xx" appears.
- Start gyroscope (switch on gyro frame) cage for one minute by pressing lever at front of frame to driver's left.
- 9) Insert, lock, and rewind digital cassette.
- 10) Remove digital cassette, and wind foreward by hand until tape is visible.
- 11) Insert and lock cassette; switch on write enable. Insure that top light on control box is lit and bottom three are blank.

(Continued)

Fig. 24 Vehicle Data Run Procedure

- Place electronics rack switches to "Data Record" 12) and "KBD"; reset UART and parity light.
- Reset distance (switch on Nucleus equipment). 13)
- Key in data set number by: "AD", OO12, DA, (data 14) set #).
- To run, press: "AD", 0200, "GO"; run for no more 15) than 14 minutes per cassette side.
- For further data runs, repeat steps 9-15. 16)

| Run #         | Date | Route Name         | Type of<br>Run                        | Comments                                   |
|---------------|------|--------------------|---------------------------------------|--------------------------------------------|
| <u>Null w</u> | Date | Noule Maine        |                                       | o on and the b                             |
| 30            | 7/26 | Riverside<br>Drive | Constant<br>Speed                     | 5 mph,<br>10 mph                           |
| 31            | 7/26 | Riverside<br>Drive | Constant<br>Speed                     | 15 mph                                     |
| 32            | 7/26 | Riverside<br>Drive | Constant<br>Speed                     | 20 mph,<br>25 mph                          |
| 33            | 7/26 | Riverside<br>Drive | Constant<br>Speed                     | 25 mph<br>30 mph                           |
| 34            | 7/26 | Riverside<br>Drive | WOT/Coast                             | 3 runs                                     |
|               |      |                    |                                       |                                            |
| 40            | 7/28 | Campus             | Variable                              |                                            |
| 41            | 7/28 | Campus             | Variable                              |                                            |
| 42            | 7/28 | Bishopthorpe       | Variable                              |                                            |
| 43            | 7/28 | Bishopthorpe       | Variable                              |                                            |
|               |      |                    |                                       |                                            |
| 50            | 8/2  | Campus             | Variable                              |                                            |
| 51            | 8/2  | Campus to<br>SMAGS | Mountain                              | Run over South                             |
| 52            | 8/2  | SMAGS to<br>Campus | Mountain                              | Mountain -<br>high battery<br>temperatures |
|               |      |                    | · · · · · · · · · · · · · · · · · · · | -                                          |

Fig. 25. Data Run Summary





Fig. 27 Average Wide Open Throttle Results (Run 34)





Fig. 29 Constant Speed Results: Speed vs. Duty Cycle



Fig. 30 Constant Speed Vehicle Range Data



s = simulation results



Time



Fig. 31. Simulation Results-

Speed vs. Time

e = experimental results
s = simulation results



Fig. 32. Simulation Results-

Battery Current vs. Time

e = experimental results

s = simulation results







Fig. 33. Simulation Results-

Torque vs. Time

- e = experimental results
- s = simulation results





(seconds)

Fig. 34. Simulation Results-

Armature Voltage vs. Time

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Appendix A - System Schematics

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.







(b) Tape Stop/Start Buffer

Fig. Al Terminal Interfaces and Tape Stop/Start Foreward Line Buffer (Communications Board)



Fig. A2 Audio Cassette Tape Recorder Interface (Microprocessor)



Fig. A3 Digital Cassette Recorder Wiring (Digital Tape Unit)













| <u>Pin #(s)</u>                                                                                                                                                                                 | Connections to:                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Power and Internal<br>Connections                                                                                                                                                               |                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| 1, 3, 13, 40, 44<br>48, 49, 66                                                                                                                                                                  | No connection                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| 2<br>14<br>67<br>63<br>51<br>50<br>61<br>62<br>45<br>80, 78                                                                                                                                     | 4, 77<br>15<br>68<br>65<br>52, 64, 79<br>+5 Vdc<br>-15 Vdc<br>+15 Vdc<br>+5V thru 1K resistor<br>80<br>100K - 15V<br>-200K - 15V<br>-78                                                                                                                                                                                                                                                                                                                               |
| Analog Inputs                                                                                                                                                                                   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| 5 (IN-0)<br>6 (IN-1)<br>7 (IN-2)<br>8 (IN-3)<br>9 (IN-4)<br>10 (IN-5)<br>11 (IN-6)<br>12 (IN-7)<br>69 (IN-8)<br>70 (IN-9)<br>71 (IN-10)<br>72 (IN-11)<br>73 (IN-12)<br>74 (IN-13)<br>75 (IN-14) | Duty Cycle, cf. Fig. A13<br>Speed, cf. Fig. A17<br>Accelerometer, cf. Fig. A18<br>Torque Meter, cf. Fig. A15<br>Gyroscope, cf. Fig. A19<br>Field Voltage, cf. Fig. A12<br>Armature Voltage, cf. Fig. A12<br>Battery Voltage, cf. Fig. A12<br>Battery Current, cf. Fig. A11<br>Inner Cell Temperature,<br>cf. Fig. A20<br>Outer Cell Temperature,<br>cf. Fig. A20<br>Field Weakening Monitor,<br>cf. Fig. A14<br>Distance, cf. Fig. A16<br>Bypass Monitor cf. Fig. A14 |
| 76 (IN-15)                                                                                                                                                                                      | Not Used                                                                                                                                                                                                                                                                                                                                                                                                                                                              |

Fig. A8 MP-21 Analog Input System Connections

| Address Bus and Address                               | Select                                                                                                                    |                                                      |
|-------------------------------------------------------|---------------------------------------------------------------------------------------------------------------------------|------------------------------------------------------|
| $ \begin{array}{cccccccccccccccccccccccccccccccccccc$ | A0 $\rightarrow$ A4<br>A5<br>A6<br>A7<br>A8<br>A9<br>A10<br>A11<br>A12<br>A13<br>A14<br>A15<br>+<br>5V thru 1K resistor - | Base<br>Address<br>Selected<br>is 1COO <sub>16</sub> |
| <u>Data Bus</u><br>60 → 53                            | $DO \rightarrow D7$                                                                                                       |                                                      |
| Microprocessor Signals                                | - 7                                                                                                                       |                                                      |
| 42                                                    | R/W                                                                                                                       |                                                      |

| 46 | Ø <sub>2</sub> Clock |
|----|----------------------|
| 47 | Reset                |

| Fig. | A8 ( | (Continued) |
|------|------|-------------|
|------|------|-------------|

Κ

MP-21 Analog Input System Connections ۰,



Fig. A9 115 Vac Power Supply System

ť






Fig. All Current Shunt and Amplifier System







| Contactor<br>Function | Switch State<br>During Function |
|-----------------------|---------------------------------|
| Bypass                | Closed                          |
| Field<br>Weakening    | Open                            |

Fig. Al4 Bypass and Field Weakening Monitor Circuits







All resistors are selected, 17. tolerance Op amp is a selected ½ 747











ŀ

Fig. Al9 Gyroscope Connections



#### Appendix B

Vehicle Summary Data Sheet

Vehicle manufacturer: Battronic Truck Corp. Boyertown, Pa. Vehicle: Battronic Minivan Price and availability: \$15,000; production upon request Vehicle weight and load 1. Curb weight, kg (1bm): 2690 (5930) 2. Gross vehicle weight, kg (1bm): 2858 (6300) 3. Cargo weight, kg (1bm): 227 (500) 4. Number of passengers: 6 5. Payload, kg (1bm) 363 (800) including driver Vehicle size 1. Wheelbase, m (in.): 2.40 (94.5) 2. Length, m (in.): 3.69 (145) 3. Width, m (in.): 1.98 (77.9) 2.27 (89) 4. Height, m (in.): 5. Head room, m (in.): 1.10 (43.5) 6. Leg room, m (in.): 0.37 (14.5) 7. Frontal area,  $m^2$  (ft<sup>2</sup>): 3.9 (42) 8. Road clearance, cm (in.): 33 (13) 9. Number of seats: 6 Auxiliaries and options 1. Lights (number, type and function): 2 headlamp; 2 park; 2 brake; 1 backup; 2 sidelights at rear; 2 sidelights and directional front and rear 2. Windshield wipers: yes 3. Windshield washers: yes 4. Defroster: yes 5. Heater: yes (Hunter UH-47-6; gasoline, 7.57 liter (2 gal)) 6. Radio: No 7. Fuel gauge: yes (Curtis 11611) 8. Amperemeter: armature and 12-volt auxiliary battery 9. Tachometer: no 10. Speedometer: yes

- 11. Odometer: yes (and trip mileage indicator)
- 12. Right- or left-hand drive: left
- 13. Transmission: 2 speed; 1:1 and 1:1.96 ratios
- 14. Regenerative braking: no
- 15. Mirrors: left and right outside; interior
- 16. Power steering: no
- 17. Power brakes: no

#### Battery

Propulsion battery

- 1. Type and manufacturer: General Battery Corp. Type 56-EV-330
- 2. Number of modules: 2

- Number of cells: 56 (28 per module)
   Operating voltage, V: 112
   Capacity, Ah: 330 (at a 6 h discharge rate)
- 6. Size of each module, m (in.): height, 0.58 (23); width, 0.50 (19.5); length, 0.96 (37.75)
- 7. Weight, kg (1bm): 1043 (2300)
- 8. History (age, number of cycles, etc.): Not available

Auxiliary battery

- 1. Type and manufacturer: Titan Series 6000 Group 27; General Battery Corp.
- 2. Number of cells:
- 3. Operating voltage, V: 12
- 4. Capacity, Ah: 36 5. Size, m (in.): height, 0.20 (8); width, 0.17 (6.5); length, 0.30 (12)

Controller

- 1. Type and manufacturer: 510 R; SCR; General Electric
- 2. Voltage rating, V: 112
- 3. Current rating, A: 500
- 4. Size, m (in.): components on a mounting plate: width, 0.47 (18.5); length, 1.07 (42

Propulsion motor

- 1. Type and manufacturer: dc traction series; General Electric, 5 BT 2376C6
- 2. Insulation class: F (115°C)
- 3. Voltage rating, V: 94

- 4. Horsepower (rated), kW (hp): 31.5 (42) (0.4-h duty) 5. Size, m (in.): diameter, 0.33 (13); length, 0.61(24)
- 6. Speed (rated), rpm: 2300

Battery charger

- 1. Type and manufacturer: EV 112 A/C 30; C&D Batteries, Division of Eltra Corp.
- 2. On- or off-board type: on-board
- 3. Input voltage required, V: 120/208/240 4. Peak current demand, A: 30/15
- 5. Recharge time, h: variable
- 6. Size, m (in.): height, 0.55 (21.5); width, 0.25 (10); length, 0.56 (22)
- 7. Automatic turnoff feature: yes

Body

- 1. Manufacturer and type: Battronic van
- 2. Materials: steel
- 3. Number of doors and type: 3; left and right sliding; rear, hinged
- 4. Number of windows and type: 2 pane windshield; 1 pane each front quarter; 2 pane sliding each side door; 1 pane each rear quarter; 1 pane rear door
- 5. Number of seat and type: 1 bucket, front; 5 fiberglass bucket, in cargo area
- 6. Cargo space volume, m<sup>3</sup> (ft<sup>3</sup>): 4.76 (168) 7. Cargo space dimensions, m (ft): height, 1.63 (64); width, 1.83 (72); length, 1.83 (72), minus wheel well

Chassis

- 1. Frame
  - Type and manufacturer: Battronic Truck Corp.; box
  - B. Materials: steel
- 2. Springs and shocks
  - A. Type and manufacturer: springs 6 leaf laminated; shocks - direct-acting hydraulic cylinders
  - B. Modifications: none

3. Axles

```
A. Manufacturer: Spicer (Dana)
```

- B. Front: ClarkC. Rear: Semi-floating, hypoid gears, flanged axle shafts
- 4. Transmission
  - A. Gear ratios: rear axle, 3.07; transfer box. 1:1 and 1:1.96
  - B. Driveline ratio: 6.02:1 and 3.07:1
- 5. Steering
  - A. Type and manufacturer: worm and nut (recirculating ball); Saginaw
  - B. Turning ratio: 24:1
  - C. Turning diameter, m (ft): 11.3 (37)
- 6. Brakes
  - A. Front: hydraulic drum
  - B. Rear: hydraulic drum
  - C. Parking: mechanical on rear axle; two-position cable
  - D. Regenerative: no
- 7. Tires
  - A. Manufacturer and type: Firestone; Transport 110, 6-ply rating, load range C
  - B. Size: 6.70-15
  - C. Pressure, kPa (psi): Front: 310 (45) 310 (45) Rear:
  - D. Rolling radius, m (in.): 0.33 (12.9)
  - E. Wheel weight, kg (1bm): with drum: 23 (50.7)

# Appendix C. Signal Conditioning Requirements

Consider a single stage low pass filter:



The transfer function of this circuit, using p-operator notation and

 $Z_{T} = \text{parallel combination of } R_{1} \text{ and } C_{1}, \text{ is}$   $Z_{T} = \frac{(R_{1}/pC_{1})}{\frac{1}{pC_{1}} + R_{1}} = \frac{R_{1}}{1 + pR_{1}C_{1}} .$ 

Assuming an ideal operational amplifier,

$$V_0 = -I_x Z_T$$
 and  $I_x = \frac{V_1}{R_1}$ 

Therefore

$$v_{0} = -\frac{v_{1}}{R_{1}} \left( \frac{R_{1}}{1 + pR_{1}C_{1}} \right) = -v_{1} \left( \frac{1}{1 + pR_{1}C_{1}} \right)$$
$$\frac{v_{0}}{v_{1}} = -\frac{1}{1 + pR_{1}C_{1}}$$
(1)

The circuit is a first order system, with time constant  $\tau = R_1C_1$ , and is required to provide a running average of the input signal, filtering of high frequencies, e.g. signal pulsing generated by the chopper controller, and respond to input signal changes: the output should be at least 99 percent of its final value within one measurement interval (one second). The changing signal constraint requires approximately five time constants, i.e.

$$\tau = R_1 C_1 = \frac{1}{5} s = 0.2 s$$
 (2)

Signal averaging requires time integration, i.e.

$$V_0 = K \int_0^t V_i dt$$

or, in p-operator notation,

$$V_0 = \frac{K}{p} V_i$$

letting  $K = -\frac{1}{R_1 C_1}$ , then

$$\frac{V_0}{V_1} = -\frac{1}{pR_1C_1} .$$
 (3)

The circuit is a time integrator, cf. (1), (3), when

$$pR_1C_1 >> 1$$

or, since

$$p \rightarrow j\omega = j2\pi f,$$

$$|j2\pi fR_1C_1| >> 1 \qquad (4)$$

where f is the base signal frequency. For the chopper controller the minimum observed operation frequency, or fundamental component of the Fourier spectrum, is approximately 65 Hz. Since  $R_1C_1 = 0.2$  by (2), the inequality

$$|j2\pi(65)(0.2)| >> 1$$

or

## 81.7 >> 1

indicates that all chopper induced frequency components will be averaged within a few percent error. Generally this is adequate since the controller seldom operates at such low frequencies (only on vehicle start-up). If further averaging is required, e.g. duty cycle, a two stage filter with overall time constant  $\tau = 0.2$ seconds has inequality (4)

$$(2\pi fR_1C_1)^2 >> 1$$

or

$$(81.7)^2 >> 1$$

therefore

6675 >> 1 ,

j,

and error due to high frequency components is correspondingly reduced.

| Data         |
|--------------|
| Route        |
| Campus       |
| D            |
| A P PE ND IX |

| 1 IME<br>(SEC) | DULT<br>CTCL E | INCLINE<br>(DEC) | SPEED<br>(EM/HB) | ACCEL<br>16) | (H)<br>(H) | TORCUE<br>IN 1-N1 | T T T T T T T T T T T T T T T T T T T | 18477<br>(81 | ¥.         | ۶Ē          | ×     | 1 E H PEN<br>DUT | ATURES |
|----------------|----------------|------------------|------------------|--------------|------------|-------------------|---------------------------------------|--------------|------------|-------------|-------|------------------|--------|
|                |                | •                |                  |              |            |                   |                                       |              |            |             |       |                  |        |
|                |                |                  |                  | 144.         |            |                   | 113.7                                 |              | 1.12       | •           | 100   |                  | 1.50   |
|                | 4121           | •                | 2                |              |            | 2.51              | 1                                     |              |            | •           | 100   | 33.6             | 33.6   |
|                | 122.           |                  | 52.5             |              | 1961       | 13.2              | 113.1                                 |              | 1.15       | •           | 001   | 33.6             | 39.6   |
| 22.0           | 261 .          |                  |                  |              | 162.5      |                   | 114.8                                 | 37.4         | 20.5       | •           | 170   | 33.6             | 19-6   |
|                | 211.           |                  |                  |              | 169.2      |                   | 116.0                                 |              | <b>1.</b>  |             | 100   | 33.6             | 19.6   |
|                |                | •                |                  | 213          | 175.1      |                   | 9.91                                  |              | •          |             | 100   |                  | 33.6   |
|                |                | -                |                  |              | 4 · 1 0 1  |                   | 116.6                                 |              | •          |             | 100   | 33.6             | 15.4   |
|                |                | -                |                  |              | 116.5      |                   | 9.9                                   |              | <b>.</b> . | •           | 100   | •••              | 33.6   |
|                |                | - '              |                  |              | 1.1.61     |                   | 2.711                                 |              | •          | •           | 100   |                  | 19-91  |
|                |                | :                | 2.61             | 191-         | 196.6      |                   | 117.2                                 |              | •          | •           | 001   | 31.6             | 35.6   |
|                |                | •                | 2.5              |              | 2 - 1 4 2  |                   |                                       | 1.26         | 2.11       |             | 100   |                  | 3.4.6  |
|                |                |                  | 2-61             | 12.1.        | 200.7      |                   | 11.0.7                                | 1 - 12 1     | 1.1.1      | -           | 100   | •••              | 1.5    |
|                |                | - •              | 3.85             |              |            |                   | 2 • 1 1                               |              | ~ ~ ~      |             | 100   |                  | 5      |
|                |                | •                |                  |              |            |                   |                                       |              |            |             |       | ••••             |        |
|                |                |                  | 1.22             |              |            |                   |                                       | 121          |            | ::          | 100   |                  |        |
|                |                |                  |                  |              |            |                   |                                       |              |            |             |       |                  |        |
|                |                |                  |                  |              |            |                   |                                       | 2 2<br>2 - 1 |            |             |       |                  |        |
|                |                |                  | 1.44             |              |            |                   |                                       |              |            |             | 100   |                  |        |
|                |                |                  |                  |              |            |                   |                                       |              |            | •           |       |                  |        |
|                |                |                  |                  |              |            |                   |                                       |              |            |             |       |                  |        |
|                |                |                  |                  |              |            |                   |                                       |              |            |             | 100   |                  |        |
|                |                |                  |                  |              |            |                   |                                       |              |            |             | 100   |                  |        |
|                |                |                  |                  |              |            |                   | 117.9                                 |              |            |             | 0.11  |                  |        |
|                |                | -                |                  |              | 274.6      |                   | 117.2                                 |              |            |             | 001   | 11.              |        |
| 17.6           |                |                  |                  |              | 276.1      |                   | 116.6                                 |              |            |             | 001   |                  | 19.6   |
|                |                |                  |                  | 620.         | 276.1      |                   | 117.2                                 |              |            | -           | 100   | 33.6             | 1.6    |
| 1.1.           |                |                  |                  | 124.         | 276.1      | -13.4             | 116.6                                 | 10.7         | -          |             | 0 U I | 11.6             | 19.6   |
|                | 11,            | •                | :                |              | 276.1      | 5.4-              | 117.2                                 |              |            | 4           | 0U1   | 33.6             | 33.6   |
| ••••           |                |                  | :                | 110.         | 279.5      | -13.4             | 117.2                                 | 1            | •          | ~           | 170   | 33.6             | 39.4   |
| 1.21           |                | •                | 1•2              |              | 276.9      | 1-761             | 119.4                                 | 51.5         | 7.6        | •••         | 001   | 31.6             | 1.55   |
|                |                | -1.1             |                  |              | 270.0      | 22.4              | 116.0                                 | 32.0         | 1.1        | •           | 001   | 33.6             | 33.4   |
|                |                | -1.7             | 3.5              |              | 278.9      | 91.4              | 119.4                                 | 37.4         | 7.8        | 2.1         | 100   | 11.6             | 33.6   |
|                |                |                  | ;;               | ::::         | 279.0      | 51.4              | 1.5.1                                 | 1.2.         | •          | •<br>-      | 100   | 11.6             | 13.6   |
|                |                |                  | ]                |              | 2.1.5      |                   | 11.5                                  | [ • 2 1 1    | []]        | ~ ~         | 100   | ••••             |        |
|                |                |                  |                  |              | 2 • 2 • 2  |                   |                                       | 2.101        |            |             |       |                  |        |
|                |                |                  |                  |              |            |                   |                                       |              |            |             |       |                  |        |
|                | 547 .          |                  |                  |              | 248.3      | 128.2             |                                       |              | 0.20       | 1.1         | 100   |                  | 33.6   |
| 1.1            | 217.           | 4                | 1.1              |              | 1.162      | 154.5             |                                       | 11:.3        |            | 1.1         | 001   | 11.6             | 33.6   |
| 12.6           |                | •:•              | 12.1             | 120.         | 1-162      | 367.1             | 103.7                                 | 234.7        | 1.60       | 3.1         | 2     | 11.6             | 19.6   |
| 13.4           | 111.           | 5.3              | 11.5             | 1201-        | 1.475      | 3+4-4             | 183.7                                 | 1.113        | • • • •    | 3.1         | 120   | 11.6             | 33.4   |
|                | [1]            | 3.5              | •••              | 121          | 312.4      | 522.9             | 1.1.1                                 | 1.453        | 2.24       |             | 001   | 13.4             | 13.6   |
| 19.1           | Ę              | 9.7              | 1.1              | 121          | 1.905      | 314.0             | 11.5                                  | E * 6 2 2    | • 2 • •    | <b>J.</b> I | 501   | 33.4             | 13.6   |
|                |                |                  | 13.6             | 126.         | 318.5      | 246.3             |                                       | 2 . 4 2 2    |            | • • •       | 001   | 11.6             | 39.6   |
|                |                |                  | 1.1              |              |            |                   |                                       | 1.022        |            | 2           | 170   | 1.6              | 1.5    |
|                |                | 1.6              |                  |              |            | 276.3             |                                       | 1.128        |            |             | 100   | <b>11.4</b>      | 9.6    |
|                |                |                  |                  | 132          |            | 5.1.5             |                                       |              |            | ~           | 8     |                  |        |
|                |                |                  |                  |              | 324.0      | 4-2/2             |                                       |              |            | 2           |       |                  |        |
|                |                |                  |                  |              | 2 · 2 ( 5  |                   |                                       |              |            |             |       |                  |        |
| 1.1.           |                |                  |                  |              |            |                   |                                       |              |            | :2          | 2     |                  |        |
|                |                |                  |                  |              |            |                   |                                       |              |            | , ,         |       | , , , ,          |        |

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|       | •     | 1       |           | •         |             |       |           |              |                     |     |       |      |
|-------|-------|---------|-----------|-----------|-------------|-------|-----------|--------------|---------------------|-----|-------|------|
| 26.   |       | 2.2     |           |           | 269.6       |       | 215.3     | 1.9.         | <b>~</b> . <b>/</b> | 901 | 33.6  | 13.4 |
|       |       |         |           |           | 243.2       |       | 196.6     | <b>\$5.1</b> | 2.5                 | 001 | 33.6  | 35.6 |
|       | 5.7   | 17.0    | . 151     | 354.5     | 232.1       | 1.481 | 196.6     | 11.5         | 2.7                 | 001 | 13.6  | 33.6 |
| 5.1.1 |       | 16.3    | . 1 5 1   | 159.1     | 232.1       | 105.5 | 1.161     | 1.1          | 2.7                 | 100 | 33.6  | 19.6 |
| 1.    | 9 . P |         | .121.     | 361.3     | 6.122       |       | 201.3     | 15.3         | 2.7                 | 001 | 33.6  | 39.6 |
| . 155 | 5.7   | [6.3    | 122       | 367.4     | 119.4       | 119.6 | 112.3     | 19.4         | • •                 | 001 | 33.6  | 33.6 |
| . 88  | 5.7   | 2.11    | 13        | 311.6     | <b>?</b> ;  | 114.5 | 1.56      | 11.9         | •                   | 001 | 33.6  | 13.4 |
|       | 5.3   | 11.6    | 51        | 314.4     | - 1 - 2     | 1.5.4 | 23.4      |              | •                   | 100 | 33.6  | 13.4 |
|       | ;     |         | 191       | 317.0     | -13.4       | 115.4 | 16.7      | 1.1          | •                   | 001 | 33.6  | 35.6 |
|       | ;     | 5.7     | 192       | 378.9     | 5.7         | 116.8 | 17        | •            | 4                   | 001 | 33.6  | 35.6 |
| 1.11  | 4<br> | 2.1     |           | 379.5     | 5.4.        | 116.4 | 1         | -            | -                   | 001 | 33.6  | 39.4 |
| 1.11  | •••   |         |           | 374.5     |             | 116.9 | 14.6      |              | •                   | 001 | 11.6  | 1.52 |
| 1.110 | ;     |         | 120.      | 379.9     |             | 116.9 |           | -            |                     | DUL | 3.1.6 | 1.2  |
| 1.111 | ;;    | •••     | 113       | 379.1     | 1<br>1<br>1 |       |           |              |                     | 0.1 |       |      |
|       |       |         | 111       | 379.6     | 22.0        | 116.1 | 20.1      |              | 1.2                 | 100 | 11.6  |      |
| 191 . |       | .,      |           | 1111      |             | 118.2 | 1.11.1    | 14.7         |                     | 0 T |       |      |
| . 332 | ;;    |         | . 223     | 1.142     | 1.129       | 102.0 |           |              |                     | Lno | 11.6  |      |
|       | 5.7   | 7.1     | 561.      | 342.2     | 5 . 6 . 5   | 101.2 |           |              |                     | 100 |       |      |
| .433  | 5.7   | 1.1     | 364.      | 2.09.3    | 1 . 2 . 2   |       | 2.15.1    | 15.2         |                     | 100 |       |      |
| . 153 |       | 12.6    | .1.05     | 100.5     |             | 103.1 | 4 4 6 4   | 11.          |                     | 0.1 |       |      |
| . 157 | ;;    | 14.2    |           | 392.6     | 1.245       | 113.1 | 7.967     |              |                     | 100 |       |      |
| [1].  | ;     | 1.1     | 191.      | 375.9     | 9226        | 103.1 | 2.79.3    | 42.2         | 3.1                 | DU1 | 1.0   |      |
| 17    | 3.5   | 16.3    |           | 111.5     | 211.5       | 111.3 | 224.7     | 1.54         | 1.1                 | 100 | 11.6  | 15.  |
| 17.   | 7.7   | 17.1    | 300.      | 4.514     | 2 5 9 . 9   | 111.9 | 210.6     | 1.11         | 2.7                 | 100 | 33.6  | 33.6 |
| . 511 | 1.6   | 19.5    | 571       | 5.814     | 2.615       | 37.5  | 213.9     | 1.1          | 2.7                 | 001 | 33.6  | 35.6 |
| . 535 | ~~~   |         | 100.      | 115.1     | 252.1       | 183.7 | 229.3     | 51.6         | 2.7                 | 001 | 31.6  | 1.65 |
|       |       | 21.3    |           | 121-2     | 1.525       | 183.7 | 229.3     | <b>13.9</b>  | 2.2                 | 001 | 33.6  | 33.6 |
|       |       | 1 • 2 2 |           |           | 4.452       |       | 2.4.5     | 55.5         | s.5                 | 100 | 33.6  | 33.1 |
|       |       |         | 111       |           | 6 • 6 2 2   | 113.7 | 1.422     | 57.0         | 5 i                 | 100 |       | 5    |
|       |       |         |           |           |             |       | 1 • • 2 2 | 31.45        | £ •2                | 100 |       |      |
|       | •     |         |           |           | 1.115       |       | 212.3     |              | 5.                  | 100 |       |      |
|       |       |         |           |           |             | 1.11  | 219.3     | 2 • 3 9      | 6 ° 2               | 50  | •••   |      |
|       |       |         |           | 2.144     |             |       | 212.3     |              | <b>.</b>            | 100 | 97.6  | 19.1 |
|       |       | 2       |           |           | 1.11        |       |           | 5.29         | <b>.</b>            | 120 | 9.6   |      |
|       | ;;    |         |           |           |             |       | 4-112     |              | <b>.</b>            |     |       |      |
|       |       |         |           |           |             |       |           |              | 1.2                 | 2   |       |      |
|       | ;.    |         |           |           |             |       |           |              | <b>F</b> •2         | B   |       |      |
|       |       |         |           |           |             |       |           |              |                     | 2   |       |      |
|       |       |         |           |           |             |       |           |              |                     |     |       |      |
|       |       |         |           |           |             |       |           |              |                     |     |       |      |
|       |       |         |           |           |             |       |           |              |                     |     |       |      |
|       |       |         |           |           |             |       |           |              |                     |     |       |      |
|       |       |         |           |           |             |       |           |              |                     |     |       |      |
| 1     |       | 27.7    | 21.       | 595.8     | 11.2        | 1.1.1 |           |              |                     |     |       |      |
|       | 1.2   | 27.0    | 1111-     | 542.4     |             |       |           |              |                     | 100 |       |      |
|       | 1.1   | 13.5    |           | 1.1.5     | 5.4.        |       | 1.02      | 6 . 2 1      | •                   | 100 | 11.1  | 13.  |
|       | • • • | 1.15    | 610       | 575.4     | 1.1         |       | 20.1      | 12.9         | •                   | 007 | 13.4  | 1.51 |
|       | •     | 11.4    | 1211-     | 5 + 2 + 2 |             | 114.0 | 1.02      |              |                     | 170 | 13.4  |      |
|       | •     | 8.35    | • • • • • | 561.3     |             | 119.4 | 20.1      |              |                     | 100 | 1.1   | 15.4 |
| :     | 2.2   | 1.1     | 131       |           |             |       | 27.4      | 1.1.2        |                     |     | 13.4  | 19.  |
|       |       |         |           |           |             |       |           |              |                     |     | •     |      |

| 1116  | DULY<br>CYCLE | INCLINE  | SPEED<br>(KM/MR) | ACCEL<br>16) | 0151<br>(M) | TOAGUE<br>(M1-N) | 11184     | 18117<br>(A) | 4 E         | ¥Đ    | 2    | TENTER      | TURES        |
|-------|---------------|----------|------------------|--------------|-------------|------------------|-----------|--------------|-------------|-------|------|-------------|--------------|
|       |               | 1        |                  |              |             |                  |           |              |             |       |      | •           | •            |
| 56.1  | 264.          | 5.7      | 19.2             | .151.        | 695.3       | £ • / • 2        | 116.5     | 1.111        | 19.2        | \$°2  | 0U 1 | 9.65        | 33.6         |
| 57.1  | . 916         | 1.1      | 11.5             | 164          | 5.018       | 261.9            | 111.2     | 224.7        | 51.1        | 2.7   | 001  | 33.9        | 33.6         |
| \$1.1 | . 523         | 1.11     | 11.5             | 155          | 412°4       | 271.6            | 1.03.7    | 1.465        | 49.2        | •••   | 100  | 13.9        | 13.6         |
| 51.1  | . 523         | 4.1      | 17.7             | 121          | 8 - 8 2 9   | 296.1            | 103.1     | 230.7        |             | •••   | סען  | 11.9        |              |
| 1.1   | . 516         | <b>1</b> | 17.1             | - 135        | 623.1       | 311.0            | 1.5.1     | 213.1        | 16.9        | 1.1   | 001  | 11.4        | 13.4         |
|       | . 527         |          | 13.6             | 117          | 424.7       | 1.160            |           | 271.5        | 16.9        | 3.5   | 001  | 13.9        | 15.4         |
|       | . 515         |          | 19.6             | . 151        | 614.3       | 374.0            | 1.1.1     | 248.8        | 1.94        | 3.5   | 001  | <b>11.7</b> | 39.6         |
|       | 116.          |          |                  | <b></b>      | 6,10, 3     | 1.1.1            | 1.1.1     | 205.5        | 1.1         | 9-1   | 001  | 33.9        | 35.4         |
|       | . 5 1 5       | 7.9      | •••              |              | 1-2-1       | 193.7            | 1.1.1     | 2.15.5       | 1.31        | 3.5   | 001  | 11.1        | 35.8         |
| 65.0  | . 515         | ••••     | [··]             | 132          | 545.8       | 391.7            | 1.00.1    | 2.162        | 15.3        | 3.7   | 001  | 91.9        | 13.4         |
| 56.6  | 166.          | •••      |                  | 122          | 691.1       | 4.12.5           | 101.2     | 2.112        | 15.3        | 3.7   | 001  | 11.9        | 35.4         |
| 67.0  | 125.          | 7.4      | •••              |              | 153.1       | 4 E Z * 5        | 101.2     | 2.165        | 11.5        | 3.7   | 001  | 11.4        | 39.6         |
| 1.1   | 11.2          | 1.1      | 1.1              | 132          | 639.3       | 402.5            | 100.2     | 2.11.2       |             | 3.7   | 100  | 51.9        | 33.6         |
| 1.1   | 162.          |          | •••              | 2017         | \$63.2      | 5-244            | 100.2     | 2.125        | 44.5        | 3.7   | 001  | 33.9        | 35.4         |
| 71.1  | 163.          | 1.1      |                  | :::.         | 447.2       | 1.165            | 100.2     | 2.165        | <b>11.9</b> | 3. 7  | 001  | 33.9        | 35.4         |
| 7     | .531          |          | 1.1              | 151          | 671.2       | 1.165            | 100.2     | 2.165        | 45.3        | 5°2   | 001  | 53.9        | 35.6         |
| 12.1  | 115.          | 1.1      | 5.1              | 121.         | 415.4       | 1,192            | 100.2     | 211.2        | 5.44        | • •   | 001  | 33.9        | 35.1         |
| 13.1  | .531          | :        | •••              |              | 1.1.1       | 4.2.5            | 100.2     | 2 . 1 . 2    | 5.13        | 39    | 001  | 33.9        | 33.6         |
|       | 116.          |          | •••              |              | 4.239       | 1.165            |           | 2.072        | 45.3        | 3.7   | 001  | 33.9        | 33.6         |
| 75.8  | 115.          | 7.9      |                  |              | 647.6       | \$ "28 \$        | 100.2     | 2.162        | 5.77        | 5.5   | 001  | 13.4        | 35.6         |
|       | . 527         | 4.7      |                  | F20.         | 1.161       |                  | 100.2     | 2.165        | 1.1         | 2.7   | 100  | 13.4        | 33.4         |
|       | 16.           | •••      | 19-6             | 132          | 195.4       | 1466             | 199.2     | 2-862        |             | 1.1   | 001  | 93.9        | 39.0         |
|       | 575.          | 5.7      |                  |              | 766.4       | 367.1            | 1.8.5     | 1.115        | 1.6.1       | 1.5   | 100  | 13.4        | 35.1         |
|       |               | 4.4      | 17.7             |              | 1 8 9 7 8   |                  | 100.1     | 271.5        |             | 1.5   | 110  | 13.4        | 39.0         |
|       |               |          | 2.61             |              | 110.5       | 2.216            | 0 - 2 0 1 | 1 - 2 9 2    |             |       | 001  | 13.9        | 1.1          |
|       |               |          |                  |              |             |                  |           |              |             |       | 100  | • • •       | 19.1         |
|       |               |          |                  |              |             |                  | 1.1.1     | 1.12         |             | •     | 100  |             | 33.4         |
|       |               |          |                  |              | 1.621       | • • • • •        |           |              |             | •     | 100  |             | <b>9</b> •52 |
|       |               |          |                  |              |             |                  |           |              | •••         |       |      |             |              |
|       |               |          |                  |              |             |                  |           |              |             |       |      |             |              |
|       |               |          |                  |              |             |                  |           |              |             |       | 5    |             |              |
|       |               |          |                  |              |             |                  |           |              |             | •     |      |             |              |
|       |               |          |                  |              |             |                  |           |              | •           |       |      |             |              |
|       |               | ; -      |                  |              |             |                  |           |              |             | •     |      |             |              |
|       |               |          |                  |              | 1.95.7      |                  | 116.0     |              |             |       | 501  |             |              |
| 1.1   |               | 2.7      |                  | 36           | 114.1       |                  | 116.4     | 1.1.1        |             |       | 001  | 13.9        | 35.0         |
| 11.1  | 11.           | 2.7      | :                |              | 1.927       | 2 - 96 - 2       | 113.1     |              | <b>6.</b> 3 | •••   | 100  | 33.9        | 13.6         |
|       | . 367         | 1.1      |                  |              | 1.011       | 649.7            | 101.2     | 323.0        | 23.8        | 5.3   | 2    | 11.9        | 13.0         |
|       | 111           | <b>.</b> |                  | 162.         | 2.2.2       | 532.9            | 1.1       | 327.6        | 33.2        | ;     | 0U1  | 53.4        | 33.0         |
|       | . 514         | 5.0      | 13.5             | 151.         | 5.5.2       |                  | 1.1.      | 2.102        | 1.1         |       | 170  | •••         | 33.4         |
|       | 165.          |          |                  |              | 2.9.5       | 371.0            |           | 290.1        | • • • •     | 5.3   | 5    | 11.4        | 39.1         |
|       |               | 51       | 1.1              | 21,          | 1.121       | 6.225            |           | 1.142        | 2.64        |       | 50   |             |              |
|       |               |          |                  |              | 1.0.1       | 5.1.5            | 102.5     | 1.125        | 5           |       | 100  |             |              |
|       |               |          |                  |              | 1.591       |                  | 1.101     |              |             |       | 120  | •••         |              |
|       |               | -        |                  |              |             |                  | 1.101     |              | 1.12        | 7 • 7 | 5    |             |              |
|       |               |          |                  |              | 2.87        | 1.721            | 2 • / 4   | 1.441        | 1.72        |       | 50   |             |              |
|       |               |          |                  |              |             |                  |           |              |             |       | 5    |             |              |
|       |               | •••      |                  |              |             |                  |           |              |             |       | 2 i  |             |              |
|       |               |          |                  |              |             | J - F = = =      |           |              |             |       | 3    |             |              |
| 1.1   | 116.          |          | 2                | 11           |             | 191.7            |           |              | 5           |       | Ē    |             | Ľ            |

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| ALTURES<br>EM          | 36.1   | 1.10         |      |         |         | 36.1         | 36.1   | 3.4.5 |      |        |        | 36.1     | 36.1   | 38.1   | 36.1   | 36.1   | 1.40      | 36.1       |         | 1.4 |     |      |         |       | 34.1   | 36.1    | 36.1  | 16.3  |     |        | 5.45     | 34.3         | 36.3          | 36.3    |            |          |           |          |       |        | 36.3   | 34.3   |      |             |
|------------------------|--------|--------------|------|---------|---------|--------------|--------|-------|------|--------|--------|----------|--------|--------|--------|--------|-----------|------------|---------|-----|-----|------|---------|-------|--------|---------|-------|-------|-----|--------|----------|--------------|---------------|---------|------------|----------|-----------|----------|-------|--------|--------|--------|------|-------------|
| TEMPE<br>OU1           | 34.1   | 1.15         |      |         |         | 1.12         | 1.15   | 1.1   |      |        |        |          | 1.1    | 31.4   | 34.4   | ***    | 31.1      |            |         |     |     |      |         |       |        | 1       |       |       |     |        | 11.4     | 31.1         |               | 34.6    |            |          |           |          |       |        |        | 34.4   |      |             |
| 2                      | â      | N 1          |      |         |         | 100          | 100    | 100   |      |        | 001    | DUD      | Duf    | DUL    | 0U1    | 001    | 100       | 100        |         | 100 |     |      | 110     | 001   | 120    | 100     | 011   | 120   |     | 100    | 001      | 001          | 106           | 001     | 500        | 100      | 21        | 170      |       | 170    | 001    | 120    | 23   |             |
| ¥Ē                     | •••    |              |      |         |         | •            | •      |       | <br> |        |        |          |        | 4      | -      | •      | •         |            | •       |     | •   |      |         |       |        |         | •     | •     | ••• |        |          | •            | •             |         | <b>.</b> . |          | •         | <u> </u> |       | : •    |        | •      | -    | •           |
| sê                     | 11.5   |              |      |         | 1.1     | 57.2         | 31.6   |       |      |        |        |          | •••    | •      | •••    | 1.6    | 1.6       |            |         | • • | ••• |      |         |       |        | 2 • 2   | •:-   | •     |     |        | -        | 1.1          | • • •         | • •     | •          |          |           |          |       | -      |        | •      | •    | •           |
| [MTT<br>(A)            | 11.2   | 2-260        |      | 337.    | 323.0   | 1.5.1        | 149.8  | 5-951 |      |        |        | 11       |        | 1.1    |        | 11     | 14.0      | 19.7       |         |     |     |      |         |       | 20.1   |         | 1.11  |       |     |        |          | 14.1         |               |         |            |          |           |          |       |        | 1.01   | 1.11   |      |             |
| VBATT<br>(V)           | 19.5   | 96.1         |      |         | 1.66    | 1.1.1        | 1.1.1  | 107.0 |      |        | 116.0  | 116.0    | 115.4  | 116.9  | 116.8  | 116.9  | 116.6     |            | 2 • 1 1 |     |     |      |         | 116.6 | 116.0  | 116.6   | 116.6 | 117.2 | 2   |        | 117.2    | 116.6        | 116.6         | 117.8   | 117.2      | 2 • 7 11 | 2 • 1 1   |          |       | 117.2  | 117.2  | 117.2  | 2.71 |             |
| ,<br>106,00E<br>(M1-N) | 1.523  | 234.4<br>222 |      | 172.4   | 172.4   | 1.91         | 5 - 53 |       |      | 22 -   | -13.4  | - 22.2   |        | -12.4  |        | -17.4  | -11.4     |            |         |     |     |      | - 1 1 - | 22    |        | - 22-2  | -13.4 | - 13. |     |        | - 22 - 2 | - 22.2       | • • • • • • • |         |            |          | 2 ° 2 2 • |          |       |        |        | 2 3 -  |      |             |
| 1310                   | 1146.7 | 2-6511       |      | 1.213.7 | 1.221.1 | 1243.8       | 1256.5 | 5.2.2 |      | 2.2661 | 1.9161 | 1362.9   | 1376.2 | 1349.9 | 1.11.1 | 1414.5 | 14 25 . 2 | 2 . 95 4 1 |         |     |     |      |         |       | 1.13.1 | 1.191.7 | 1.171 | 51161 |     | 1923.1 | 1539.7   | 1920.1       | 1513.0        | 1992.7  |            |          |           |          |       | 1602.6 | 1.99.3 | 1611.1 |      | 3 0 A 3 A 1 |
| ACCEL                  | 160    |              |      | 121     | 111.    |              |        |       |      |        | . 166  | 151      |        | -,111  | 11.,   |        | 2.        |            |         |     |     |      |         |       | 621.   | 111.    | 11    |       |     |        |          | 120          | 228.          |         | C2 8 • •   |          |           |          |       | 5 1    | 121    |        |      |             |
| 5766D                  | 16.1   |              | 1.14 |         | \$5.4   | 56.1         | 55.1   |       |      |        | 53.4   | 51.2     | 51.1   |        | 1.5    | 7.1    | 1.54      |            |         |     |     | 17.7 |         |       | 21.3   | 12.7    |       |       |     |        | 27.8     | <b>1.1</b> 2 | <b>3</b> .5   |         | 1.12       |          |           |          |       | 87.T   |        | 21.3   |      |             |
| I PCLINE               | 2.2    |              |      | -       | •-1     | •            | -      | ~ ~   |      |        | •      | <b>.</b> | ŗ      | ÷      | •      |        | <b>.</b>  |            |         |     |     |      |         |       | - 2.1  | - 2.1   | 1.2-  |       |     |        | 1.2.     | - 2 . 1      | - 1.1         | - 2 - 1 |            |          |           |          |       |        | 211    | 2-1-   | ~    |             |
| DULT                   | 1. 1.1 |              |      | 1.000   | 1.011   |              |        |       |      |        |        |          |        | 9. 113 |        |        |           |            |         |     |     |      |         |       |        | ••••    |       |       |     |        | 0.111    | 11.          | :             |         |            |          |           |          |       |        |        |        |      |             |
| 11 PC<br>14[1          | 1.147  |              |      | 1.693   | 1.111   | <b>[67.0</b> |        |       |      |        | 11.1   | 112.4    | 1.9.1  |        |        |        |           |            |         |     |     |      |         |       | 1111   |         | 1.162 |       |     |        | 11.1     | 11.1         |               |         |            |          |           |          | 1.5.1 | 116.1  | 1.7.1  |        |      |             |

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|            |        | 5-11-5           | 191 -        | 1635.0      |                    | 1 1 6 . 0                               | 1.65      |         | •••      | 100  | •••             |              |
|            |        |                  |              | 1645.0      | 1-252              |                                         | 2.14.1    |         |          | 100  |                 |              |
| 1.3        |        | 1.15             | .173         | 1.1511      | 2.645              | 111.9                                   | 229.3     | 56.3    | 1.1      | 100  | 34.6            | 31.3         |
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|            |        | 11               |              | 1725.5      | 75.1               | 119.6                                   | 1.161     | 57.4    |          | 100  | 34.4            | 36.3         |
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|            |        |                  |              | 1.151.1     | 75.1               |                                         |           |         |          | 170  |                 |              |
|            |        |                  |              |             |                    |                                         |           |         |          |      |                 |              |
| 2          |        | 32.4             |              | 1776.4      |                    | 189.6                                   | 1.1.7     | 9.15    | 1.2      | 170  |                 | 36.3         |
| 2:2        |        | 32.1             | 154          | 1784.9      | 13.2               | 113.1                                   | 1.1       | 33.6    | -        | 001  | 1.1             | 16.3         |
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| ~          | ~      | 1.1              | 159          | 1024.2      | 5.7                | 116.4                                   |           | •       | -        | 001  |                 | 1.1          |
| -          |        | [6.]             | 151          | 1.833.1     | 5.4                | 116.5                                   |           | 1.6     | 4        | 100  | 34.6            | 34.3         |
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| -          | -      | 21.1             | 161.         | 1966.       | 292.1              | 103.1                                   | 236.7     | 5.5     |          | 001  | 34.4            | 36.3         |
| ÷.         | _      | 1.22             | 120.         | 1971.0      | 2.1.2              | 103.1                                   | 234.0     | 97.4    | 2.7      | 170  |                 |              |
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> Colin E. Jones and David Embree, "Correlation of the 290 nm Luminescence Band in SiO<sub>2</sub> with the Oxygen Vacancy EPR and

Optical Absorption Band," Paper BI11 presented at the New York meeting of the American Physical Society, February 2-5, 1976.

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Colin E. Jones and David Embree, 'Cathodoluminescence Studies of S10, - Na, Cl, Ge,

and Oxygen Vacancy Results," Paper presented at the International Topical Conference on the Physics of SiO<sub>2</sub> and Its

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### Phi Eta Sigma Eta Kappa Nu IEEE

Future Plans: Work with Bell Laboratories, Reading, Pennsylvania