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Computation Offloading and Resource Allocation in Vehicular Networks Based on Dual-side Cost Minimization

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\textbf{Abstract}—The proliferation of smart vehicular terminals (VTs) and their resource hungry applications imposes serious challenges to the processing capabilities of VTs and the delivery of vehicular services. Mobile Edge Computing (MEC) offers a promising paradigm to solve this problem by offloading VT applications to proximal MEC servers, while TV white space (TVWS) bands can be used to supplement the bandwidth for computation offloading. In this paper, we consider a cognitive vehicular network (CVN) that uses the TVWS band, and formulate a dual-side optimization problem, to minimize the cost of VTs and that of the MEC server at the same time. Specifically, the dual-side cost minimization is achieved by jointly optimizing the offloading decision and local CPU frequency on the VT side, and the radio resource allocation and server provisioning on the server side, while guaranteeing network stability. Based on Lyapunov optimization, we design an algorithm called DDORV to tackle the joint optimization problem, where only current system states, such as channel states and traffic arrivals, are needed. The closed-form solution to the VT-side problem is obtained easily by derivation and comparing two values. For MEC server side optimization, we first obtain server provisioning independently, and then devise a continuous relaxation and Lagrangian dual decomposition based iterative algorithm for joint radio resource and power allocation. Simulation results demonstrate that DDORV converges fast, can balance the cost-delay tradeoff flexibly, and can obtain more performance gains in cost reduction as compared with existing schemes.

\textbf{Index Terms}—Computation offloading, mobile edge computing, resource allocation, stochastic optimization, vehicular networks.

I. INTRODUCTION

With the rapid development of Internet of Vehicles (IoV), vehicles become smarter in supporting intelligent applications, such as autonomous driving, video-aided real-time navigation, and interactive gaming [1]–[4]. Many of these applications are computationally-intensive, power-hungry, delay-sensitive, and bandwidth-demanding, which on the one hand, consumes a large amount of energy and imposes great pressures on the processing capabilities of vehicular terminals (VTs) [5], and on the other hand, poses a great burden on radio access networks [6].

To tackle the issues such as poor terminal processing capabilities and high energy consumption, mobile edge computing (MEC) [7]–[18] enabled vehicular networking [20], [21] has been regarded as a promising solution, where the processing of VT applications is pushed to the adjacent radio access networks. By attaching MEC servers to roadside units, i.e., MEC enabled roadside units (MRSU) which are deployed along the roadside offering wireless access to VTs, an MEC based integrated communication and computation platform can be constituted. By offloading computations to MEC servers, many complex VT applications can be enabled, and/or the energy consumption of VTs can be reduced, and/or the response of applications can be accelerated [9]–[14].

Since the input data for computation offloading should be transmitted to MEC servers via radio access networks through vehicular-to-roadside (V2R) communications, the chronic problem of radio spectrum scarcity has to be first considered, otherwise the performance and efficiency of task offloading may be deteriorated even neutralized. Long-term evolution-vehicle (LTE-V) [22] and dedicated short-range communications (DSRC) [23] both have limitations when employed for computation offloading in vehicular networks. The cellular network based LTE-V is facing issues of the explosive growth of mobile data traffic (in contrast to the scarcity of licensed cellular spectrum) and the relatively expensive cost of using the licensed spectrum. More importantly, V2R communications are usually more appealing during long journeys along motorways where cellular 3G/4G/5G coverage and services are typically unsatisfactory [6], [24]. For DSRC, many studies have shown that the bandwidth is insufficient for supporting resource-demanding V2R communications [2], [3], [25]. The short transmission range of WiFi systems have limited their usage in V2R scenarios, where a large number of WiFi APs along the road would be needed for seamless coverage, which is uneconomical, and will result in frequent handovers of high-speed VTs [6], [24].

In the meanwhile, plenty of TV White Spaces (TVWS) bands, which have desirable long-distance propagation char-
acteristics [6], [24], [25], can be used as a feasible supplement to the limited DSRC bandwidth [3], [6], [24]–[27], and related standards have been put forwarded by relevant organizations such as U.S. Federal Communications Commission (FCC) [28]. Therefore, to solve the related wireless spectrum problems, in this paper we will investigate vehicular computation offloading using TVWS band for wireless data transmission. However, new problem arises: to exploit TVWS band, the basic principle is the protection of primary users (PUs), and the IEEE 802.22 PUs could transmit with a power up to 4 W [29], while the secondary users (SUs), such as VTs performing computation offloading in a cognitive vehicular network (CVN), are only permitted to use a power no more than 100 mW [29]. Consequently, the offloading from VTs may be blocked by 802.22 PUs. Therefore, the power asymmetry problem should be settled when VTs reuse the TVWS spectrum.

In most existing works, computation offloading optimizations are usually performed unilaterally, i.e., only concerning the user-side or the server-side performance. On the user side (e.g., VTs), while offloading tasks to MEC servers to save energy consumption, VTs need to minimize costs including the energy consumption in data transmission and the fees for the computation and communication services [17], by optimizing offloading decisions and the allocation of local CPU frequency. On the server side (e.g., MRSU), while it makes profit from providing VTs with computing services and the corresponding data transmission services in data offloading, it needs to minimize costs including the cost for renting wireless bandwidth, and the electricity bills for running edge servers, by optimizing the allocation of radio resources and edge servers.

Considering that MRSU and VTs have conflict objectives while each trying to minimize its own cost, in this paper, we formulate two intercoupled dual-side cost minimization problems in an integrated IEEE 802.22 based vehicular framework. The dual-side cost minimization involves stochastic optimization problems, which is much more challenging than unilateral user-side or server-side optimization because on the one hand, the two stochastic optimization problems are intercoupled with each other and the optimization in each frame is also intercoupled, and on the other hand, both stochastic optimization problems involve a large amount of state information as well as control variables. With the help of Lyapunov optimization theory, we devise low complexity algorithms to solve the two intertwined problems.

The main contributions are summarized as follows:

- We develop a task offloading framework for an IEEE 802.22-CVN coexisting network, where the IEEE 802.22 channel is reused by the CVN for computational task offloading, with the unique features of the TVWS wireless channels such as temporal and spatial changes in channel availability, and FCC’s requirements for the protection of PUs taken into consideration.
- We formulate a dual-side cost minimization in an integrated framework under a competition scenario where VTs and MRSU aim to minimize their own costs. On the VT-side, each VT optimizes its computation offloading decision and local CPU frequency control independently so as to minimize its cost, while on the MRSU-side, the server provisioning, the IEEE 802.22 burst (which will be detailed below) assignment, and the transmit power control are jointly optimized to minimize the cost of MRSU.
- Leveraging Lyapunov optimization, we decouple the two stochastic optimization problems into independent per-frame optimizations, without requiring any knowledge of future task arrivals or network state information. In each frame, the VT-side offloading decision is obtained by comparing the cost of local processing and that of task offloading, and the VT’s local CPU frequency is obtained by the derivative of the objective function. For MRSU-side optimization, we first devise simple algorithm for server provisioning, and then we develop a continuous relaxation and Lagrange dual decomposition based low-complexity algorithm to obtain the joint IEEE 802.22 burst allocation and transmit power control policies.
- Simulation results verified the convergence of our proposed iterative radio resource allocation algorithm, the tradeoff between the cost and queue length, and the performance of our proposed joint optimization algorithm compared with other existing algorithms.

The remainder of this paper is organized as follows. Related works are presented in Section II. Section III and Section IV introduce the system model and the dual-side problem formulation, respectively. In Section V, we transform the original formulated problem into per-frame optimization by employing Lyapunov optimization. The VT-side per-frame optimization is solved in Section VI, and the MRSU-side per-frame problem is settled in Section VII. In Section VIII we present the complexity analysis of our proposed algorithms. Simulation results are provided in Section IX. Finally, the paper is concluded in Section X.

II. RELATED WORKS

Recent years, MEC-based (or fog computing based) computation offloading has attracted a great deal of attentions and has stimulated extensive researches from distinct perspectives in terms of different metrics. Specifically, task partitioning and offloading policy is jointly optimized to maximize the energy conservation [9] or to minimize the energy consumption [10]. The works in [9], [10] were then extended to multi-user scenarios, where except offloading decision is optimized, the joint optimization of transmit power control and computation resource allocation [11], of radio bandwidth and computation resource allocation [12], of transmit power control, computation and radio bandwidth allocation [13] or resource block (RB) allocation [14] were also studied in different multi-user scenarios.

In the above references [9]–[14], the authors only considered the performance of processing a single task, nevertheless, for applications like multi-media and file backup, etc., the coupling among the random task arrivals should not be neglected, so long-term performance metrics and stochastic task models are more suitable. Reference [15] studied offloading decision optimization to minimize the average execution cost. The authors in [16] considered the joint optimization of offloading
policy, the local CPU speed control, and network interface selection to minimize the time-averaged expected total average energy consumption.

However, the formulations in [9]–[16] are unilateral optimizations, which could not reflect the practical situation very well. The authors in [17] designed a practical dual-side optimization framework, where code offloading, computation resource allocation, and network interface selection policies were jointly optimized for mobile users, and service pricing were optimized for the service provider. The formulation with joint radio and computation resource allocation optimization for multi-user MEC systems was proposed in [18] to minimize the long-term averaged total power consumption of the MEC server and all its served mobile devices.

MEC-enabled vehicular networks have also attracted much attention from many researchers in recent years. The authors in [5] proposed an MEC based computation offloading framework for CVNs to minimize the VTs’ cost in task offloading, while guaranteeing task processing delay and considering VTs’ mobility. In [20], in order to maximize the economical profit of service providers while guaranteeing the delay tolerance of tasks, the authors developed a distributed algorithm to jointly optimize offloading decision making and computation resource allocation. The authors in [21] presented a vehicular fog computing architecture where vehicles acted as the infrastructure nodes to provide communication and computation services.

Nevertheless, since successful and efficient task offloading highly depends on the wireless channels, opening source of more available wireless band is more urgent to mitigate the spectrum scarcity of DSRC. The authors in [6], [24], [25] novelly proposed to open source of TVWS for wireless data transmission. The authors in [26], [27] ulteriorly proposed a coexistence framework including a CVN and an 802.22 network, by appropriate radio resource allocation scheme, spare IEEE 802.22 TVWS channels could be reused by CVNs and the spectrum shortage issue in CVNs could be relieved.

### III. System Model

#### A. Basic Concepts and Scenario Description

IEEE 802.22 [29] was designed for broadband access employing the TVWS band in low population density regions. In the standard, the frame length is 10 milliseconds, and each frame is partitioned into an uplink and a downlink subframe. The uplink scheduling information of 802.22 PUs is incorporated in the downlink messages, which are broadcasted by the 802.22 base station (BS) at the beginning of each frame. PUs then access the TVWS channel according to the received uplink scheduling information. Through listening to the 802.22 network deployed in the same area, VTs can obtain the uplink scheduling information of 802.22 network. By implementing appropriate resource allocation schemes, the low power vehicular network could coexist with the high power 802.22 network, and reuse the TVWS channels of 802.22 network. To be in line with 802.22, in this paper, time is also partitioned into discrete frames and each is with a length of $T = 10$ milliseconds [29].

As shown in Fig. 1, the road is partitioned into $K$ segments, and each is covered by an RSU. When a VT moves from a segment to another, it needs to register with the new RSU. Suppose a VT moves at an average speed of 20 m/s, and the average coverage region of an RSU is 500 m, then the vehicle needs to register with a new RSU every 50 seconds, and can move at most 0.2 m during each frame. Consequently, the network can be considered to be quasi-static where VTs and wireless channels keep unchanged in each frame but can vary in different frames. We focus on MEC enabled V2R communication for the emerging intelligent nonsafety applications [3]. Those applications are usually computation-intensive and energy-demanding, however, contribute much to the commercial success of vehicular MEC. In accordance with the 802.22 standard and many existing works on CVN [26], [27], we adopt a centralized periodic model for each segment, i.e., scheduling is performed independently by each RSU among all its covered VTs in each frame, so in the following we will discuss the optimization within a certain RSU as a representative.

![Fig. 1: Architecture of MEC-enabled vehicular networks.](Image)

![Fig. 2: Structure of 802.22 uplink subframe.](Image)

![Fig. 3: Scheduling of VTs.](Image)

There’s a TVWS channel consisting of several sub-channels to be reused by all the VTs. In 802.22, the basic resource element can be scheduled is called a “burst” [29], which is a two-dimensional element, consisting several subchannels in frequency domain and some orthogonal frequency division multiplexing (OFDM) symbols in time domain. There are two
different types of uplink bursts in 802.22 standard. Type 1 burst occupies the whole uplink subframe in time domain, while a type 2 burst occupies part of the uplink subframe, which is called normal burst. For instance, burst 1 is a type 1 burst, and bursts 2–4 are type 2 bursts in Fig. 2. The uplink subframe is divided into several "burst intervals" (BIs) [29] on the basis of normal type 2 burst. We denote the set and the number of BIs as $L = \{1, 2, \ldots , L\}$ and $L$, respectively. In IEEE 802.22 standard, $L \in \{1, 2, 3, 4\}$ [29], so there are two BIs in Fig. 2, where bursts 1, 2, 3 are located in BI$_1$, and bursts 1, 4 belong to BI$_2$. Let $M = \{1, 2, \ldots , M\}$ and $M$ be the set and number of PUs, respectively, and $C_{m,l} \in \{0, 1\}$ be the burst–BI indicator, where $C_{m,l} = 1$ means burst $m$ locates in BI$_l$, and $C_{m,l} = 0$ otherwise. Each burst has been allocated to a PU in advance. In order to avoid unacceptable interference to PUs, FCC requires that the total transmit power of all the secondary users (SUs, i.e., VTs in our framework) sharing a TVWS channel should be no more than a threshold $P_{\text{max}}$ (which is currently defined as 100 mW [28]) in each BI. For instance, both the total transmit power of VTs 1, 3 in BI$_1$ and the total transmit power of VTs 2, 3 in BI$_2$ should be no more than $P_{\text{max}}$ in Fig. 3.

B. Computation Tasks and Data Arrival Models

Let $\mathcal{N} = \{1, 2, \ldots , N\}$ and $N$ be the set and the number of VTs served by each MRSU, and assume that each VT is running fine-grained tasks [18]: at the beginning of each frame $t$, $D_n(t)$ bits of computation task arrive at VT $n$, with a processing density $\lambda_n$ (in CPU cycles/bit). Without loss of generality, we assume $D_n(t)$ is i.i.d. over frames and may have an arbitrary probability distribution, and is limited by $0 \leq D_n(t) \leq D_{n,\text{max}}$. Since our system works in tiny frames and the tasks are fine-grained and arriving at each frame, so we can suppose the tasks do not have instantaneous delay constraints in each frame similar to many existing works [9], [16]–[19]. Then the task of VT $n$ on frame $t$ can be denoted as $\Lambda_n(t) = \{D_n(t), \lambda_n(t)\}$. In our MEC enabled vehicular network, $\Lambda_n(t)$ can be processed locally by VT $n$ or be offloaded and executed remotely by its attached MRSU according to different offloading decisions. Our system works in frames and scheduling is performed in each uplink subframe (which is called frame for short) [26], [27].

C. Local processing Model

Let $x_n(t)$ denote the offloading decision of VT $n$ on slot $t$, where $x_n(t) = 1$ indicates the application is offloaded to MRSU, and $x_n(t) = 0$ represents the application is processed by VT $n$ locally.

In each frame $t$, if task $\Lambda_n(t)$ is executed locally, the power consumption of VT $n$ is $p_n^{\text{loc}}(t) = k(f_{n}^{\text{loc}}(t))^2$, where $k$ is a constant coefficient related to the CPU chip architecture [18], and $f_{n}^{\text{loc}}(t)$ denotes the local processing capability (in CPU cycles/s) of VT $n$, which is constrained by $0 \leq f_{n}^{\text{loc}}(t) \leq f_{n,\text{max}}^{\text{loc}}$. Thus, in each frame $t$, the local execution time and energy consumption of VT $n$ are given by $T_n^{\text{loc}}(t) = \frac{D_n(t)}{f_n^{\text{loc}}(t)}$, and $E_n^{\text{loc}}(t) = kD_n(t)\lambda_n(f_{n}^{\text{loc}}(t))^2$ (in J), respectively.

D. Remote Processing Model

If VT $n$ offloads its task $\Lambda_n(t)$ on slot $t$, then all its arrival input data of size $D_n(t)$ will be transmitted to MRSU through the shared TVWS wireless links, afterwards $\Lambda_n(t)$ will be processed by MRSU, and finally the processing result is sent back to the VT. Since the processing result is usually very tiny, we neglect the downlink output return process, and only the uplink communication is discussed [12], [13], [18].

1) Communication Model: As the TVWS channel is shared by all PUs and reused by all VTs, before we perform burst allocation among all the VTs, we should first determine how PUs occupy a TVWS channel in each frame. We define the occupancy of a TVWS channel as a random variable $t_{PU}$, which refers to the spare time before a PU returns and occupies the 802.22 channel [26], [27]. Suppose the MRSUs know the information of the probability density function (PDF) $f(t_{PU})$ and cumulative distribution function (CDF) $F(t_{PU})$ of $t_{PU}$ [6], [27], [28], [30], while exact behaviors of PUs are not clear. Since PUs transmit with high power, consequently, the data transmission of VTs can be interrupted by PUs’ return with non-zero probabilities. We consider the data transmission of a VT to be successful when the data is transmitted before PU returns, while the remaining transmission is blocked by PU and the transmitted data is deemed to be lost. Let $t_m$ represent the time duration between two start times, i.e., the start time of burst $m$ and the start time of the current uplink subframe. Then the valid transmission duration $\bar{T}_m$ of a VT on burst $m$ can be obtained as follows. (i) If PUs return to burst $m$ after the VT have finished the transmission, the whole burst is usable and we have $\bar{T}_m = T_m$. (ii) On the country, if PUs disturb the VT’s transmission, then the valid transmission time $\bar{T}_m = t_{PU} - t_m$. As $t_{PU}$ is a random variable, the expected valid transmission time $\bar{T}_m$ of a VT on burst $m$ can be given by the following Eq. (1). Since $t_m$, $T_m$ and $F(t_{PU})$ are all known at the MRSUs, and thus $\bar{T}_m$ is a constant for each burst $m$.

Next we discuss burst allocation among VTs. Define $s_{nm}(t)$ as burst allocation indicator, where $s_{nm}(t) = 1$ represents burst $m$ is allocated to VT $n$ in frame $t$, and $s_{nm}(t) = 0$ otherwise. Let $T_m$ and $B_m$ denote the time duration and bandwidth of burst $m$, $p_{nm}^{\text{PU}}(t)$ and $G_{nm}^{\text{PU}}(t)$ represent the transmit power and channel gain of PU $m$ on burst $m$ in frame $t$, and $p_{nm}(t)$ and $G_{nm}(t)$ denote the transmit power and channel gain of VT $n$ on burst $m$, respectively. In order to enable tractable analysis, we neglect the interference between MRSUs. Then the maximum transmit (in bps) rate of VT $n$ on burst $m$ in frame $t$ can be given by

$$r_{nm}(t) = B_m \log_2 \left(1 + \frac{p_{nm}(t)G_{nm}(t)}{p_{nm}^{\text{PU}}(t)G_{nm}^{\text{PU}}(t) + \sigma^2}\right),$$

where $\sigma^2$ is the power of additive Gaussian white noise. Thus,
the maximum transmit rate of VT \( n \) in frame \( t \) is given by
\[
 r_n(t) = \sum_{m \in M} s_{n,m}(t) r_{n,m}(t) \frac{T_m}{T}. \tag{3}
\]

Denote the transmit power of VT \( n \) in task offloading as \( p_n(t) = \sum_{m \in M} p_{n,m}(t) \), and thus, the energy consumption (in J) in task offloading is given by
\[
 E_n^{off}(t) = p_n(t) T. \tag{4}
\]

2) Server Provisioning Model: Suppose there are \( q_{max} \) edge servers in each MRSU, and each is with a processing capability \( s_e \) (in CPU cycles/s). When VT \( n \) offloads its task to MRSU, MRSU should determine the number of edge servers \( q_{n}(t) \) allocated to the task of VT \( n \). The process is called server provisioning [17], and the total number of allocated servers is constrained by \( \sum_{n \in N} q_{n}(t) \leq q_{max} \).

E. Queuing Model and Related Concepts

In our system, two data queues are formulated for the computation tasks of each VT \( n \), i.e., the user-side queue \( Q_n(t) \) (in bits), and the MRSU-side queue \( Z_n(t) \) (in bits). The queues \( Q_n(t) \) and \( Z_n(t) \) stand for the unfinished tasks of each VT \( n \), and they evolve according to
\[
 Q_n(t+1) = \max\{Q_n(t) - T r_n(t) x_n(t), 0\} + D_n(t), \tag{5}
\]
\[
 Z_n(t+1) = \max\{Z_n(t) - q_{n}(t) \frac{\lambda^e}{\lambda_n} T x_n(t), 0\} + r_n(t) T x_n(t). \tag{6}
\]

Definition 1: A discrete time queue \( Q(t), t \in \{0, 1, \ldots\} \) is strongly stable if satisfies: \( \hat{Q} = \lim_{t \to \infty} \frac{1}{t} \sum_{\tau=0}^{t-1} E\{Q(\tau)\} < \infty \) [31], [32].

Definition 2: If all the individual queues are stable, the network is stable [31], [32].

Remark 1: According to the Little’s Theorem [31], [32], under given traffic arrival rate, the average delay is in direct proportion to the average queue length. Therefore, Definitions 1 and 2 indicate that, if the system is assured to be stable, the average delay can be guaranteed to be finite.

F. Cost Model

Next we discuss the monetary cost of VTs and MRSU, respectively.

1) Cost of VTs: In local processing, the monetary cost of VT \( n \) can be given by
\[
 U_n^{loc}(t) = \alpha_n E_n^{loc}(t) = \alpha_n k D_n(t) \lambda_n \left( f_n^{loc}(t) \right)^2, \tag{7}
\]
where \( \alpha_n \) (in $/J) is a human-determined weight coefficient, which is used to convert energy consumption into money and depends on the human-sensitivity on money and energy consumption [17].

When VT \( n \) offloads its task for remote processing, it will consume different costs and need to pay different fees: (i) The energy consumption of VT \( n \) in data transmission (in J). (ii) The TVWS channel service fee \( \theta r_n(t) \) that VT \( n \) has to pay to MRSU, where \( \theta \) (in $/bit) is the price of transmitting per bit data. (iii) Task processing fee \( \eta \lambda_n r_n(t) \) that VT \( n \) needs to pay to MRSU, where \( \eta \) (in $/cycle) is the price for processing each unit CPU cycle task. So the monetary cost of VT \( n \) in task offloading is
\[
 U_{n}^{off}(t) = [\alpha_n p_n(t) + \theta r_n(t) + \eta \lambda_n r_n(t)] T, \tag{7}
\]
and the total cost of all UEs can be given by
\[
 U(t) = \sum_{n=1}^{N} U_{n}(t). \tag{7}
\]

2) Cost of MRSU: MRSU possesses and operates the MEC servers, and leases radio bandwidth (in bps) from wireless network operator. By providing computation offloading services, MRSU earns the same amount of money paid by VTs for wireless data transmission and task processing. The price it charge from VTs for data transmission is \( \theta \) (in $/bit), and for task processing is \( \eta \) (in $/cycle). On the other hand, it has to pay electricity bills to grid operator for operating edge servers, and pay to wireless network operator for renting radio bandwidth. Denoting \( e(t) \) as the electricity bills (in $) for running an edge server, and the price for renting radio bandwidth (in bps) from wireless network operator as \( \delta \) (in $/bit), the cost of each MRSU in frame \( t \) is given by
\[
 U_{MRSU}(t) = \sum_{n \in N} \left[ q_{n}(t) e(t) + \delta r_{n}(t) T - \theta r_{n}(t) T - \eta \lambda_n r_{n}(t) T \right] x_{n}(t) \tag{8}
\]
A summary of the mainly used notations are presented in TABLE I.

IV. DUAL SIDE PROBLEM FORMULATION

In this section, we construct two optimization problems under a competition scenario. One is the VT-side problem (P_\text{V}), while the other one is the MRSU-side problem (P_\text{M}), respectively.

A. VT-side Optimization Problem

The objective of VT-side optimization problem (P_\text{V}) is to minimize the average total cost of all the VTs subjecting to the queue stability of both VT-side and MRSU-side, so as to guarantee all the computation tasks be executed within a finite time. We need to optimize the offloading decision \( x_t(t) = \{x_1(t), ..., x_N(t)\} \) and the local CPU frequency \( f_\text{loc}^\text{oc}(t) = \{f_1^\text{oc}(t), ..., f_N^\text{oc}(t)\} \) given under MRSU’s policy. Problem (P_\text{V}) is given by

\[
(P_\text{V}) : \quad \min_{x(t), f_\text{loc}^\text{oc}(t)} \bar{U} = \lim_{T \to \infty} \frac{1}{T} \sum_{t=0}^{T-1} \mathbb{E}\{U(t)\}
\]

s. t. (C_1): \( \bar{Q}_n < \infty, \bar{Z}_n < \infty, \forall n \in \mathcal{N}, \)
(C_2): \( x_n(t) \in \{0, 1\}, \forall n \in \mathcal{N}, \)
(C_3): \( 0 \leq f_\text{loc}^n(t) \leq f_\text{max}^n, \forall n \in \mathcal{N}, \) (9)

where (C_1) ensures the network is stable; (C_2) is the binary constraints on computation offloading decisions; and (C_3) is the CPU-cycle frequency constraint for each VT.

B. MRSU-side Optimization Problem

The MRSU-side optimization (P_\text{M}) aims at minimizing the average cost of MRSU, by jointly optimizing the burst allocation \( S(t) = \{s_{nm}(t)\}, \forall n \in \mathcal{N}, \forall m \in \mathcal{M}, \) the transmit power control \( P(t) = \{p_{nm}(t)\}, \forall n \in \mathcal{N}, \forall m \in \mathcal{M}, \) and the server provisioning \( q(t) = \{q_1(t), ..., q_N(t)\}, \) and the problem is formulated as

\[
(P_\text{M}) : \quad \min_{q(t), S(t), P(t)} \bar{U}_\text{MRSU} = \lim_{T \to \infty} \frac{1}{T} \sum_{t=0}^{T-1} \mathbb{E}\{U_\text{MRSU}(t)\}
\]

s. t. (C_1): \( \bar{Z}_n < \infty, \forall n \in \mathcal{N}, \)
(C_2): \( \sum_{n \in \mathcal{N}} s_{nm}(t)p_{nm}(t)G_{nm}(t) \leq \beta_m, \forall m \in \mathcal{M}, \)
(C_3): \( \sum_{n \in \mathcal{N}} c_{m} s_{nm}(t)p_{nm}(t) \leq P_\text{max}, \forall \ell \in \mathcal{L}, \)
(C_4): \( \sum_{n \in \mathcal{N}} s_{nm}(t) \leq 1, \forall m \in \mathcal{M}, \)
(C_5): \( s_{nm}(t) \in \{0, 1\}, \forall n \in \mathcal{N}, \forall m \in \mathcal{M}, \)
(C_6): \( p_{nm}(t) \geq 0, \forall n \in \mathcal{N}, \forall m \in \mathcal{M}, \)
(C_7): \( \sum_{n \in \mathcal{N}} q_n(t) \leq q_\text{max}, \) (10)

where (C_2) means that the interference caused by burst reuse for VT’s uplink transmission should be no more than a threshold \( \beta_m \) to ensure the QoS of PU \( m; \) (C_3) is used to satisfy FCC’s requirement that the total transmit power of

SU's on a TVWS channel should be no more than a threshold. \( (C_M4) \) and \( (C_M5) \) represent a burst can be allocated to at most one VT to avoid interference within the MRSU; \( (C_M6) \) indicates the transmit power should be non-negative; \( (C_M7) \) constrains the number of allocated edge servers cannot exceed that MRSU possesses.

Remark 2: Problems \( (P_\text{V}) \) and \( (P_\text{M}) \) are difficult to solve, since they are stochastic optimization problems where optimization should be performed at each time slot, and a great deal of the channel and task buffer state information need to be handled, and large amounts of optimization variables should be determined. Moreover, the optimal decisions are temporally correlated due to the random arrivals of tasks [18].

V. PROBLEM TRANSFORMATION

In the following, we propose online algorithms to tackle problems \( (P_\text{V}) \) and \( (P_\text{M}) \) based on Lyapunov optimization theory, leveraging which we can resolve the formulated stochastic optimization problems efficiently by solving deterministic problems at each frame [18], without requiring any future information about task arrivals, network status, etc., and only the current state information is required [31, 33].

Let \( Q(t) = \{Q_1(t), ..., Q_N(t)\}, Z(t) = \{Z_1(t), ..., Z_N(t)\}, \) and \( \Theta(t) = \{Q(t), Z(t)\}, \) basing on which the Lyapunov functions for \( (P_\text{V}) \) and \( (P_\text{M}) \) are given by

\[
L(\Theta(t)) = \frac{1}{2} \sum_{n \in \mathcal{N}} Q_n^2(t) + \frac{1}{2} \sum_{n \in \mathcal{N}} Z_n^2(t), \quad (11)
\]

\[
L(Z(t)) = \frac{1}{2} \sum_{n \in \mathcal{N}} Z_n^2(t). \quad (12)
\]

Then the Lyapunov drift functions \( \Delta(\Theta(t)) \) and \( \Delta(Q(t)) \) are given by

\[
\Delta(\Theta(t)) = \mathbb{E}\{L(\Theta(t+1)) - L(\Theta(t))|\Theta(t)\}, \quad (13)
\]

\[
\Delta(Z(t)) = \mathbb{E}\{L(Z(t+1)) - L(Z(t))|Z(t)\}, \quad (14)
\]

where \( L(\Theta(t+1)) - L(\Theta(t)) \) satisfies

\[
L(\Theta(t+1)) - L(\Theta(t)) \leq \sum_{n \in \mathcal{N}} \left[ Q_n(t) - \frac{Q_n(t)G_{nm}(t)}{\lambda_n} \right] x_n(t) + B_1 + B_2, \quad (15)
\]

and \( L(Z(t+1)) - L(Z(t)) \) satisfies

\[
L(Z(t+1)) - L(Z(t)) \leq \sum_{n \in \mathcal{N}} Z_n(t) x_n(t) + B_2. \quad (16)
\]

The constants \( B_1 \) and \( B_2 \) in Eq. (11) are given by

\[
B_1 = \frac{1}{2} \sum_{n \in \mathcal{N}} \left( (D_{\text{max}})^2 + \left( \frac{x_{\text{max}}}{\lambda_n} + \frac{f_{\text{max}}}{\lambda_n} \right)^2 \right),
\]

\[
B_2 = \frac{1}{2} \sum_{n \in \mathcal{N}} \left( \left( \frac{x_{\text{max}}}{\lambda_n} \right)^2 + \left( q_{\text{max}} \frac{s_c}{\lambda_n} \right)^2 \right). \quad (17)
\]
<table>
<thead>
<tr>
<th>Symbol</th>
<th>Definition</th>
</tr>
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<tbody>
<tr>
<td>$D_n(t)$</td>
<td>The size of the arrived task of VT $n$ at frame $t$ (in bits)</td>
</tr>
<tr>
<td>$\lambda_n$</td>
<td>Processing density of the arrived tasks of VT $n$ (in CPU cycles/bit)</td>
</tr>
<tr>
<td>$f_{\text{max}}$</td>
<td>Maximum local processing capability of each VT (in CPU cycles/s)</td>
</tr>
<tr>
<td>$q$</td>
<td>The total number of edge servers each MRSU possess</td>
</tr>
<tr>
<td>$P_{\text{max}}$</td>
<td>The threshold of the total transmit power of all SUs on a TVWS channel</td>
</tr>
<tr>
<td>$Q_n(t)$, $Z_n(t)$</td>
<td>VT-side and MRSU-side queue backlog of the task of VT $n$ in frame $t$</td>
</tr>
<tr>
<td>$I_m,D_m$</td>
<td>The time duration and bandwidth of burst $m$</td>
</tr>
<tr>
<td>$d_m$</td>
<td>The valid transmission duration of a VT on burst $m$</td>
</tr>
<tr>
<td>$p_m^V(t)$</td>
<td>The transmit power of PU $m$ on burst $m$ in frame $t$</td>
</tr>
<tr>
<td>$G_m^V(t)$</td>
<td>The channel gain between PU $m$ and the BS</td>
</tr>
<tr>
<td>$p_{nm}(t)$, $G_{nm}(t)$, $r_{nm}(t)$</td>
<td>The transmit power, channel gain and transmit rate of VT $n$ on burst $m$ in frame $t$</td>
</tr>
<tr>
<td>$\mathbf{x}(t)$, $\mathbf{x}_n(t)$</td>
<td>Offloading decision vector and offloading decision of VT $n$ in frame $t$</td>
</tr>
<tr>
<td>$\mathbf{f}(t)$, $\mathbf{f}_n(t)$</td>
<td>Local CPU clock frequency vector of all VTs and of VT $n$ in frame $t$</td>
</tr>
<tr>
<td>$\mathbf{S}(t)$</td>
<td>Matrix of all VTs’ burst allocation in frame $t$</td>
</tr>
<tr>
<td>$s_{nm}(t)$</td>
<td>Indicator of whether burst $m$ is allocated to VT $n$ in frame $t$</td>
</tr>
<tr>
<td>$\mathbf{P}(t)$</td>
<td>Matrix of all VTs’ transmit power control in frame $t$</td>
</tr>
<tr>
<td>$\mathbf{q}(t)$</td>
<td>Vector of server provisioning in frame $t$</td>
</tr>
<tr>
<td>$q_n(t)$</td>
<td>The number of edge servers allocated to the task of VT $n$ in frame $t$</td>
</tr>
<tr>
<td>$N,M$</td>
<td>The set and number of all VTs</td>
</tr>
<tr>
<td>$N_1,N_1$</td>
<td>The set and number of remote processing VTs</td>
</tr>
<tr>
<td>$\mathcal{L}$</td>
<td>The set and number of burst intervals (BIs)</td>
</tr>
<tr>
<td>$\mathcal{M}$, $\mathcal{M}_s$</td>
<td>The set and number of primary users (PUs)</td>
</tr>
<tr>
<td>$\theta$</td>
<td>The price of transmitting per bit data via wireless channels (in S/bit)</td>
</tr>
<tr>
<td>$\eta$</td>
<td>The price for processing each unit CPU cycle task (in S/cycle)</td>
</tr>
<tr>
<td>$s_c$</td>
<td>The CPU clock frequency of an edge server (in cycles/s)</td>
</tr>
<tr>
<td>$e(t)$</td>
<td>The price of electricity (in $S$) for running an edge server</td>
</tr>
<tr>
<td>$\alpha_n$</td>
<td>Coefficient used to convert energy consumption into money (in $S/J$)</td>
</tr>
<tr>
<td>$k$</td>
<td>Coefficient used to model local processing energy consumption depending on chip architecture</td>
</tr>
<tr>
<td>$U_n(t)$, $U_{\text{MRSU}}(t)$</td>
<td>The cost of VT $n$ and MRSU in frame $t$</td>
</tr>
<tr>
<td>$U$, $U_{\text{MRSU}}$</td>
<td>The time averaged expected cost of all VTs’ and of MRSU</td>
</tr>
</tbody>
</table>

For notation simplicity, we denote $B = B_1 + B_2$.

In the above derivation procedure, we have adopted the following inequality

$$
(\max\{Q - b, 0\} + A)^2 \leq Q^2 + A^2 + b^2 + 2Q(A - b),
$$

$$
\forall Q \geq 0, \forall b \geq 0, \forall A \geq 0. \quad (18)
$$

According to Lyapunov optimization theory, in order to minimize $U$ in (P$_V$) and $U_{\text{MRSU}}$ in (P$_M$), the drift-plus-penalty functions $\Delta(\mathbf{\Theta}(t)) + V \mathbb{E}\{U(t) | \mathbf{\Theta}(t)\}$ and $\Delta(\mathbf{Z}(t)) + V \mathbb{E}\{U_{\text{MRSU}}(t) | \mathbf{Z}(t)\}$ should be considered, which are bounded by

$$
\Delta(\mathbf{\Theta}(t)) + V \mathbb{E}\{U(t) | \mathbf{\Theta}(t)\}
\leq B_2 + \sum_{n \in N} \mathbb{E}\left\{ Z_n(t)T \left( r_n(t) - q_n(t) \frac{s_c}{\lambda_n} \right) x_n(t) \right\} \mathbf{Z}(t)
$$

$$
+ V \sum_{n \in N} \mathbb{E}\left\{ q_n(t) e(t) - (\theta + \eta \lambda_n - \delta) r_n(t) T \right\} x_n(t) \right\} \mathbf{Z}(t)
$$

$$
= B_2 + \sum_{n \in N} \mathbb{E}\left\{ Z_n(t)T \left( r_n(t) - q_n(t) \frac{s_c}{\lambda_n} \right) \right\} \mathbf{Z}(t)
$$

$$
+ V q_n(t) e(t) - V (\theta + \eta \lambda_n - \delta) r_n(t) T \}, \quad (19)
$$

and

$$
\Delta(\mathbf{Z}(t)) + V \mathbb{E}\{U_{\text{MRSU}}(t) | \mathbf{Z}(t)\}
$$

$$
\leq B_2 + \sum_{n \in N} \mathbb{E}\left\{ Z_n(t)T \left( r_n(t) - q_n(t) \frac{s_c}{\lambda_n} \right) x_n(t) \right\} \mathbf{Z}(t)
$$

$$
+ V \sum_{n \in N} \mathbb{E}\left\{ q_n(t) e(t) - (\theta + \eta \lambda_n - \delta) r_n(t) T \right\} x_n(t) \right\} \mathbf{Z}(t)
$$

$$
= B_2 + \sum_{n \in N} \mathbb{E}\left\{ Z_n(t)T \left( r_n(t) - q_n(t) \frac{s_c}{\lambda_n} \right) \right\} \mathbf{Z}(t)
$$

$$
+ V q_n(t) e(t) - V (\theta + \eta \lambda_n - \delta) r_n(t) T \}, \quad (20)
$$

where $V \in (0, \infty)$ (in bit$^2$/S) is a non-negative control parameter used to strike a balance between the cost and delay, i.e., how much we put our emphasis on cost reduction or processing delay reduction. The notations $N_1$ and $N_1 = \{1, 2, ..., N_1\}$ denote the number and the set of VTs who offload their tasks for remote processing. Then, the original long-term minimization problems (P$_V$) and (P$_M$) can be transformed into the following optimization problems (P$_V$) and (P$_M$), which minimize the upper bound of the drift-plus-penalty functions (19)-(20) in each frame $t$, respectively.


\begin{align}
(P_4^1): & \quad \min_{x(t), f^{loc}(t)} \sum_{n \in N} \left\{ (1 - x_n(t)) \left[ V \alpha_n k D_n(t) \lambda_n (f^{loc}_n(t))^2 - \frac{Q_n(t)}{\lambda_n} f^{loc}_n(t) T \right] +
\right.
onumber \\
& \left. + x_n(t) T \left[ V \left( \alpha_n p_n(t) + \theta r_n(t) + \eta \lambda_n r_n(t) \right) + Z_n(t) \left( r_n(t) - q_n(t) \frac{s_c}{\lambda_n} \right) - Q_n(t) r_n(t) \right] \right\} 
\text{s.t.} \ (C_V 2), (C_V 3) \ \text{in} \ (P_V),
\end{align}

and

\begin{align}
(P_4^M): & \quad \min_{q(t), S(t), P(t)} \sum_{n \in N} \left\{ (Z_n(t) - V (\theta + \eta \lambda_n - \delta) r_n(t) T + \left( V e(t) - \frac{s_c T}{\lambda_n} Z_n(t) \right) q_n(t) \right\} 
\text{s.t.} \ (C_M 2) - (C_M 7) \ \text{in} \ (P_M).
\end{align}

By Little’s law [31], the average delay is proportional to the average queue backlog under given average data arrival rate, so in the following, we will use the two terms, i.e., average delay and average queue backlog interchangeably.

VI. Solve the VT-side per Frame Optimization Problem \((P_4^1)\)

Each VT considers the VT-side and the MRSU-side queue backlogs, the networks status, and then it needs to determine the offloading decision and the corresponding CPU cycle frequency optimization in local processing model, where the following two cases should be taken into consideration:

1) If VT \(n\) selects local processing model \((x_n(t) = 0)\), it needs to further determine the optimum local CPU cycle frequency \(f^{loc}_n(t)\) as follows

\begin{align}
\Omega^{loc}_n(t)^* &= \min_{f^{loc}_n(t)} V \alpha_n k D_n(t) \lambda_n (f^{loc}_n(t))^2 - \frac{Q_n(t)}{\lambda_n} f^{loc}_n(t) T \\
& \text{s.t.} \ (C_V 3) : 0 \leq f^{loc}_n(t) \leq f_{max},
\end{align}

the closed form solution to which can be easily obtained as

\begin{align}
f^{loc}_n(t)^* &= \min \left\{ \frac{Q_n(t) T}{2 V \alpha_n k D_n(t) \lambda_n^2}, f_{max} \right\},
\end{align}

and we can obtain \(\Omega^{loc}_n(t)^* = V \alpha_n k D_n(t) \lambda_n (f^{loc}_n(t)^*)^2 - \frac{Q_n(t)}{\lambda_n} f^{loc}_n(t)^* T\), consequently.

2) If \(x_n(t) = 1\), we can directly obtain

\begin{align}
\Omega^{off}_n(t)^* &= V \left( \alpha_n p_n(t) + \theta r_n(t) + \eta \lambda_n r_n(t) \right) T + Z_n(t) \left( r_n(t) - q_n(t) \frac{s_c}{\lambda_n} \right) T - Q_n(t) r_n(t) T.
\end{align}

By comparing the values of \(\Omega^{loc}_n(t)^*\) and \(\Omega^{off}_n(t)^*\), the offloading decision of each VT \(n\) can be given by

\begin{align}
x_n^*(t) &= \begin{cases} 
1, & \text{if } \Omega^{loc}_n(t)^* > \Omega^{off}_n(t)^* \\
0, & \text{otherwise}
\end{cases}
\end{align}

Remark 3: In order to obtain offloading decision in \(26)\), each VT \(n\) needs to compare which decision, i.e., local processing or task offloading, is more beneficial in the aspects of cost reduction and queue stability. It can be observed that, when the VT-side queue \(Q_n(t)\) is much longer than the MRSU-side queue \(Z_n(t)\), and the wireless networks are in good conditions, task offloading \((x_n(t) = 1)\) is much preferred.

Remark 4: The VT-side optimization algorithm is low in computational complexity, since each variable can be determined by comparing two values, and the two values can be obtained in closed form.

VII. Solve the MRSU-side per Frame Optimization Problem \((P_4^M)\)

Next we solve the MRSU-side problem \((P_4^M)\). It can be decomposed into two sub-problems, i.e., the server provisioning subproblem and the radio resource allocation subproblem. Since there’s no coupling between the two subproblems, we can solve them independently.

A. Server Provisioning Subproblem

The server provisioning subproblem is given by

\begin{align}
\min_{q(t)} \sum_{n \in N_1} \left[ V e(t) - \frac{s_c T}{\lambda_n} Z_n(t) \right] q_n(t) \\
\text{s.t.} \ (C_M 7) : \sum_{n \in N_1} q_n(t) \leq q_{max}. \tag{27}
\end{align}

Observe problem (27) we can know that for a certain VT \(n\), if \(V e(t) \geq \frac{s_c T}{\lambda_n} Z_n(t)\), it will be allocated with no edge server. Denote the set of the VTs who may be allocated with edge servers as \(J\), and we have \(J = \{n \in N_1 \ | \ V e(t) < \frac{s_c T}{\lambda_n} Z_n(t)\}\). From the objective of problem (27) we can further know that among the VTs in \(J\), MRSU prefers to assign edge servers to the VT whose MRSU-side queue backlog \(Z_n(t)\) is large. The larger \(Z_n(t)\) is, the more unfinished tasks are queued, and MRSU will allocate more edge servers to VT \(n\) for delay reduction. The proposed server provisioning algorithm is summarized in Algorithm 1.

B. Radio Resource Allocation Subproblem

In this section, we will tackle the radio resource allocation subproblem embedded in \((P_4)\), where transmit power control \(P(t)\) and burst assignment \(S(t)\) among all the offloading VTs in \(N_1\) should be determined as follows

\begin{align}
\max_{S(t), P(t)} \sum_{n \in N_1} \left[ V (\theta + \eta \lambda_n - \delta) - Z_n(t) \right] r_n(t) T \\
\text{s.t.} \ (C_M 2) - (C_M 6), \tag{28}
\end{align}

where

\begin{align}
r_n(t) = \sum_{m \in M} s_{nm}(t) \frac{\tilde{p}_m}{P_{\text{max}}(t)} e^{-B_m \log_2 \left( 1 + \frac{\tilde{p}_m(t) s_{nm}(t)}{P_{\text{max}}(t) e^{\frac{\tilde{p}_m(t)}{B_m}} + \rho} \right)}. \nonumber \end{align}

However, (28) is still an MINLP problem and can be generally NP-hard [19]. Observing (28), the difficulty comes from two aspects, i.e., the integer constraints and the non-convex objective function. Under this observation, the proposed continuous relaxation and Lagrangian dual decomposition based method works as follows.
Algorithm 1 Server Provisioning Algorithm

Initialization:
1: Initialize $\mathcal{J} = \emptyset$.

Iteration:
2: for $n = 1 : N_1$ do
3: Calculate the value of $V_e(t) - \frac{x^T}{x_T} Z_n(t)$.
4: if $V_e(t) - \frac{x^T}{x_T} Z_n(t) < 0$ then
5: $\mathcal{J} = \mathcal{J} \bigcup n$.
6: end if
7: end for
8: Sort all the VTs in $\mathcal{J}$ in ascending order according to the value of $[V_e(t) - \frac{x^T}{x_T} Z_n(t)]$.
9: for $j = 1 : |\mathcal{J}|$ do
10: $q_j(t) = \min \left\{ \left\lfloor \frac{Z_j(t) + \lambda_j}{\beta_j} \right\rfloor, q_{\text{max}} \right\}$.
11: $q_{\text{max}} = q_{\text{max}} - \left\lfloor \frac{Z_j(t) + \lambda_j}{\beta_j} \right\rfloor$.
12: if $q_{\text{max}} \leq 0$ then break.
13: end if
14: end for
15: end for
16: Output: $q(t) = \{q_n(t)\}, n \in N_1$.

1) Convert Problem (28) to a Convex Programming Problem: First, by relaxing the integer constraint into $s_{nm}(t) \in [0,1]$, and replacing $p_{mn}(t)$ with $y_{nm}(t) = s_{nm}(t)p_{mn}(t)$, $\forall n \in N_1, \forall m \in \mathcal{M}$, we can obtain

$$\max_{S(t), Y(t)} \sum_{n \in N_1} \sum_{m \in \mathcal{M}} \left[ V(\theta + \eta \lambda_n - \delta) - Z_n(t) \right] B_m T_m s_{nm}(t)$$

$$\log_2 \left( 1 + \frac{y_{nm}(t) G_{nm}(t)}{s_{nm}(t) \left( \frac{P_{\text{PU}}(t) G_{\text{PU}}(t)}{G_{nm}(t)} + \sigma^2 \right)} \right)$$

s.t. (C$_M^2$) : $\sum_{n \in N_1} y_{nm}(t) G_{nm}(t) \leq \beta_m, \forall m \in \mathcal{M}$,

(C$_M^3$) : $\sum_{n \in N_1} \sum_{m \in \mathcal{M}} y_{nm}(t) C_{ml} \leq P_{\text{max}}, \forall l \in \mathcal{L}$,

(C$_M^4$) : $\sum_{n \in N_1} s_{nm}(t) \leq 1, \forall m \in \mathcal{M}$,

(C$_M^5$) : $s_{nm}(t) \in [0,1], \forall n \in N_1, \forall m \in \mathcal{M}$,

(C$_M^6$) : $y_{nm}(t) \geq 0, \forall n \in N_1, \forall m \in \mathcal{M}$. (29)

Proposition 1: Problem (29) is jointly convex in $S(t)$ and $Y(t)$.

Proof: See Appendix A. }

Although the convex problem (29) could be settled by general algorithms such as Interior Point Method, which however suffers from high time complexity. Next we use Lagrangian dual decomposition technique to design a low-complexity algorithm. The partial Lagrangian function of (29) is given in (30), where $\mu(t) = \{\mu_m(t)\} \geq 0, m \in \mathcal{M}$ and $\omega(t) = \{\omega_l(t)\} \geq 0, l \in \mathcal{L}$ are dual variables corresponding to constraints (C$_M^2$) and (C$_M^3$) in (29), respectively.

As problem (29) is convex, zero duality gap can be guaranteed. Thus, we can solve the following dual problem to obtain the optimal radio resource allocation.

$$\min_{\mu(t), \omega(t)} \max_{S(t), Y(t)} L(S(t), Y(t), \mu(t), \omega(t))$$

$$= \min_{\mu(t), \omega(t)} \max_{S(t), Y(t)} L(S(t), Y(t), \mu(t), \omega(t)). \quad (31)$$

2) Obtain Power Allocation: According to (31), assuming dual variables $\mu(t)$ and $\omega(t)$ and burst allocation $S(t)$ are given, we can first obtain the transmit power through solving $Y(t)$. Using KKT conditions [34] and by letting

$$\frac{\partial L(S(t), Y(t), \mu(t), \omega(t))}{\partial y_{nm}(t)} = 0,$$

we can solve $y_{nm}(t)$ as

$$y_{nm}(t) = \frac{T_m B_m \left[ V(\theta + \eta \lambda_n - \delta) - Z_n(t) \right]}{\ln(2) \left( \mu_m(t) G_{nm}(t) + \sum_{l \in \mathcal{L}} \omega_l(t) C_{ml} \right)}$$

$$- \frac{P_{\text{PU}}(t) G_{\text{PU}}(t) + \sigma^2}{G_{nm}(t)} s_{nm}(t). \quad (32)$$

Since $y_{nm}(t) = s_{nm}(t)p_{nm}(t)$, we can obtain the optimal transmit power control policy as

$$p_{nm}(t) = \frac{T_m B_m \left[ V(\theta + \eta \lambda_n - \delta) - Z_n(t) \right]}{\ln(2) \left( \mu_m(t) G_{nm}(t) + \sum_{l \in \mathcal{L}} \omega_l(t) C_{ml} \right)} - \frac{P_{\text{PU}}(t) G_{\text{PU}}(t) + \sigma^2}{G_{nm}(t)} s_{nm}(t), \quad (33)$$

where $x^+ = \max\{x, 0\}$.

3) Obtain Burst Assignment: In this step, by plugging the obtained optimal transmit power allocation back to Lagrangian function, we solve the optimal burst assignment. For notation simplicity, we define $d_{nm}(t)$ and $E(t)$ as follows

$$d_{nm}(t) = \left[ V(\theta + \eta \lambda_n - \delta) - Z_n(t) \right]$$

$$\bar{T}_m B_m \log_2 \left( 1 + \frac{p_{nm}(t) G_{nm}(t)}{P_{\text{PU}}(t) G_{\text{PU}}(t) + \sigma^2} \right)$$

$$- \left( \mu_m(t) G_{nm}(t) + \sum_{l \in \mathcal{L}} \omega_l(t) C_{ml} \right) p_{nm}(t),$$

$$E(t) = \sum_{m \in \mathcal{M}} \mu_m(t) \beta_m + \sum_{l \in \mathcal{L}} \omega_l(t) \delta_{l,m}.$$ (34)

where $d_{nm}(t)$ is the weight coefficient and $E(t)$ is a constant. Then the optimal burst assignment can be obtained by solving

$$\max_{S(t)} \sum_{n \in N_1} \sum_{m \in \mathcal{M}} d_{nm}(t) s_{nm}(t) - E(t)$$

s.t. (C$_M^5''$) : $\sum_{n \in N_1} s_{nm}(t) \leq 1, \forall m \in \mathcal{M},$

(C$_M^5''$) : $s_{nm}(t) \in [0,1], \forall n \in N_1, \forall m \in \mathcal{M}. \quad (35)$

It is not difficult to know, problem (35) is the sum of a set of linear functions w.r.t $s_{nm}(t)$, Consequently, burst allocation $s_{nm}(t)$ must be binary, since the optimal value of a linear function is obtained at the endpoints. Therefore, the optimal burst assignment $s_{nm}(t)$ can be given by

$$s_{nm}(t) = \begin{cases} 1, & \text{if } n = \arg \max_n d_{nm}(t) \& d_{nm}(t) > 0, \\ 0, & \text{otherwise.} \end{cases} \quad (36)$$
\[
L(S(t), Y(t), \mu(t), \omega(t)) = \sum_{n \in N_1} \sum_{m \in M_1} \left[ V(\theta + \eta \lambda_n - \delta) - Z_n(t) \right] B_m T_m s_{nm}(t) \log_2 \left( 1 + \frac{y_{nm}(t) G_{nm}(t)}{s_{nm}(t)(p^{\text{PU}}_m(t) G_{nm}^{\text{PU}}(t) + \sigma^2)} \right) \\
- \sum_{m \in M_1} \mu_m(t) \left( \sum_{n \in N_1} y_{nm}(t) G_{nm}(t) - \beta_m \right) - \sum_{l \in L} \omega_l(t) \left( \sum_{n \in N_1} \sum_{m \in M_1} y_{nm}(t) C_{ml} - p^{\text{max}} \right). \tag{30}
\]

**Remark 5:** Equation (36) manifests that a burst should be allocated to the VT with the maximum positive weight upon it. If all VTs are in deep fading and with negative weights, this burst should not be allocated to any VT, since using it will lead to more waste but less profit.

4) **Dual Variables Update:** To solve the outer minimization problem in (31) (also called master dual problem), a subgradient method [35] can be used to update the dual variables. More specifically, the update can be performed as

\[
\mu_m^{i+1}(t) = \left[ \mu_m^i(t) - h_m^i(t) \left( \beta_m - \sum_{n \in N_1} y_{nm}(t) G_{nm}(t) \right) \right]^+, \quad \forall m \in M, \tag{37}
\]

\[
\omega_l^{i+1}(t) = \left[ \omega_l^i(t) - k_l^i(t) \left( p^{\text{max}} - \sum_{n \in N_1} \sum_{m \in M_1} y_{nm}(t) C_{ml} \right) \right]^+, \quad \forall l \in L, \tag{38}
\]

where \( i \) is the iteration index; \( h_m^i(t) \) and \( k_l^i(t) \) are the sequences of scalar step sizes of the \( i \)th iteration. In this paper we adopt square summable but not absolute summable step sizes [35]. The Lagrangian dual variables are updated iteratively until the required precision is satisfied.

The procedure for joint transmit power control and burst assignment is summarized in Algorithm 1 as follows.

**Algorithm 2** Lagrangian Dual Decomposition Based Burst Assignment and Power Allocation Algorithm

**Initialization:**
1: Initialize \( \mu^0(t), \omega^0(t), t^{\text{max}} \) and the precision \( \epsilon \). Set \( i = 0 \).

**Iteration:**
2: while \( i \leq t^{\text{max}} \) do
3: Perform transmit power allocation \( p_{nm}^i(t), \forall n, m \) according to (33).
4: Perform burst assignment \( s_{nm}^i(t), \forall n, m \) based on (36).
5: Update Lagrangian dual variables \( \mu(t), \omega(t) \) according to (37) and (38), respectively.
6: if \( |\mu^{i+1}(t) - \mu^i(t)| < \epsilon \) and \( |\omega^{i+1}(t) - \omega^i(t)| < \epsilon \) then
7: \( s_{nm}^i(t) = s_{nm}^i(t), p_{nm}^i(t) = p_{nm}^i(t) \).
8: break.
9: else
10: \( i = i + 1 \).
11: end if
12: end while
13: Output: \( S^*(t) = \{ s_{nm}^*(t) \}, P^*(t) = \{ p_{nm}^*(t) \} \).

Till now, our dual-side optimization in a competition scenario is solved, and the joint optimization of offloading decision making and local CPU frequency control is obtained for VT-side, and the joint optimization of transmit power allocation, burst assignment and server provisioning is obtained for MURS-side. Detailed procedure is summarized in Algorithm 2, which is referred to as dual-side dynamic joint task offloading and resource allocation algorithm in vehicular networks (DDORV).

**Algorithm 3** Dual-side Dynamic Joint Task Offloading and Resource Allocation Algorithm in Vehicular Networks (DDORV)

1: At each frame \( t \):
2: MRSU-side optimization
3: Observe the current MRSU-side queues \( Z_n(t) \) and channel states in each frame \( t \).
4: Obtain server provisioning \( q(t) \) according to Algorithm 1.
5: Perform transmit power control \( P(t) \) and burst assignment \( S(t) \) according to Algorithm 2.
6: VT-side optimization
7: Observe the current queues \( Q_n(t) \) and \( Z_n(t) \) and channel states in each frame \( t \).
8: for \( n \in N \) do
9: Obtain the values of \( \Omega_n^{\text{loc}}(t)^* \) and \( \Omega_n^{\text{off}}(t)^* \).
10: if \( \Omega_n^{\text{loc}}(t)^* > \Omega_n^{\text{off}}(t)^* \) then
11: Set \( x_n(t) = 0 \);
12: Obtain \( f_n^{\text{loc}}(t) \) based on (24);
13: else
14: Set \( x_n(t) = 1 \);
15: end if
16: end for
17: Base on the above obtained VT-side optimization results \( \{ x(t), f_n^{\text{loc}}(t) \} \) and MRSU-side results \( \{ S(t), P(t), q(t) \} \), update queues \( Q_n(t) \) and \( Z_n(t) \) according to Eqs. (5) and (6).

**VIII. Complexity Analysis**

The computational complexity of our joint dual-side optimization algorithm DDORV in Algorithm 3 comes from two aspects, i.e., the MRSU-side and the VT-side optimization.

The complexity of the MRSU-side complexity mainly determined by Steps 4 and 5. In Step 4, the complexity comes form server provisioning, i.e., Algorithm 1. In Algorithm 1, the complexity of Steps 2-7 is \( O(N) \), the complexity of sorting in Step 8 is \( O(N^2) \), and the complexity of Steps 9-15 is \( O(N) \). Therefore, the total complexity of Algorithm 1 is \( O(N) + O(N^2) + O(N) = O(N^2) \). The complexity of Step 5 comes from Algorithm 2. In Algorithm 2, the complexity of power allocation and burst allocation in Steps 3 and 4 are \( O(NM) \), the complexity of dual-variable-update
in Step 5 is $O(M) + O(L)$. The subgradients need $O(\frac{1}{\epsilon})$ iterations to converge. So the complexity of Algorithm 2 is 
\[ \frac{1}{\epsilon^2} \left( O(NM) + O(NM) + O(M) + O(L) \right) = O(\frac{NM}{\epsilon^2}) \]. 
The complexity of VT-side optimization is $O(N)$. Therefore, the complexity of DDORV in Algorithm 3 is $O(N^2) + O(\frac{NM}{\epsilon^2}) + O(N) = O(\frac{NM}{\epsilon^2})$.

IX. SIMULATION RESULTS

In this section, we provide simulations to verify the performance of our proposed algorithms. Our simulations are conducted on a Matlab-based simulator. We simulate an MEC enabled mixed CVN and 802.22 network in a $5km \times 5km$ square area, where an MRSU locates at the center of the region, PUs are scattered uniformly through the region, and VTs are distributed in an $1km \times 10m$ road [27]. A TVWS channel with 6 MHz is reused by all the VTs, which is equally divided into 20 subchannels in frequency domain. The channel occupancy $t_{PU}$ of PUs follows Gamma distribution, and the CDF of $t_{PU}$ is $F(t_{PU}) = 1 - e^{-5t_{PU}} - 5t_{PU} e^{-5t_{PU}}$ [27]. For simplicity, the wireless channel gain including the channel gain $G_{nm}(t)$ of VT $n$ on burst $m$ takes values randomly in $[1, 5]$, and the channel gain $G_{PU}(t)$ between PU $m$ and the BS is supposed to take values randomly in $[5, 10]$, and be i.i.d. over frames [36]. Time is divided into frames, suppose the length of each uplink frame is $T = 9 ms$, and each frame contains $L = 4$ BIs. There are $M = 44$ bursts, each of which occupies one subchannel in frequency domain. Bursts 1 to 12 are type 1 burst, which covers the whole uplink subframe in time domain, while the rest 32 bursts are type 2 bursts with a length $T/4$ in time domain [27]. The PU interference limit at the BS for PU $m$ (i.e., $\beta_m$) is chosen based on the criterion that SINR at the BS is no less than $10 dB$, i.e., $SINR_m = \frac{\rho_m G_{PU}(t)}{\beta_m + \sigma^2} \geq 10$. Detailed parameters are summarized in Table II.

In the following, we will first evaluate the convergence of the proposed iterative radio resource allocation algorithm, i.e., Algorithm 2. Then we verify how parameter $V$ affects the tradeoff between the cost of VTs and the length of queues. Finally, we assess the performance of DDORV form the VT-side and MRSU-side, respectively.

A. Convergence of Algorithm 2

In Fig. 4, we show the convergence of Algorithm 2 by plotting the two sets of dual variables $\omega = [\omega_l], l \in \mathcal{L}$ and $\mu = [\mu_m], m \in \mathcal{M}$ versus the number of iterations in the lhs and rhs subfigure, and there are $|\mathcal{L}| = 4$ curves of $\omega = [\omega_l], l \in \mathcal{L}$ and 44 curves of $\mu = [\mu_m], m \in \mathcal{M}$, respectively. Fig. 3 demonstrates that both the two sets of dual variables converge at the $5th$ iteration, demonstrating our Lagrangian dual decomposition based Algorithm 2 converges considerably fast.

B. Performance of DDORV Versus Control Parameter $V$

Fig. 5 depicts how the control factor $V$ make a trade off between the cost $U$ and the average delay. When $V$ gets larger, the cost $U$ decrease, however, the length of queue $Q + Z$ will

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Number of VTs $N$</td>
<td>20</td>
</tr>
<tr>
<td>Length $T$ of uplink frame</td>
<td>9 (ms) [27]</td>
</tr>
<tr>
<td>Number of bursts $M$</td>
<td>44 [27]</td>
</tr>
<tr>
<td>Number of BIs $L$ in each frame</td>
<td>4 [27]</td>
</tr>
<tr>
<td>Interference limit $\beta_m$ for PU $m$</td>
<td>10 dB [27]</td>
</tr>
<tr>
<td>Transmit power $p_m^{\text{PU}}(t)$ of PU $m$</td>
<td>0 $\sim$ 4 W [28]</td>
</tr>
<tr>
<td>VT transmit power threshold $P_m^{\text{max}}$</td>
<td>100 W [29]</td>
</tr>
<tr>
<td>Bandwidth $B_m$ of burst $m$</td>
<td>$\frac{5}{8}$ MHz [27]</td>
</tr>
<tr>
<td>Number of Types 1 and 2 bursts</td>
<td>12, 32 [27]</td>
</tr>
<tr>
<td>Arrived input data size $D_m(t)$</td>
<td>0.1 $\sim$ 1 Mbit</td>
</tr>
<tr>
<td>Processing density $\lambda_m$ (cycles/bit)</td>
<td>10 $\sim$ 1000</td>
</tr>
<tr>
<td>Max. local processing capability $P_m^{\text{max}}$</td>
<td>1.4 (G cycles/s) [17]</td>
</tr>
<tr>
<td>Coefficient $k$ for modeling local processing energy consumption</td>
<td>$10^{-2}$ [18]</td>
</tr>
<tr>
<td>Number of edge servers $q_m^{\text{max}}$</td>
<td>$N + 10$</td>
</tr>
<tr>
<td>Processing capability of each edge server $s_c$</td>
<td>3 (G cycles/s) [17]</td>
</tr>
<tr>
<td>Electricity price per edge server $e(t)$</td>
<td>$1 \times 10^{-5}$</td>
</tr>
<tr>
<td>Energy-money weight parameter $\alpha_m$</td>
<td>$2.44 \times 10^{-8}$ ($/$/J) [17]</td>
</tr>
<tr>
<td>Price $\theta$ for MRSU renting radio bandwidth from operator</td>
<td>$0.5 \times 10^{-10}$ ($$/bit)</td>
</tr>
<tr>
<td>Price $\eta$ that MRSU charges from VTs for wireless data trans.</td>
<td>$1.16 \times 10^{-10}$ ($$/bit)</td>
</tr>
<tr>
<td>Price $\eta$ MRSU charges from VTs for task processing</td>
<td>$3 \times 10^{-10}$ (in $$/cycle)</td>
</tr>
</tbody>
</table>
grow, which will lead to more delay in task processing. Thus, it can be known that the control parameter $V$ can make a good balance between the cost and the length of data queues.

C. Performance Comparison for VT-side

Next, we show the performance for VT-side optimization of DDORV, by comparing it with local processing (denoted as Local-pro) and MRSU processing (denoted as MRSU-pro). In Local-pro, all VTs process their tasks locally using the maximum local CPU frequency. In MRSU-pro, all VTs offload their tasks to MRSU, and MRSU-side resource allocation optimization is performed among them.

1) Impact of the number of VTs $N$: In Fig. 6, we show the comparison on the cost and the queue backlog of all VTs’ versus the number of VTs, respectively. As can be seen from Fig. 6 (a), with the number of VTs $N$ increases, the user cost keeps increasing, which is the same for all algorithms. On the other hand, with the number of VTs increase, the available bursts and edge servers allocated to each VT decrease, as a result, the length of data queue increase, which is shown in Fig. 6 (b). Therefore, the two sub-figures indicate a tradeoff between the cost and the length of data queue. However, thanks to a multi-dimensional optimization, DDORV can always outperforms other algorithms, which perform only a certain dimensional optimization and consequently, more cost will be spend.

2) Impact of task parameter: In Figs. 7 and 8, we show how the task parameters affect the cost of VTs, including the input data arrival $D_n(t)$ and the processing density $\lambda_n$, respectively. Observing the two figures, it can be known that the cost of VTs grows linearly with respect to the two parameters, which is in accordance with the VT-side cost model in Eq. (7), where the cost of each VT $n$ is proportional to both $D_n(t)$ and $\lambda_n$.

From the two figures it can also be known that the performance of MRSU-pro is a little worse than DDORV in performance, which indicates that the MRSU-side optimization plays a bigger role in the dual-side optimization framework.

D. Performance Comparison for MRSU-side

Then we show the performance for MRSU-side optimization of DDORV, by comparing it with Ser-pro-only and Radio-only algorithms. In Ser-pro-only, only server provisioning is optimized, and in Radio-only, only radio resource allocation (including IEEE 802.22 burst and power allocation) is optimized [26], [27], among all the VTs who choose task offloading. According to (8), it can be known that the profit of MRSU equals to the minus cost of MRSU, and the problem in (10) to minimize the cost of MRSU equals to maximizing the profit of MRSU. Therefore, we consider the profit of MRSU instead of discussing the cost for easy observation.

Since the pricing policy (i.e., $\theta$, $\delta$, $\eta$, and $e(t)$) plays an important role on the economical profit of MRSU, we show the performance comparison between the algorithms under different pricing policies in Figures 9, 10, 11, and 12.

As can be seen from Figs 8 and 10, the profit of MRSU grows with the increase of the price that it rents radio and computation resources to VTs, i.e., $\theta$ and $\eta$. Figs. 9 and 11 show that the profit of MRSU decrease with the price $\delta$ it rents radio resource from wireless network operators, and the electricity bill $e(t)$ to active per edge server.

X. Conclusions

In this paper, we have studied a dual-side stochastic optimization framework in an MEC enabled IEEE 802.22-CVN coexistence system. Then two problems, i.e., the VT-side and the MRSU-side optimization problems, were formulated, in order to minimize the averaged cost of VTs and the MRSU, respectively. Employing Lyapunov optimization theory, we have proposed a low-complexity online algorithm DDORV, where the two problems are solved in a integrated framework in each frame. Simulation results have verified the convergence of the iterative radio resource allocation algorithm, the tradeoff between the average cost of VTs and the average queue length, and have shown DDORV can performs well compared with other schemes.

Appendix

Appendix A. Proof of Proposition 1

Proof: For notation simplicity, denote constant $|V(\theta + \eta \lambda_n - \delta) - Z_n(t)|B_nT_m$ as $A$. According to convex optimization [34], when $f(x)$ is concave, then the perspective function $g(x,t) = tf(x/t)$ is concave, too. Since $A_{nm}(t) \log_2 \left( 1 + \frac{y_m(t)G_{nm}(t)}{s_{nm}(t)(p_u^{m}(t)G_{mm}(t)+\sigma^2)} \right)$ is the perspective function of concave function $A \log_2 \left( 1 + \frac{y_m(t)G_{nm}(t)}{p_u^{m}(t)G_{mm}(t)+\sigma^2} \right)$, it preserves concavity, too. As the sum of several concave functions is still concave, $\sum_{n\in N_1, m\in M} A_{nm}(t) \log_2 \left( 1 + \frac{y_m(t)G_{nm}(t)}{s_{nm}(t)(p_u^{m}(t)G_{mm}(t)+\sigma^2)} \right)$ is also concave. On the other hand, (C2) – (C6) are all linear constraints. Thus, (28) is a convex optimization programming that maximize a concave function over a convex set.

References


Fig. 6: (a) Total cost of all VTs vs. the number of VTs \( N \), (b) queue backlog vs. the number of VTs \( N \).

Fig. 7: Total cost of all VTs vs. the data arrival \( D_n(t) \).

Fig. 8: Total cost of all VTs vs. processing density \( \lambda_n \).

Fig. 9: Profit of MRSU versus the wireless data transmission price \( \theta \) charged for form VTs.

Fig. 10: Profit of MRSU versus the price \( \delta \) that it rents radio bandwidth from wireless network operator.

Fig. 11: Profit of MRSU versus the price \( \eta \) that it charges for task processing form VTs.

Fig. 12: Profit of MRSU versus the electricity bills \( e(t) \) that it pays to grid operator for running an edge server.


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