

DEVELOPMENT OF HIGH PERFORMANCE DETECTOR TECHNOLOGY FOR UV AND NEAR IR APPLICATIONS

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ABSTRACT

Sensing and imaging for ultraviolet (UV) and near infrared (NIR) bands has many applications for NASA, defense, and commercial systems. Recent work has involved developing UV avalanche photodiode (UV-APD) arrays with high gain for high resolution imaging. Various GaN/AlGaN *p-i-n* (PIN) UV-APDs have been fabricated from epitaxial structures grown by MOCVD on GaN substrates with avalanche gains higher than 5×10^5 , and significantly higher responsivities. Likewise, the SiGe material system allows the demonstration of high-performance detector array technology that covers the 0.5 to 1.7 μm wavelength range for visible and NIR bands of interest. We have utilized SiGe fabrication technology to develop Ge based PIN detector devices on 300 mm Si wafers. We will discuss the theoretical and experimental results from electrical and optical characterization of the detector devices with various n^+ region doping concentrations to demonstrate low dark currents below 1 μA at -1 V and high photocurrent. Recent results from these detector arrays for UV and NIR detection will be presented.

1. GAN/ALGAN UV AVALANCHE PHOTODIODE DEVELOPMENT

Detection of shorter UV wavelengths allows for increased spatial resolution, smaller pixels, and larger formats. Figure 1 shows a schematic cross-sectional

view of the epitaxial layers and device structure for $\text{Al}_{0.05}\text{Ga}_{0.95}\text{N}$ UV-APDs [1].

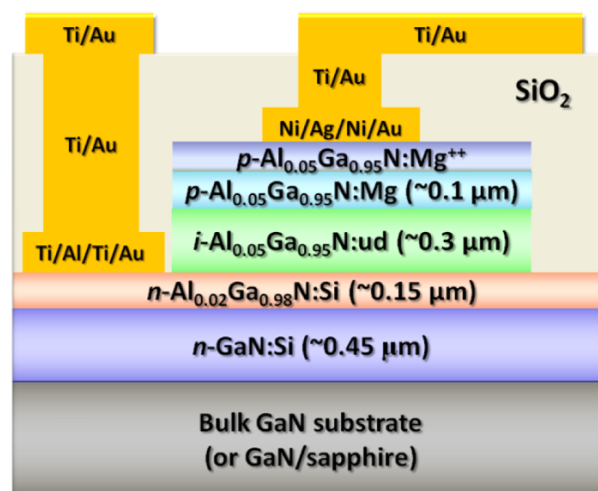


Figure 1. Device structure cross-section of GaN *p-i-n* APD on bulk GaN substrate [1].

Figure 2 presents the current-voltage characteristics and avalanche gain for an $\text{Al}_{0.05}\text{Ga}_{0.95}\text{N}$ UV-APD operating at ~ 280 nm [2]. Under dark conditions, the device demonstrated low leakage current under 1 pA (corresponding to dark current density of less than 10^{-7} A/cm²) up to a reverse voltage of around 60 V. We believe that low defect density native substrates and high-quality metal organic chemical vapor deposition (MOCVD) epitaxial growth technologies are the keys to the successful implementation of robust high gain, large area UV detectors.

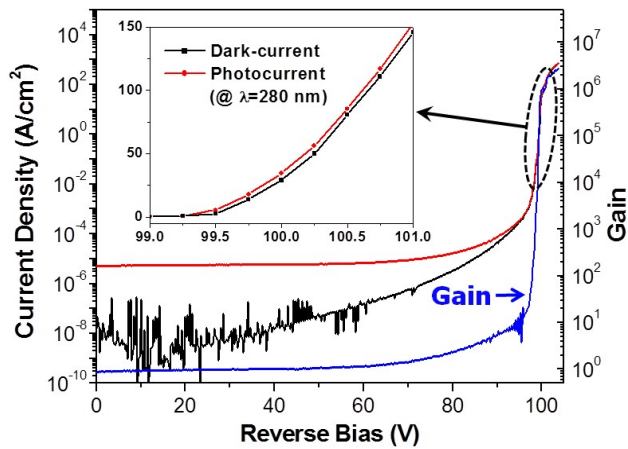


Figure 2. Current density and gain plotted vs. reverse bias for $\text{Al}_{0.05}\text{Ga}_{0.95}\text{N}$ UV-APD with mesa diameter of $30\ \mu\text{m}$ with and without UV illumination at $280\ \text{nm}$. Inset shows dark current and photocurrent plotted over $99\ \text{V}$ to $101\ \text{V}$ reverse bias range [2].

The detection wavelength of interest in the UV band, given the utilization of YAG lasers, is $355\ \text{nm}$. UV detection devices operating at this wavelength are very achievable with the AlGaIn material system. We are currently working on optimizing the GaN/AlGaIn material growth for $355\ \text{nm}$ peak detector performance (Figure 3) [3].

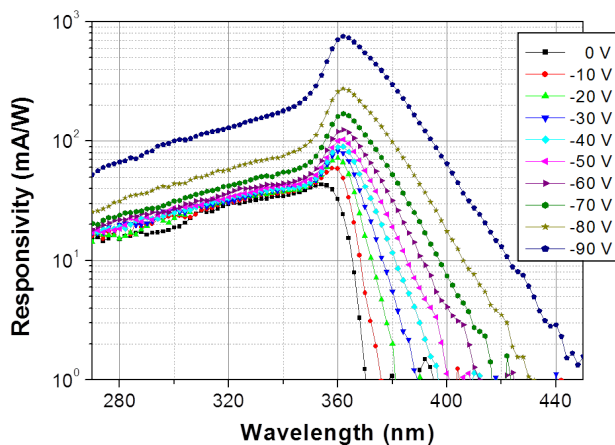


Figure 3. Reverse-biased voltage-dependent spectral response of photocurrent measured at room temperature for $70\ \mu\text{m}$ diameter $\text{Al}_{0.05}\text{Ga}_{0.95}\text{N}$ *p-i-n* UV-APD, showing peak responsivity at $354\ \text{nm}$ under zero bias [3].

2. SIGE VISIBLE-NIR DETECTOR ARRAY DEVELOPMENT

SiGe offers a low-cost means for developing visible to near-infrared (vis-NIR) sensors that do not require any cooling and can operate typically from $\sim 0.4\text{--}1.6\ \mu\text{m}$, which range can be extended further up to $2\ \mu\text{m}$, e.g., with inclusion of quantum dots. The attractive features of SiGe based detectors and detector arrays include taking advantage of silicon-based technology that can enable very small feature sizes and compatibility with silicon CMOS readout integrated circuits (ROICs) for signal processing.

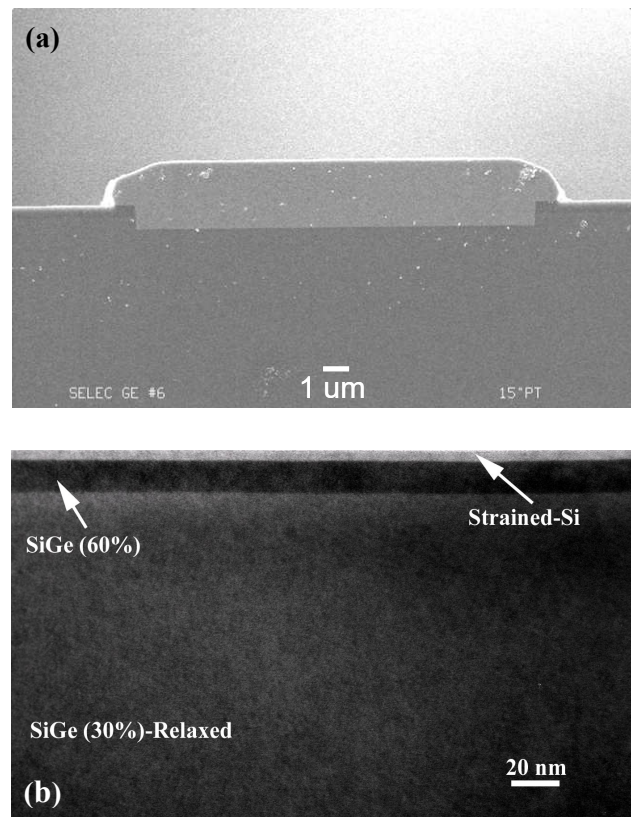


Figure 4. (a) Scanning electron microscope (SEM) cross-section micrograph of selective-epitaxial grown region of Ge-on-Si test structure (b) Transmission electron microscopy (TEM) cross-section micrograph of strained Si/strained SiGe (60% Ge)/relaxed SiGe (30% Ge) high-mobility heterostructure.

SiGe epitaxial growth processes are compatible with both front- and back-end Si CMOS fabrication technologies. An attractive feature of CMOS-compatible SiGe NIR detectors and imaging arrays is that they can be fabricated on 12-inch (300 mm) Si wafers, further decreasing costs and maximizing production output.

One significant advantage of this approach is that selective epitaxial growth of SiGe can be utilized to heterogeneously integrate SiGe photodiodes with CMOS circuitry, using manufacturing infrastructure that is already widely installed for production of SiGe layers and CMOS integrated circuits. In addition to leveraging installed infrastructure and heterogeneous integration of imaging arrays with readout electronics, our approach targeted room temperature operation. This process can also be extended to include pixel designs that stack the SiGe photodiodes on top of CMOS pixel circuitry to maximize the fill factor of the imaging array.

Compared to SiGe based metal-semiconductor-metal (MSM) detector devices, SiGe detectors with PIN structures are generally stable, linear, and provide fast response times, low noise, and high responsivity. In addition, Ge/SiGe PIN photodetectors have been reported that provide relatively low dark currents ranging from low μA down to nA (corresponding to dark current densities typically in the $\mu\text{A}/\text{cm}^2$ to mA/cm^2 range). Vertical SiGe-based PIN photodetector structures grown utilizing a thin buffer layer exhibit significant built-in electric fields of several kV/cm inside the intrinsic Ge/SiGe layers, which results in shorter transit times and less carrier recombination at dislocations or point defects and thus higher detector collection efficiency, enabling improved device quality and smaller detector dimensions [3].

Subsequently, cross-sectional off-contact TEM and XSEM analyses of the layer structure of the SiGe devices, shown in Figure 5, show material growth with well-defined layer interfaces [4]. The cross-sectional TEM image [Figure 5(a)] and XSEM results [Figure 5(b)] of a device with screen oxide displayed uniform compositions and relatively sharp transitions between

the layers. Magnification factors associated with the TEM and XSEM images are 57kX and 35kX, respectively. The thicknesses of the intrinsic Ge and oxide layers were determined from the TEM image to closely match those measured from previous XSEM image data.

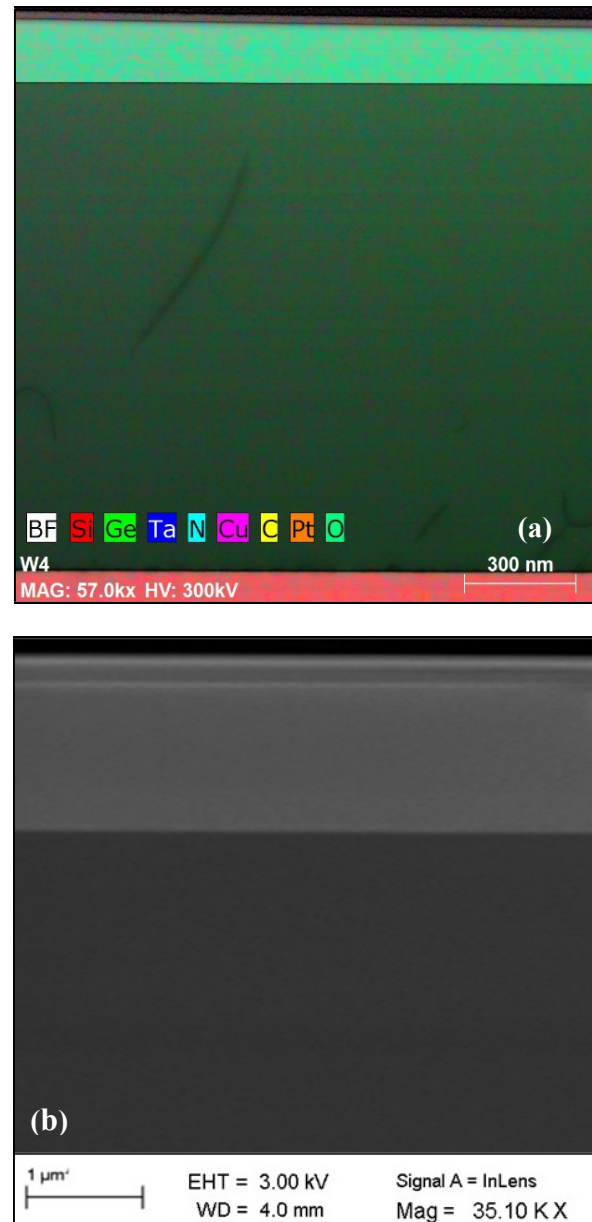


Figure 5. (a) Cross-sectional TEM image of detector device, providing material compositions (color-coded) and layer structure down to Si substrate. (b) Corresponding cross-sectional XSEM image with a less magnified view, i.e., showing larger area [4].

The I-V characterization results featured dark/leakage currents down to 100 nA at -1 V, and 1 μ A at -2 V. The measured photocurrent remained relatively low, particularly above 1 V bias; this aspect became the primary focus for improvement in subsequent devices.

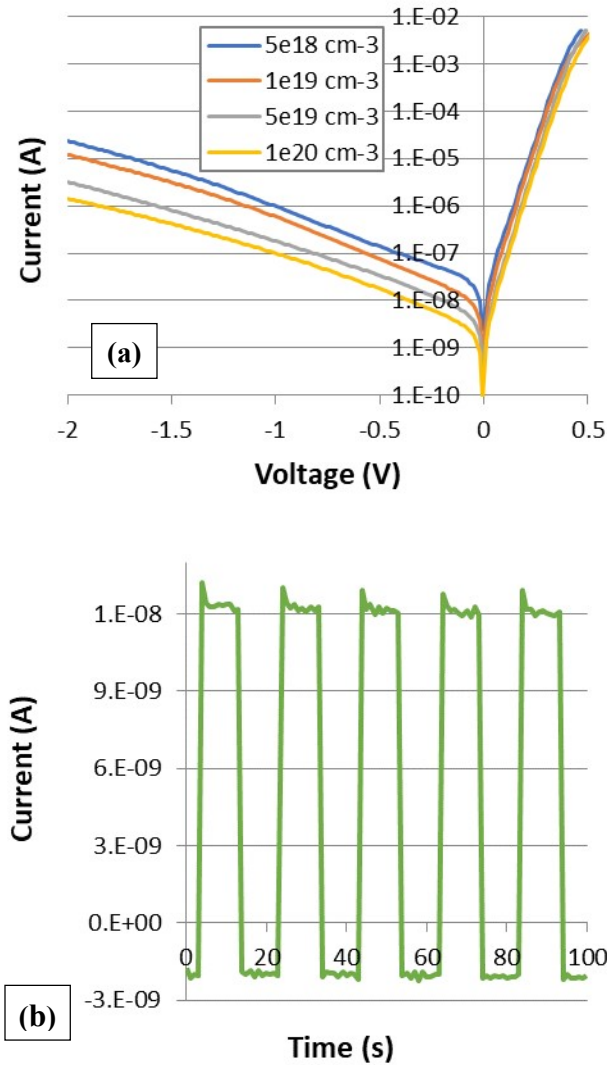


Figure 6. (a) Comparison of dark/leakage current I-V curves with various n^+ region doping concentrations. (b) Time-varying photoresponse measured at 0 V bias.

Figure 6(a) displays the effect of doping concentration on the I-V behavior of representative devices from each quadrant. Devices corresponding to the highest doping of 10^{20} cm⁻³ had lowest dark currents at reverse bias. Figure 6(b) shows the associated photoresponse at zero-bias while modulating the incident NIR

radiation on and off. Backside Al contacts were implemented for these devices.

3. REFERENCES

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