High temperature rapid thermal annealing of phosphorous ion implanted InAs/InP quantum dots

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The effect of high temperature annealing of the InAs/InP quantum dots (QDs) containing a thin GaAs interlayer is investigated. The QDs are rapid thermally annealed at 750, 800, 850, and 900 °C for 30 s. The QDs with the GaAs interlayer show good thermal stability up to 850 °C as well as enhanced integrated photoluminescence (PL) intensity and reduced PL linewidth. The effect of high energy (450 keV) phosphorous ion implantation at room temperature with doses of $5 \times 10^{11}-5 \times 10^{13}$ ions/cm² with subsequent high temperature (750–850 °C) rapid thermal annealing is also studied. A large implantation-induced energy shift of up to 309 meV (400 nm) is observed. The implanted samples annealed at 850 °C show reduced PL linewidth and enhanced integrated PL intensity compared to the implanted samples annealed at 750 °C. © 2007 American Institute of Physics. [DOI: 10.1063/1.2710006]

InAs quantum dots (QDs) grown on InP substrates are promising active materials for high performance optoelectronic devices operating at long wavelength region $(1.3-2 \ \mu m)$ and suitable for optical fiber communications. The self-assembled QDs grown by the Stranski-Krastanov growth mode have large fluctuations in size, shape, strain, and composition leading to inhomogeneous broadening of the photoluminescence (PL) emission. But narrower PL emission is required for device applications. Narrower PL linewidth as well as blueshift of the PL emission was reported in rapid thermally annealed InGaAs/GaAs QDs (Ref. 1) and InAs/GaAs QDs.² Enhancements in the integrated PL intensity were also observed in the annealed InAs/GaAs QDs covered with a SiO₂ film.^{2,3} However, Babiński et al.⁴ reported the disappearance of InAs/GaAs QDs at a temperature of about 800 °C due to strain-enhanced lateral In/Ga interdiffusion. InAs QDs suffer 7.2% lattice mismatch on GaAs substrates. InAs QDs have less lattice mismatch of 3.2% on InP substrates resulting in relatively large InAs QDs with a thin wetting layer [~1.5 ML] (Ref. 5). Chia et al.⁶ performed rapid thermal annealing of InAs/InP QDs from 600 to 900 °C and did not observe any disappearance of the QDs. However, they reported a large thermal energy shift (more than 325 meV) due to annealing of the InAs/InP QDs at 900 °C. Girard et al.⁷ performed thermal annealing of InAs/InP QDs at 650-800 °C for 210 s and reported an increase in the PL energy up to 120 meV revealing poor thermal stability of the InAs/InP QDs. None of these groups grew InAs/InP QDs containing thin GaAs interlayers but a thin GaAs interlayer has been reported to improve the optical and structural qualities of InAs/InP QDs.^{8,9} In this letter, we present the results of rapid annealing of the InAs/InP QDs containing a thin GaAs interlayer at temperatures ranging from 750 to 900 °C for 30 s. Our results show that the GaAs interlayer improves the thermal stability of the InAs/InP QDs. Our results also show that high temperature annealing reduces the PL linewidth and enhances the integrated PL intensity and thereby improves the uniformity and the optical quality of the InAs/InP QDs.

To monolithically integrate active/passive components of optoelectronic devices, selective and controllable spatial band gap tuning is necessary. Post growth OD intermixing can increase the band gap energy and reduce the PL linewidth. Ion implantation-induced QD intermixing has many advantages, such as compatibility with planar processing and reproducibility. The degree and depth of intermixing can be selectively controlled by choosing right implant conditions such as ion dose, ion energy, implantation temperature, and mask profile. In this letter, we also report phosphorous ion implantation at room temperature directly over the QD region followed by high temperature (750–850 °C) rapid thermal annealing of the samples. We observe a large implantation-induced energy shift of up to 309 meV and show that high temperature annealing does not reduce differential band gap energy shift significantly owing to good thermal stability of the QDs. Our results also show that high temperature annealing improves the integrated PL intensity and reduces the PL linewidth of the P ion implanted InAs/InP QDs.

InAs QDs are grown on semi-insulating (100) InP substrates using horizontal flow metal-organic chemical vapor deposition at a pressure of 180 mbar. Trimethylindium, trimethylgallium, PH₃, and AsH₃ are used as precursors, and H₂ as the carrier gas. First a 700 nm InP layer is grown at 650 °C. To minimize the As/P exchange reaction a 0.6 nm thick GaAs interlayer is also grown at 650 °C before the growth of a QD layer. The growth temperature is then reduced to 520 °C and the InAs QDs are deposited for 6.5 s at the rate of 0.16 nm/s. The QDs are immediately capped without any growth interruption with a 300 nm InP layer while the temperature is increased to 650 °C. Atomic force microscopy on uncapped QDs and plan-view transmission electron microscopy on capped QDs are carried out to measure the height and size of the QDs. The QDs have a mean height of 8.2 nm and a mean diameter of 25 nm.

The QD samples are implanted at room temperature with P ions with doses of $5 \times 10^{11} - 5 \times 10^{13}$ ions/cm². The energy of the ions is 450 keV which places the damage peak directly over the QD region. The implantation is carried out at 7° off the $\langle 100 \rangle$ direction to minimize channeling effects. The un-

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FIG. 1. (a) Photoluminescence (PL) spectra measured at 77 K of the asgrown and the as-grown and annealed InAs/InP quantum dots (QDs). The QDs are annealed at 750, 800, 850, and 900 °C for 30 s. A Gaussian fit of the PL spectrum of the QDs annealed at 850 °C is shown by the line with closed circles. (b) The full widths at half maximums (FWHMs) of the PL spectra and the integrated PL intensities of the QDs annealed at different temperatures. The data for the as-grown QDs are plotted for 650 °C due to their thermal treatment at this temperature during growth.

implanted as-grown samples are annealed for 30 s at 750, 800, 850, and 900 °C in argon ambient using a rapid thermal processor. The implanted samples are annealed at either 750 or 850 °C for 30 s to study the effects of annealing temperature on the implanted samples. To protect the top InP layer from the excessive loss of P atoms during annealing, the samples are sandwiched between two fresh pieces of semiinsulating InP substrates. Photoluminescence (PL) measurements are carried out at 77 K using the 532 nm line of a frequency doubled diode pumped solid state laser. The PL signal is collected by a thermoelectrically cooled InGaAs photodetector with a built-in preamplifier after dispersing through a 0.5 m monochromator.

The PL spectra of the as-grown sample and the as-grown samples annealed at 750, 800, 850 and 900 °C for 30 s are shown in Fig. 1(a). The PL spectrum of the sample annealed at 850 °C has a dip at 0.895 eV due to atmospheric absorption. A Gaussian fit of this spectrum is shown by the line with closed circles and used to calculate the PL peak energy and the full width at half maximum (FWHM). The thermal energy shift for annealing at temperatures less than 800 °C is very small. Salem *et al.*⁵ reported no significant energy shift in their InAs/InP quantum stick samples annealed at 650 °C but reported a large energy shift for annealing temperatures higher than 700 °C. In our case, the thermal energy shift for the QD samples annealed at 850 °C is 54 meV, much smaller than the energy shift observed by Chia et al. in the InAs/InP QDs annealed at the same temperature.⁶ Chia et al. grew InAs QDs directly on InP buffers without growing any GaAs interlayer between the QD layer and the InP buffer layer. They observed prominent increase in the thermal energy shift as annealing temperature increased up to 900 °C. In our case, the thermal energy shift for the QD sample an-Downloaded 24 Feb 2008 to 150.203.178.41. Redistribution subject to AIP license or copyright; see http://apl.aip.org/apl/copyright.jsp



FIG. 2. The implantation-induced energy shift and the full width at half maximum (FWHM) of the photoluminescence (PL) spectrum of the InAs/InP quantum dots (QDs) implanted with various P ion doses. The FWHMs of the PL spectra of the unimplanted and annealed QDs are also shown (by solid arrows) for comparison. All samples are annealed at either 750 or 850 °C for 30 s.

nealed at 900 °C is quite large (204 meV) but it is less than the thermal energy shift (more than 325 meV) of the similar samples grown by Chia et al. We believe that a thin GaAs interlayer minimizes the As/P exchange reaction and improves the thermal stability of the InAs/InP QDs. GaAs is under tensile strain due to 3.7% lattice mismatch on InP substrates and can partially compensate the compressive strain of the InAs QDs. It should reduce the strain gradient for interdiffusion leading to better thermal stability. Similar effect was observed in the InGaAs/GaAs QDs grown with GaP strain compensation layers and was attributed to the partial compensation of the strain in the QDs by the tensilestrained GaP layer.¹⁰

The large thermal energy shift observed in our samples at 900 °C is due to material degradation. We have observed a rough surface after annealing at 900 °C due to P desorption from the InP cap layer. This P outdiffusion creates defects leading to strong intermixing in the QDs. As a result of material degradation, the integrated PL intensity, decreases and the FWHM increases as shown in Fig. 1(b). But the samples annealed at temperatures lower than 900 °C show much enhanced integrated PL intensity compared to the as-grown sample, as shown in Fig. 1(b). Similar increase in the integrated PL intensity was observed in annealed InAs/InP QDs⁶ and InAs/InP quantum sticks.³ Rapid thermal annealing may decrease nonradiative recombination centers leading to an increase in the PL intensity. Since growth temperature rises to 650 °C during the InP cap layer growth, the FWHM and the integrated PL intensity for the as-grown sample are plotted for 650 °C in Fig. 1(b). In addition to the increase in the integrated PL intensity, the FWHM decreases to 64 and 63 meV for the samples annealed at 800 and 850 °C, respectively, from the FWHM of 70 meV for the as-grown sample. It shows that high temperature annealing also decreases the overall variation in OD size, strain, and composition profile.

Figure 2 shows the implantation-induced energy shifts of the QDs annealed at either 750 or 850 °C or for various implantation doses. The implantation-induced energy shift is defined as the difference between the PL energy peaks of the implanted and as-grown (unimplanted) samples which are annealed at the same temperature. The implantation-induced energy shift increases with increasing implantation dose up to the dose of 5×10^{12} ions/cm² irrespective of annealing temperature. The implantation-induced energy shift for the QD sample implanted with a dose of 5×10^{12} ions/cm² and



FIG. 3. The integrated photoluminescence (PL) intensity of the InAs/InP quantum dots (QDs) implanted with various P ion doses. The integrated PL intensities of the unimplanted and annealed samples (only annealed) are also shown for comparison. The QDs are annealed at either 750 or 850 °C for 30 s.

annealed at 750 °C for 30 s is 309 meV (400 nm). For the doses more than 5×10^{12} ions/cm² the implantation-induced energy shift starts to decrease from the maximum value. As the implantation dose increases, more complex defect clusters start to form. As a result the implantation-induced energy shift reduces due to the reduction in the availability of point defects which cause intermixing. The thermal energy shift of the sample annealed at 850 °C is 46 meV more than that of the sample annealed at 750 $^{\circ}$ C as can be seen in Fig. 1(a). Hence the implanted samples annealed at 850 °C show reduced implantation-induced energy shift compared to the implanted samples annealed at 750 °C. However, for low ion doses (less than 10^{12} ions/cm²), the implantation-induced energy shifts in the samples annealed at 850 °C are similar to the shifts in the samples annealed at 750 °C. This shows that although the higher annealing temperature causes larger thermal energy shift, high temperature annealing initiates more implantation-induced intermixing when the implantation dose is low.

The FWHM of the samples implanted with various doses and annealed at either 750 or 850 °C are also shown in Fig. 2. The values of the FWHM of the unimplanted QDs annealed at either 750 or 850 °C are shown (by solid arrows) for comparison. It is interesting to note that for low doses the FWHM for the implanted and annealed sample decreases noticeably compared to that of the annealed but unimplanted QD sample. Ion implantation-induced intermixing reduces the overall variation in QD size, shape, strain, and composition profiles of the QDs. The FWHM continues to decrease up to the implantation dose of 1×10^{13} ions/cm², but for higher doses the FWHM increases. As noted earlier, at higher doses more residual defects remain in the samples even after annealing leading to nonuniform intermixing. Similar effect was observed by Salem et al. in InAs/InP quantum stick samples.⁵ The FWHM for the sample annealed at 850 °C is less than that for the sample annealed at 750 °C for each dose. As noted earlier, high temperature annealing initiates more intermixing and thereby improves the uniformity in shape, size, strain, and composition of the QDs leading to further reduction in the FWHM.

In Fig. 3, we compare the integrated PL intensity of the QD samples implanted with various doses and annealed at either 750 or 850 °C. Compared to the as-grown and annealed (unimplanted) samples, the implanted samples annealed at 750 °C show continuous decrease in the integrated

PL intensity as the implantation dose increases. The PL intensity does not recover well due to insufficient removal of nonradiative defects as the implantation dose increases. High temperature annealing is required to remove these defects more efficiently. As shown in Fig. 3, although the integrated PL intensity for the as-grown (unimplanted) sample annealed at 850 °C is less than that of the as-grown (unimplanted) sample annealed at 750 °C, the integrated PL intensities for the implanted samples annealed at 850 °C are greater than those of the implanted samples annealed at 750 °C for all implantation doses. In general, the PL intensity recovery is not good for doses higher than 5×10^{12} ion/cm². As pointed out earlier, defects are created directly into the QD layer due to the ion energy used in this experiment. For higher doses, too many defects are created into the QD layer and annealing even at high temperature cannot remove them completely. If the damage peak is not created into the QD layer, the PL intensity recovery could be better. However, the damage peak created too far away from the QD layer may result in very little or no intermixing.

In conclusion, we have studied the effects of high temperature annealing of the InAs/InP QDs containing a thin GaAs interlayer. These QDs show good thermal stability up to 850 °C. The rapid thermally annealed QD samples show enhanced integrated PL intensity. The OD samples annealed at high temperatures (800 and 850 °C) show reduced FWHM which is attractive for QD based optoelectronic devices. We have also studied the effects of high temperature annealing on the P ion implantation-induced intermixing of the InAs/InP QDs. The QD sample implanted with 5 $\times 10^{12}$ ions/cm² and annealed at 750 °C shows the implantation-induced energy shift of up to 309 meV (400 nm). High temperature annealing does not reduce the implantation-induced component of energy shift by large extent; it actually initiates additional ion implantation-induced intermixing when the implantation dose is low. The implanted samples annealed at 850 °C show reduced FWHM and enhanced integrated PL intensity compared to the implanted samples annealed at 750 °C. Our results show that high temperature annealing after P ion implantation could be beneficial for selective band gap tuning in InAs/InP QD system paving the way to monolithically integrate QD based optoelectronic devices.

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