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Steady State Distribution of a Hyperbolic Digital TanLock Loop with Extended Pull-in Range for Frequency Synchronization in High Doppler Environment

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Abstract

A hyperbolic arctan based Digital Tanlock Loop (D-TLL) operating with complex signals at base-band or intermediate frequencies in high Doppler environments is treated here. The arctan based loop, known as the tanlock loop (TLL), is used in software defined radio architectures for frequency acquisition and tracking. The hyperbolic nonlinearity intentionally introduced within the phase detector extends the pull-in range of the frequency for a given loop, compared to the normal D-TLL, allowing a wider frequency acquisition range which is suitable for high Doppler communications environment. In this paper we study the steady state phase noise performances of such a feedback loop for additive Gaussian noise using stochastic analysis. The stochastic model of a first-order hyperbolic loop and the theoretical analysis for the corresponding statistical distribution of the closed loop steady state phase noise are presented. The theoretical results are also verified by simulations.

Index Terms

Digital Tanlock Loop, D-TLL, arctan, hyperbolic loop, steady state distribution, steady state phase noise, pull-in range, Frequency Synchronization

I. INTRODUCTION

Synchronization of carrier frequency and phase is a well know problem in the field of communications [26]-[35], in this paper we address the phase synchronization problem using the tan locked loops. Analog and Digital, Tanlock Loops (TLL) are extensively used in many applications such as

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communications, radar, sonar, navigation etc, for controlling and tracking signals. In this paper we consider the usage of such loops for frequency synchronization in software implemented digital receivers. The treatment and noise analysis of the loops for such applications with the arctan based phase detector [1]-[8] (TLL) and the sinusoidal based phase detector (traditionally known as the phase-locked loop) [12]-[24] have been treated well in literature. The major difference between the two is that, the 'sine' based loop shows different loop characteristics to the 'arctan' based loop due to their individual functional behaviors. The 'arctan' based TLL is known to have extended tracking capabilities in general compared to the 'sine' based loops [1],[16]. The noise analysis for the 'sine' based loop is well treated in literature, namely [13]-[21], and likewise the noise analysis of the TLL is also well treated in [1]-[2]. In the recent years, variations to the traditional TLL by incorporating an additional delay in the loop (time delay tanlock loop - TDTL) is also investigated in [3], [4]. In this paper, we are interested in improving the acquisition performance of the TLL by considering an additional nonlinear signal processing block placed within the loop. The model we consider here is a hyperbolic nonlinear model together with an arctan function [10]-[11], which is different from the above mentioned 'arctan' only and 'sine' only models. Other nonlinear loop models [9] such as the 'logarithmic' model is also presented in [8] for TLL. From the models presented in [9], the hyperbolic model shows improved pull-in range [10]-[11] by trading off with the phase noise performances, hence our interest in this model, and we investigate the loop further in this paper. For applications with unknown frequency variation, with a high dynamic range caused due to heavy Doppler in the received signal, increased acquisition-range is quite important for accurate frequency synchronization. The hyperbolic loop may be used in such situations with careful considerations of the steady state phase noise which we present in this paper. Here we study the steady state statistical distribution of the hyperbolic loop and compare and contrast the phase noise performances with the normal D-TLL and the 'sine' phase detector based phase locked loop in the simulations for additive Gaussian noise. This would allow system engineers to understand the degradation in the phase noise performances of the hyperbolic loop and decide whether it is acceptable to deploy such loops in their systems. The stochastic model

3

that we develop here for a first-order hyperbolic loop results in a first-order Markov process, and by using this Markov model we seek solutions for the steady state statistical distribution for the phase noise within the loop. The solution for the phase noise distribution involves in computing a hard to solve integral for which we adopt numerical techniques to perform the computation.

In Section-II we present the complex signal model that we consider here, the hyperbolic nonlinearity based TLL and its corresponding loop design. In Section-III we analyse the statistical properties of the hyperbolic phase detector when it operates under additive white Gaussian noise. In Section-IV we derive the first-order Markov process corresponding to the first-order loop presented in section-II. The calculation of the steady state distribution and the corresponding simulation results are presented in Sections-V and VI respectively. Sections-VII and VIII present the steady state phase jitter performance of the hyperbolic loop and the extended pull-in characteristics, respectively, and finally in section-IX we present two classical synchronization examples where the hyperbolic loop can be used for frequency synchronization and its advantages in real life.

II. LOOP MODEL

The generalised loop model of a typical Digital-TLL has an error detector (phase detector), a loop filter and a numerically controlled oscillator known as the NCO. The NCO is the equivalent of the voltage controlled oscillator (VCO) in its analog counterpart. In our model, the phase detector is a four quadrant arctan(.) function that maps the input argument onto the four quadrant phase plane followed by a hyperbolic function g(.). For a standard D-TLL the g(.) function is only a gain factor. A detailed block diagram of our loop design is depicted in Figure-1. In Figure-1 the thicker arrow lines denote complex signals (real and imaginary) and the thinner arrow lines denote real signals. Further, D(z) and V(z) are the transfer functions of the loop filter and the NCO, respectively.

The discrete complex single-tone signal r input to the loop with frequency f, amplitude A, and an arbitrary phase shift of β , corrupted with additive white Gaussian noise (AWGN), is given by,

$$r[n] = A \exp\{2\pi j f T_s n + j\beta\} + \eta[n] \tag{1}$$

For the signal model we assume a line of sight scenario with a slowly fading (constant) amplitude A and a slowly varying (constant) phase β , which are true for an equalized channel in a slow flat fading environment. In (1), T_s is the sampling period of the discrete signal, with $f_s = 1/T_s$ being the sampling frequency, and η is the complex Gaussian noise process. The Gaussian noise is expressed by its in-phase and quadrature components as,

$$\eta = n_c + jn_s \tag{2}$$

where, n_c and n_s are two discrete independent Gaussian random processes with zero mean and σ^2 variance with a double sided power spectral density of N_0 (Watts/Hz) each. At this point we define the signal to noise ratio as $SNR = A^2/\sigma^2$. The output signal x from the NCO is given by,

$$x[n] = \exp(-j\theta[n]) \tag{3}$$

where, $\theta[n]$ is the n^{th} sample from the NCO. The received signal r is multiplied by x using a complex multiplier to generate an error signal e. The output of the complex multiplier e is then input to the arctan function to estimate the phase difference between the received and the local signals, that is,

$$\varphi_e = \arctan(Im\{e\}/Re\{e\}) \tag{4}$$

where, $Im\{.\}$ and $Re\{.\}$ are the imaginary and real parts of the complex signal respectively. The estimated phase error φ_e is then input to the hyperbolic function g(.) to generate φ_h ,

$$\varphi_h = \sinh(\varphi_e) \tag{5}$$

where, g(.) = sinh(.). We define $\phi[n]$ as the actual phase difference between the local and the received signals, which is given by,

$$\phi[n] = \theta_{in}[n] - \theta[n] \tag{6}$$

where, θ_{in} is given by,

$$\theta_{in}[n] = 2\pi f T_s n + \beta \tag{7}$$

We also present here the linear model of the D-TLL in order to compare the hyperbolic loop. In the absence of noise, the output of the arctan function gives a precise estimate of the phase difference between $-\pi$ and π (that is $\varphi_e = \phi$), this is also valid for very little noise, or very high SNR. In the ideal case (in the absence of noise) a linear model is sufficient to precisely define the D-TLL. Such a linear model is depicted in Figure-2. The corresponding closed loop transfer function H(z) defining the linear loop (in Figure-2), is given by,

$$H(z) = \frac{D(z)V(z)}{1 + D(z)V(z)}$$
(8)

We note here that the linear model of the 'sine' phase detector based loop is also similar to (8) [16], [13]. Since we are interested only in a first-order loop the loop filter is considered to be of unity gain. The NCO transfer function V(z) is given by,

$$V(z) = \frac{k}{z - 1} \tag{9}$$

where, k is the NCO parameter that controls the loop, or equivalently considered as the closed loop gain of the first-order loop. Finally, we define the term closed loop bandwidth for the linear loop, as [13],

$$2B_L = \frac{B_i}{2\pi j} \oint_{|z|=1} H(z) H^*(1/z^*) z^{-1} dz$$
(10)

where, B_i is the noise equivalent bandwidth of the input signal. The loop bandwidth is used to conveniently define the steady state phase jitter performance of the linear loop, we present this here in order to compare and contrast the analytical results of the hyperbolic loop model given in Figure-1 with the linear model given in Figure-2. A table of solutions for the contour integral in (10) can be found in [13] for different orders of loop.

III. PHASE DETECTOR CHARACTERISTICS

The statistical properties of the hyperbolic arctan based phase detector are important to derive a stochastic model for the nonlinear loop considered here. In this section we present the statistical properties of the hyperbolic phase detector. Let us define α as,

$$\alpha = \sinh(\arctan\{Im\{\kappa\}/Re\{\kappa\}\})$$
(11)

where, $\kappa = r$, (*r* is given by (1)) with f = 0. It should be noted here that the characteristics of α is different from φ_h (given by equation (4)), where the former is an open loop estimate and the latter is a closed loop estimate of the parameter of interest. Then from [10], [11], the statistical distribution $f_{\alpha}(\alpha; \mu_1, \mu_2)$ of α is given by,

$$f_{\alpha}(\alpha;\mu_1,\mu_2) = \cosh(\alpha)\Gamma_{\alpha}(\alpha) \tag{12}$$

where, $\Gamma_{\alpha}(\alpha)$, μ_1 and μ_2 are given in (13), (14) and (15). (Please note the typo error in [10] for the expression Λ . Expression (14) is the corrected one)

$$\Gamma_{\alpha}(\alpha) = \begin{cases} \exp(-\varsigma/2) \left[\frac{1}{2\pi} - \exp\left(\frac{\Lambda_{1}}{2}\right) \left(\frac{\Lambda_{1}}{2\pi}\right)^{\frac{1}{2}} Q\left(\sqrt{\Lambda_{1}}\right) \right] & \text{for } 0.7325\pi < \alpha \le 3.6761\pi \\ \exp(-\varsigma/2) \left[\frac{1}{2\pi} + \exp\left(\frac{\Lambda}{2}\right) \left(\frac{\Lambda}{2\pi}\right)^{\frac{1}{2}} \left(1 - Q\left(\sqrt{\Lambda}\right)\right) \right] & \text{for } -0.7325\pi < \alpha \le 0.7325\pi \\ \exp(-\varsigma/2) \left[\frac{1}{2\pi} - \exp\left(\frac{\Lambda_{2}}{2}\right) \left(\frac{\Lambda_{2}}{2\pi}\right)^{\frac{1}{2}} Q\left(\sqrt{\Lambda_{2}}\right) \right] & \text{for } -3.6761\pi < \alpha \le -0.7325\pi \end{cases}$$

$$(13)$$

where,

$$\mu_{1} = A\sin(\beta), \\ \mu_{2} = A\cos(\beta), \\ \varsigma = \frac{\mu_{1}^{2} + \mu_{2}^{2}}{\sigma^{2}} = SNR, \\ \Lambda = \frac{\left[\tan\left(\alpha\right)\mu_{1} + \mu_{2}\right]^{2}}{\left[1 + \tan^{2}\left(\alpha\right)\right]\sigma^{2}} \quad (14)$$

$$\Lambda_{1} = \frac{\left[\tan\left(\pi + \alpha\right)\mu_{1} + \mu_{2}\right]^{2}}{\left[1 + \tan^{2}\left(\pi + \alpha\right)\right]\sigma^{2}} \\ \Lambda_{2} = \frac{\left[\tan\left(-\pi + \alpha\right)\mu_{1} + \mu_{2}\right]^{2}}{\left[1 + \tan^{2}\left(-\pi + \alpha\right)\right]\sigma^{2}} \\ \text{and} \\ Q(x) = \frac{1}{2\pi} \int_{x}^{\infty} \exp\left(-u^{2}/2\right) du \quad (15)$$

Now, let us rewrite α as,

$$\alpha = \alpha_m + \nu \tag{16}$$

where, α_m is the expected value of the random variable α , given by,

$$\alpha_m = \int_{-\pi_0}^{\pi_0} \alpha f_\alpha(\alpha) d\alpha \tag{17}$$

with $\pi_0 = 3.6716\pi$, and ν is a time varying process. The random process ν has similar statistical properties as α but with $\mu_1 = 0$ and $\mu_2 = 1$ (where, μ_1 and μ_2 are defined in (14)). That is, ν is a zero mean random process with the statistical distribution given by $f_{\alpha}(\alpha; 0, 1)$. Figure-3 shows the hyperbolic open loop phase noise distribution for various SNR levels. The distribution of the hyperbolic phase noise, similar to any other noise models, becomes wider as the SNR reduces. However, when the mean input phase is changed, while the SNR is a constant, the distribution shows some significant variations. Figure-4 shows the variation in the distribution when the input phase is changed. For higher input phase values (the input phase is β in this case) the distribution becomes wider as expected, and this can be verified by considering the characteristics of the hyperbolic function given by (5).

Further, the combined (arctan and hyperbolic) phase detector gives a mean output error when additive noise is present in the input signal. That is $\alpha_m \neq \sinh(\beta)$, rather, it has a non-unity d.c. gain associated with it, especially when the SNR is low. By considering the d.c. gain of the hyperbolic phase detector [10], [11] we can write the expected value of the output phase α_m as,

$$\alpha_m = K_p \sinh(\beta) \tag{18}$$

where, K_p is the phase detector gain, given by,

$$K_p = \left(\frac{d\alpha_m}{d\alpha}\right)|_{\alpha=0} \tag{19}$$

It should be noted here that K_p is a function of the input SNR, and $K_p = 1$ for SNR > 18dB[10]. Now, we can re-write α as,

$$\alpha = K_p \sinh(\beta) + \nu \tag{20}$$

7

The variation of K_p and the characteristic curves with SNR for the hyperbolic phase detector can be found in [10], [11]. In the following section, the results in (20) is directly applied to the error signal e at the output of the complex multiplier in order to derive the stochastic model for the hyperbolic loop.

IV. STOCHASTIC MODELING

In this section we provide a stochastic model for the first-order feedback loop given in Figure-1 with D(z) = 1. The error signal *e* at the output of the multiplier is given by,

$$e[n] = A \exp\{j\phi\} + \tilde{\eta}[n]$$
(21)

where,

$$\tilde{\eta} = \exp\{-j\theta[n]\}\eta[n] \tag{22}$$

It is rather straight forward to verify that $\tilde{\eta}[n]$ has similar statistical properties as the original noise process $\eta[n]$ given by (2). i.e. $\tilde{\eta}[n] \sim CN(0, \sigma^2)$, where CN(a, b) denotes a complex Gaussian distribution with mean a and variance b. Then, by using equations (5) and (20), we can rewrite the process $\varphi_h[n]$ at the output of the hyperbolic phase detector as,

$$\varphi_h = K_p \sinh(\phi[n]) + \chi[n] \tag{23}$$

where, $\chi[n]$ is statistically similar to ν described in equation (16). Then, by using the time domain expression for the NCO given by (9) and along with equations (5), (6) and (20), one could arrive at the following first-order stochastic difference equation for the phase error process $\phi[n]$ as,

$$\phi[n+1] = \phi[n] - K_p k \sinh(\phi[n]) + \Omega + k\chi[n]$$
(24)

where, $\Omega = 2\pi fT_s$. Further, the steady state phase error φ_{ss} , the value of $\varphi_e[n]$ as $n \to \infty$, can be derived from (24) by letting $\varphi_e[n] = \varphi_e[n-1] = \varphi_{ss}$ for $SNR = \infty$. Thus, we get,

$$\varphi_{ss} = \sinh^{-1}(\Omega/k) \tag{25}$$

The corresponding linear loop given in Figure-2 has a steady state phase offset of $\varphi_{ss-l} = \Omega/k$. As we see, the first-order loop has a non-zero steady state phase error, increasing with the received frequency f, imposing an upper limit on the maximum acquirable frequency for a frequency step input. This maximum acquirable frequency is known as the pull-in frequency $f_{pull-in}$ and is given by letting the steady state phase error to $\varphi_e = \pi$. Thus for the hyperbolic loop we get a higher pull-in frequency than the linear loop, which is the main attraction of the hyperbolic loop. In other words, a specific pull-in range could be achieved by the hyperbolic loop with a smaller value of k compared to the D-TLL. In the following sections we analyse the steady state phase noise performance of the hyperbolic loop by computing the steady state distribution, and compare the phase jitter performances with the D-TLL.

V. STEADY STATE CLOSED LOOP PHASE ERROR DISTRIBUTION: FIRST ORDER LOOP

The steady state statistical distribution of the phase error process (phase noise) is presented here for the first-order loop described in (24). We adopt similar techniques used by Aaron in solving for the steady state distribution for a non-uniformly sampled digital phase-locked loop [12] that has a sinusoidal type of phase detector characteristics. From (24) we clearly see that $\phi[n]$ is a discrete time continuous variable Markov process of first order. The statistical distribution of such a process satisfies the Chapman-Kolmogrov equation [25] for a given initial phase error of $\phi[0] = \phi_0$, which is given by

$$P_{n+1}(\phi|\phi_0) = \int_{-\pi_0}^{\pi_0} Q_n(\phi|z) P_n(z|\phi_0) dz$$
(26)

where,

$$P_n(z|\phi_0) = \sum_{m=-\infty}^{\infty} p_n(z + 2m\pi_0|\phi_0)$$
(27)

Since $\phi[n]$ and $\chi[n]$ are independent (from (24)), the transition probability follows the distribution,

$$Q_n(\phi|z) = \frac{1}{k} \sum_{m=-\infty}^{\infty} f_\alpha(\frac{\phi}{k} + 2m\pi_0; A\sin(u), A\cos(u))$$
(28)

where, u is given by,

$$u = z - K_p k \sinh(z) + \Omega \tag{29}$$

In (27), $p_n(\phi|\phi_0)$ is the distribution of $\phi[n]$ given the initial condition ϕ_0 , and in (28), $Q_{n-1}(\phi|z)$ is the distribution of the transition probability of $\phi[n]$ given that $\phi[n-1] = z$, which is periodic with a period of $2\pi_0$. The integral in (26) is nontrivial to solve in order obtain a closed form solution, therefore we seek numerical techniques to solve the integral. Then, by using (26), and starting with $P_0(z|\phi_0) = \delta(z - \phi_0)$ for $\phi_0 = 0$, where $\delta(z)$ is the dirac-delta function, we iteratively compute the statistical distribution of the phase error process $\phi[n]$, by letting n=0,1,2... until the successive distributions for $\phi[n]$ shows negligible differences (i.e. distribution reaching the steady state). The values of m for the summation in the integration process used here were m = -4, 3...3, 4, and the required number of iterations to compute the steady state distribution from the C-K integral in (26) depends on the number of samples required for the loop to reach steady state, this again depends on the value of k. For example, for k=1 the loop theoretically achieves steady state with the first iteration, therefore the number of iterations to compute the steady state distribution from (26) is also one. In the following section we provide some simulation results to verify the theoretical analysis performed here.

VI. SIMULATION RESULTS

In this section we present the simulation results for the hyperbolic loop, and compare the simulated steady state distribution with the theoretically computed distribution from (26). Figure-5 shows the simulated and the theoretical distributions for the phase error process. From the results we see the theoretical distribution match closely with the distribution obtained from simulations. In the figure, two cases with different SNR, f, and k values are presented to verify our analytical model. Figure-6 depicts the steady state distribution for different values of k for SNR = 7dB. Again, we see the simulated results match the theoretical results as shown in the figure. From Figure-6, we also see how the steady state distribution flattens out with increasing k giving rise to higher phase noise within the loop. Therefore, it is quite important to maintain low values for

k to control the phase noise, but as we see from Section-VIII reducing k theoretically reduces the pull-in range of the loop. However, by using the hyperbolic loop instead of the D-TLL one could reduce k without reducing the required pull-in range of the loop, which is the main advantage of using the hyperbolic loop. We explain this further with a system example in Section-IX.

VII. STEADY STATE PHASE JITTER: FIRST ORDER LOOP

The steady state phase jitter (the square root of the phase error variance) is a key parameter describing the synchronization degradation in designing a communication system. Due to the random phase disturbances in the synchronized signal the probability of bit error in digital communications degrades and is usually quantified using the value of phase jitter. Here we present the steady state phase jitter of the hyperbolic loop and compare the performances with the D-TLL and the 'sine' phase detector based loop. The theoretical expression for the phase jitter of the linear loop is given by [13],

$$\sigma_{\phi} = \sqrt{N_0 B_L} \tag{30}$$

where, $B_L = f_s k/(2-k)$. The phase jitter for the hyperbolic loop is computed by calculating the variance from the steady state distribution obtained from the C-K integral. Figure-7 depicts the phase jitter performances of the hyperbolic loop. From the figure we see that the simulation results closely match with the theoretical results. Figure-8 compares the phase jitter performance of the hyperbolic loop with the D-TLL and the 'sine' phase detector based loop. The degradation in the steady state jitter performances of the hyperbolic loop can be seen from this figure, especially for SNR < 13dB. This is the major drawback of the hyperbolic loop compared to the D-TLL. However, using the extended pull-in range property of the hyperbolic loop, the NCO constant k can be reduced in order to achieve better jitter performances, we explain this in a later section as an example.

VIII. EXTENDED PULL-IN FEATURES OF THE HYPERBOLIC D-TLL: NOISELESS ANALYSIS

In this section we analyse the extended pull-in capability of the hyperbolic loop for the noiseless case. For the D-TLL the pull-in range is found by letting $\phi_{ss-l} = \pi$ and obtaining an expression for f, resulting in,

$$f_{pull-in-DTLL} = kf_s/2 \tag{31}$$

Likewise, the theoretical pull-in range of the hyperbolic loop is obtained by letting $\phi_{ss} = \pi$, resulting in,

$$f_{pull-in-Hyp} = \sinh(\pi)kf_s/(2\pi)$$
(32)

From (31) and (32), we see that the lock-in range of the hyperbolic loop is theoretically improved by a factor of 3.67 compared to the D-TLL. We show the corresponding results in Figure-9 by tracking a 910Hz signal with k = 0.1 at $f_s = 5kHz$. The figure shows the error signal within the loop, which should reach a steady state (constant) value after acquiring the received frequency. The theoretical maximum frequency acquirable by the D-TLL for the given values of k and f_s is 250Hz (from (31)), and as we see from Figure-9 the hyperbolic loop achieves lock for a frequency of 910Hz (that is, the error signal of the hyperbolic loop becomes constant after acquiring the received frequency whilst the other does not), which is almost 3.6 times the acquirable frequency of the D-TLL, this verifies the claim of having extended pull-in range when using the hyperbolic loop over the D-TLL. The figure also shows the 'sine' based loop which is unable to acquire the frequency for the given case.

A. Phase Plane Analysis of the Hyperbolic Loop

The acquisition performance of the first-order nonlinear loop is best analysed by considering the phase plane trajectories which are the possible solutions for the difference equation described in (24) in the absence of noise. The phase plane portrait is generated by plotting the phase error on the x-axis and the frequency error on the y-axis, with time. This shows us the convergence of the loop towards the equilibrium point corresponding to the steady state phase and the frequency error values. Here we show a few phase plane trajectories obtained using simulations for the noiseless case in order to understand the behavior of the nonlinear loop.

Figure-10 and Figure-11 show the phase plane trajectories for k = 1 and k = 0.8, respectively. The acquisition process of the signal frequency is well observed in the two figures with the frequency error eventually becoming zero and the loop attaining lock. The arrow markings shown in Figure-10 denote the directions of the trajectories starting at different frequency errors and converging towards a zero frequency error. The phase plane portrait presented in Figure-11 also follows similar directions as per the trajectories given in Figure-10. The differences in the acquisition processes between the D-TLL, 'sine' based loop, and the hyperbolic loop are clearly observed in Figure-11. From the figure, we see how the steady state phase error values for the hyperbolic loop are squeezed-in when compared with the D-TLL and the 'sine' based loop theoretically achieving a greater lock-in range. We also observe that the 'sine' loop diverges from the steady state point between $-\pi$ and π which is an indication of the first order 'sine' loop being unable to acquire the frequency. For higher values of loop gain (higher values of k), the hyperbolic loop is required for convergence. Such analysis using control theories for nonlinear loops are subjected to further studies and is beyond the scope of this paper.

IX. SYNCHRONIZATION SYSTEMS EXAMPLES USING THE HYPERBOLIC LOOP

In this section we provide two examples (two scenarios) to show how the hyperbolic loop outperforms the traditional D-TLL. In the first example, we fix the required pull-in range, or in other words we have a predefined value for the signal frequency pull-in range, and show how the steady state phase jitter could be reduced by reducing the loop gain using the hyperbolic loop. In the second example, we fix the steady state phase jitter to be a predefined value and improve the frequency pull-in range of the received signal using the hyperbolic loop. The former example is best suited for applications that require improved phase jitter performances (typically any mobile digital communication system), and the latter is best suited for receivers requiring extended pull-in

14

range capabilities (typically highly mobile communication links such as low earth orbital satellite communications or wireless communications on high speed trains at higher frequency bands).

A. Example-1:

In this example, let us define a required pull-in range of $\pm 2 \times 10^{-3}$ percent of the carrier frequency f_c . For $f_c = 2.4GHz$, the lock-in frequency requirement is then given by $\pm 48kHz$. Considering the feedback loops to be used at base-band, after the down conversion process, to track residual frequency error present in the received signal with a sampling frequency of $f_s = 4.8 \times 10^5 Hz$, the minimum required value of k for the D-TLL computed from equation (31) is given by k = 0.2. For the same scenario however, the hyperbolic loop requires a minimum value of k = 0.0544 to achieve the same pull-in range. Therefor, by reducing the NCO parameter k we can reduce the phase jitter within the loop. The improvement on phase jitter by using the hyperbolic loop is depicted in Figure-12 for k = 0.2 and k = 0.06. From the figure, we clearly see the improvement on the phase jitter performances. The system shows an improvement of around 6dB to 8dB above the threshold and around 1dB to 4dB of improvement below the threshold. The SNR improvement becomes higher when the required pull-in range increases further.

B. Example-2:

In the second example, for a specific system operating at SNR = 15dB let us define an acceptable phase jitter that the receiver could tolerate to be $\sigma_{\phi} = 0.05rad$. For the D-TLL, using Figure-12 we see that a value of k = 0.2 can achieve the required phase jitter performance. Using Figure-12 again, the same phase jitter performance also can be achieved using the hyperbolic loop for k = 0.2. The maximum acquirable frequency offset (Doppler frequency) for the D-TLL in this case is given by $\pm 48kHz$, which corresponds to a maximum transmitter-receiver relative speed of $v = 26000 kmhr^{-1}$ in a mobile communication environment. This is a typical LEO satellite system operating at Ku-band (20GHz). However, by using the hyperbolic loop, the maximum theoretical frequency offset (Doppler frequency) that could be achieved is $\pm 176kHz$, which then

can be used with a Ka-band (30GHz) communication link for the same LEO satellite system without any degradation in the phase jitter performances.

X. CONCLUSION

A hyperbolic nonlinearity based digital tanlock loop and its corresponding steady state and acquisition performances were presented for a first-order loop. The hyperbolic loop gives an extended pull-in range at the expense of the steady state phase jitter for a given loop gain when compared with a digital tanlock loop. However, we have shown that, by using the hyperbolic loop we could achieve equivalent or better phase jitter performance for a specific system by effectively reducing the loop gain. This is made possible by extending the pull-in range when using the hyperbolic loop.

A stochastic difference equation for the hyperbolic loop was presented and the corresponding steady state closed loop distribution of the phase noise was obtained using the Chapman-Kolmogrov equation. The theoretical results were verified using simulations. The steady state phase jitter of the hyperbolic loop was also analysed and was compared with the traditional digital tanlock loop and the 'sine' phase detector based phase locked loop. Finally, two classical examples were provided where the hyperbolic loop could improve the performances of the receiver in wireless communications.

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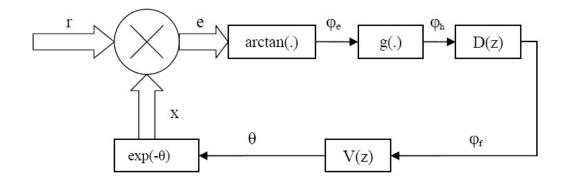


Fig. 1. Block Diagram of the complex signal based hyperbolic Digital Tanlock Loop

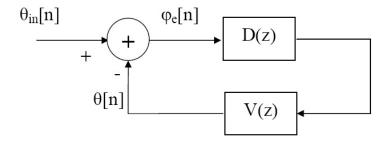


Fig. 2. Linear model of the D-TLL and the 'sine' phase detector based digital phase-locked loop in the absence of noise

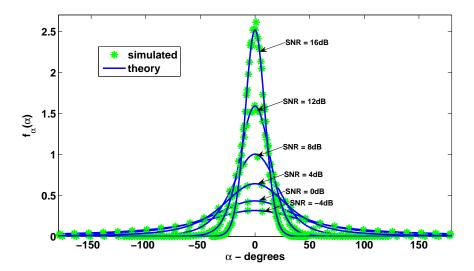


Fig. 3. Open loop hyperbolic phase noise distribution for various SNR, with $\beta = 0$

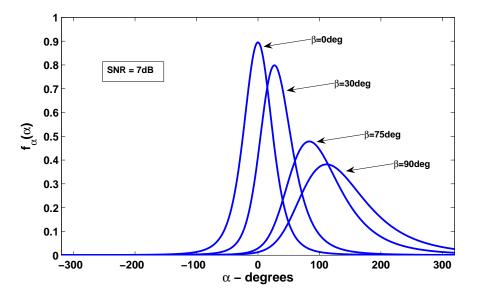


Fig. 4. Open loop hyperbolic phase noise distribution variation with β

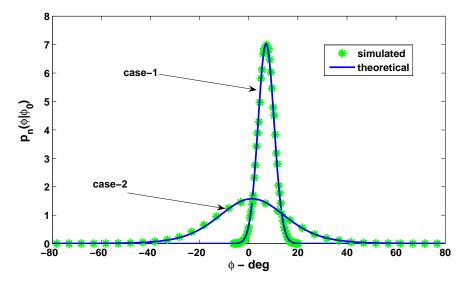


Fig. 5. Steady state phase error distributions for the first-order hyperbolic loop for, case-1: SNR = 25dB, k = 1, f = 200Hz, $f_s = 10kHz$, case-2:SNR = 10dB, k = 0.8, f = 20Hz, $f_s = 10kHz$

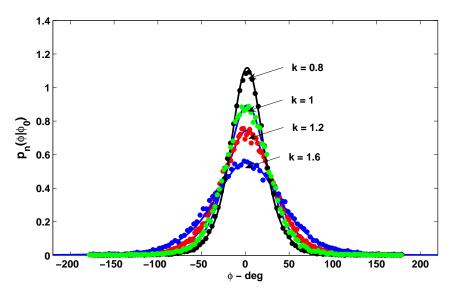


Fig. 6. Steady state phase error distribution for the first-order hyperbolic loop for f = 50Hz, $f_s = 10kHz$ and SNR = 7dB; - Theory; * simulation

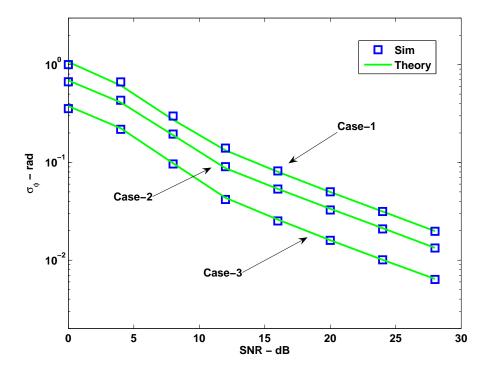


Fig. 7. Steady state phase jitter for the first-order hyperbolic loop; case-1: k = 0.6, f = 5Hz, $f_s = 500Hz$, case-2: k = 0.2, f = 10Hz, $f_s = 2000Hz$, case-3: k = 0.04, f = 5Hz, $f_s = 5000Hz$

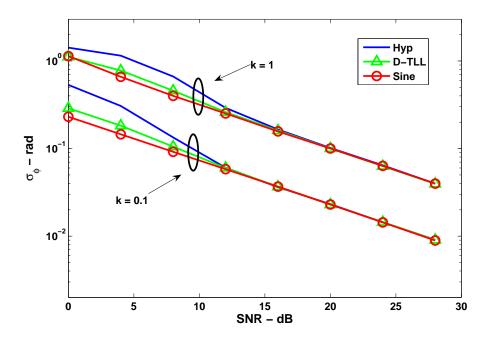


Fig. 8. Comparing the steady state phase jitter of the first-order hyperbolic loop with the Digital-TLL, and the 'sine' phase detector based PLL; f = 50Hz, $f_s = 50kHz$

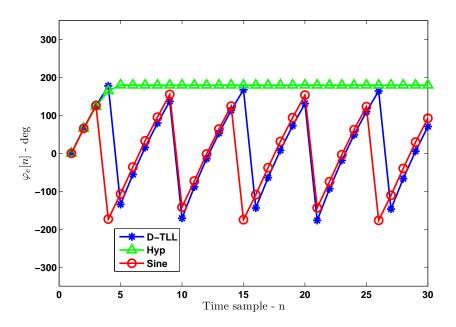


Fig. 9. Comparing the pull-in capability of the first-order hyperbolic loop with the Digital-TLL and the 'sine' based loop; $f = 910Hz, f_s = 5kHz$ and k = 1

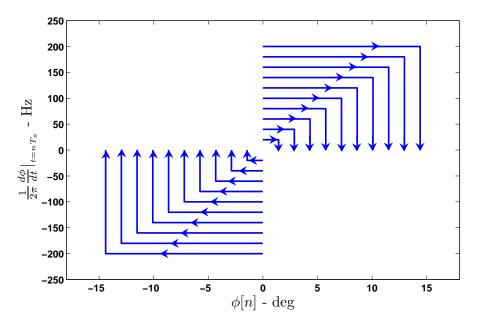


Fig. 10. Phase Plane Portrait: Hyperbolic loop with $f_s = 5kHz$ and k = 1

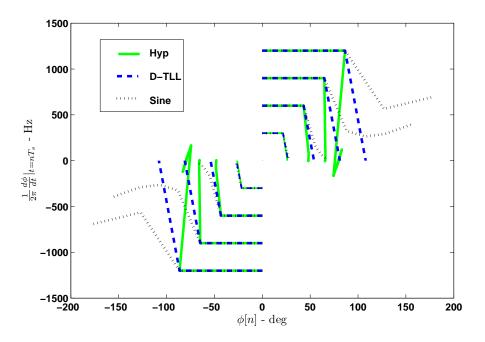


Fig. 11. Comparing the Phase Plane Portraits of the Hyperbolic, D-TLL, and the 'sine' phase detector based digital phase locked loop, with $f_s = 5kHz$ and k = 0.8

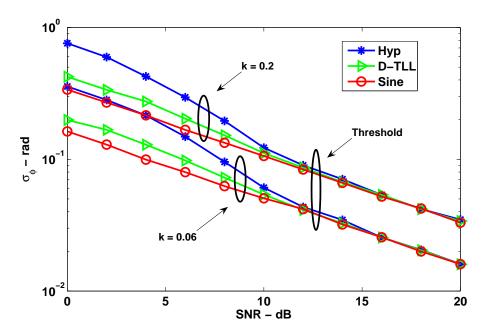


Fig. 12. Example-1: Performance improvement in steady state phase jitter using the hyperbolic loop, with $f_s = 4.8 \times 10^5$