Electrically Small Folded Slot Antenna Utilizing Capacitive Loaded Slot Lines

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Abstract — This paper presents an electrically small, coplanar waveguide fed, folded slot antenna that uses capacitive loading. Several antennas are fabricated with and without capacitive loading to demonstrate the ability of this design approach to reduce the resonant frequency of the antenna, which is analogous to reducing the antenna size. The antennas are fabricated on Cu-clad Rogers DuriodTM 6006 with multilayer chip capacitors to load the antennas. Simulated and measured results show close agreement, thus, validating the approach. The electrically small antennas have a measured return loss greater than 15 dB and a gain of 5.4, 5.6, and 2.7 dBi at 4.3, 3.95, and 3.65 GHz, respectively.

Index Terms — Miniature Antenna, Capacitive Loading, Coplanar Waveguide, Folded Slot Antenna.

I. INTRODUCTION

The size of commercial, wireless products continues to shrink, and as they do so, the range of applications integrated within the package increases. Other applications such as implanted, wireless, biomedical sensors and wireless sensors embedded within engines and machinery must also be small so as not to harm the patient or interfere with the machines' operation. This demand for small size has created the need for the development of small antennas. However, the radiation efficiency, directivity, and impedance match of electrically small antennas is limited [1].

Various methods to reduce the size of printed antennas have been reported, with most of the methods relying on the loading of the antenna. A microstrip fed slot radiator was reactively loaded to arbitrarily reduce the length of the slot radiator without affecting the impedance match [2]; a gain of -3 dBi and a fractional bandwidth of 0.034 % were demonstrated for an antenna of 0.2 X 0.2 λ_0 . A microstrip fed slot antenna with inductive and capacitive loading has been reported with 59% size reduction [3], but gain was not reported. A microstrip fed circular slot antenna that is capacitively loaded was demonstrated with a 23% size reduction [4]. In this paper, a coplanar waveguide (CPW) fed folded slot antenna with capacitive loading is reported for the first time to the best of our knowledge. Electromagnetic simulations and measured return loss and radiation patterns are presented to demonstrate the reduction in the antenna operation frequency by 22 % while maintaining a constant antenna size.

II. ANTENNA DESIGN AND SIMULATIONS



Figure 1: Schematic of standard CPW fed folded slot antenna.

A standard, CPW fed, folded slot antenna without capacitive loading is shown in Fig. 1. The antenna is comprised of a pair of slotline resonators that are folded such that they meet along the center line of the CPW. A virtual open circuit exists on the antenna where the slots meet at the y axis. The antenna is designed such that the mean path length of each of the folded slots is $\lambda_0/2$ if the parasitic reactance due to the 90 degree bends and T-junction is ignored. In the antennas presented here, the mean path length is 29 mm, so a frequency of 5.2 GHz is expected. The antenna dimensions, which are shown in Table 1, are optimized with High Frequency Structure Simulator (HFSS) [5] for a resonant frequency of 5 GHz when fabricated on Rogers RT DuriodTM 6006 with a

dielectric constant of 6.15, substrate thickness of 0.635 mm and a 34 μ m-thick (0.5 oz.) copper laminate on its top side (no ground plane). The antenna is fed by a CPW line with an S = 2.3 and W = 0.3 mm, which equates to a characteristic impedance (Z_o) of approximately 50 Ω .

To decrease the frequency of optimum impedance match without changing the ground plane dimensions (29 X 40 mm²), mean path length, the antenna length (c+2a=27 mm), or CPW dimensions, chip capacitors are added to the antenna as shown in Fig. 2. HFSS is used to optimize the folded slot antenna's remaining dimensions, which are listed in Table 1, for three antennas with capacitors of 0.1, 0.3, and 0.8 pF.



Figure 2: Schematic of folded slot antenna with capacitor loading.

III. MEASUREMENT PROCEDURES

The antennas are fabricated on Rogers RT DuriodTM 6006 using standard lithography and copper etching. To facilitate measurements, a SMA to microstrip launcher adapted for CPW is soldered to the board. ATC 600F Series chip capacitors (0.1, 0.3, and 0.8 pF) are used for loading the slot antennas. Larger capacitors could not be used because this required slots that are narrower than 0.25 mm, which could not be etched with sufficient accuracy. The folded slot antennas with and without capacitive loading were characterized using an Agilent E8364B Precision Network Analyzer (PNA). A short-open-load coaxial calibration is performed to place the calibrated reference plane at the SMA launcher.

The radiation patterns are measured in a far field chamber. The antenna gain is determined by using the substitution method with a known gain horn with a specified accuracy of ± 0.8 dBi.

IV. SIMULATED AND MEASURED RESULTS

The simulated and measured reflection coefficient for the antennas without and with capacitors is shown in Fig. 3. It is seen that the antenna without capacitor was predicted to resonate at 5 GHz, but it actually resonated at 4.7 GHz. Other than this one case, the measured resonant frequency matches the simulated resonant frequency well. It is also seen that the 10 dB return loss fractional bandwidth decreases as the capacitor loading increases, from 15% without loading to 4.7% with the 0.8 pF capacitor. The S-parameter results are summarized in Table 1. Although not shown, it is seen in Table 1 that the resonant frequency is decreased by changing the dimensions of the slots without changing the mean path length, but the impedance match is very poor, with only a 5 dB return loss. Thus, the loading capacitors perform two functions, they increase the electrical path length allowing the antenna to operate at a lower frequency and they improve the impedance match.



Figure 3: (a) Simulated and (b) measured $|S_{11}|$ for antennas without and with capacitors.

The measured radiation patterns for the antennas are shown in Fig. 4. It is seen that for no capacitive loading and small capacitor loading, the agreement between measured and simulated is very good. For larger loading, the simulated H-plane pattern still shows a near omnidirectional pattern, whereas, the measured H-plane pattern has 15 dB nulls at ± 90 degree. It is also seen that the cross-pol patterns increase in strength as the level of loading is increased. However, it is seen that the gain is not degraded as the loading is increased, maintaining a level close to 5 dBi as the resonant frequency decreases from 4.7 to 3.7 GHz. These results are summarized in Table 1.





Figure 4: Measured and simulated radiation characteristics for (a) no capacitor, (b) 0.1 pF, (c) 0.3 pF, and (d) 0.8 pF capacitors. (all plots in dBi)

V. CONCLUSION

Coplanar waveguide fed folded slot antennas may be loaded with capacitors at the midpoint of the two folded slots to reduce the frequency that the antenna is matched to 50 Ω . It is shown in this paper that a frequency reduction of 22% can be obtained without increasing the antenna size. This may be inverted and an antenna operating at a desired frequency may be reduced in size by 22 %. The gain of the antenna was not decreased significantly as the frequency is reduced, but the 10 dB return loss fractional bandwidth is reduced by a factor of 3. It is believed that this method may be extended further, and was only limited here due to the fabrication tolerance of the slots, which become narrower as the capacitive loading is increased.

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Mean Path 10 dB BW 10 dB BW e+2d C(pF)Fo Gain Gain с e (mm) Length (%) (mm) (mm) (GHz) (%) (dBi) (dBi) (mm)(meas.) (sim.) (meas.) (sim.) 25 2.0 4.0 29 4.7 15 20 5.3213 2.6629 25 2.0 4.0 29 0.1 4.3 7.8 17 5.3510 4.2331 29 26 2.0 3.0 4.2 5.1298 4.7607 26 2.0 3.0 29 0.3 3.95 5.1 12 5.5785 4.4457 26.5 2.0 2.5 29 4 4.1631 4.8590 26.5 2.0 2.5 29 0.8 3.65 4.7 3 2.6387 4.2076

Table 1: Antenna dimensions and summary of results.