

N85-32433

# EXCIMER LASER ANNEALING FOR FABRICATION OF LOW-COST SOLAR CELLS

SPIRE CORP.

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## Program Goal

TO DETERMINE IF PULSED EXCIMER LASER ANNEALING (PELA) IS COST EFFECTIVE COMPARED TO BASELINE PROCESS.

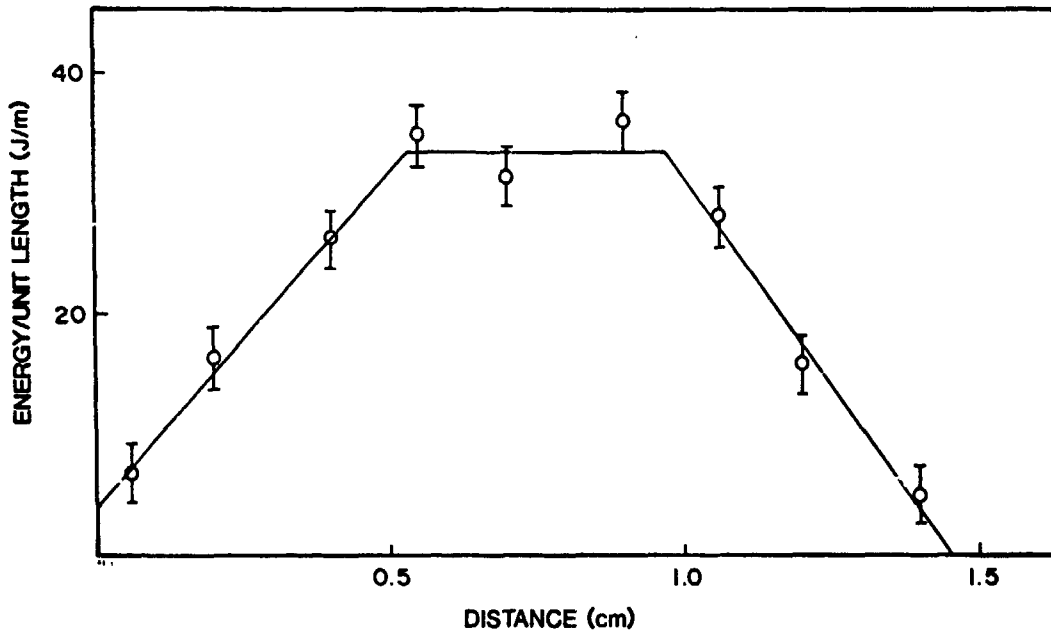
| <u>BASELINE PROCESS</u> | <u>LASER PROCESS</u> |
|-------------------------|----------------------|
| CLEAN                   | CLEAN                |
| DRY                     | DRY                  |
| DIFFUSE JUNCTION        | ION IMPLANT          |
| ALUMINUM BSF            |                      |
| CLEAN                   |                      |
| PRINT Ag BACK           | PRINT Ag BACK        |
| PRINT Ag FRONT          | PRINT Ag FRONT       |
| LASER CUT               | LASER CUT            |
| TEST AND SORT           | TEST AND SORT        |

## PROCESS DEVELOPMENT

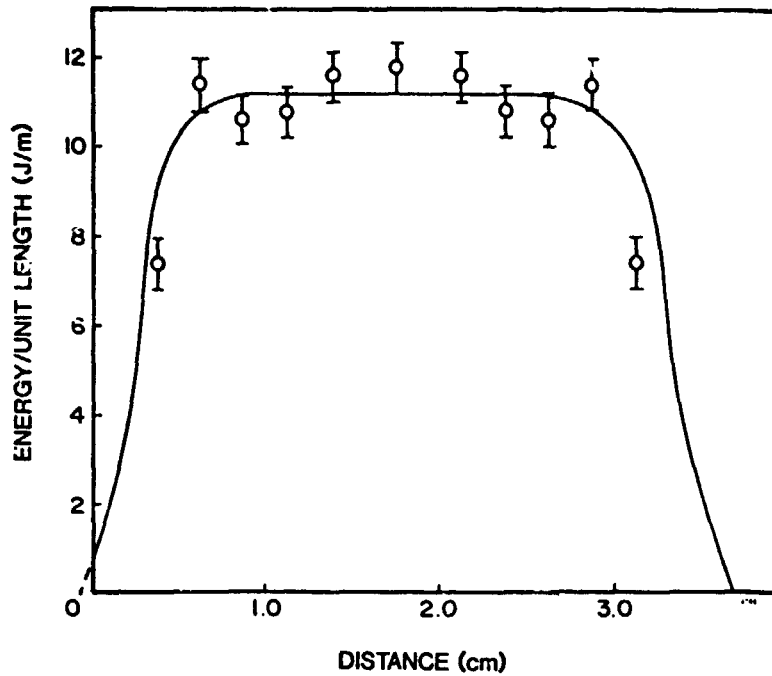
### Objectives

- BUILD AN EXCIMER LASER PULSED ANNEAL APPARATUS
- DEVELOP ANNEAL PROCESSING FOR HIGH EFFICIENCY CELLS
- FABRICATE 300 SOLAR CELLS
- PERFORM ECONOMIC ANALYSIS

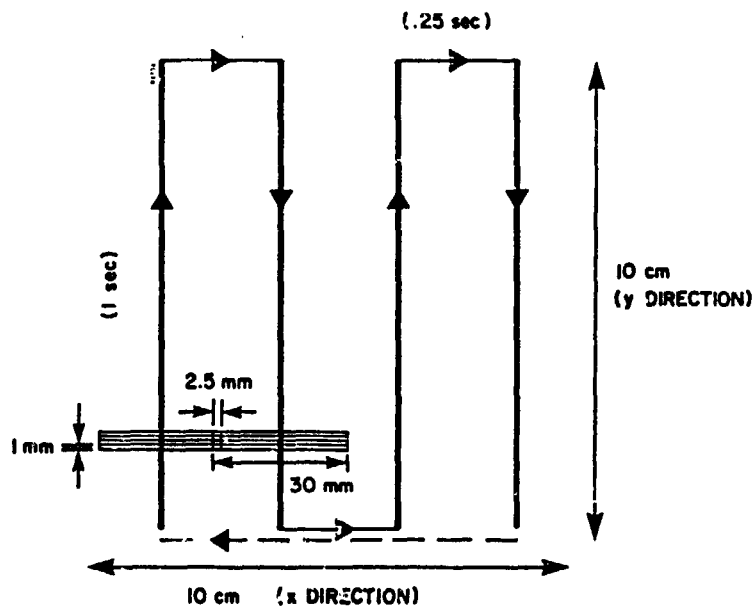
Fluence Measured Across Beam Width (at Lens)



Fluence Measured Across Beam Length (at Lens)



Scanning Pattern for Annealing a 100 cm<sup>2</sup> Wafer  
(Total Transit Time at 10 cm/s Is 5.5 Seconds)

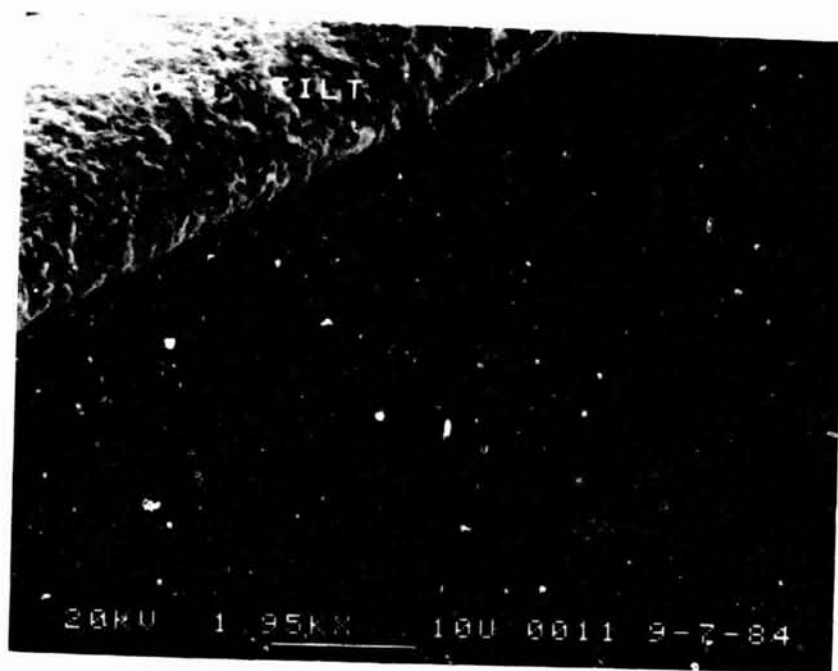


## PROCESS DEVELOPMENT

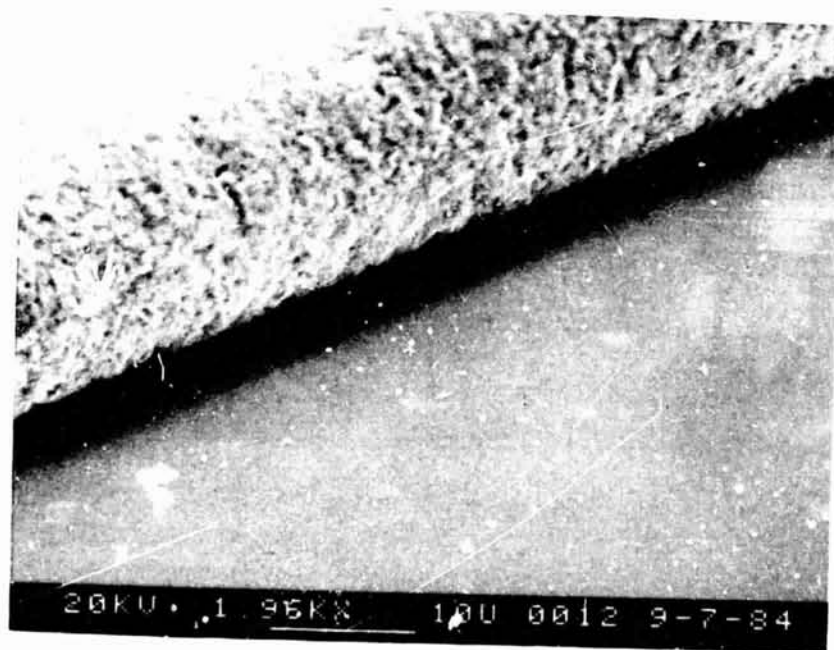
### Implantation Parameters

|        | FRONT<br>(TEXTURED)                 | FRONT<br>(POLISHED)                 | BACK<br>(EITHER)                  |
|--------|-------------------------------------|-------------------------------------|-----------------------------------|
| ION    | P <sup>+</sup>                      | P <sup>+</sup>                      | B <sup>+</sup>                    |
| ENERGY | 10 keV                              | 10 keV                              | 25 keV                            |
| DOSE   | $4.3 \times 10^{15} \text{cm}^{-2}$ | $2.5 \times 10^{15} \text{cm}^{-2}$ | $5 \times 10^{15} \text{cm}^{-2}$ |

Pulsed Excimer Laser Annealing Polished Surfaces



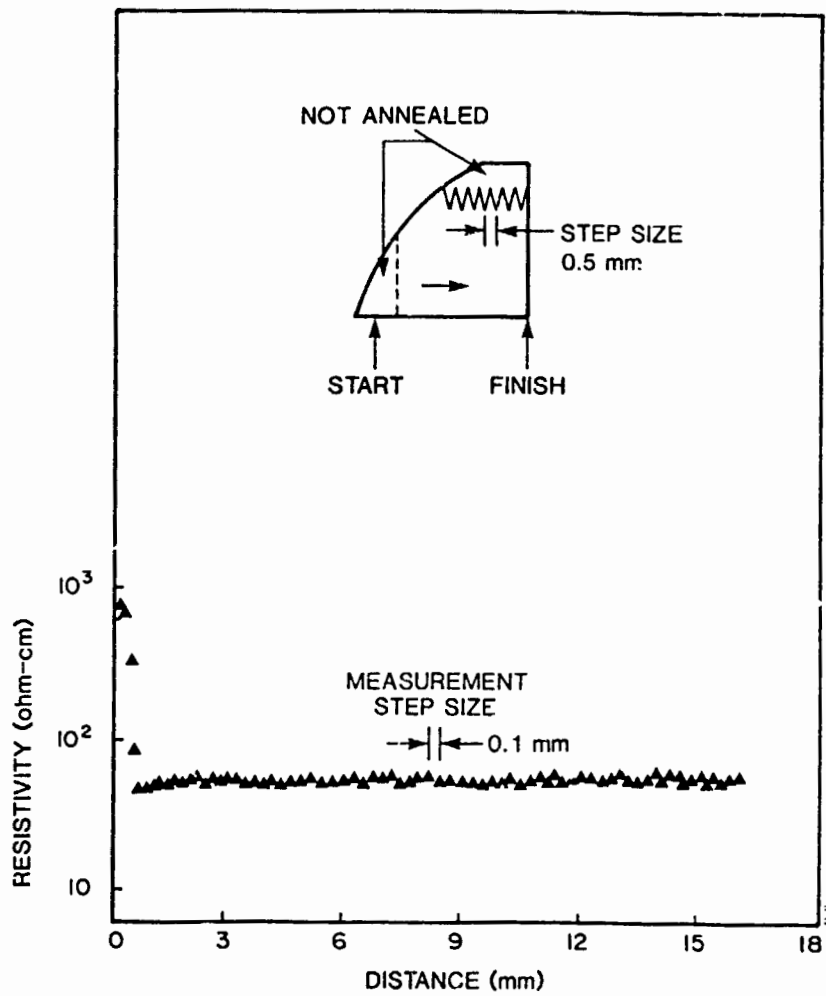
FURNACE  
ANNEAL



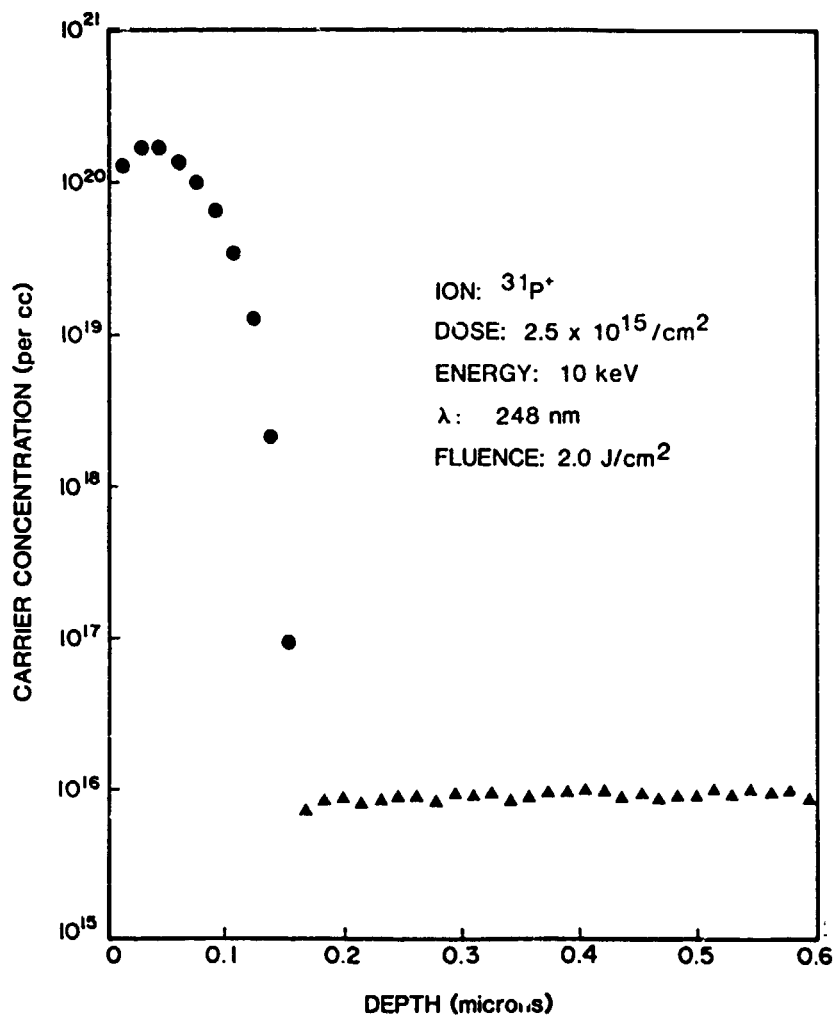
PELA

# PROCESS DEVELOPMENT

## Sheet Resistance Uniformity of PELA Sample 4520-1b



PELA Junction Depth Profile, Sample 4520-16



PROCESS DEVELOPMENT

Efficiency vs Laser Fluence: Polished  
Wafers, No AR Coating

| LOT                          | $\eta$<br>(%) | FLUENCE<br>(J/CM <sup>2</sup> ) | NO. OF<br>PULSES |
|------------------------------|---------------|---------------------------------|------------------|
| I<br><br>$\lambda = 248$ nm  | 8.9           | 1.2                             | 1-2              |
|                              | 8.4           | 1.8                             | 1                |
|                              | 8.9           | 1.9                             | 2-3              |
|                              | 9.1           | 2.0                             | 1-2              |
|                              | 9.1           | FURNACE CONTROL                 |                  |
| II<br><br>$\lambda = 308$ nm | 7.3           | 0.8                             | 1                |
|                              | 8.1           | 1.0                             | 4                |
|                              | 9.7           | 1.4                             | 1                |
|                              | 10.5          | 1.8                             | 1                |
|                              | 10.2          | 1.8                             | 2-3              |
|                              | 7.5           | FURNACE CONTROL (?)             |                  |

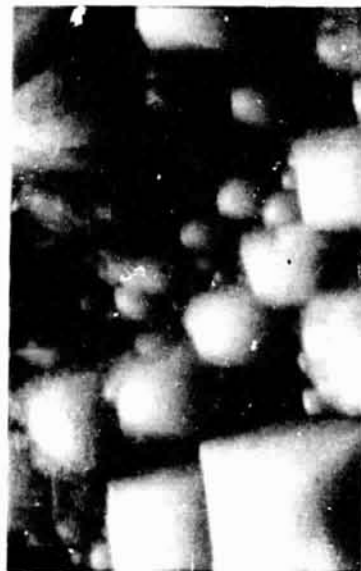


ORIGINAL PAGE IS  
OF POOR QUALITY

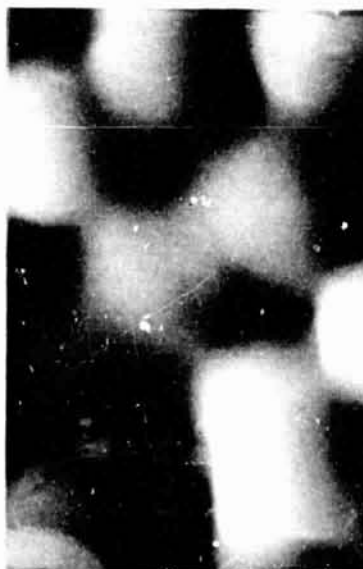
Melting of Texture-Etched Surfaces



NOT PULSED



1.4 J/cm<sup>2</sup> 1 PULSE



2 PULSES

5 µm



>1.8 J/cm<sup>2</sup> 2 PULSES

PROCESS DEVELOPMENT

Efficiency vs Laser Fluence: Texture-Etched Wafers, No AR Coating

| LOT                      | $\eta$ (%) | FLUENCE (J/CM <sup>2</sup> ) | NO. OF PULSES |
|--------------------------|------------|------------------------------|---------------|
| I<br>$\lambda = 248$ nm  | 10.8       | 1.2                          | 1-2           |
|                          | 10.5       | 1.8                          | 1             |
|                          | 8.2        | 1.8                          | 2             |
|                          | 9.2        | 2.0                          | 1-2           |
|                          | 12.9       | FURNACE CONTROL              |               |
| II<br>$\lambda = 308$ nm | 9.1        | 0.8                          | 1             |
|                          | 8.7        | 0.8                          | 2             |
|                          | 9.1        | 0.8                          | 4             |
|                          | 11.8       | 1.0                          | 4             |
|                          | 12.4       | 1.4                          | 2             |
|                          | 8.8        | 1.8                          | 2             |
|                          | 8.1        | FURNACE CONTROL (?)          |               |

Best Cell to Date

IMPLANT:  $31\text{P}^+$   $2.5 \times 10^{15}$  ions/cm<sup>2</sup> 10 keV

ANNEAL: XeCl LASER, 1.8 J/cm<sup>2</sup> 1 pulse  
minimum overlap

$V_{oc}$  = 78 mV

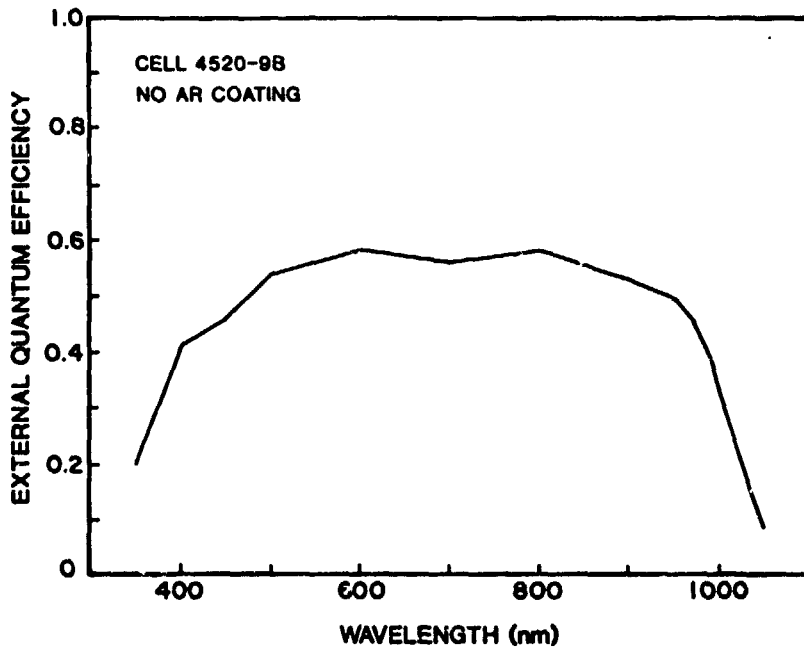
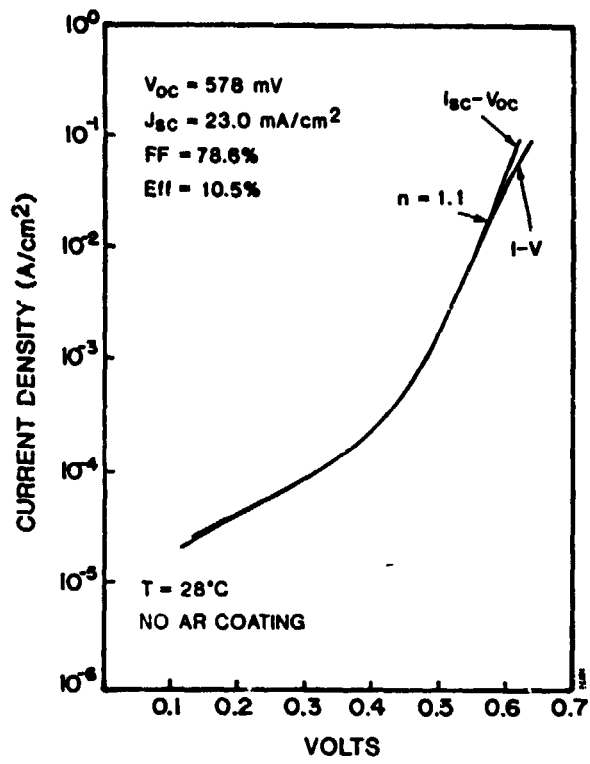
$J_{sc}$  = 23.0 mA/cm<sup>2</sup>

FF = 78.6%

EFF = 10.5%

WITH AN AR COATING, EFFICIENCY WOULD  
BE ABOUT 15%

PROCESS DEVELOPMENT



## PROCESS DEVELOPMENT

### Can the Laser Deliver Enough Power to Rapidly Anneal a Large Wafer?

THE 50 WATT LASER ANNEALED A 4" ROUND  
POLISHED WAFER, A 4" ROUND TEXTURED WAFER,  
AND A 10 cm x 10 cm SILSO WAFER, EACH IN  
UNDER 10 SEC.

#### Laser Parameters

|             |                             |
|-------------|-----------------------------|
| GAS         | Kr, F <sub>2</sub> , and Ne |
| WAVELENGTH  | 248 nm                      |
| POWER       | 50 watts                    |
| REP. RATE   | 160 Hz                      |
| PULSE WIDTH | 20 nanoseconds              |

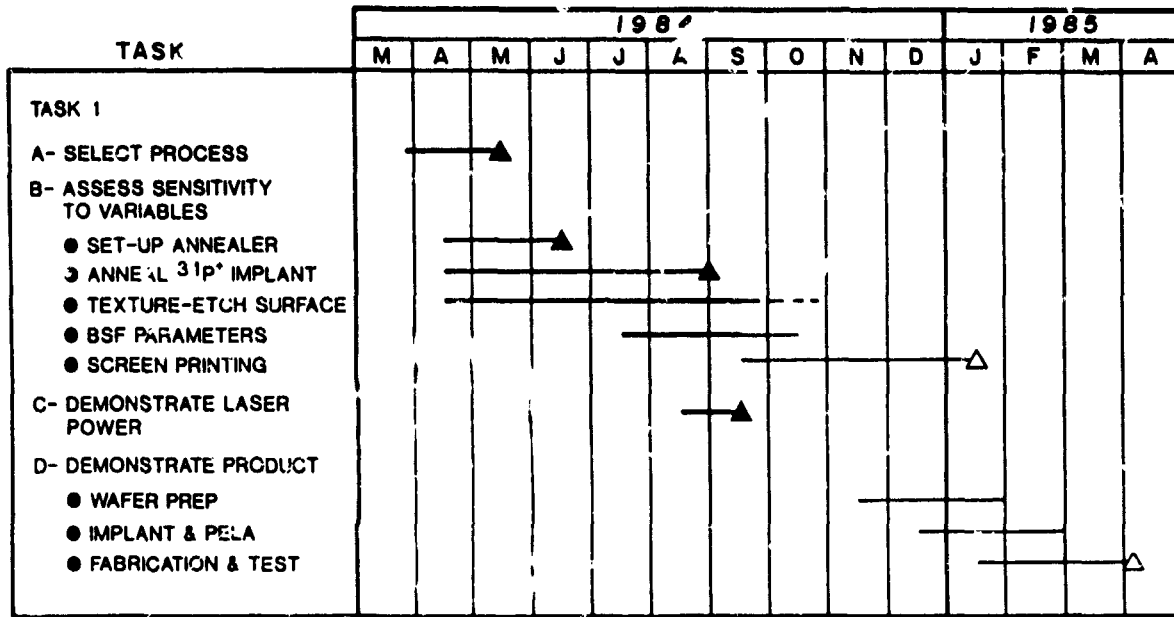
#### Anneal Parameters

|             |                                  |
|-------------|----------------------------------|
| FLUENCE     | ~1.4 J/cm <sup>2</sup> at sample |
| SPOT SIZE   | ~0.7 mm x 25 mm                  |
| TABLE SPEED | 10 cm/sec.                       |

#### Summary of Process Variables

- LASER POWER OF 2J/cm<sup>2</sup> IS REQUIRED FOR POLISHED WAFERS, LESS FOR TEXTURED WAFERS.
- WAVELENGTH (KrF vs. XeCl) IS NOT IMPORTANT.
- BEAM UNIFORMITY MUST BE BETTER THAN 5% BUT NOT NEED NOT BE BETTER THAN 2%.
- DUST IS NOT TOO IMPORTANT.
- UNANNEALED AREAS REDUCE J<sub>sc</sub> BUT DO NOT SHUNT JUNCTION.
- OVERLAP IS IMPORTANT FOR TEXTURED WAFERS.

Program Schedule



Summary

- AN EXCIMER LASER ANNEALER HAS BEEN BUILT AND TESTED.
- SOLAR CELL EFFICIENCY, WITHOUT AR, OF UP TO 10.5% HAS BEEN ACHIEVED (~ 15% WITH AR).
- REQUIRED THROUGHPUT FOR ECONOMICAL OPERATION APPEARS FEASIBLE AT THIS TIME.