



lightly loads a wave generator whose specifications are 15 volts into 600 ohms). This stage is coupled to the main output stages through the 500  $\mu$ fd capacitor to guard against bias changes and hence base width clipping under heavy current loads.

The degenerative coupling of the paralleled transistors provides load balancing, stability, and wave shaping. At high currents, the rate of change of current and the magnitude of the voltage excursion are both reduced, a desirable feature in these kinds of tests. The output voltage swing referenced to the emitter is a positively-going pulse rising off the dc level of the bias voltage. The amplitude of the pulse is easily controlled by the amplitude of the input drive.

The duty cycle in this application is dictated by the thermal inertia characteristics of the thermionic converter and is about 1 to 5 percent. The main bank of power transistors can easily pass a triangular current pulse of over 300 amperes peak with a base width of 2 to 3 milliseconds. The nonlinear characteristics

of thermionic converter loads permit larger base widths at this current level. The energy in the current pulse can be increased by increasing the size of the energy storage capacitors.

**Notes:**

1. Although specifically designed for thermionic converter testing, the load cell adapts to other similar high-current, low-voltage uses.
2. No additional documentation is available. Specific questions, however, may be directed to:  
Technology Utilization Officer  
Lewis Research Center  
21000 Brookpark Road  
Cleveland, Ohio 44135  
Reference: B70-10470

**Patent status:**

No patent action is contemplated by NASA.

Source: R. Breitwieser and E. J. Manista  
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