



Title	Transport properties and electrical device characteristics with the TiMeS computational platform: Application in silicon nanowires
Author(s)	Sharma, Dimpy; Ansari, Lida; Feldman, Baruch; Iakovidis, M.; Greer, James C.; Fagas, Gíorgos
Publication date	2013
Original citation	Sharma, D., Ansari, L., Feldman, B., Iakovidis, M., Greer, J. C. and Fagas, G. (2013) 'Transport properties and electrical device characteristics with the TiMeS computational platform: Application in silicon nanowires', Journal of Applied Physics, 113(20), 203708 (8pp). doi: 10.1063/1.4807578
Type of publication	Article (peer-reviewed)
Link to publisher's version	http://aip.scitation.org/doi/10.1063/1.4807578 http://dx.doi.org/10.1063/1.4807578 Access to the full text of the published version may require a subscription.
Rights	© 2013, AIP Publishing LLC. This article may be downloaded for personal use only. Any other use requires prior permission of the author and AIP Publishing. The following article appeared in Sharma, D., Ansari, L., Feldman, B., Iakovidis, M., Greer, J. C. and Fagas, G. (2013) 'Transport properties and electrical device characteristics with the TiMeS computational platform: Application in silicon nanowires', Journal of Applied Physics, 113(20), 203708 (8pp). doi: 10.1063/1.4807578 and may be found at http://aip.scitation.org/doi/10.1063/1.4807578
Item downloaded from	http://hdl.handle.net/10468/4725

Downloaded on 2018-08-23T20:19:25Z



UCC

University College Cork, Ireland
Coláiste na hOllscoile Corcaigh

Transport properties and electrical device characteristics with the TiMeS computational platform: Application in silicon nanowires

D. Sharma¹, L. Ansari¹, B. Feldman, M. Iakovidis, J. C. Greer, and G. Fagas¹

Citation: *Journal of Applied Physics* **113**, 203708 (2013); doi: 10.1063/1.4807578

View online: <http://dx.doi.org/10.1063/1.4807578>

View Table of Contents: <http://aip.scitation.org/toc/jap/113/20>

Published by the [American Institute of Physics](#)

Articles you may be interested in

[Simulation of junctionless Si nanowire transistors with 3 nm gate length](#)

Applied Physics Letters **97**, 062105 (2010); 10.1063/1.3478012

AIP | Journal of
Applied Physics

Save your money for your research.
It's now **FREE** to publish with us -
no page, color or publication charges apply.

Publish your research in the
Journal of Applied Physics
to claim your place in applied
physics history.

Transport properties and electrical device characteristics with the TiMeS computational platform: Application in silicon nanowires

D. Sharma,^{1,a)} L. Ansari,^{1,a)} B. Feldman,² M. Iakovidis,¹ J. C. Greer,¹ and G. Fagas^{1,b)}

¹Tyndall National Institute, University College Cork, Cork, Ireland

²Department of Materials and Interfaces, Weizmann Institute of Science, Rehovoth 76100, Israel

(Received 10 March 2013; accepted 6 May 2013; published online 30 May 2013)

Nanoelectronics requires the development of *a priori* technology evaluation for materials and device design that takes into account quantum physical effects and the explicit chemical nature at the atomic scale. Here, we present a cross-platform quantum transport computation tool. Using first-principles electronic structure, it allows for flexible and efficient calculations of materials transport properties and realistic device simulations to extract current-voltage and transfer characteristics. We apply this computational method to the calculation of the mean free path in silicon nanowires with dopant and surface oxygen impurities. The dependence of transport on basis set is established, with the optimized double zeta polarized basis giving a reasonable compromise between converged results and efficiency. The current-voltage characteristics of ultrascaled (3 nm length) nanowire-based transistors with p-i-p and p-n-p doping profiles are also investigated. It is found that charge self-consistency affects the device characteristics more significantly than the choice of the basis set. These devices yield source-drain tunneling currents in the range of 0.5 nA (p-n-p junction) to 2 nA (p-i-p junction), implying that junctioned transistor designs at these length scales would likely fail to keep carriers out of the channel in the off-state. © 2013 AIP Publishing LLC. [<http://dx.doi.org/10.1063/1.4807578>]

I. INTRODUCTION

The continuous miniaturization of electronic transistors pursued by semiconductor industries enables more functionality for a fixed die area. However, scaling devices to sub-deca nanometer gives rise to short-channel and quantum tunneling effects that degrade device performance^{1,2} and to keep up with scaling research has motivated new transistor designs that use a range of nanostructured materials.³ These types of devices require for their description taking explicitly into account new physical phenomena and materials properties at the nanoscale. For example, strong quantum confinement in one- and two-dimensions occurs, respectively, in ultrathin body and nanowire-based field-effect-transistors (FETs)^{2,4} and the high surface to volume ratio allows for the manifestation of size effects^{5,6} and volume inversion.^{7,8} Direct source-drain quantum mechanical tunneling^{3,9} and gate-tunneling leakage arise,¹⁰ obstructing the way to reducing power consumption.^{11,12} Manifestations of differing properties of nanoscale materials compared to their bulk counterparts have also been demonstrated. For the prototypical material of silicon nanowires, surface functionalization schemes result in tuning the electronic and transport properties,^{13–15} the effective masses of charge carriers become heavier,¹⁶ dopants may deactivate,¹⁷ and the deformation potentials and electron-phonon scattering can become highly anisotropic.¹⁸

Various semi-classical methods have been elaborated to simulate the current-voltage of conventional transistors and are used to reduce costs and shorten the design cycle.¹⁹ The need to develop such *a priori* technology evaluation that

extends to the nanoscale is significant as the traditional trial-and-error experimental design of nanodevices becomes more time consuming and expensive. Over the last decade, the description of electronic quantum transport based on computational methods has become one of the core topics in atomic-scale modeling and device simulations,²⁰ and the explicit electronic structure of materials has been considered from approximate methods that use empirical bulk parameterization¹⁵ to first-principles approaches,^{21–23} and approximations thereof.^{13,23–25}

In this paper, we first discuss our own quantum transport implementation (TiMeS—Transport in Mesoscopic Systems). Separating the step that provides the electronic structure description from the transport module, TiMeS offers a cross-platform solution that allows efficient calculations of materials transport properties and realistic device simulations to extract current-voltage and transfer characteristics. Previously, TiMeS was applied to predict transistor behavior in junctionless nanowire-based gate-all-around (GAA) architectures with just a 3 nm gate length.^{9,25} The excellent device characteristics at such ultra-scaled dimensions have been recently confirmed.²⁶ The simulation results were obtained using an approximate Density-Functional Tight-Binding (DFTB) approach based on the Harris-Foulkes functional of density functional theory (DFT).²⁷ Here, we make use of the TiMeS modularity to interface with a DFT package based on numerical atomic orbitals (NAOs). We demonstrate TiMeS flexibility by applying it to study the basis set dependence of the mean free path in silicon nanowires with dopant and surface oxygen impurities and the current-voltage characteristics of ultrascaled nanowire devices.

Theoretical studies based on first-principles DFT is one of the widely used methods to describe accurately the atomic

^{a)}D. Sharma and L. Ansari contributed equally to this work.

^{b)}Author to whom correspondence should be addressed. Electronic mail: Georgios.Fagas@tyndall.ie

geometry and to provide materials design guidelines in an affordable computational time without introducing system dependent parameters. In a recent study,²⁸ we benchmarked NAOs against plane waves (PWs) basis sets and discussed the impact of basis set on the electronic properties of SiNWs. PWs are simpler to converge; however, computational requirements have limited their use in studies of quantum transport. On the other hand, basis sets made of atomic orbitals (AOs) can be more efficient motivating their implementation for large-scale order-N calculations.²⁹ There have been numerous studies comparing the structural properties and electronic spectra resulting from PW and AO implementations (see, e.g., Ref. 28 and references therein). Surprisingly, with few exceptions, a similar elaboration on the impact for transport properties has attracted little attention despite the need to reach the same level of confidence for device design and evaluation.

Strange *et al.* performed benchmark calculations of the transmission spectrum for a set of five single-molecule junctions.³⁰ Using a Wannier transformation on DFT Hamiltonians in the PW basis sets enabled them to calculate the transmission function and to compare with a NAOs transport implementation. They concluded that a double zeta polarized basis sets suffices for the particular systems. This confirmed earlier work by Bauschlicher *et al.* on the gold-benzene-1,4-dithiol-gold junction.³¹ In Ref. 32, Driscoll and Varga study the dependence of quantum conductance on basis sets by comparing localized basis sets with extended non-localized polarized basis. They find that convergence with localized basis sets is more demanding due to sensitivity in describing the self consistent potential. Hermann *et al.* also identify issues with the use of large non-orthogonal Gaussian-type AOs.³³ They show that basis sets of triple-zeta quality or higher can result in an artificially high transmission. These results imply that despite the extensive study over decades of the convergence properties of AOs in quantum chemistry,³⁴ it is necessary to check the transferability of tabulated AOs as well as the construction of NAOs in different chemical environments for *a priori* evaluation of nanoscale devices.

The structure of the paper is as follows. In Sec. II, the background on the computational methodology to analyze materials properties and device characteristics at the nanoscale is expanded. In Sec. II B in particular, we provide the implementation details of the TiMeS cross-platform simulation tool. This is based on employing Hamiltonian matrix descriptions from different electronic structure codes and combining these with Green's functions methods to calculate the quantum-mechanical scattering matrix (S-matrix).

Charge self-consistency in the presence of applied voltages is treated within the non-equilibrium Green's functions (NEGF) framework. In Sec. III A, TiMeS is applied to the calculation of the mean free path and the basis set convergence for silicon nanowires with dopant and surface oxygen impurities is established. Optimized double zeta polarized basis sets give a reasonable compromise between converged results and efficiency. The current-voltage characteristics of ultrascaled (3 nm long) nanowire-based transistors with p-i-p and p-n-p doping profiles is investigated in Sec. III A 1. The quantitative interplay between basis set dependence and charge self-consistency is analyzed and it is found that the latter has a more pronounced effect on the device characteristics. Interestingly, these devices yield relatively large source-drain tunneling in the off-state (currents in the range of 0.5 nA to 2 nA for the p-n-p and p-i-p junctions, respectively), which can have a detrimental effect in the device performance. Section IV concludes with a brief discussion of the results.

II. COMPUTATIONAL METHODOLOGY

A. Geometry and electronic structure

The geometry and electronic structure are calculated using DFT. Throughout the simulations, the PBE generalized gradient approximation (GGA) to the exchange correlation potential is used together with norm-conserving pseudopotentials. We focus on hydrogenated silicon nanowires grown along the [110] crystallographic direction with lattice constant of 3.84 Å along with the wire axis and diameter equal to 1.15 nm. Impurities are introduced in the supercell by either substitution of silicon atoms in the pristine structure with dopants (see Figure 1) or changing the surface termination and introducing oxygen defects for the model described in Ref. 14. The structures were geometry optimized with force threshold of 0.01 eV/Å and using optimized double zeta polarized basis set for both silicon and impurity atoms. Surface-passivating hydrogens are treated in a minimal basis set. The total energy using these bases is a good approximation to the complete basis set limit as shown previously.²⁸ The supercell consisted of eleven unit cells and its size extended by $42.24 \text{ \AA} \times 25 \text{ \AA} \times 25 \text{ \AA}$ to introduce a vacuum separation between periodic images of the nanowires. Monkhorst-Pack k-point sampling was applied on a $4 \times 1 \times 1$ grid with the higher sampling aligned to the nanowire axis.

Here, we employ the DFT implementation in the OpenMX code as developed by Ozaki.^{35,36} This expresses

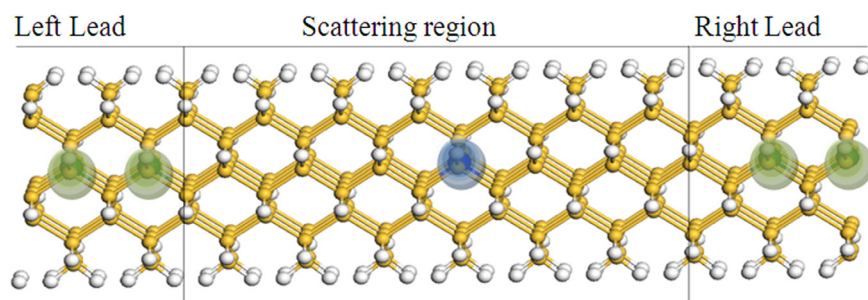


FIG. 1. Schematic setup of one of the investigated nanowire structures. A silicon nanowire grown along the [110] direction is depicted with p-n-p doping profile. Substitutional boron (green sphere on line) is used to dope the semi-infinite periodic leads whereas the “scattering region” incorporates a phosphorus dopant (blue sphere on line).

the Kohn-Sham wavefunction ψ_i as a linear combination of NAOs

$$\Psi_i(\vec{r}) = \sum_n \sum_{\mu=1}^K C_{i\mu} \phi_{\mu}(\vec{r} - \vec{r}_n). \quad (1a)$$

The set $\{\phi_{\mu}; \mu = 1, 2 \dots K\}$ of pseudoatomic wavefunctions, referred as primitives, is obtained by solving the Schrödinger equation for an atom in a slightly modified environment that accounts to some extent for the orbital relaxation when bonding; n is the site index and μ includes the angular momentum l , magnetic quantum number m , and the multiplicity index p , namely, $\mu \rightarrow p, l, m$. The basis set notation smpm'dm'' is used to indicate that m , m' , and m'' functions are used to expand s, p, and d orbitals, respectively, relating to the quantum chemistry notation of single-zeta, double-zeta, and so on, depending on the number of primitives per valence state. In practice, the NAOs $\{\phi_{\mu}\}$ are further expressed as a linear combination of pseudoatomic orbitals with the same (l, m) index pair, namely,

$$\phi_{\mu}(\vec{r}) = \sum_q \alpha_{\mu q} \chi_{\eta}(\vec{r}) \quad (1b)$$

with $\eta \rightarrow q, l, m$ and $\mu \rightarrow p, l, m$. This leads to contracted or optimized numerical atomic orbitals, thereby improving the numerical efficiency and accuracy.^{28,35} Orbital optimization reduces the size of the basis and reduces effects associated with basis sets overcompleteness. The notation ϕ_{nm} indicates abbreviation of basis sets, e.g., s32 means that two optimized s orbitals are constructed from three primitive functions.

B. Transport properties

We have developed a modular transport simulator TiMeS that allows us to assess both the intrinsic electronic transport properties of materials and the electrical characteristics of devices. Our transport module requires a single particle Hamiltonian of the system expressed in a localized basis set. Based on Green's function techniques, the S-matrix and hence the transmission $T(E)$ of charge carriers injected at energy E can be calculated from a single-particle Hamiltonian H in either the low-bias (non-self-consistent (NSC)) approximation, or in fully self-consistent (SC) NEGF theory. The interfacing across various electronic structure platforms and the SC NEGF extensions build on previous work on the transport properties of mesoscopic systems^{38,39} and molecular junctions.^{24,40}

1. Low-bias regime

We emphasize that the NSC version of TiMeS operates as a post-processing step after an electronic structure computation and allows for extracting transport properties in the low-bias regime. Importantly, TiMeS is completely modular in that it requires, and has, no information about the representation used by the electronic structure code *except* for the single-particle (typically Kohn-Sham) Hamiltonian matrix H_{ij} and overlap matrix $S_{ij} = \langle i|j \rangle$ elements. TiMeS currently has interfaces to accept this information from OpenMX,³⁵⁻³⁷

DFTB⁺,²⁷ and the Quantum Espresso plane-wave DFT code via transformation to Wannier orbitals.^{41,42}

Like other localized-orbital electronic transport codes based on Landauer or NEGF theory,²¹⁻²⁴ TiMeS calculates the self-energy of the semi-infinite electrodes based on the surface Green's function for the given "on-site" and "hopping" Hamiltonian and overlap matrices for the electrodes.⁴³ The entire transport region is broken into three sub-regions: the two electrodes and a scattering region (see Fig. 1). The electrodes are semi-infinite repetitions of periodic cells. These cells and the scattering region must be "principal layers" such that overlap elements vanish for orbitals separated beyond the adjacent principal layers. Therefore, just one "hopping" Hamiltonian is needed for each electrode.

With the non-Hermitian self-energies $\Sigma_{L,R}$ to represent semi-infinite electrodes, TiMeS performs inversion to solve for the Green's function $G(E) = (E - H - \Sigma_L - \Sigma_R)^{-1}$. This step is performed independently for each energy E ; hence, a parallel Message Passing Interface (MPI) implementation is used to reduce the computational cost. From $G(E)$, the scattering matrix is calculated using the channel eigenstates of the leads.^{38,44} This approach enables monitoring the computational flow and numerical stability by checking the unitarity of the S-matrix at each energy step. It also allows decomposition into individual channel contributions, thereby yielding direct information on the effect of the various scattering mechanisms between channels.^{38,39,44} The explicit calculation of the channel eigenstates can be used further to obtain the band structure of the leads including the forbidden energy domain where modes are decaying exponentially along the periodic direction.⁴⁰ Additional information is obtained by analysis of the density of states¹⁴ as extracted from diagonal matrix elements of the Green's functions for any section of the device.

2. Self-consistent transport using NEGF

Starting with this low-bias algorithm, we extended TiMeS to perform fully self-consistent NEGF calculations in a fashion retaining TiMeS' modularity. Following Ke *et al.*²³ for applying boundary conditions (BCs) to the electrode regions allows us to implement NEGF independently of the electronic structure step. Solution for the Green's function G from H_{KS} gives equivalent information to solving the Kohn-Sham (KS) equations, so G contains information on the state of the system.^{23,44} TiMeS calculates the electronic density matrix (DM) from the (typically KS-DFT) Hamiltonian output by the electronic structure code. But note that unlike the electronic structure step, which is typically carried out with periodic or cluster BCs, TiMeS finds the DM for the *open* system, with self-energies representing the semi-infinite electrodes. The DM is calculated according to

$$\begin{aligned} \text{DM} = & -\frac{1}{\pi} \int_{-\infty}^{+\infty} dE \text{Im}[G_C^r(E) f^L(E - \mu_L; T_e)] \\ & + \frac{1}{2\pi} \int_{-\infty}^{+\infty} dE [G_C^r(E) \Gamma_R(E) G_C^f(E)^{\dagger}] [f^R(E - \mu_R; T_e) \\ & - f^L(E - \mu_L; T_e)], \end{aligned} \quad (2a)$$

where G_C^r is the retarded Green's function in the scattering region, f is the Fermi function at electronic temperature T_e , and $\mu_{L,R}$ are the chemical potentials of the two leads. The spectral density is defined via $\Gamma_{L,R} = i[\Sigma_{L,R} - \Sigma_{L,R}^\dagger]$. The first term in Eq. (2a) is the equilibrium (linear-response) contribution to the DM, and the second term is the non-equilibrium (polarization and current flow) contribution, which vanishes when $\mu_L = \mu_R$. The integration is performed by Gaussian quadrature, and the equilibrium component may also use complex contour integration (starting appreciably below the lowest energy band), such that each integral usually requires evaluation of $G(E)$ at ~ 50 or fewer energies E . Note that even in a zero-bias calculation, the linear-response density in NEGF can differ from the non-self-consistent result because of the inclusion of the self-energies $\Sigma_{L,R}$, representing open BCs rather than the periodic BCs of the electronic structure step. In the limit of an ideal calculation, the central region should be made long enough that the BCs are virtually irrelevant, but this is not always the case in practice. In the case of an applied voltage bias, convergence is ensured by extending the scattering region.

The charge density can be computed straightforwardly from the DM according to

$$\rho(r) = \sum_{\mu,\nu} \varphi_\mu^*(r) \text{Re}[(\text{DM})_{\mu,\nu}] \varphi_\nu(r) \quad (2b)$$

and this density is used in the electronic structure code to extract the new device region Hamiltonian matrix. As a general principle, codes based on KS-DFT can find the entire system Hamiltonian H_{KS} from knowledge of just the electronic density. Likewise, the DFT-based tight-binding code DFTB^{+27} can also be re-started using atomic Mulliken charges as input. The source code of the electronic structure packages is modified to read in the re-start density variables before proceeding with a single KS step to find the new H_{KS} . To facilitate calculations of gated devices, we also modified OpenMX to include the gating potential from a cylindrical GAA shell of charge.⁴⁵ Source-drain voltages are applied by including a potential step in the electrodes and then the charge density is iterated to self consistency.²³

This process of computing DM from H and vice versa iterates until the charge density converges, after which the S-matrix and transmission $T(E)$ are computed for the converged density. Presently, we find that simple mixing of the charge density suffices for NEGF calculations, but more sophisticated mixing algorithms can easily be incorporated. We implemented the NEGF loop and mixing using a Python script, so each TiMeS and H determination step run as independent modules. The non-self-consistent and the NEGF flowchart are shown in Figure 2. This flexibility and modularity allows for easy continuation of interrupted runs, as well as interchange and comparison of different electronic structure approaches or density functionals. In the present work, matrix representations of the Hamiltonians of the relaxed structures were obtained from OpenMX as described in Sec. II A. The program flow is specifically designed to allow a modular incorporation of differing electronic structure representations of the single-particle Hamiltonian.

III. RESULTS AND DISCUSSION

A. Transport properties

We discuss the impact of numerical atomic orbitals in materials transport properties focusing on charge carrier scattering in SiNWs due to common impurities. The example of Ref. 14 is taken as initial system of reference, where there is a detailed discussion on the scattering behavior due to oxygen defects varying the oxidation state of the Si surface. In this model, the surface Si atom is locally oxidized to the formally Si^{2+} state by forming a Si-O-Si back bond and with a hydroxyl instead of hydrogen for passivating the surface dangling bond (see inset of Fig. 3). The presence of the oxygen defect is the origin for surface roughness at the atomic scale.

Following the method in Ref. 46, the mean free path for electrons injected at energy E is estimated from $\lambda(E) = \frac{T_s(E)}{T_c(E)} \cdot l_d$, where T_s and T_c are the transmission values across the wire with and without local oxidation, respectively, and l_d is the mean distance between defects. The mean free path in the energy range of the first valence sub-band is shown in Fig. 1 for various basis sets, namely, single-zeta, single-zeta polarized, and double-zeta polarized. Here, $l_d = 9.6$ nm which corresponds to a defect density $n = 5 \times 10^{19} \text{ cm}^{-3}$. There is in

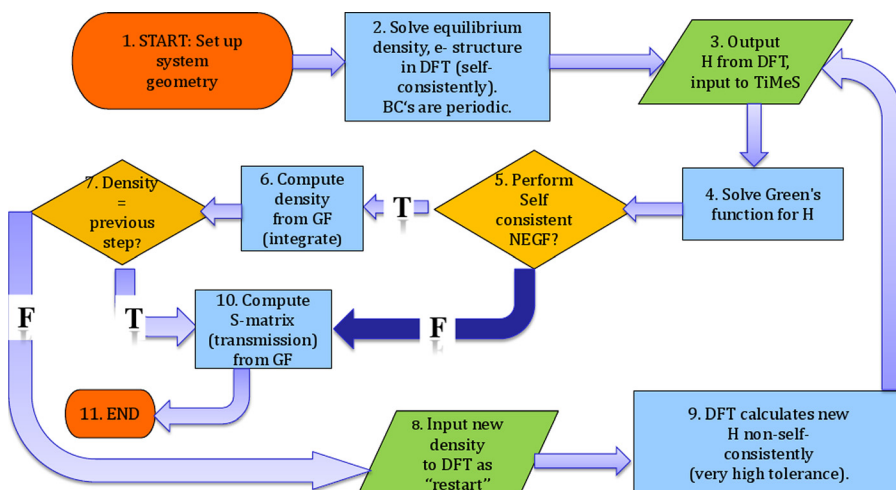


FIG. 2. The TiMeS flowchart shows the cross-platform of quantum transport implementation. The non-self-consistent step is indicated by the dark (blue on line) arrow. T and F stand for “true” and “false,” respectively.

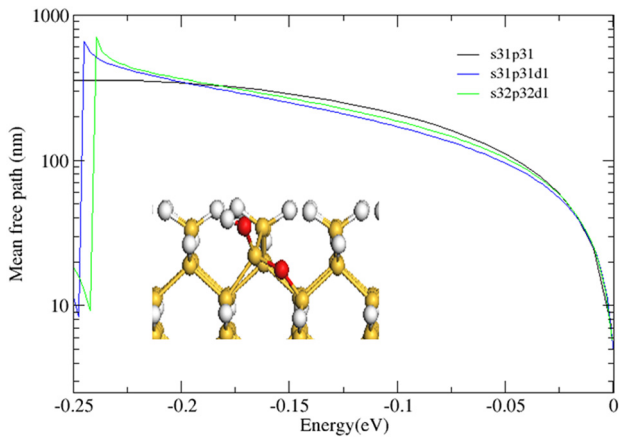


FIG. 3. Mean free path of locally oxidized Si nanowire (structure described in text) for the indicated basis sets and defect density $n = 5 \times 10^{19} \text{ cm}^{-3}$ (mean distance between impurities $l_d = 9.6 \text{ nm}$). The energy range corresponds to the first valence sub-band of the electronic structure obtained with the double-zeta polarized basis set.

overall good agreement with Ref. 14 where a minimal basis set was used within the Density Functional Tight Binding approximation. Interestingly, the mean free path obtained from the various basis sets shows variation of just up to 0.01% within an energy range of 0.2 eV using the double-zeta polarized results as a reference.

The above transport results confirm the weak dependence of λ on the basis set when the prevalent scattering mechanism is non-resonant scattering which is regularly observed for typical dopant impurities^{46,47} and oxidation defects.¹⁴ Using a simple analysis based on band structures, previous predictions attribute mean free path variations to small changes in the group velocity.²⁸ It may be expected that differing group velocity and effective mass will have the most significant impact in the transport coefficients when different electronic structures are used, hence, the need to calibrate to the experiment is introduced.

To consider the case of strong scattering, we study the common example of a boron substitutional impurity which can act as a p-type dopant.^{47,48} The transmission of holes injected at energies within the first-valence sub-band is plotted in Fig. 4. Calculations using single-zeta, single-zeta polarized, double-zeta polarized, and triple-zeta polarized bases are shown. There is overall qualitative agreement between the various basis sets notwithstanding the evident resonant backscattering that strongly suppresses transmission. However, the quantitative discrepancy in the transmission may lead to significant overestimations of the nanowire conductivity. Similar results are found for an n-type dopant impurity, namely, substitutional phosphorus.

It is evident from Fig. 4 that using the basis set with double-zeta and polarization closely tracks the result obtained with triple-zeta polarized basis. Hence, the double-zeta basis may be viewed as a good compromise between size and accuracy in the application of atomic basis sets to predict from first-principles the characteristic transport length scales and intrinsic transport properties in materials. This behavior is similar to conductance estimations in transport across molecular junctions where

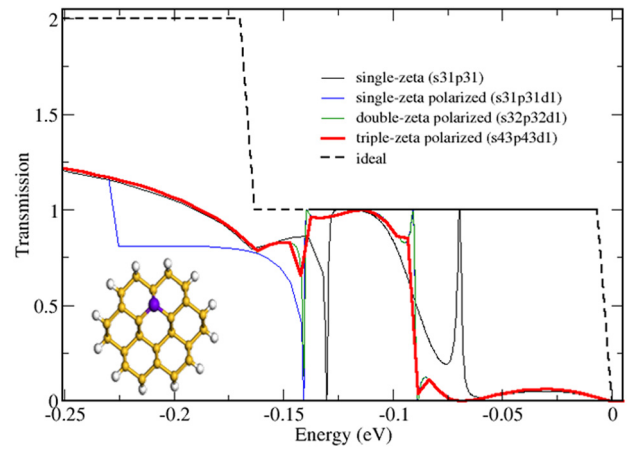


FIG. 4. Transmission of holes across a silicon nanowire with a boron impurity for the indicated basis sets. The structure and the contraction scheme are described in the text. The energy range corresponds to the first valence sub-band of the electronic structure obtained with the double-zeta polarized basis set.

convergence needs to be ensured by enlarging the size of the basis set.^{30,31}

1. Electrical characteristics

Applying our electronic transmission methodology, in this section, the electrical characteristics of SiNW based p-n-p and p-i-p junctions will be explored. The p-n-p junction is constructed from boron doped leads with an n-type scattering region between the two p-type leads (see Fig. 1). The scattering region includes a single phosphorus dopant in the nanowire lattice. For the p-i-p junctions, the scattering region is intrinsic. The total length of the scattering region is 2.7 nm. In a nanowire-based FET, the intrinsic region is surrounded by a cylindrical gate to yield a GAA configuration. Given that the localization radius of dopant impurity states is approximately 1.5 nm and that the channel length should be at least two times larger than the nanowire diameter,⁴⁹ these models describe the smallest junctioned nanowire transistors that can be envisioned using conventional doping strategies. The results are obtained using the two different transport algorithms which have already been described in Sec. II B.

Figure 5 illustrates the transmission properties of the p-n-p and p-i-p SiNW junctions at different source-drain bias voltages, applied along the nanowire axis, as predicted from different orbital basis sets. As expected, Fig. 5 shows that at small source-drain bias voltages the self-consistent converged transmission $T(E, V_{DS})$ does not differ from the linear-response approximation $T(E)$ (curve indicated as $V_{DS} = 0$) considerably. For larger bias, applying the NEGF self-consistence loop increases the transmission for all basis sets used. The larger basis has an effect on the electronic structure alignment of the scattering region with the available channels of the leads as seen by the comparison of the transmission onset between minimal (Figs. 5(a) and 5(c)) and polarized basis sets (Figs. 5(b) and 5(d)). The transmission is also affected at energies well below the top of the valence band. These differences on the energy dependence of transmission result in variation in the current estimates as shown below. However,

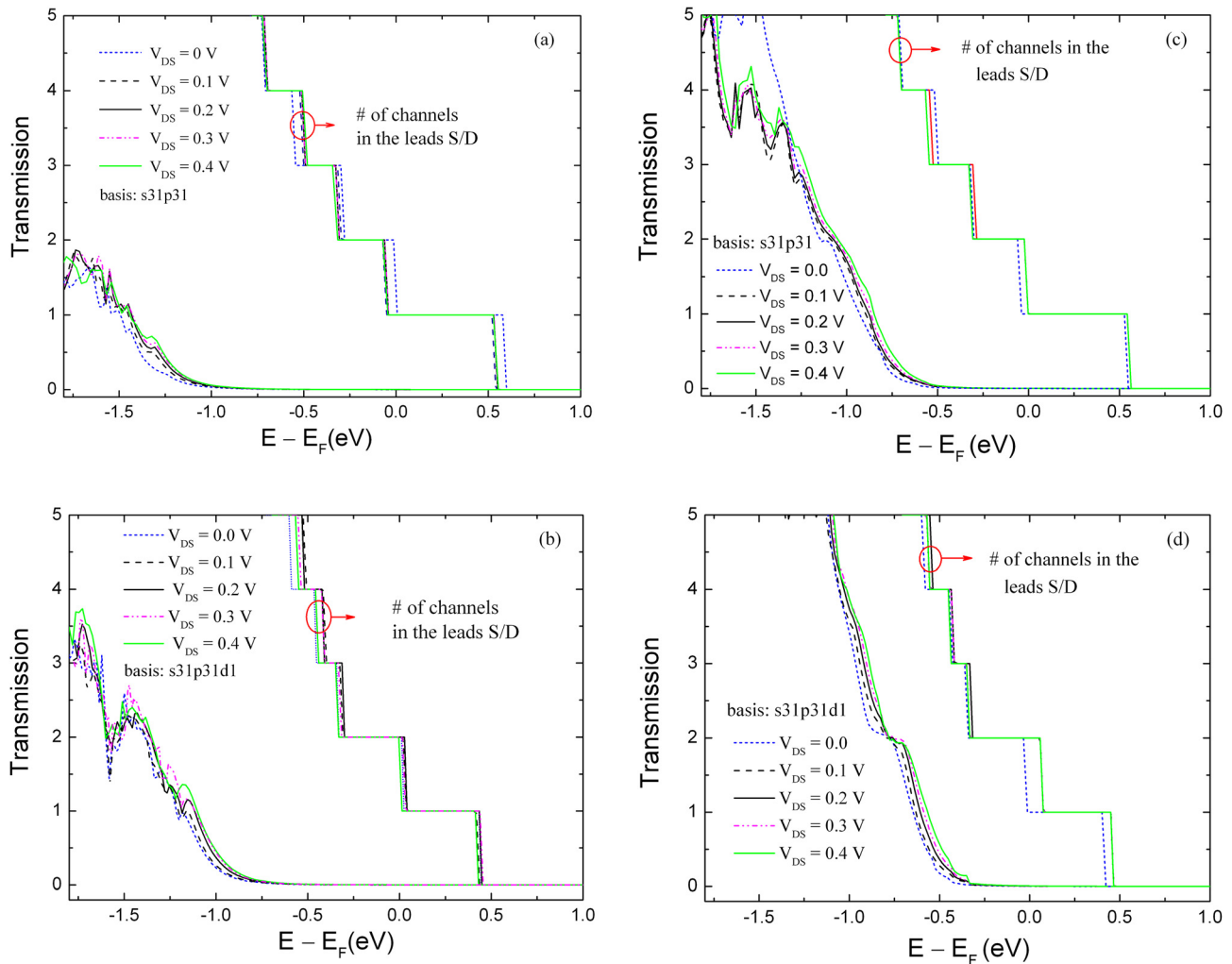


FIG. 5. Transmission properties of p-n-p junctions using (a) s31p31 and (b) s31p31d1 basis sets. Panels (c) and (d) show the transmission of the p-i-p junctions considering s31p31 and s31p31d1 orbital basis sets, respectively. The Fermi level is the reference energy.

it is apparent that the addition of d-polarization functions does not significantly alter the self-consistency dependence of the transmission on applied voltage.

The current-voltage (I - V) characteristics of p-n-p and p-i-p junctions based on $T(E, V_{DS})$ and $T(E)$ are plotted in Figs. 6(a) and 6(b), respectively. The I - V characteristics using the double-zeta polarized basis set and without self-consistency (based on $T(E)$) is also illustrated in Fig. 6 for comparison. The minimum orbital basis set (s31p31) predicts higher current flow for both junctions compared to orbital basis sets with polarization (s31p31d1). For example, at $V_{DS} = 0.4$ V, the p-n-p junction with s31p31 basis set, the current is 12% larger if we consider the current characteristic of s31p31d1 as the reference in the fully self-consistent approach. Also, this figure confirms that the difference between SC and non-SC results is greater at larger bias voltages for both structures as is intuitively expected.

The currents calculated with the non-SC loop are lower than applying the computationally more demanding NEGF method. This is attributed to an overestimation of the polarization effect induced when a bias is applied, whereas, SC allows for the redistribution of charges at non-equilibrium. This is important to take into account when simulating

current-voltage characteristics to extract the device performance. Here, this yields an underestimation of the source-drain current. Elsewhere,⁹ we have shown that estimates for switching between off- and on-states in nanowire transistors are much worse if self-consistency is disregarded in the calculation of the transfer characteristics. In that case, there is no redistribution of the charges in the channel to lower the barrier as gate voltage is applied and the tunneling current is overestimated in the non-SC case. From Fig. 5, it is deduced that the error is more significant than using a lower quality basis set.

The I - V characteristic of the p-i-p junction shows more sensitivity at higher bias voltages compared to the p-n-p junction. The reason is that there is larger scattering for the p-n-p junction due to a larger in-built potential compared to the p-i-p junction (see Fig. 5). This corresponds to higher level of injected electronic charges in the p-i-p junction yielding a higher current at large bias. Finally, the source-drain tunneling currents of the devices for the two doping profiles are around 0.5 nA and 2 nA for p-n-p and p-i-p junction, respectively. These values are an order of magnitude higher than what is suitable for high performance logic and larger than what can be achieved by junctionless Si nanowire devices,^{9,25}

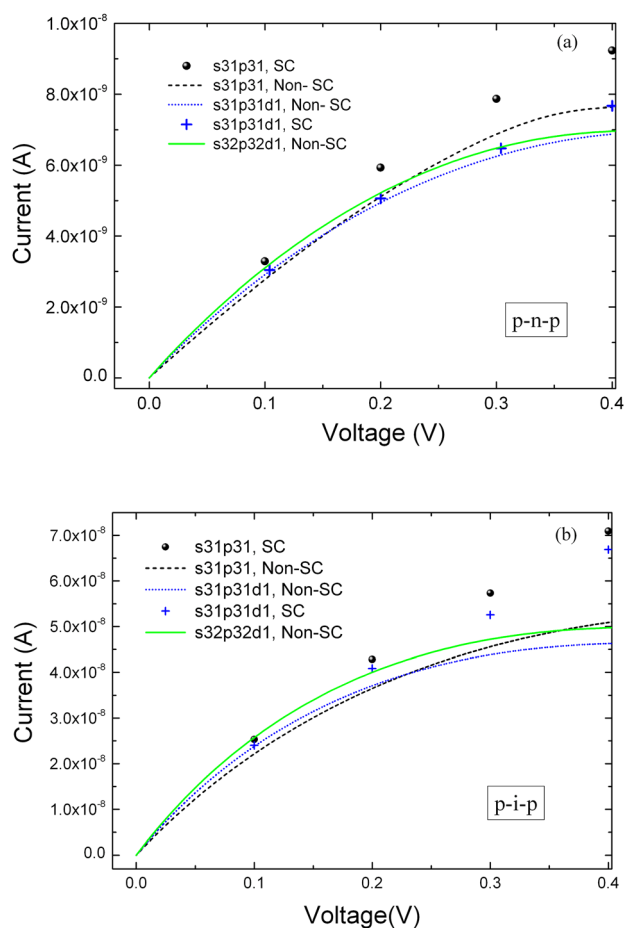


FIG. 6. Current-voltage characteristics of (a) p-n-p and (b) p-i-p junctions. Different orbital basis sets are compared for both structures. The scattered points show results based on the self-consistent $T(E, V_{DS})$ and the lines correspond to the linear response approximation $T(E)$.

that is, FETs with homogeneous source-channel-drain doping similar channel dimensions and NW orientation. Supporting our previous findings,²⁵ this suggests that at such scales the distribution of charge carriers around the dopant blurs the boundaries of a junction over a distance comparable to the channel length. Therefore, besides being very difficult to fabricate, junctioned FET designs at this scale will fail to effectively keep carriers out of the channel in the off-state.

IV. CONCLUDING REMARKS

To summarise, in this paper, we evaluate the impact of numerical atomic orbitals on electron transport properties mainly on charge carrier scattering in [110] oriented SiNWs with dopant impurities. A relatively weak dependence of the mean free path on the NAO basis set was found using explicit calculations of quantum transport that combine DFT with the Green's function formalism. This is in agreement with estimates based on a simple band analysis. For the case of weak scattering from impurities, the choice of the NAOs basis is considerably less important than for impurities inducing strong backscattering. It is found that optimised double-zeta orbitals with d polarisation functions reproduce the results with triple-zeta polarised basis; even in the case of suppressed transmission, the double zeta polarised

basis offers a good compromise between accuracy and computational efficiency. The single zeta basis set yields only qualitatively correct results.

We also assess the impact of various numerical atomic orbital approximations on the transport properties of silicon nanowire setups with p-n-p and p-i-p doping profiles. Interestingly, although the current-voltage characteristics of such ultrascaled (3 nm length) nanowire-based transistors show the expected sensitivity to the choice of the NAOs basis set, it is found that charge self-consistency affects mostly the source-drain current. Regarding the possibility to use these devices, it is concluded that the source-drain tunneling currents are relatively high—of the order of 0.5 nA and 2 nA for p-n-p junction and p-i-p junction, respectively—making it difficult to achieve good device performance for these architectures on the length scale considered.

ACKNOWLEDGMENTS

This research was funded by Science Foundation Ireland (SFI) under the Principal Investigator Grant No. 06/IN.1/I857. Partial support was provided through the European Union 7th Framework ICT-FET-Proactive program, SiNAPS project under Contract No. 257856. We also acknowledge computing resources provided by SFI to the Tyndall National Institute and by the SFI and Higher Education Authority Funded Irish Centre for High End Computing.

- ¹G. Declerck, "A look into the future of nanoelectronics," *Dig. Tech. Pap.—Symp. VLSI Technol.* **2005**, 6–10 (2005).
- ²N. Singh, A. Agarwal, L. K. Bera, T. Y. Liow, R. Yang, S. C. Rustagi, C. H. Tung, R. Kumar, G. Q. Lo, N. Balasubramanian, and D.-L. Kwong, "High-performance fully depleted silicon nanowire (diameter ≤ 5 nm) gate-all-around CMOS devices," *IEEE Electron Device Lett.* **27**, 383 (2006).
- ³H.-S. P. Wong, "Beyond the conventional transistor," *Solid-State Electron.* **49**, 755–762 (2005).
- ⁴K. Trivedi, H. Yuk, H. C. Floresca, M. J. Kim, and W. Hu, "Quantum confinement induced performance enhancement in sub-5-nm lithographic Si nanowire transistors," *Nano Lett.* **11**, 1412–1417 (2011).
- ⁵M. Nolan, S. O'Callaghan, G. Fagas, and J. C. Greer, "Silicon nanowire band gap modification," *Nano Lett.* **7**, 34 (2007).
- ⁶R. Rurali, "Structural, electronic, and transport properties of silicon nanowires," *Rev. Mod. Phys.* **82**, 427 (2010).
- ⁷H. Pan and Y. P. Feng, "Semiconductor nanowires and nanotubes: Effects of size and surface-to-volume ratio," *ACS Nano* **2**, 2410–2414 (2008).
- ⁸N. R. Das, M. W. Shinwari, M. J. Deen, and J.-S. Lee, "Electron states in a silicon nanowire in the presence of surface potential and field," *Nanotechnology* **23**, 415201 (2012).
- ⁹L. Ansari, B. Feldman, G. Fagas, J.-P. Colinge, and J. C. Greer, "Subthreshold behavior of junctionless silicon nanowire transistors from atomic scale simulations," *Solid-State Electron.* **71**, 58–62 (2012).
- ¹⁰E. J. Nowak, I. Aller, T. Ludwig, K. Kim, R. V. Joshi, C.-T. Chuang, K. Bernstein, and R. Puri, "Turning silicon on its edge [double gate CMOS/FinFET technology]," *IEEE Circuits Devices Mag.* **20**, 20–31 (2004).
- ¹¹B. Davari, R. H. Dennard, and G. Shahidi, "CMOS scaling for high performance and low power—the next ten years," *Proc. IEEE* **83**, 595 (1995).
- ¹²A. M. Ionescu and H. Riel, "Tunnel field-effect transistors as energy-efficient electronic switches," *Nature* **479**, 329 (2011).
- ¹³J. Hu, T. W. Odom, and C. M. Lieber, "Chemistry and physics in one dimension: Synthesis and properties of nanowires and nanotubes," *Acc. Chem. Res.* **32**, 435–445 (1999).
- ¹⁴G. Fagas and J. C. Greer, "Ballistic conductance in oxidized Si nanowires," *Nano Lett.* **9**, 1856–1860 (2009).
- ¹⁵M. Y. Bashouti, R. T. Tung, and H. Haick, "Tuning the electrical properties of Si nanowire field-effect transistors by molecular engineering," *Small* **5**, 2761 (2009).

- ¹⁶Z. Yun, C. Rivas, R. Lake, K. Alam, T. B. Boykin, and G. Klimeck, "Electronic properties of silicon nanowires," *IEEE Trans. Electron. Devices* **52**, 1097–1103 (2005).
- ¹⁷M. T. Bjork, H. Schmid, J. Knoch, H. Riel, and W. Riess, "Donor deactivation in silicon nanostructures," *Nat. Nanotechnol.* **4**, 103–107 (2009).
- ¹⁸F. M. Armando, G. Fagas, and J. C. Greer, "Deformation potentials and electron–phonon coupling in silicon nanowires," *Nano Lett.* **10**, 869–873 (2010).
- ¹⁹D. Esseni, P. Palestri, and L. Selmi, *Nanoscale MOS Transistors: Semi-Classical Transport and Applications* (Cambridge University Press, 2011).
- ²⁰M. Macucci, G. Iannaccone, J. Greer, J. Martorell, D. W. L. Sprung, A. Schenk, I. I. Yakimenko, K.-F. Berggren, K. Stokbro, and N. Gippius, "Status and perspectives of nanoscale device modelling," *Nanotechnology* **12**, 136–142 (2001).
- ²¹J. Taylor, H. Guo, and J. Wang, "Ab initio modeling of quantum transport properties of molecular electronic devices," *Phys. Rev. B* **63**, 245407 (2001).
- ²²M. Brandbyge, J.-L. Mozos, P. Ordejón, J. Taylor, and K. Stokbro, "Density-functional method for nonequilibrium electron transport," *Phys. Rev. B* **65**, 165401 (2002).
- ²³S.-H. Ke, H. U. Baranger, and W. Yang, "Electron transport through molecules: Self-consistent and non-self-consistent approaches," *Phys. Rev. B* **70**, 085410 (2004).
- ²⁴R. Gutierrez, G. Fagas, G. Cuniberti, F. Grossmann, R. Schmidt, and K. Richter, "Theory of an all-carbon molecular switch," *Phys. Rev. B* **65**, 113410 (2002).
- ²⁵L. Ansari, B. Feldman, G. Fagas, J.-P. Colinge, and J. C. Greer, "Simulation of junctionless Si nanowire transistors with 3 nm gate length," *Appl. Phys. Lett.* **97**, 062105 (2010).
- ²⁶S. Migita, Y. Morita, M. Masahara, and H. Ota, "Fabrication and demonstration of 3-nm-channel-length junctionless field-effect transistors on silicon-on-insulator substrates using anisotropic wet etching and lateral diffusion of dopants," *Jpn. J. Appl. Phys., Part 1* **52**, 04CA01 (2013).
- ²⁷B. Aradi, B. Hourahine, and T. Frauenheim, "DFTB+, a sparse matrix-based implementation of the DFTB method," *J. Phys. Chem. A* **111**, 5678–5684 (2007).
- ²⁸D. Sharma, H. H. Arefi, and G. Fagas, "Atomic basis sets for first-principles studies of Si nanowires," *Comput. Theor. Chem.* **991**, 32–39 (2012).
- ²⁹S. Goedecker, "Linear scaling electronic structure methods," *Rev. Mod. Phys.* **71**, 1085–1123 (1999).
- ³⁰M. Strange, I. S. Kristensen, K. S. Thygesen, and K. W. Jacobsen, "Benchmark density functional theory calculations for nanoscale conductance," *J. Chem. Phys.* **128**, 114714 (2008).
- ³¹C. W. Bauschlicher, J. W. Lawson, A. Ricca, Y. Xue, and M. A. Ratner, "Current-voltage curves for molecular junctions: The effect of CI substituents and basis set composition," *Chem. Phys. Lett.* **388**, 427 (2004).
- ³²J. A. Driscoll and K. Varga, "Convergence in quantum transport calculations: Localized atomic orbitals versus nonlocalized basis sets," *Phys. Rev. B* **81**, 115412 (2010).
- ³³C. Herrmann, G. C. Solomon, J. E. Subotnik, V. Mujica, and M. A. Ratner, "Ghost transmission: How large basis sets can make electron transport calculations worse," *J. Chem. Phys.* **132**, 024103 (2010).
- ³⁴V. Blum, R. Gehrke, F. Hanke, P. Havu, V. Havu, X. Ren, K. Reuter, and M. Scheffler, "Ab initio molecular simulations with numeric atom-centered orbitals," *Comput. Phys. Commun.* **180**, 2175 (2009).
- ³⁵T. Ozaki, "Variationally optimized atomic orbitals for large-scale electronic structures," *Phys. Rev. B* **67**, 155108 (2003).
- ³⁶T. Ozaki and H. Kino, "Efficient projector expansion for the ab initio LCAO method," *Phys. Rev. B* **72**, 045121 (2005).
- ³⁷See <http://www.openmx-square.org/> for source code availability, manual, and examples.
- ³⁸G. Fagas, "Vibrational properties of complex solids," Ph.D. dissertation (Lancaster University, 2000).
- ³⁹G. Fagas, G. Tkachov, A. Pfund, and K. Richter, "Geometrical enhancement of the proximity effect in quantum wires with extended superconducting tunnel contacts," *Phys. Rev. B* **71**, 224510 (2005).
- ⁴⁰G. Fagas, A. Kambili, and M. Elstner, "Complex-band structure: A method to determine the off-resonant electron transport in oligomers," *Chem. Phys. Lett.* **389**, 268 (2004).
- ⁴¹M. Shelleya, N. Poilvert, A. A. Mostofi, and N. Marzari, "Automated quantum conductance calculations using maximally-localised Wannier functions," *Comput. Phys. Commun.* **182**, 2174, (2011).
- ⁴²A. Calzolari, N. Marzari, I. Souza, and M. Buongiorno Nardelli, "Ab initio transport properties of nanostructures from maximally localized Wannier functions," *Phys. Rev. B* **69**, 035108 (2004).
- ⁴³I. Rungger and S. Sanvito, "Algorithm for the construction of self-energies for electronic transport calculations based on singularity elimination and singular value decomposition," *Phys. Rev. B* **78**, 035407 (2008).
- ⁴⁴S. Datta, *Electronic Transport in Mesoscopic Systems* (Cambridge University Press, 1999).
- ⁴⁵J. P. Colinge, *Finfets and Other Multi-Gate Transistors* (Springer, 2008).
- ⁴⁶T. Markussen, R. Rurali, A.-P. Jauho, and M. Brandbyge, "Theory put into practice: First-principles modeling of transport in doped silicon nanowires," *Phys. Rev. Lett.* **99**, 076803 (2007).
- ⁴⁷M. V. Fernandez-Serra, Ch. Adessi, and X. Blase, "Surface segregation and backscattering in doped silicon nanowires," *Phys. Rev. Lett.* **96**, 166805 (2006).
- ⁴⁸Y. Cui, X. Duan, J. Hu, and C. M. Lieber, "Doping and electrical transport in silicon nanowires," *J. Phys. Chem. B* **104**, 5213 (2000).
- ⁴⁹J.-P. Colinge, "Multiple-gate SOI MOSFETs," *Solid-State Electron.* **48**, 897–905 (2004).