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Performance Analysis of a 2-D Time-wavelength OCDMA Wavelength-Aware Receiver with Beat Noise

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ABSTRACT

The effect of beat noise on two-dimensional time-wavelength optical code-division multiple-access systems utilising wavelength-aware receivers is examined. A derivation of a general formula for the bit error probability taking into consideration multiple access interference (MAI) and other noise sources is given. In addition, a comparison between the system performance of such a receiver and the traditional configuration is presented. Studies to date that have focused only on the MAI limited case showed that the wavelength-aware configuration yields a better performance when compared to the traditional receiver. When beat noise is considered, the numerical results reveal that the performance of wavelength-aware receiver is very sensitive to beat noise and is not superior over the traditional receiver.

1. INTRODUCTION

Two-dimensional (2-D) time-wavelength (TW) optical code-division multiple-access (OCDMA) systems have attracted much attention since they offer a larger code size, superior auto-and cross-correlation properties and higher bandwidth efficiency [1-2]. Low co-channel interference is inherent to 2-D codes since interfering pulses must overlap in both time and wavelength. Recently, a wavelength-aware receiver (WAR) [3] was introduced as an effective way to enhancing the system performance over the traditional configuration [4], doubling the spectral efficiency. However, such a study has only been carried out under the multiple access interference (MAI) limited case.

One of the limiting factors with TW schemes and indeed other OCDMA coding approaches, is beat noise (BN) [5-6] which occurs when different optical signals – the main signal and crosstalk term(s) - at nominally the same frequencies are incident simultaneously on a photodetector. Due to the action of the square law detector, the photocurrent owing to BN is much greater than that attributed to the incident optical power of the crosstalk signal, producing severe system performance degradations.

In this paper, continuing on previous work [3], the behaviour of a 2-D TW OCDMA system employing wavelength-aware receivers taking into account beat noise is examined. The system model and assumptions for a 2-D TW OCDMA scheme utilising coherent laser sources are given in Section 2. In Section 3, a general formula for the error probability of the system is introduced. Within Section 4, results and discussions are presented and finally, conclusions are drawn in Section 5.

2. SYSTEM DESCRIPTION

2.1 System model

The 2-D OCDMA system under consideration consists of multiple transmitters and receivers within a star configuration. At the transmitter, the optical source is modulated according to the user data and the output is encoded in two dimensions. In normal multi-user environments, the encoded signals of the intended user and other users are simultaneously incident at the receiver. The following assumptions are applied in the analysis [5-6]:

- all signals are on-off keyed (OOK) and in a non-return to zero (NRZ) format.
- code chips are generated using arrays of single mode lasers or tunable lasers.
- chirp-free external modulation and infinite modulation depth.
- chip synchronous case, resulting in an upper bound in relation to the asynchronous system performance. This does not infer that the system operation is synchronous; chip asynchronicity is assumed.
- all interferers are of the same intensity at the receiver.
- data and interferer pulses are of the same polarization state (worst case).
- the frequency separation between chips at different wavelengths exceeds the receiver electrical bandwidth. Also, the frequency between interferer chips at different wavelengths exceeds the receiver's electrical bandwidth. Thus, the model focuses only the beat noise caused by the overlap of identical wavelengths.

2.2 Principle of operation

Figure 1 shows the block diagram of the WAR,

comprising an optical 2-D TW decoder followed by arrays of photodetectors and electronic thresholding elements, the outputs of which are connected to an electronic AND gate. The number of photodetectors is equivalent to the code weight (active chips). Each branch, independently, decides on the existence of each active chip within the intended code. A bit "1" is detected if at least one pulse is present at each branch and the absence of a pulse is sufficient to declare a "0" bit. It is clear that the WAR requires more hardware than the traditional receiver since the latter comprises a decoder, single PD and ET.

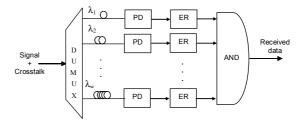


Figure 1. Wavelength aware receiver architecture. PD denotes the photodetector and ET is an electronic threshold element.

3. PERFORMANCE EVALUATION

In this section, the system performance in terms of bit error rate (BER) is derived taking into account the dual effects of MAI and beat noise. The main difference between beat noise and MAI is that BN affects both bits "1" and "0" with additional levels of beat noise expected when transmitting bit "1". The error contributions owing to both data bits must be evaluated. Consider the case of transmitting a desired data bit "0"; an error occurs when the receiver decides a bit "1" due to detection of a mark in all "0" positions. Such a case is conditioned by the distribution of interference patterns. Assume that "j" interference patterns produce hits with the desired code and every interference patterns) representing all interfering users. Note that each basic interference pattern contains all the permutation of such a vector. The number of vector permutations and the corresponding multinomial probability distribution (*MPD*) for a specific value of "*j*" can be expressed respectively as follows [2]:

$$NDP(\overline{\alpha}) = \frac{w!}{\prod_{i} R(\alpha_{i})}$$
(1)

$$MPD(\overline{\alpha}; F_j) = \frac{j!}{w^j \cdot \prod_{d=1}^w (\alpha_d !)}$$
(2)

where $R(\alpha_i)$ is the repetition of element α_i in the vector and w is the code weight. Amongst F_j , a sub-set of vectors or patterns will create errors, denoted by a set G_j . When transmitting a bit "0", G_j contains all non-zero vectors affecting all "0" chip positions. The probability of error due to transmitting bit "0", P(E|0), can be conditioned on the following events. "i" users among the (K-1) probable interferers may transmit a data bit "1" with probability equal to "1/2". Among these "i" users, there exist "j" users that may have a hit at the auto-correlation peak of the desired user viz. crosstalk. The average probability of a hit follows a binomial distribution and is equal to " h_{av} ". Such a parameter is dependent on the 2-D TW code utilised. Therefore, the bit error probability for the intended user on transmission of bit "0" is given by:

$$P(E \mid 0) = \frac{1}{2} \sum_{i=w}^{K-1} \binom{K-1}{i} \cdot 2^{-(K-1)} \cdot \sum_{j=w}^{i} \binom{i}{j} h_{av}^{j} \cdot (1-h_{av})^{i-j} \cdot \sum_{\alpha_d \in G_j} P_E(\overline{\alpha}_d)$$
(3)

 $P_E(\overline{\alpha})$ is the probability of error given that the appropriate pattern is received. The minimum number of users required to produce an error, for the case of transmitting bit "0", is equal to the code weight. Consequently, the summations in Equation (3) starts with i=w. The probability, $P_E(\overline{\alpha})$, can be computed as follows:

$$P_{E}(\overline{\alpha}_{d}) = \prod_{m=1}^{W} NDP(\alpha_{d}) . MPD(\alpha_{d}) . Q(\frac{p_{d} . D - \alpha_{m} . P_{c}}{\sqrt{\sigma_{th}^{2} + \sigma_{sh_{0}}^{2} + \sigma_{RIN_{0}}^{2} + \sigma_{BN_{0}}^{2}}})$$
(4)

where P_d and P_c are the instantaneous optical chip power of the data signal and crosstalk respectively and D is the threshold level. The variance of thermal, shot, relative intensity noise (RIN) and beat noise can be expressed respectively by:

$$\sigma_{th}^{2} = \frac{4.K_{B}.T.B_{e}}{R_{L}}, \ \sigma_{sh_{0}}^{2} = 2.q.I.B_{e}, \ \sigma_{RIN_{0}}^{2} = RIN.I^{2}.B_{e}, \ \sigma_{BN_{0}}^{2} = 2.P_{c}.P_{c}.\binom{\alpha_{m}}{2}$$

$$I = \alpha_{m}.P_{c}$$
(5)

is $\sum_{m=1}^{\infty} \alpha_m = j$. Combining equations (3) and (4), the probability of error due to the transmission of bit "0" can be written as follow:

$$P(E \mid 0) = \frac{1}{2} \sum_{i=w}^{K-1} \frac{\binom{K-1}{i}}{2^{(K-1)}} \sum_{j=w}^{i} \binom{i}{j} h_{av}^{j} \cdot (1-h)^{i-j} \sum_{\alpha_d \in G_j} \prod_{m=1}^{w} NDP(\alpha_d) \cdot MPD(\alpha_d) \cdot Q(\frac{p_d \cdot D - \alpha_m \cdot P_c}{\sigma_{tot_0}})$$
(6)

Now consider the case of transmitting a data bit "1"; an error occurs when at least one active chip, i.e. wavelength, is detected in the time slot of bit "0". In this case, the damage vectors are both zero and non-zero since a wrong decision at any chip position will produce an error. Therefore, $F_j = G_j$ and consequently, more damage patterns producing errors are likely. Now the minimum number of users required to produce an error is 1 instead of w. Following the previous equations, the probability of error resulting from the transmission of bit "1" can be written as:

$$P(E \mid 1) = \frac{1}{2} \sum_{i=1}^{K-1} \frac{\binom{K-1}{i}}{2^{(K-1)}} \sum_{j=1}^{i} \binom{i}{j} h_{av}^{j} \cdot (1-h_{av})^{i-j} \sum_{\overline{\alpha}_{d} \in F_{j}} \sum_{m=1}^{w} NDP(\overline{\alpha}_{d}) \cdot MPD(\overline{\alpha}_{d}) \cdot Q(\frac{P_{d} + \alpha_{m} \cdot P_{c} - P_{d} \cdot D}{\sqrt{\sigma_{ih}^{2} + \sigma_{kh_{1}}^{2} + \sigma_{kh_{1}}^{2} + \sigma_{kh_{1}}^{2}})$$
(7)

The noise variance in equation (7) is similar to (5) but the average current is $I = P_d + \alpha_m P_c$ and the variance of beat noise is given by:

$$\sigma_{BN_1}^2 = 2.\alpha_m.P_d.P_c + 2.P_c.P_c.\begin{pmatrix}\alpha_m\\2\end{pmatrix}$$
(8)

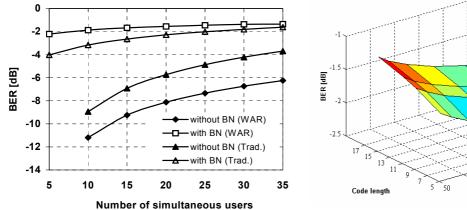
Thus, the total bit error rate for wavelength-aware receivers is the summation of equations (6) and (7). For MAI limited case, the probability of error can be expressed as:

$$P_E = \frac{1}{2} \sum_{i=w}^{K-1} \binom{K-1}{i} \cdot 2^{(K-1)} \cdot \sum_{j=w}^{i} \binom{i}{j} \cdot h_{av}^j \cdot (1-h)^{i-j} \cdot \sum_{\overline{\alpha}_d \in G_j} \prod_{m=1}^{w} NDP(\overline{\alpha}_d) \cdot MPD(\overline{\alpha}_d)$$
(9)

The analysis framework developed previously can be best illustrated by the following example. Let the number of interference be *j*=6 and the code weight *w*=3. Thus, all the possible combinations of interference patterns are: F_6 ={(6,0,0),(5,1,0),(4,2,0),(4,1,1),(3,3,0),(3,2,1),(2,2,2), and all vectors permutations}. Thus, the total number of vectors in F_6 =28 and all those vectors may affect the decision of bit "1". On the other hand, amongst F_j , only the non-zero vectors containing any non-zero element can produce an error for bit "0". Thus, G_6 = {(4,1,1),(3,2,1),(2,2,2)}. According to equation (1), the *NDP* of the basic vectors are *NDP*(4,1,1)=3!/(1!.2!)=3, *NDP*(3,2,1)=3!/(1!.1!.1!)=6, and *NDP*(2,2,2)=3!/3!=1; the total number of non-zero vectors including their permutations are 10. The corresponding multinomial probability distributions can be evaluated according to equation (2) such that: *MPD*(4,1,1)=6!/(3⁶.4!.1!.1!)=0.0411 and so on.

4. RESULTS AND DISCUSSIONS

System performance is calculated assuming that a bit rate 100MHz, $R_L = 1 \text{ k}\Omega$, RIN = -150 dB/Hz, $P_d = Pc = 1.42 \text{ mW}$ and employing a 2-D carrier-hopping prime code (CHPC) of weight w = 7 and length N = 101 [2]. For CHPC, $h_{av} = w/N$. The bit error rate (BER) is plotted as a function of number of simultaneous users with and without BN and compared to the traditional receiver (Fig. 2). It is clear that the results emphasize the superior performance of WAR over the traditional receiver for the MAI limited case is evident. However, with BN, the performance of WAR is worse than the traditional receiver; 2dB at 5 users. Furthermore, as the number of users' increases this penalty becomes smaller. The main source of the limits in WAR or the traditional receiver is the error due to receiving a bit "1". For WAR, if any chip is corrupted by BN according to a specific interference pattern, then the decision is false, independent of other chips. The traditional receiver decides whether the receiving bit is "1" or not based on the accumulative effect of BN on all chips. So, for the same interference pattern, if one chip is affected, BN does not necessarily affect the overall decision. The situation for the MAI case is reversed where the only limiting factor is due to transmission of bit "0". If all chips in WAR are simultaneously corrupted, an error occurs. This probability is very low when compared to the traditional receiver where, if sufficiently high levels of beat noise occur at any chip, then an error may result. However, increasing



the code length results in slightly improved performance since the probability of a hit is reduced whereas the performance is worsens with increasing code weight (Fig. 3).

Figure 2. BER versus number of simultaneous users with/without BN for WAR and traditional receiver.

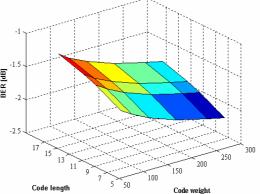


Figure 3. BER versus (weight, length).

5. CONCLUSIONS

A performance analysis of a 2-D time-wavelength OCDMA system utilising coherent light sources and a wavelength-aware configuration at the receiver has been presented. The analysis includes major system noise sources but focuses on beat noise and multiple access interference. Results show that the detrimental effect of beat noise on such a system and, contrary to the MAI limited case, the performance of WAR under beat noise yields no improvement when compared to the traditional receiver. Although the WAR geometry appears to offer improved performance, the analysis show that when all form of interference is considered, and especially beat noise, the performance of WAR degrades significantly. This result has implications for the use of such a receiver.

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