NOVEL INTEGRATION OF CONDUCTIVE-INK CIRCUITRY WITH A PAPER-BASED MICROFLUIDIC BATTERY AS AN ALL-PRINTED SENSING PLATFORM

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TITLE:	Novel Integration of Conductive-ink Circuitry with		
	a Paper-based Microfluidic Battery as an All-		
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ABSTRACT

Novel Integration of Conductive-ink Circuitry with a Paper-based Microfluidic Battery as an All-printed Sensing Platform

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The addition of powered components for active assays into paper-based analytical devices opens new opportunities for medical and environmental analysis in resourcelimited applications. Current battery designs within such devices have yet to adopt a ubiquitous circuitry material, necessitating investigation into printed circuitry for scalable platforms. In this study, a microfluidic battery was mated with silver-nanoparticle conductive ink to prototype an all-printed sensing platform. A multi-layer, two-cell device was fabricated, generating 200 μ A of direct electrical current at 2.5 V sustained for 16 minutes with a power loss of less than 0.1% through the printed circuitry. Printed circuitry traces exhibited resistivity of 75 to 211 10⁻⁵ Ω ·m. Resistance of the printed traces increased upwards of 200% depending on fold angle and directionality. X-ray diffraction confirmed the presence of face-centered cubic silver after sintering printed traces for 30 minutes at 150°C in air. A conductivity threshold was mapped and an ink concentration of 0.636 μ L·mm⁻³ was identified as the lower limit for optimal electrical performance.

Keywords: silver nanoparticle ink, microfluidic battery, percolation threshold, paperbased analytical device, µPAD, printed circuitry, x-ray diffraction, resistivity

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1. INTRODUCTION

1.1 Project Definition

1.1.1 Problem Statement

Microfluidic paper-based analytical devices (µPADs) are ultra-low cost, open-source, scalable, portable, and combustible solutions to the need for biological and chemical sensing assays in resource-limited environments. These devices print hydrophobic channels on paper substrate sheets using solid-wax ink and capitalize on capillary flow to move small-volume liquid samples toward regions with pre-deposited reagents, providing a chemical sensing platform tunable to the analyte. In their current form, they are limited in their inclusion of active assays, powered electronics and automatable features. The microfluidic battery is a multi-layer µPAD with pre-deposited electrolytes and metal-film electrodes, generating electrochemical power from the galvanic potential. As such, active assays and powered electronics have been added to the envelope of solutions able to be addressed by µPADs. However, current microfluidic battery designs use copper tape as the circuitry material to deliver terminal voltages spatially with the device layers, limiting automation capability and feature size. Therefore, the integration of printable external circuitry onto the cellulose substrate and into the battery design is valuable for advancing the automation of this chemical sensing platform. The subsequent question to ask is: Can a printable circuitry material be integrated into a microfluidic battery to displace the existing technology used?

1.1.2 Central Hypothesis

A printed circuitry material can be integrated into microfluidic battery design without compromise to battery performance, mechanical properties, packaging, and assembly processes to create an all-printed platform.

1.1.3 Scope

The key deliverable was a working prototype of a microfluidic battery with circuitry external to galvanic cells, yet integrated with the substrate, assembly, and function of the device. The scope of this work includes only the material selection, integration, and characterization of the circuitry material. Success was measured by the satisfaction of quantitative, singular, verifiable, non-conflicting, unambiguous requirements set forth in the design section (Defense Acquisition University Press, 2001). The short-circuit current and open-circuit voltage were recorded to define the performance of the battery. The conduction threshold of ink concentration was mapped using a matrix of sintering parameters; the presence of bulk silver was confirmed using x-ray diffraction. To characterize the circuitry's stability to mechanical fatigue, folding was induced and electrical resistance was recorded.

1.1.4 Limitations & Constraints

Limitations of scope narrow the context of this research's findings, but the objective is only to obtain a modicum of knowledge or progress toward the envelope of solutions μ PADs offer. Thus, the following constraints have been applied to the scope of this investigation:

- The design is constrained to an existing microfluidic battery platform (Thom, Lewis, DiTucci, & Phillips, 2013; Thom, Yeung, Pillion, & Phillips, 2012).
- Chemical sensing functions and features are excluded from the design of the platform.
- The findings of device and ink performance do not extend beyond the context of the cellulose-paper substrate.
- The theoretical value of percolation threshold is not defined, nor is the error between it and the empirically established value quantified.

1.2 System Requirements

The system requirements set forth can be divided into three tiers of relevance: the circuitry material, the integration of that material into the device and the platform interface. The circuitry material must be conductive such that the performance of the microfluidic battery is not inhibited. The circuitry material must also be stable on the substrate over time to allow for assembly and packaging. The integration of the printed circuitry must be mechanically robust and low-temperature to preserve the flexibility of the paper substrate. As an all-printed platform, the device must enable data collection sufficiently long enough for existing assays or sensing. The platform must also be able to interface with the electronic components used in such assays.

2. BACKGROUND

This master's thesis integrates three key technology platforms to test the central hypothesis: wax printing of microfluidic paper-based analytical devices (μ PADs), microfluidic batteries and reactive silver-nanoparticle conductive ink.

2.1 Wax Printing

Wax printing is the micro-patterning of solid wax as ink on sheets of cellulose-paper substrate to create simple, inexpensive microfluidic devices. These wax traces are patterned on paper using various printing methods such as photolithography, screenprinting, and solid-wax inkjet printers. By heating under prescribed profiles, the solid wax wicks through the depth/thickness of the paper, creating a sealed channel for microfluidic flow between hydrophobic traces (Carrilho, Martinez, & Whitesides, 2009). Microfluidic paper-based analytical devices (µPADs) are chemical or biological assays that capitalize on both the nano-scale phenomena of microfluidics and inexpensive, simple fabrication of wax printing to create diagnostic tests. Hydrophobic channels are patterned onto cellulose paper substrates that direct flow of liquid samples to detection pads. Reagents required for chemical sensing of the analyte are pre-deposited onto the cellulose substrate at specific locations and concentrations. Qualitative results (e.g. color change) can be designed into the chemical sensing reaction; quantitative data collection uses external equipment (e.g. glucose readers, spectrophotometers, cell phones) to interpret the assay (Carrilho et al., 2009; Cate, Adkins, Mettakoonpitak, & Henry, 2015; Park, Li, McCracken, & Yoon, 2013).

These innovations are particularly well suited for remote testing in resource-limited countries because they are built on an ultra-low-cost platform and carry minimal effort for end-of life handling (i.e. disposal via burning). The minimal embedded energy and safe, reliable disposal circumvents the embedded high infrastructure costs of typical laboratory testing (Carrilho et al., 2009; Cate et al., 2015).

The salient lesson is that no single μ PAD technology has proven superior. The power and versatility of μ PADs is their tunable nature: devices can vary in substrate, patterning method, reagent composition, target analyte, and data collection. The number of permutations is countless, as are the possible applications.

2.2 Microfluidic Battery

Microfluidic batteries are "galvanic cells incorporated directly into a multilayer paperbased microfluidic device." Aqueous solutions of electrolytes for each electrode half-cell and the salt bridge that connects the half-cells are pre-deposited onto different layers of the device; the water is evaporated before assembly. The deposited electrolytes contact the metal foil electrodes and are connected via a salt bridge, creating a full galvanic cell. Upon the addition of a solvent (in this case, deionized water), the ions of the electrolyte and salt are remobilized in solution, and the reduction and oxidation reactions at the electrodes can proceed. Thus, these devices are "activated" or "turned on" via the addition of a fluid. Furthermore, the use of microfluidic channels to connect each halfcell adds to the relevance of the battery's "fluidic" nature. Determining factors of the power generated are volumetric density of ions, feature size, arrangement of cells, and choice of electrode metals. The influence of each is well characterized and tunable for desired performance (Thom, Lewis, DiTucci, & Phillips, 2013).

To create a microfluidic battery, a multi-layer device using alternating layers of hydrophobic dual-sided adhesive layers and hydrophilic paper layers with per-patterned wax-printed hydrophobic channels is assembled to direct deionized water to an unsaturated NaNO₃ salt bridge connecting two metal electrodes, specifically aluminum and silver (Thom, Lewis, DiTucci, et al., 2013; Thom et al., 2012). The approach of this study is based on this design, but deviates from prescribed electrolyte densities as well as hydrophobic and circuitry materials.

2.2.1 Galvanic Potential

The generation of electrical potential using galvanic cells is well established and the core principle of the microfluidic battery. An electrical circuit of electron and ion channels is connected to two dissimilar metals, creating a complementary flux of dissociated electrons and ions from the corresponding reduction-oxidation reaction at each electrode surface. (Figure 1, [Hazmat2, 2012]) The resulting flow of electrons through the external circuitry is used to power an electrical load until the reaction becomes thermodynamically stable and nonspontaneous. Upon reaching this equilibrium, charges no longer conduct and current through the load diminishes and reaches a terminus.



Figure 1. Diagram of a galvanic cell

Given the half-cell reactions,

$$Ag^{+} + e^{-} \rightleftharpoons Ag(s) \tag{1}$$

$$Al^{3+} + e^{3-} \rightleftharpoons Al(s) \tag{2}$$

and their associated standard electrode potentials,

$$E_{Ag}^{\circ} = 0.7996 \text{ V}$$

 $E_{Al}^{\circ} = -1.662 \text{ V}$

we expect a standard electrode potential (E°_{cell}) of the redox reaction

$$E_{cell}^{\circ} = E_{cathode}^{\circ} - E_{anode}^{\circ}$$
(3)

to yield a theoretical potential of 2.46 V. This value agrees with our expectations of spontaneity ($\Delta G^{\circ} \leq 0$) described by the standard electrode potential identity of Gibb's free energy of as

$$\Delta G^{\circ} = -nFE_{cell}^{\circ} \tag{4}$$

where *n* is the molar ratio of electrons to reaction products and *F* is the Faraday constant of 9.64850332 (10⁴) C mol⁻¹. Furthermore, this theoretical potential value is 35% above the design requirement for threshold voltage discussed in later sections, so the silveraluminum system is appropriate for use in this device.

2.2.2 Layer Functions

There are eight (8) discrete layers of alternating hydrophobicity of the microfluidic battery, each serving a separate function required for power generation. (Table 1)

Layer	Substrate	Patterning Method	Function
1	Adhesive tape	Mechanical stamping	Sample input
2	Cellulose paper	Solid-wax inkjet	Distribute sample
3	Adhesive tape	Mechanical stamping	Salt bridge input
4	Cellulose paper	Solid-wax inkjet; NaNO3 pre-deposited	Reconstitute salt bridge
5	Adhesive tape	Mechanical stamping	Isolate electrode pads
6	Cellulose paper	Solid-wax inkjet; AgNO ₃ /AlCl ₃ pre-deposited	Electrolyte conduction; redox interface
7	Adhesive tape	Mechanical stamping	Affix electrodes
8	Copper tape	Mechanical cutting	External circuitry

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By constricting the exterior dimensions of each layer to be identical, wax-printed features and vertical channels remain aligned during assembly. As an added benefit, alignment errors after assembly can be identified visually. (Figure 2)



Figure 2. Exploded diagram of device layers

The microfluidic battery not only capitalizes on the capillary flow of deionized water input; the water itself reconstitutes the pre-deposited salt bridge and electrolytes by dissolving these reagents back into solution. The ion path of the galvanic cell is thus activated upon the reconstitution of this salt bridge. (Figure 3) The electron path provided by copper-tape circuitry is already attached to the opposing face of the electrodes, ready to conduct upon "turn-on" of the device.



Figure 3. Microfluidic path of activating solvent

2.3 Conductive Ink

Pioneers of the industry have attempted mapping and addressing the needs for circuitry in devices that use electrical potential for μ PAD functions. In the interim, copper tape has been nearly ubiquitous placeholder for prototyping.

A thorough investigation of solid metal wires using zinc or tin, attached to the substrate with a bead of conductive epoxy, has been conducted to mimic techniques used currently in the printed-circuit board industry (Siegel et al., 2010). Patterning methods are well established and abundant for metallic wires, such as chemical or physical deposition, and have already been implemented on a variety of substrate materials. Such methods lend themselves to high accuracy and small feature size, both of which are particularly well suited for automation. However, high equipment, process and material costs prohibit their viability for salable platforms.

Graphite electrodes and circuitry have drawn significant interest for the inexpensive, numerous carbon precursors, variety of solution chemistries, and tunable electrical properties of different carbon allotropes. Inkjet printing methods of carbon-based inks are numerous, readily available and inexpensive, but uniformity and reliability of conductive traces is limited by the capacitive properties of graphite. Higher conductivity allotropes such as carbon nanotubes can be implemented but increase cost considerably and their end-of-life disposal is poorly understood (Lan et al., 2013).

Conductive nanoparticle inks exhibit attractive qualities for flexible printed media. Several permutations of investigations varying patterning method and copper, silver, or gold nanoparticles have been conducted, capitalizing on elevated homologous temperatures and conductivity limits. However, these precursors costs and processing temperatures can be prohibitive and thus limit their applicability. Copper and gold nanoparticles are already used in biomedical sensing, but copper oxide formation is difficult to mitigate without complex sintering chemistries. Additionally, gold is a conflict mineral and contradicts the purpose of servicing resource-limited or contaminated environments (Adkins, Boehle, & Henry, 2015; Cate et al., 2015; Shari, Ghobadian, Cavalcanti, & Hashemi, 2015; Thom, Lewis, DiTucci, et al., 2013).

Conductive inks are electrically conductive liquid solutions that are stable under ambient conditions but, upon printing, their solvent evaporates leaving solid particulates adhered to the substrate media. Conductive inks allow for the deposition of conductive material with high precision and resolution using conventional patterning methods such as screen and inkjet printing. These materials are especially relevant when patterning on substrates such as paper or PET which require low processing temperatures (under 200°C) (Glavan,

Walker, Lewis, Whitesides, & Link, 2016; Kim & Moon, 2005; Perelaer, Abbel, Wünscher, Jani, & Lammeren, 2012). Ink-based circuitry complements both the ultralow-cost basis and manufacture scaling of μ PADs. Their incorporation is especially simpler when patterning techniques such as screen or inkjet printing are utilized because both are established methods and can be automated.

This study implements silver-nanoparticle conductive ink that is vetted specifically for high conductivity, proven printability, and minimal oxide formation. In addition, this conductive ink is well suited for the mild temperatures required for devices using the cellulose paper substrate as a platform. Furthermore, the incorporation of the conductive ink can be seamless with the multi-layer design of microfluidic batteries.

2.3.1 Synthesis

The conductive ink is synthesized through a modified Tollens' reaction as prescribed in (Walker & Lewis, 2012). The resulting solution is a diamminesilver (I) complex in an ammonium formate solvent containing 22 wt % silver. Stable in ambient conditions up to six (6) months, the solution only precipitates silver particles when exposed to air (i.e. when patterned on substrate). Upon printing, the labile amine ligands evaporate and silver nanoparticles ranging from 2-200 nm in diameter remain. (Figure 4) By design, sintering at temperatures below 100° C are purported to achieve the conductivity of bulk silver within 15 minutes (Walker & Lewis, 2012).



Figure 4. Chemical species evolution of conductive ink

2.3.2 Percolation Theory

Percolation theory in the context of material science is the mapping of bonding between sites of a given three-dimensional network. Within any such network there exists a threshold, p_e above which the sites are continuously connected within a given volume. The percolation threshold, p_e , is defined as the "concentration (occupation probability) pat which an infinite cluster appears for the first time in an infinite lattice" (Christensen, 2002). In the context of sintering solid metal particles, it can be used to define the minimum concentration of dispersed particles required for atomic diffusion. Above this critical value, conductive networks have sufficient bonding for electrical conductivity and atomic diffusion to occur (George & Sebastian, 2008). In the case of this study, is has been shown that "conductivity in conductor-insulator system is zero below p_e and increases with a power law above p_e " (Heitjans & Kärger, 2005). Theoretical values of this threshold can be determined using simulation and modeling, given various particle geometry, size distribution, and concentration, but empirical approaches employing percolation models have been shown to be highly accurate for metallic silver networks of submicron particle size (Deptuck, Harrison, & Zawadzki, 1985; Holm & Cima, 1989). In this study, the threshold will be mapped operationally as a minimum ink concentration, circumventing the need for simulation or modeling.

2.3.3 Conductivity

Conductivity, the reciprocal of resistivity, is defined (for elemental metallic solids) as a material's ability to conduct electrical current, often expressed as

$$\sigma = n_e q \mu_e \tag{5}$$

where n_e is the number density per volume of conduction electrons available, q is the elementary charge of an electron (C), and μ_e is the electron mobility (m² V⁻¹ s⁻¹). The density and charge of electrons cannot be changed; therefore, the only method accessible to increase conductivity is the increase of electron mobility by manipulating the material's crystal lattice. The determining factor of electron mobility is scattering time, even more so in metallic nanoparticles (as is the case for this research), where dimensional quantum confinement and interfacial/boundary scattering are nontrivial. Reducing the short-range disorder causing interfacial scattering, namely surface roughness, results in a disproportionate/nonlinear increase in electron mobility (Ferry, Goodnick, & Bird, 2009). It has been shown that for networks of silver nanoparticles with high surface area to volume ratio, the interfacial resistance is appreciable, which attributes to the deviation of conductivity from their bulk state. Densification increases the number of "percolating pathways for electron" travel and lowers interfacial resistance, resulting in an increase in conductivity (or decrease in resistivity) (Perelaer et al., 2012).

The resistance (Ω) of bulk materials, modeled as a simple discrete wire, is described by

$$R = \rho \, \frac{L}{A} \tag{6}$$

where ρ is resistivity (Ω m), *A* is cross sectional area (m) of the electron path, and *L* is the length (m) of wire. As densification increases and additional electron pathways are established, the effective area of conduction also increases. Therefore, the integral consideration for conductivity of silver nanoparticle inks is the degree to which the network is connected and sintered.

2.3.4 Sintering

Sintering is the process of treating solid, discretely separate particulates with temperature and/or pressure to create a continuous network of grains from a powder precursor. The applied kinetic energy creates a diffusion gradient between the interior and exterior of the network. The surface and grain-boundary diffusion of atoms fuse the interfaces of these particles together, after which sufficient diffusion pathways are established for lattice diffusion. Upon the change in atomic transport regime, exterior atoms move inward along the gradient and occupy the cavities/voids in the powder network, resulting in a reduction in volume (i.e. densification) and grain growth. This process is differentiated from other heat treatments by the absence of liquefaction, as the treatment temperatures are significantly below the material melting point. The temperature at which a material is sintered is expressed as the homologous temperatures, defined as the fraction of the material's melting temperatures (T/T_m) . This homologous temperature is a key thermodynamic variable for modulating which regime/mechanism of atomic diffusion (i.e. surface, grain boundary, or lattice) is dominant in a crystal lattice. It is then deduced that melting temperature is a defining material property in the sinter profile of a precursor (Knoerr, Kraft, & Schletz, 2010; Rahaman, 2003).

It has been established that particles with especially high surface area to volume ratios experience a decrease in melting temperature (Asoro, Damiano, & Ferreira, 2009; Greer & Street, 2007). The change in surface energy's contribution to free energy of these systems account for the non-linear deviation from the calculated relationship between melting point and particle size, namely below 100 nm (Allen, Bayles, Gile, & Jesser, 1986). These nano-scale phenomena and size effects are most applicable to solid particles with spherical geometry (Wronski, 2002). In the case of nanometer-sized particles, a decrease in melting temperature corresponds directly to increases homologous temperature and therefore improves the sintering efficiency at lower temperatures.

3. DESIGN

3.1 Functional Requirements

This study approaches the hypothesis by fabricating a prototype device that adds a conductive-ink circuitry layer to an existing microfluidic battery design derived from *Thom et al.*

The functional requirements are prescribed to be quantitative, singular, verifiable, nonconflicting and unambiguous statements, translated from the system requirements and tested in methods conforming to standard practices for quantification (Cate et al., 2015; Defense Acquisition University Press, 2001; Shari et al., 2015):

- Absolute resistance accounting for no more than 5% loss of power generated.
- Circuitry material must be chemically stable at 25°C in air on the cellulose substrate for 30 days (Glavan et al., 2016).
- Device must be assembled at temperatures below 200° C to preserve mechanical properties of the substrate.
- Functionality at bend angle (θ) of 90° must be preserved with maximum 20% loss of conductivity at 180° (Siegel et al., 2010).
- Battery must generate power for a full-duration half-max minimum of 5 minutes (Ellerbee et al., 2009; Park et al., 2013).
- Battery must maintain power levels above the voltage threshold and forward current of commercially-available LEDs, specifically 1.8V and 100 μA (LITEON Optoelectronics, 2006).

3.2 Microfluidic Battery

3.2.1 Design Adaptations

The design of the fluidic battery was derived from the work conducted by *Thom et al*. These batteries have proven effective in generating sufficient power for on-chip components (including LEDs). This solution employs design permutations adapted from the original research and chosen as a compromise between feature size, complexity, and maximum potential (Thom, Lewis, DiTucci, et al., 2013; Thom et al., 2012). These compromises include:

- A linear arrangement of cells in series was chosen because it raises operating voltage (2.7V) to above the threshold of common surface-mount LEDs (1.8 V).
- A 9mm² electrode size extends the device "on" time to minutes, which is experimentally favorable for testing and characterization. Additionally, this electrode size is a footprint proportionate to the size of interfacing features, namely the salt bridge. Alignment is paramount during assembly; larger features can be more forgiving of human error.
- A choice of metal-salt electrolyte density of 400 μg·mm⁻³ of AgNO₃ (or AlCl₃) was chosen because although it has the highest rate of current decay, it increases peak current that, for the sake of prototyping will be easier to characterize.
- Choice of salt bridge electrolyte and concentration was prescribed as 9.6 µmol·mm⁻³ NaNO₃ and requires no tailoring for performance. The maximum salt bridge volume (~2.07 mm³) was chosen despite the increase in resistance, delay of turn-on time and decrease in maximum current. These design sacrifices were made for the decrease in current decay rate and increase in assembly ergonomics.

Given the balance between the choices of electrolyte density and salt bridge volume a moderate-duration and high-power current profile is established.

3.2.2 Hydrophobic Layer Material

To circumvent the difficult handling and processing (i.e. laser cutting) of the carpenter's tape prescribed in the original battery design, the hydrophobic layer material was chosen to be 3M[™] High Performance Acrylic Adhesive 200MP Transfer Tape. Model 467MP was specified for the following reasons (3M Corporation, 2014):

- Adhesive thickness 0.06 mm
 - This feature provides a smaller z-axis travel of the sample within vertical channels of the hydrophobic layers
- Double sided, double backed
 - This feature provides superior handling during cutting and assembly stages. Furthermore, contamination is reduced because exposure time is minimal
- Excellent solvent resistance
 - The presence of metal-salt electrolytes creates high-activity solutions within the device that interface with the hydrophobic layer
- High shear strength
 - Delamination is a critical failure for these devices, thus preservation against slippage and edge lifting is required during assembly, packaging, and distribution stages

- Water and humidity resistance
 - Minimal diffusion of water into the hydrophobic layer is desired
- Long-term stability with moderate temperature range
 - Subassemblies that include the tape layer may require sintering in future platform designs and this process-flexibility should not be precluded.

3.2.3 Printed Circuitry Layer

The eighth (8th) layer must satisfy each purpose of the copper tape, primarily the connection between terminals of galvanic cells. As a secondary purpose, it should simplify assembly and decrease misalignment error.

The proposed design consists of a rectangular center trace connecting the two galvanic cells, and two side "L"-shaped traces rerouting terminal voltage to a 300 µm gap, the minimum allowable by wax printing but maximum for the surface-mount LED footprint. Figure 5) The side trace channel width minimum of 2 mm is required for wetting of the precursor solution; the 4 mm base acts as a reservoir during deposition. In previous design iterations, smaller channel widths were too narrow for ink wetting and thus did not receive sufficient solute for sintering. (Figure 6) The center trace is spaced specifically to align a wax trace between the electrodes with each pair. By matching the exterior dimensions to layers 1-7, alignment is unambiguous and less error-prone during assembly.



Figure 5. Schematic of circuitry layer



Figure 6. Preliminary circuitry design

The value of incorporating a printed circuitry layer as an all-printed platform is visible when superimposed on the original battery design. (Figure 7)



Figure 7. Exploded view of device with printed-circuitry

3.3 Conductive Ink

The selection to use mild-temperature silver-nanoparticle conductive ink can be explained by the relation between its performance and the functional requirements:

- The silver becomes incorporated with substrate pore network after sintering, so fatigue behavior may be unlike the graphite electrodes or zinc metal traces of other researchers' attempts. Additionally, printing is required only once per trace because conductivity is achieved on both sides (Siegel et al., 2010).
- The mild temperature sintering (below 200°C) is attractive for preserving substrate mechanical properties (i.e. flexibility) (Walker & Lewis, 2012).
- The theoretical conductivity within reach is sufficient for preserving the power generated (Walker & Lewis, 2012).
- The ink has proven scalable for automated printing methods (Walker & Lewis, 2012).
- Low-viscosity of the solution means deposited liquid will conform to the hydrophobic barriers of wax-printed design, so creating complex circuitry traces will be nested in the most robust process of assembly, namely the wax printing.
- The ink is stable for 3 months so the burden of storage is low and it experiences no appreciable loss in potency.

3.3.1 Sinter Profile

The formation of a "well-connected, continuous, and conductive network" has been shown to develop sharply at sintering temperatures above 150°C on silicon substrates. The reliability of this network formation increases with peak temperature, yet isothermal holds are sufficient to activate sintering provided they are above the critical value (Greer & Street, 2007). It is deduced that densification increases significantly above this temperature, because, given a large increase in homologous temperature when above the percolation threshold, a change is expected in the dominant diffusion mechanism (namely, surface diffusion initiates fusion and creates necks, then switching to grain boundary diffusion which removes void, densifies, and grows grains) (Rahaman, 2003). Therefore, the test matrix of sinter profiles is simplified by using isothermal holds marginally below, at, and above 150°C.

4. EXPERIMENTAL METHOD

4.1 Printed Features

The solid wax ink was patterned onto Whatman Chromatography No. 1 paper using a Xerox Phaser 6500N inkjet printer. Sheets of patterned paper were heated in an MTI Compact Forced Air Convection Oven for 190°C for 120 seconds to cure hydrophobic channels. After individual layers were cut, 4 μ L of saturated NaNO₃ solution was deposited using a micropipette onto the salt bridge channel and left in air for 30 minutes to evaporate. Separately, 3 μ L volumes of 1M AlCl₃ and 3M AgNO₃ solutions were deposited via micropipette into their respective electrolyte pads and evaporated.

4.2 Cutting/Stamping

Adhesive layers are stamped and punched using a cutting stencil that aligns "vertical channels", or holes, to their correct position relative to the exterior dimensions of the device (Figure 8) With this stencil, one copy of each hydrophobic layer is cut and thus wastes minimal material on duplicates. Alignment during assembly is determined at the exterior corners for each layer, a user-friendly feature implemented after fabricating exploratory stencil prototypes.



Figure 8. Schematic of hydrophobic-layer cutting stencil

4.3 Printed Circuitry

Multiple sheets of equally sized (22 mm x 11 mm) wax-printed channels on paper substrate received conductive ink printing via micropipette and varied in sintering profile. The sinter profile prescribed by literature of 90°C at 15 minutes failed in preliminary testing, most likely from a change in substrate. Research of a comparable ink found that temperatures above 130°C exhibit reliable sintering on similar media. Additionally, it was found that higher-temperature isothermal-holds performed equally to multi-stage, lower-temperature sinter profiles (Greer & Street, 2007). Thus, an isothermal hold of 150°C was chosen and samples were varied in treatment times from 15 to 90 minutes (in non-linear increments) and from ink concentrations of 10-100 μ L·mm⁻³ in (10 μ L increments). Upon identifying a narrower range of ink concentrations and sinter times exhibiting significant changes in resistivity, a three-factor experiment (varying in sinter

time, sinter temperature, and ink concentration) was performed to highlight the ink's conductivity threshold. After print parameters of sufficient performance were identified, circuitry traces (layer 8) were printed, sintered, and then included in assembly.

4.4 Fabrication

Three processes, namely the electrolyte deposition, preparation of printed circuitry, and mechanical stamping of tape layers, are conducted in parallel to prepare componentry for the assembly stage. (Figure 9) During assembly, 2 mm diameter discs of TechniCloth TX609 cellulose/polyester unaligned fiber wipes are cut and inserted in the vacancy of hydrophobic tape layers to reduce the empty space. These discs saturate and swell to create a conduit for capillary flow between layers, reducing the minimum sample volume required. Fully assembled devices are compressed using a 35 lb weight for 30 minutes.



Figure 9. Diagram of assembly process

5. RESULTS

Several characterization methods were implemented to quantify the device's performance in terms of the requirement metrics. The resistance of sintered circuitry traces measured the absolute power loss through the traces as well as their stability over time. The change in resistance of printed traces upon folding at angles varying from -180° (acute folding) to 180° (obtuse folding) measured flexibility. X-ray diffraction confirmed the presence of bulk silver upon sintering. Resistance value measured after samples exposed to a matrix of sinter times, sinter temperatures, and ink concentrations mapped the conductivity threshold. Open-circuit voltage and short-circuit current profiled the power generation of the battery with the print-optimized circuitry included.

5.1 Resistivity

Resistance of the center trace and side traces was measured using an Innova 3320 digital multi-meter and observed to be $0.7 \pm 0.2 \Omega$ and $1.1 \pm 0.4 \Omega$, respectively. (Figure 10) These values remained stable in ambient conditions for 30 days without noticeable change in resistance. Compared to the internal resistance of the battery (over 2.53 k Ω) these values account for a less than 0.1% loss in battery power. Trace thickness was measured using Vernier calipers and found to vary significantly, from as low as the cellulose paper's thickness (0.18 mm) to as large as 0.5 mm. A resistivity range of 75 to 211 10⁻⁵ Ω ·m was calculated, multiple orders of magnitude from the theoretical value of bulk silver 1.6 to 2 × 10⁻⁸ Ω ·m at 300K (Greer & Street, 2007; Tanner & Larson, 1968).



Uneven solvent evaporation

Figure 10. Sintered circuitry layer

Several failure modes were present upon sintering: loss of definition in wax-printed channels, inconsistent sintering in narrow-channel sections of the side traces and uneven solvent evaporation resulting in thickness variations. Possible causes of these phenomena are discussed in later sections.

5.2 Flexibility

Fatigue is a primary failure mode of flexible printed media and therefore a strong metric for a material's candidacy. Electrical conduction is the primary functionality of the ink, so characterizing its performance under the primary failure condition is the most appropriate – hence the comparison of resistivity against fold angle is salient.

Fold angle is defined as acute when the circuitry is folded against itself, relative to the printed face. Obtuse folding occurs in the opposite direction, or in a "backwards" fashion

relative to the printed face (Siegel et al., 2010). (Figure 11) Samples were prepared by printing 0.636 μ L·mm⁻³ of conductive ink on 8 x 2.75 x 0.18 mm wax-printed traces, then sintered at 150°C for 30 minutes. Resistance measurements were taken by folding samples along 90° and 180° edges of a machined insulator, namely polyvinyl chloride (PVC). A minimum two-fold increase in resistance was observed upon folding, regardless of angle or direction. Increases as high as 1500% were observed in the obtuse condition, indicating behavior sensitive to directionality, where unequal changes in resistance were observed in fold angle whether acute (-180° to 0°) or obtuse (0° to 180°).



Figure 11. Resistivity of circuitry traces with respect to fold angle

5.3 X-ray Diffraction

X-ray diffraction patterns were collected using a Siemens Diffraktometer D5000. A scan was taken ranging from 10-140° 2θ in 0.1° increments at a rate of 10° per minute. As a baseline, the diffraction pattern of layer 8 with wax traces but without conductive ink

applied ("empty") was collected. (Figure 12) shows two high-intensity, low-angle, broad peaks often corresponding with semi-crystalline domains of cellulose I. The characteristic double-peak at 14.8° and 16.5° (2θ) correspond to the (101) plane reflection of cellulose I, while the narrow, intense peak at 22.6° 2θ corresponds to (002). A third, shallow signal found at 34.4° 2θ matches the (004) reflection; slower scan speeds could extract additional resolution to this peak. Relative intensities, locations, and widths strongly match literature. (Costa et al., 2014; Veigas et al., 2012)



Figure 12. Diffraction pattern of unprinted circuitry layer

It must be noted that peaks for solid wax were not observed in the diffraction pattern but were present in the sample tested; it is likely the mounting configuration can be rearranged to include wax traces within the detector's scan area. Immediately following testing of the "empty" layer, the diffraction pattern was collected under identical conditions for a layer-8 sample with the conductive ink printed and sintered. (Figure 13) The tested sample was treated with the highest volumetric ink density of $0.5 \,\mu L \cdot mm^{-3}$.



Figure 13. Diffraction pattern of sintered circuitry layer

The characteristic peaks of cellulose 1 were again present but with intensities lower relative to the numerous narrow peaks typical of close-packed lattices. Given that pure solid silver exhibits face-centered cubic symmetry, it is no surprise the (111) plane reflection is present at 38.3° 2θ and strongest. Additional reflections [i.e. (200), (220), (311), (222)] at their respective locations (i.e. 44.5°, 64.6°, 77.5°, 81.7° 2θ) only strengthen the conclusion that bulk silver was formed after sintering. Additionally, the

absence of peaks corresponding to silver acetate indicates complete sintering of supernatant (Theivasanthi & Alagar, 2012; Walker & Lewis, 2012; Young, 1975).

5.4 Conductivity Threshold

Conductivity threshold was mapped by measuring resistance of a given trace volume when sinter time, temperature, and ink concentration are varied. To conserve ink precursor and simplify the test matrix a 150°C isothermal-hold sintering profile was chosen for preliminary testing. (Figure 14) Sintering times above 30 minutes exhibit highest sensitivities at moderate concentrations, approximately 0.5 μ L·mm⁻³, but insensitive elsewhere. The ink concentration, however, was observed to decrease resistivity exponentially above 0.3 μ L·mm⁻³, regardless of time. It must be noted that empty cells in the matrix correspond to data points that were omitted due to either unstable readings, compromised deposition, or sample damage.



Figure 14. Two-factor matrix of conductivity threshold

Upon identifying a narrower range of ink concentrations and sinter times at which an operating limit for ink concentration could be established, another test matrix was conducted to include variations in sinter temperature. (Figure 15) While a temperature increase appeared to have larger effect at low concentration (i.e. $0.36 \ \mu L \cdot mm^{-3}$), the impact was observed to diminish above $0.5 \ \mu L \cdot mm^{-3}$. Conversely, at higher temperatures (150°C), the effect of increasing concentration has little impact on resistivity. In this case, it may be possible that labile amine ligand evaporation occurred sooner in the heat treatment and thus the concentration sensitivity is not visible.



Figure 15. Three-factor matrix of conductivity threshold

5.5 Battery Performance

5.5.1 Open-circuit Voltage

Open circuit voltage (V_{oc}) of a representative sample device was sustained for 40+ minutes at 2.5 V with less than 10% degradation after 70 minutes. (Figure 16) Although performance of each cell (approximately 1.2 -1.3 V) is poor in relation to the theoretical potential (2.4 V), the performance agrees with the reported voltage profile of the original authors. The profile of a failed device is also plotted to illustrate the behavior of a device with failure in one cell. A short explains the delayed increase and subsequent sudden decrease to approximately half the peak voltage. Measurement of a zero voltage across that cell confirms the short; measurement of the other cell accounts for approximately all 1.3V.



Figure 16. Open-circuit voltage profile

5.5.2 Short-circuit Current

Short-circuit current (I_{sc}) measurements were taken using an ammeter and 1.5 Ω load resistor, both in series with the fluidic battery. The current profile of the three best performing devices were plotted to represent ideal current profile. (Figure 17) The sharp increase in current at approximately 3-6 minutes indicates the reconstitution of salt bridges and "turn on" of the battery. The convergence of values at 20-25 minutes indicates a cessation of the reduction-oxidation reaction generating galvanic power. The full-duration half-max time (16 minutes, 200 μ A) again correlates strongly with the original battery design.



Figure 17. Mean short-circuit current profile

6. DISCUSSION

6.1 Resistivity

It is expected that the cellulose substrate creates significant electron scattering, but so far only empirical or qualitative relationships have been determined between pore characteristics and metal nanoparticle-patterned circuitry (Hsieh, Kim, Nogi, & Suganuma, 2013). In fact, the resistivity of cellulose 1 *itself* is heavily dependent on fiber direction, pore network, impurity or solvent concentration, and temperature, ranging from 10^9 to $10^{10} \Omega$ ·m at ambient conditions (Takahashi & Takenaka, 1983). Additionally, there is an order of magnitude change in resistivity for silver at film thicknesses below 500 nm, but it appears an unlikely influence given the deposition volume (Lacy, 2011). Given the results, it is inferred that the cellulose has an appreciable effect on electron scattering within the final circuitry. The characterization of relative influence between cellulose and silver on electron mobility is outside the scope of this paper but would be insightful for further development. The loss of definition of wax-printed channels is accounted for by a change in surface energy at the ink-wax interface. Again, this reaction is multi-faceted and beyond the scope of this study, but it is clear that elevated sinter temperatures provide favorable kinetics for this reaction as it is less prevalent at 100°C isothermal holds. The inconsistent sintering and uneven thickness can be attributed to the concentration gradient of nanoparticles adhered to the cellulose pore network. These variations may also compound the failure modes during folding.

A large error in resistivity is attributed to the poor resolution of thickness measurements. This error can be estimated by differentiating the resistance model used with respect to cross-sectional thickness (Equation 6),

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$$d\rho = \frac{RdA}{l} \tag{7}$$

$$d\rho = \frac{Rw}{l}dt \tag{8}$$

Given a 0.05 mm resolution in thickness measurements, an estimated resistivity error of 5.86 $10^{-6} \ \Omega \cdot m$ is calculated. However, despite this range of error, it does not bridge the order-of-magnitudes difference between the experimental and ideal resistivity values. Scanning electron microscopy and profilometry should be used to identify the range of trace thickness with more resolution and thus narrow this error.

6.2 Flexibility

A key advantage of conductive ink during material selection was the symmetry of electrical properties regardless of which side experienced deposition. The silver nanoparticles were assumed to embed with the porous cellulose network omnidirectionally. By comparison, printed metal wires do not share this symmetry and are thus sensitive to fold directionality (Siegel et al., 2010). It appears this symmetry advantage is also not present using this ink as circuitry, most likely attributable to the variation in grain structure. Unpredictable dispersion of nanoparticles during printing creates a non-uniform grain structure after sintering. Subsequently, the debonding, channeling, and surface-crack failure modes found often in thin-film structures would explain the poor fatigue characteristics in this circuitry material's non-uniform, poorly sintered grain structures (Hutchinson, 1996). It has been speculated that, due to the differences in compressive and tensile stresses of bend directionality, the small cracks formed during acute folding do not fracture the entire percolating network, while the large cracks from obtuse bending create creases that sever the electrical connection

between sides (Siegel et al., 2010). Scanning electron microscopy may also provide insight regarding the nanoparticle adhesion variations within the pore network, grain structures after sintering, and failure modes present during folding.

6.3 X-ray Diffraction

The diffraction patterns provide overwhelming confidence in the formation of bulk silver after sintering. The narrow full-width half-max of the metallic peaks indicate high crystallinity, which implies a well-sintered Ag-nanoparticle network (Song et al., 2014). The relative intensities of reflections between the cellulose and silver planes served as an interesting indicator of the structural and surface changes in the substrate, but do not translate to weighted fractions of the hybrid conductive network. Considering resistivity is dependent on this metal-porous media interaction, x-ray diffraction would again serve as an integral tool for mapping the relationship between volumetric particle density deposited and electrical performance.

6.4 Conductivity Threshold

There are three competing factors influencing the conductivity threshold of the printed ink. Firstly, A minimum concentration of dispersed nanoparticles defines the percolation threshold, below which electrical conduction and atomic diffusion are precluded. As evidenced by the invariant resistivity of all 0.8 μ L·mm⁻³ samples, the printed circuitry is insensitive to both high sinter temperature and extended sinter times. In conditions with low dispersion rates of nanoparticles after printing (i.e. low ink concentrations), the lack of a percolating network precludes electron conduction.

Secondly, a minimum temperature defines the percolation temperature, above which the evaporation of amine ligands is possible. It has been established that the minimum temperature of percolation is heavily dependent on the evaporation of labile nanoparticle ligands (Anto, Sivaramakrishnan, Chua, & Ho, 2010). While on polymer substrates the Ag-nanoparticle conductive ink used exhibited an evaporation temperature of 90°C, preliminary sintering tests showed minimal change until above 100°C. At temperatures above this threshold, the printed circuitry was conductive and only sensitive to treatment time and ink concentration. Therefore, it can be assumed that the conductivity threshold for this system (ink on cellulose) is also determined by the thermal stability of the amine ligands within the cellulose pore network. Studying the absorption profile of this ink using Fourier-transform infrared spectroscopy while varying exposure to different sinter times and temperatures could identify the evaporation temperature more accurately.

Thirdly, upon the formation of a percolating network of sufficiently connected dispersed nanoparticles, atomic diffusion is possible and sintering can occur. Given the reduced melting temperatures of metallic nanoparticles and homologous temperature dependence on diffusion-mechanism, extended heating (150°C for 30+ minutes) may experience a minimal but non-trivial shift into the densification regime of sintering. The reduction of stacking fault densities during densification translates to reduced electron scattering, thus increasing conductivity. Such coalescence methods of aggregated nanoparticles are evident in similar metallic systems (i.e. Au) but not yet explored on cellulose substrates (Ingham et al., 2011). This could account for the final decrease in resistivity below 10^4 Ω ·m in samples with highest concentrations, times, and temperatures. Profilometry of

high-temperature long-exposure samples could be used to confirm the change in trace volume.

6.5 Battery Performance

6.5.1 Open-circuit Voltage

The equipment manufacturer specifies a maximum instrumental error of ± 0.2 V in potential measurements (Innova Electronic Corporation, 2016). This error accounts for 8% of the measured value of the devices' peak performance (2.5V). However, the requisite voltage threshold of 1.8V for ultraviolet LEDs is more than 25% below the microfluidic battery performance and thus does not reduce confidence in satisfying the functional requirement.

6.5.2 Short-circuit Current

A maximum instrumental error of $\pm 1.5\%$ is specified for ultra-low current measurements. Short-circuit current was used to determine "on" time, defined as the full-duration half-max of the current profile. This error in current measurement translates to a ± 0.88 minute error in FDHM, a trivial impact on power generation when satisfying the functional requirement.

7. CONCLUSION

7.1 Measuring Success

The purpose of defining unambiguous testable system requirements is to measure the success of the hypothesis in quantitative terms. (Table 2) The circuitry material was specified to be both conduct and stable; the low absolute resistance of the circuitry was dwarfed by the internal resistance of the battery and experienced no change no noticeable changed over time. The integrated circuitry was specified to preserve both robust flexibility and mild temperatures; while the circuitry can be sintered successfully using a 150°C isothermal, the fatigue during folding failed to perform with the flexibility of competing approaches. The platform was specified to provide sufficient data collection time and power to interfacing components. The full-duration half-max of power generation was 16 minutes, far exceeding the requisite 5 minutes. The threshold voltage and forward current minimums of commercially available LEDs were both exceeded.

System	Functional Quantitative		Success
Requirement	Requirement Metric		Achieved
Conductive	< 5% power loss	< 1.5 Ω absolute, <0.1% R _{circuit} /R _{internal}	Yes
Stable	Stable at STP	$\Delta ho = 0$ over 30 days	Yes
Flexible	$< 20\% \Delta \rho$ at 90°	> 200% Δρ at 90°	No
Low-temperature	All T < 200°C	150°C, 0.636 μL·mm ⁻³	Yes
Data collection	> 5 minutes "on" time	FDHM 16 minutes	Yes
Interface-able	> 1.8V, 100 μA	2.4 V, 200 μA	Yes

Table 2. Success meeting system requirements

7.2 Future Design

A future design for an automatable all-printed was developed to incorporate features new to the μ PAD industry. (Figure 18) The advent of "origami" folding features opens interesting solutions to assembly stages that are otherwise inaccurate or poorly suited for automation. The proposed design can be automated with some processes in parallel, such as electrolyte deposition and ink application. Furthermore, it is well suited for printing and assembly in replicates.



Figure 18. Future all-printed platform design

Several additional design features can be added to increase functionality of the platform or reduce the assembly errors:

- Alignment reticles can be added for assembly quality control as layer count increases.
- Electrode pad shapes or colors can differ between metals to ensure assembly accuracy.
- Use multiple electrode metals to create various voltage levels that accommodate diverse needs of electrical components.

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APPENDICES

Appendix A

Layer 1





Layer 3



Layer 5



Layer 7





Layer 4



Layer 6



Layer 8



Appendix B

Material	Amount Price		Specifications	
AlCl ₃	100 g	\$58.40	ReagentPlus®	
AgNO ₃	5 g	\$29.40	ReagentPlus [®] ,	
NaNO ₃	5 g	\$39.80	ReagentPlus [®] ,	
Al (s)	50 x 50 mm	\$55.40	99.999% pure,	
	50 50		99.9% pure,	
Ag (s)	50 x 50 mm	\$65.70	0.25 mm foil	
Copper Tape	6.3 x 16.46 mm	\$18.95	3М™ 1181	
Chromatography Paper	20 x 20 cm	\$57.60	Whatman No. 1, 100 sheets	
Transfer Tape	4 in x 60 yd 2.0 mil	\$83.78	3М™ 200MP 467MP	
Deionized Water	1.3 L	\$53.30	CAS 7732-18-5	
TechniCloth wipe	23 x 23 cm	\$271.53	TX 609, 300 sheets	
Silver acetate	5 g	\$42.10	ReagentPlus®, 99%	
Formic Acid	100 ml	\$49.20	ACS Reagent, ≥ 88%	
Ammonium hydroxide	500 ml	\$41.30	ACS Reagent, 29% NH_3 basis	

Table 3. Bill of materials

Appendix C

	Fold Angle, θ				
	-180°	-90°	0°	90°	180°
Sample 1	3.75*	4.25	1	4.75	6.50
Sample 2	0.27	0.36	1	1.27	3.63
Sample 3	1.50	1.25	1	3.00	3.50
Sample 4	2.00	1.40	1	18.4	6.20
Sample 5	2.10	1.22	1	9.3	5.55
Mean, µ	1.92	1.69	1	7.35	5.08
Standard Deviation, σ	1.25	1.48	0	6.87	1.42

Table 4. Change in resistivity during folding

*Values are represented as relative resistivity, $R_{measured}/R_0$, and are unit-less.

Appendix D

Ink	Sinter* time (minutes)						
Concentration (µL·mm ⁻³)	15	30	60	90			
0.08	3750 Ω ·m	3750 Ω ·m	3750 Ω ·m	3750 Ω ·m			
0.17	1500 Ω ·m	750 Ω ·m	750 Ω ·m	525 Ω ·m			
0.25	119.25 Ω ·m	90 Ω ∙m	-	-			
0.33	-	93.75 Ω ·m	47.36 Ω ·m				
0.50	8.25 Ω ·m	3.75 Ω ∙m	-	0.0375			
0.58	-	0.1875 Ω ·m	0.0375 Ω ·m	0.0375 Ω ·m			
0.66	1.8 Ω ∙m	0.0375 Ω ·m	0.0375 Ω·m	0.0375 Ω·m			
0.83	0.0375 Ω·m	0.0375 Ω ·m	0.0375 Ω ·m	0.0375 Ω ·m			

Table 5. Resistivity using two-factor sinter matrix

*Traces were sintered using 150°C isothermal hold profile. It must be noted that empty cells in the matrix correspond to data points that were omitted due to either unstable readings, compromised deposition, or sample damage.

Appendix E

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Table 6	Resistivity	11\$110	three.	-tactor	sinter	matrix
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	Sinter temperature						
Ink Concentration	100)°C	150°C				
-3)	Sinter time (minutes)						
(µL·mm [°])	15	30	15	30			
	15	50	15	50			
0.36	1.48 10 ⁻² Ω⋅m	1.10 10 ⁻² Ω⋅m	1.05 10 ⁻⁴ Ω·m	1.42 10 ⁻⁴ Ω·m			
0.50	1.55 10 ⁻⁴ Ω·m	1.49 10 ⁻⁴ Ω·m	4.30 10 ⁻⁵ Ω·m	4.33 10 ⁻⁵ Ω·m			
0.63	7.43 10 ⁻⁵ Ω·m	3.71 10 ⁻⁵ Ω⋅m	3.09 10 ⁻⁵ Ω·m	1.86 10 ⁻⁵ Ω·m			

Appendix F

Time	Current (µA)		Time	Current (µA)			
(min)	Trial 1	Trial 2	Trial 3	(min)	Trial 1	Trial 2	Trial 3
0	-	0.1	2.4	24	170	131	142
1	-	0.2	3.2	25	160	139	137
2	-	0.2	12.3	26	151	124	128
3	-	0.6	71.8	27	144	124	123
4	-	218	175.7	28	136	118	123
5	-	302	372	29	130	122	117
6	-	295	468	30	125	116	113
7	-	293	516	31	117	106	111
8	410	320	515	32	113	104	107
9	510	307	467	33	110	99	102
10	638	288	359	34	110	92	100
11	665	304	312	35	107	95	98
12	695	269	282	36	105	95	85
13	728	250	248	37	102	94	80
14	687	241	233	38	99	88	80
15	611	220	220	39	96	91	80
16	566	223	199	40	95	90	72
17	511	205	185	41	91	86	75
18	441	156	196	42	90	78	71
19	394	168	193	43	87	79	69
20	329	163	179	44	85	82	74
21	265	157	169	45	84	81	68
22	232	147	160		1	1	ı I
23	186	144	154				
				•			

Table 7. Short-circuit current measurements

Appendix G

Time (min)	Voltage (V)		Time (min)	Voltage (V)		
× ,	Trial A	Trial B	~ /	Trial A	Trial B	
0	1.65	2.18	26	1.21	2.49	
1	1.58	2.05	27	1.21	2.48	
2	1.65	2.2	28	1.22	2.49	
3	1.91	2.5	29	1.25	2.49	
4	2.06	2.5	30	1.24	2.48	
5	2.09	2.49	31	1.24	2.48	
6	2.17	2.51	32	1.25	2.48	
7	2.26	2.5	33	1.27	2.49	
8	2.28	2.5	34	1.27	2.48	
9	2.02	2.49	35	1.28	2.48	
10	1.6	2.49	36	1.28	2.48	
11	1.36	2.49	37	1.27	2.46	
12	1.27	2.48	38	1.29	2.47	
13	1.25	2.47	39	1.29	2.48	
14	1.22	2.5	40	1.28	2.48	
15	1.2	2.51	41	1.28	2.48	
16	1.19	2.51	42	1.28	2.47	
17	1.2	2.49	43	1.31	2.47	
18	1.2	2.48	44	1.34	2.45	
19	1.19	2.48	45	1.3	2.46	
20	1.18	2.49	-	-	-	
21	1.17	2.48	50	1.23	2.36	
22	1.17	2.46	60	1.44	2.29	
23	1.17	2.48	70	1.48	2.26	
24	1.17	2.48		-	·	
25	1.21	2.5				

Table 8. Open-circuit voltage measurements