History of Naval Postgraduate School	Biographies

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Resume of Shridhar B. Shukla, 1993-08

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Shridhar B. Shukla was born in Nagpur, India on Jan. 5, 1962. He received a B. Tech. in Electrical Engineering from the Indian Institute of Technology, Bombay, India in May 1983. In Aug. 1985, he received an M.S. in Electrical Engineering from Virginia Tech, Blacksburg, VA. Till Dec. 1989, he performed doctoral research on parallel computer architectures in the Electrical and Computer Engineering department of North Carolina State University, Raleigh, NC.

He joined the Naval Postgraduate School as an Assistant Professor of Electrical and Computer Engineering on 26 Dec. 1989. His major teaching areas are digital systems, computer architecture, and parallel processing. His research interests include high performance computer architectures, parallel processing in real-time applications, and distributed computing. He is the principal investigator of a project on *mapping of hard real-time applications on multicomputers* under the research initiation program of NPS and is a member of the real-time software development group of the *Autonomous Underwater Vehicle* project at NPS. He is a member of the IEEE Computer Society.

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