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Roll-to-Roll Printed Electronics for Standalone Smart Windows

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Symposium PM5 : Hierarchical, Hybrid and Roll-to-Roll Manufacturing for Device Applications

Nov 28

Nov 29

Nov 30

Dec 01

2016-11-30 [+](#) [Show All Abstracts](#)

Symposium Organizers

James Watkins, Univ of Massachusetts
Karen Chong, Institute of Materials
Research and Engineering (IMRE) / A-STAR
Jukka Hast, VTT Printed Intelligence
Mark D. Poliks, Binghamton University

Symposium Support

State University of New York at
Binghamton, University of Massachusetts
Amherst (Center for Hierarchical
Manufacturing)

PM5.6: Lighting and Optical Applications

Session Chairs

Jeff Morse
Mark D. Poliks

Wednesday AM, November 30, 2016
Hynes, Level 1, Room 109

9:00 AM - *PM5.6.01

Towards High-Throughput Coating and Printing of Light-Emitting Electrochemical Cells

[Ludvig Edman](#)¹

¹ The Organic Photonics and Electronics Group Umeå University Umea Sweden

[+](#) [Show Abstract](#)

9:30 AM - PM5.6.02

Large Area Flexible Lighting Elements Manufactured by Roll-to-Roll Compatible Processes

[Kimmo Keranen](#)¹, [Jyrki Ollila](#)¹, [Sami Ihme](#)¹, [Eveliina Juntunen](#)¹, [Jukka-Tapani Makinen](#)¹, [Kari Ronka](#)¹

¹ VTT Technical Research Centre of Finland Oulu Finland

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9:45 AM - PM5.6.03

All-Printed Full-Color OPD Array with a Single Organic Active Layer

[Igal Deckman](#)¹, Pierre Lechene¹, Adrien Pierre¹, Ana Claudia Arias¹

¹ University of California, Berkeley Berkeley United States

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10:00 AM - PM5.6.04

Roll-to-Roll Printed Electronics for Standalone Smart Windows

[Francesco Pastorelli](#)¹

¹ Technical University of Denmark Roskilde Denmark

[-](#) Hide Abstract

We implement a smart electrochromic plastic window using a flexible organic power transistors prepared with roll-to-roll compatible printing techniques. This innovative transistor is able to drive large currents while handling the thermal aspects in operation together with other organic printed electronics technologies such as large area organic photovoltaics(OPV) and large area electrochromic (EC) displays. We find especially that an elevated operational temperature is beneficial for the transistor characteristics.

The footprint of organic electronic technologies is important when united in complex circuitry. We present flexible organic power transistors prepared by fast (20 m min⁻¹) roll-to-roll flexographic printing of the drain and source electrode structures, with an interspace below 50 μm, directly on polyester foil. The devices have top gate architecture and were completed by slot-die coating of the organic semiconductor poly-3-hexylthiophene and the dielectric material polyvinylphenol before the gate was applied by screen printing. We explore the footprint and the practically accessible geometry of such devices with a special view toward being able to drive large currents while handling the thermal aspects in operation together with other organic printed electronics technologies such as large area OPV and large area EC. We find especially that an elevated operational temperature is beneficial with respect to both transconductance and on/off ratio. We achieve high currents of up to 45 mA at a temperature of 80 °C with an on/off ratio of 100 which is sufficient to drive large area organic electronics such as an EC device powered by OPV devices that we also demonstrate. EC materials can change their transparency when an external voltage is applied. Such materials have interesting applications in windows. Thus, one can imagine a window in an office or a greenhouse which automatically pulls the shade by making itself darker, when the temperature gets too high.

Francesco Pastorelli, Thomas M. Schmidt, Markus Hösel, Roar R. Søndergaard, Mikkel Jørgensen and Frederik C. Krebs, " The Organic Power Transistor: Roll-to-Roll Manufacture, Thermal Behavior, and Power Handling When Driving Printed Electronics", Volume 18, Issue 1, pages 51–55, January 2016, doi: 10.1002/adem.201500348

10:15 AM - PM5.6.05

Development Process and Design Rules of Inkjet-Printed Silver Grid Electrodes for Use in Highly Efficient Solution-Processed ITO-Free OLEDs

Felix Hermerschmidt¹, Ignasi Burgués-Ceballos², Lukas Kinner³, Stefan Sax³, Gerburg Schider³, Christine Boeffel⁴, Stelios Choulis², Emil List-Kratochvil¹

¹ Humboldt-Universität zu Berlin Berlin Germany, ² Cyprus University of Technology Limassol Cyprus, ³ NanoTecCenter Weiz Forschungsgesellschaft mbH Weiz Austria, ⁴ Fraunhofer Institute for Applied Polymer Research Potsdam-Golm Germany

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10:30 AM - *

Break

11:00 AM - PM5.6.06

High Volume Solution Based Roll-to-Roll Processing of OLEDs

Pim Groen¹, Robert Abbel¹, Joanne Wilson¹, Ike De Vries¹

¹ Holst Centre Eindhoven Netherlands

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11:15 AM - PM5.6.07

PDMS-Paraffin/Graphene Laminated Films with Electrothermally Switchable Haze

Jae Yeol Park¹, Jeong Yong Lee¹, Tae June Kang²

¹ Korea Advanced Institute of Science and Technology Daejeon Korea (the Republic of), ² Inha university Incheon Korea (the Republic of)

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11:30 AM - PM5.6.08

Kirigami-Inspired Nanocomposite Manufacturing for Tunable Optical Gratings

Lizhi Xu¹, Xinzhi Wang¹, Nicholas Kotov¹

¹ University of Michigan Ann Arbor United States

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11:45 AM - PM5.6.09

Low-Cost, Large-Area Patterning of Metasurfaces with Off-Axis Microsphere Photolithography

Chuang Qu¹, Edward Kinzel¹

¹ Missouri University of Science and Technology Rolla United States

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PM5.7: Large Area and High Temperature Processing**Session Chairs**

Pavel Dutta

Mark D. Poliks

Wednesday PM, November 30, 2016

Hynes, Level 1, Room 109

1:30 PM - *PM5.7.01

R2R CVD Architecture and Processes for Barrier Packaging of Next Generation Display Devices

F. Pieralisi¹, Neil Morrison¹, T. Stolley¹, G. Steiniger¹, M. Schaefer¹, J. Degenhardt¹

¹ Applied Materials WEB Coating GmbH Alzenau Germany

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2:00 PM - PM5.7.02

Integration of ALD and CVD Processes into One-Chamber—Depositing Inorganic-Organic Hybrid Layer with High-Throughput and Reliability

Hyejeong Seong¹, Bong Jun Kim¹, Hongkeun Park¹, Sung Gap Im¹

¹ Korea Advanced Institute of Science and Technology Daejeon Korea (the Republic of)

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2:15 PM - PM5.7.03

High Speed Roll-to-Roll Production of Atomically Thin (2D) Materials Using a Split Zone CVD Reactor

Piran Ravichandran Kidambi¹, Dhanushkodi Mariappan¹, Andrey Vyatskikh¹, Martin Feldmann¹, A. John Hart¹

¹ Massachusetts Institute of Technology Cambridge United States

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3:30 PM - PM5.7.04

Roll-to-Roll Processing of Single-Crystalline-Like III-V, Si and Ge Thin Films on Flexible Metal Substrates for Low-Cost and High-Performance Opto-Electronic Device Applications

Pavel Dutta¹, Monika Rathi¹, Ying Gao¹, Yongkuan Li¹, Sicong Sun¹, Yao Yao¹, Devendra Khatiwada¹, Ali Khadimallah¹, Venkat Selvamanickam¹

¹ University of Houston Houston United States

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3:45 PM - PM5.7.05

High Throughput, Room Temperature Synthesis of Cluster Assembled Nanostructured Nanocrystalline Silicon Films

Giorgio Nava¹, Francesco Fumagalli¹, Salvatore Gambino⁴, Davide Beretta¹, Gabriella Cavallo², Giancarlo Terraneo², Giorgio Divitini³, Adriano Cola⁵, Fabio Di Fonzo¹

¹ Center for Nanoscience and Technology Milano Italy, ⁴ Università del Salento Lecce Italy, ² Politecnico di Milano Milano Italy, ³ Darwin College, Cambridge Cambridge United Kingdom, ⁵ CNR Lecce Italy

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4:00 PM - PM5.7.06

Large-Area Nanoparticle Film Deposition via Hypersonic Particle Deposition

Peter Firth^{1,2}, Zachary Holman^{1,2}

¹ Arizona State University Tempe United States, ² Swift Coat Phoenix United States

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4:15 PM -

DISCUSSION TIME

4:15 PM - PM5.7.07

Printable Carbon Nanotube Microstructures for Ultra-Flexible Li-Ion Batteries

Shahab Ahmad¹, Davor Copic¹, Simon Engelke¹, Chandramohan George¹, Michael Volder¹

¹ Department of Engineering University of Cambridge Cambridge United Kingdom

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4:30 PM - PM5.7.08

High-Performance Low-Voltage Complementary Differential Amplifiers on Foil via Solution-Processed Hybrid Organic/Metal-Oxide Integration

Vincenzo Pecunia^{1,2}, Iyad Nasrallah², Mark Nikolka², Henning Sirringhaus²

¹ Institute of Functional Nano and Soft Materials Soochow University Suzhou China, ² Cavendish Laboratory University of Cambridge Cambridge United Kingdom

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4:45 PM - PM5.7.09

Considerations and Methodology to Determine R2R Manufacturing and Scaling of Electronic Devices on Flexible Stainless Steel Foil Substrates

Aditi Chandra¹, Mao Takashima¹, Raghav Sreenivasan¹, Patricia Beck¹, Scott Bruner¹, Arvind Kamath¹

¹ Thinfilm Electronics San Jose United States

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