



Grid-connected Photovoltaic Micro-inverter with New Hybrid Control LLC Resonant Converter

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implement MPPT [7]. The high DC bus voltage, acting as output of full bridge LLC resonant converter, is maintained constant with the hybrid control by using PV panel voltage V_{pv} and bus voltage V_{bus} . The reference of injected grid current is obtained via MPPT by using the grid root mean square (RMS) voltage V_{grid} , the injected grid RMS current I_{grid} , and V_{pv} . Moreover, anti-islanding protection can be also achieved by the phase-lock logic (PLL) and active frequency drift with positive feedback (AFDPF) [11]. The ac current with low total harmonic distortion (THD) is obtained through dead-beat control by using V_{bus} , grid instantaneous voltage v_{grid} , and inductor current i_L .

2.2 Hybrid control for full bridge LLC resonant converter

Zero voltage switching (ZVS) for the primary side switches and zero current switching (ZCS) for the secondary side diodes can be realized via resonance existing in LLC resonant converter. As shown in Fig. 2, vertical and horizontal axis represents the switching frequency f_s , and the phase-shift duty cycle D respectively. Power can be converted efficiently under narrow-range input voltage with control I i.e. traditional PFM, where the voltage gain is only regulated with f_s [6, 7]. Great challenges in magnetic components, system control, and system stability occur when applying control I to PV panels because of the wide-range switching frequency resulted from wide-range input voltage of PV panel and wide-range load variation. Efficient power conversion under wide-range input voltage can be realized through applying control II. As depicted in Fig.2, PFM is adopted in a region where f_s is smaller than a specific value, while higher f_s for regulation is needed, PS-PWM is used, and so f_s range is limited. The efficiency, however, will decrease under PS-PWM on occasions of high input voltage or light load due to the large D which tending to result in the increases of internal circumfluent current loss.

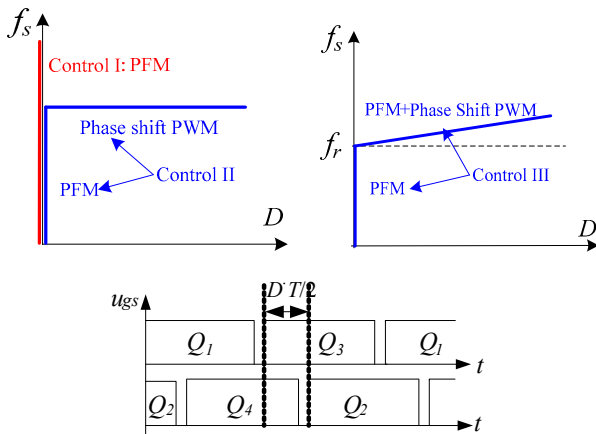


Fig.2 Control scheme for full-bridge LLC resonant converter

In order to overcome the drawbacks by adopting control methods I and II, here a new hybrid control (control III) for full bridge LLC resonant converter is proposed. In control III, there is two control mode. One is PFM control mode when converter $f_s \leq$ resonant frequency f_r (f_r is resonant frequency of the LLC converter resonant tank, and $f_r = 1 / 2\pi\sqrt{L_r C_r}$), the other is control mode of combining PFM and PS-PWM (PFM+PS-PWM) when $f_s > f_r$. Here the f_r is selected as

frequency for transition of the two control mode, and the converter f_s is designed to be equal to f_r at nominal input voltage and output load working condition. Because PFM is also introduced to combine PFM+PS-PWM for regulation, D will be reduced. Then not only the frequency range is limited effectively in favor of magnetic components design, system control and EMC filtering etc., but also the circumfluent current loss can be decreased because of small D . The new hybrid control scheme can be realized easily with digital control

Considering relatively small D , the voltage gain M of the full bridge LLC converter operating under PFM+PS-PWM control mode can be described as the following equation by using fundamental wave analysis:

$$M(f_s, D, k, Q) = \frac{\sin \frac{D'\pi}{2}}{\sqrt{\left[\left(1 - \frac{1}{f_s^2}\right) \frac{1}{k} + 1 \right]^2 + \left[\left(f_s - \frac{1}{f_s}\right) \frac{2}{1 - \cos(D\pi)} Q \right]^2}} \quad (1)$$

Where $D'=1-D$, k =magnetizing inductance L_m/L_r , Q is the quality factor of the resonant tank. The M acts as the function of f_s and D . k and Q are fixed with the constant parameters of the resonant tank. It is noted that the voltage gain M in Equation (1) will be equal to that of traditional control I when $D=0$ i.e. no combining PS-PWM. So, the Equation (1) is also the M expression for the PFM control mode via setting $D=0$.

The PFM control mode can be realized the same like that of traditional PFM. In order to get a specific voltage gain, there exists a variety of combinations f_s and D under PS-PWM+PFM mode. The combination guideline can be analyzed according to Equation (1). Because the PFM+PS-PWM control mode is introduced at $f_s > f_r$, and at $f_s > f_r$ the voltage gain M is less than 1, the following analysis will be only considered at $M < 1$. Normalized switching frequency f vs. Q and D at different M is shown in Fig.3. Here defining $f=f_s/f_r$, and " $f > 1$ " means that the LLC converter operates under PS-PWM+PFM control mode. It is can be seen that,

1. $\Delta f_1 < \Delta f_2$, which means the range of switching frequency is limited more effectively at small M . In other words, impact resulted from introduction of PS-PWM control on switching frequency is enhanced under high input voltage.

2. The effect of PS-PWM on limiting switching frequency will be weakened with the increases of Q . It means PFM control performs better than PS-PWM control under heavy loads.

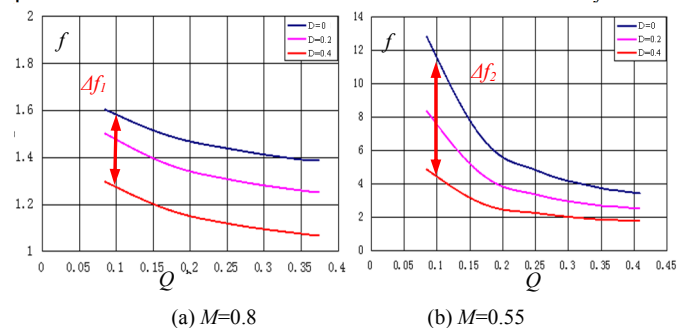


Fig.3. f vs. Q and D @ $k=3.7$ under PFM+PS-PWM mode

Therefore, the combination guidelines of PS-PWM+ PFM control mode is that introducing large weight for PS-PWM under high input voltage or light load, and large weight for PFM under relative high input voltage or heavy load. Large weight for PS-PWM means that the converter is regulated mainly by PS-PWM and mainly by PFM vice versa. This combination can achieve good trade-off range limitation of between switching frequency and D .

The program flowchart of the hybrid control is shown in Fig.4, which is described step-by-step as following. The output voltage V_{bus} of the LLC resonant converter is compared with the reference voltage in analog to digital (AD) interrupt; thus obtain error V_{error} . The converter will be controlled with PFM if the new calculated switching frequency f_s through PI controller is smaller than resonant frequency f_r . If not, the PS-PWM+PFM control mode will be activated. Then a modification of phase-shift duty cycle ΔD can be calculated according to frequency error which equaling to the differences between f_s and f_r . Finally, f_s and D will be updated by adding ΔD to a reference phase-shift duty cycle D_{base} which had been pre-stored in the register.

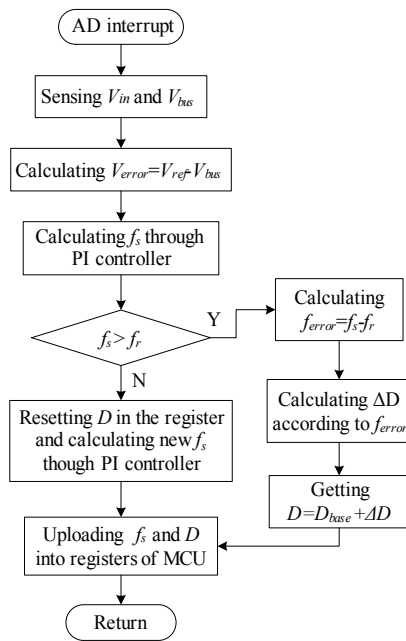


Fig.4 Program flowchart of LLC converter with hybrid control

2.3 MPPT control

Maximum power point tracking (MPPT) is necessary due to the existence of the maximum output power point in PV panel. Method of perturb and observation, which is classified into the first-stage perturbation and the second-stage perturbation, has wide applications [6] on account of its simplicity and high tracking accuracy. Compared with only PFM or PS-PWM, the hybrid control LLC resonant converter has not only two operation mode, but also two control variables f_s and D under PS-PWM+PFM mode. In order to simplify MPPT control, Perturb and observation method is adopted and applied to the second stage dc-ac inverter to implement MPPT. So, the first power stage LLC resonant

converter is only controlled to maintained constant output voltage V_{bus} . In order to avoid high sensing loss resulted from low voltage and high current output of PV panel, the perturbing power for MPPT is calculated from sensing grid V_{grid} and I_{grid} , instead of PV output current .

III. Design of key parameters and simulation

3.1 Design of resonant tank of LLC converter

The resonant tank design of LLC resonant converter has impacts on voltage gain, switching losses, and magnetic losses. Minimizing loss is the objective of designing the resonant tank on basis of analyzing the effect of resonant tank parameters on voltage gain and losses. Switches' conduction loss and turn-off loss together are predominated. They are determined by RMS current I_{Q_rms} and turn-off current i_{Q_off} , respectively. Moreover, I_{Q_rms} can also be used to evaluate the magnetic losses.

As shown in Fig.5, $I_{Q_rms}^*$, $i_{Q_off}^*$ vs. k and Q in the LLC resonant converter operating PFM mode with $f=0.8$ are plotted, respectively. Here I_{Q_rms} and i_{Q_off} are normalized as $I_{Q_rms}^*$ and $i_{Q_off}^*$. It is shown that large k and Q will achieve small currents, then resulting in the low switches loss. And M will be decreased with increasing of k and Q , resulting in wider switching frequency range. Under PFM+PS-PWM mode, the currents behavior is the same like that of PFM mode. In contrast, its M will be increased with increasing of k , as shown in Fig.6. That is to say, large k will not result in wider switching frequency range. Therefore, in the design k and Q of resonant tank, only effect of k and Q on M under PFM control mode is considered.

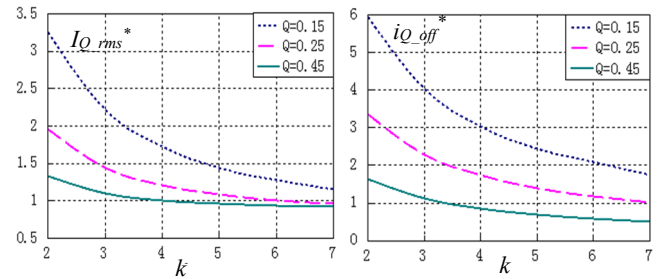


Fig.5 $I_{Q_rms}^*$, $i_{Q_off}^*$ vs. k and Q under PFM mode and $f=0.8$

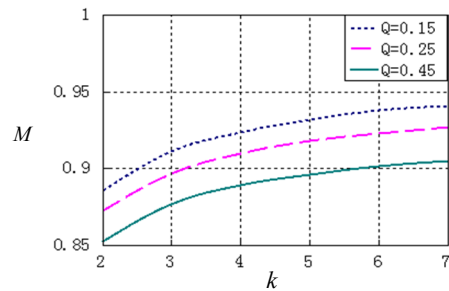


Fig.6 M vs. k and Q under PFM+ PS-PWM mode

The magnetizing current decreases effectively with larger L_m , which is proportional to the multiplication of k and Q , resulting in losses reduction in the resonant network. According to the above conclusion, rules which should be followed in design of the resonant tank are summarized as maximizing the multiplication of k and Q for minimizing losses in resonant network under meeting voltage gain.

Therefore, requirements in the design the LLC converter can be listed as following:

1. Requirements of voltage gain:

$$M(f_{min}) \geq M_{max} \quad (2)$$

$M(f_{min})$ represents the voltage gain at the minimum frequency f_{min} , while M_{max} is the maximum voltage gain under the whole ranges of input voltage and load.

2. Input impedance of the LLC converter need to be inductive in order to operate primary side switches under ZVS. In other words, $\text{Im}(Z_{in})$ must be greater than zero, where Z_{in} represents the input impedance of the converter. Therefore,

$$\omega_s L_r - \frac{1}{\omega_s C_r} + \frac{R_{ac}^2 \cdot \omega_s L_m}{R_{ac}^2 + (\omega_s L_m)^2} > 0 \quad (3)$$

Frequency making the left term of Inequality (3) equal to zero is normalized to be termed as f_{im_0} , then an inductive Z_{in} can be obtained only when inequality (4) is satisfied.

$$f_{min} > f_{im_0} \quad (4)$$

$$f_{im_0} = \sqrt{\frac{k^2 Q^2 - k - 1 + \sqrt{(k^2 Q^2 - k - 1)^2 + 4k^2 Q^2}}{2k^2 Q^2}}$$

k and Q , shown as overlapped parts in Fig.7, satisfies both Inequality (2) and (4). Then finding k and Q to maximize the multiplication of k and Q can achieve optimal design.

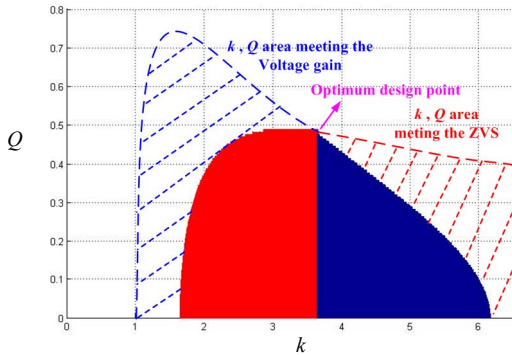


Fig.7 k, Q area meeting the voltage gain and ZVS

According the design procedure, key parameter is designed to be listed as follows for a PVMI prototype.

- PV panel voltage: 22-45V/nominal voltage 31V;
- MPPT voltage: 25-39V;
- PVMI output: 250W /220V (+10%, -15%)/ 50Hz (± 0.5 Hz);
- The LLC resonant converter:

- Output voltage or bus voltage $V_{bus} = 380$ V;
- Switching frequency f_s : 47k-110kHz, and $f_r = 65$ kHz.

Parameters of resonant tank:

- $k = 3.7$, $Q = 0.48$, $L_r = 5.7$ μ H (core size PQ32/25D, material TPW33), $C_r = 1.1$ μ F, $L_m = 21$ μ H, turns ratio of transformer: 7:44 (core size EER404513A, material TPW33);

The dc-ac inverter:

- Switching frequency: 40kHz for T3,T4; 50Hz for T1,T2;
- Inductance: $L_1 = L_2 = 2.5$ mH (core size PQ32/25D, material TPW33).

3.2 Modeling and simulation

Using the control scheme and design parameters, simulations of the PVMI are carried. Some key waveforms are shown in Fig.8. v_{gs} , v_{ds} , i_D , v_D , v_{AB} , i_{Lr} represent drive signal of MOSFET, voltage across drain and source, current and voltage of rectifier diode, voltage across A and B in Fig.1, current of resonant inductor, respectively. It is can be seen that, the LLC converter operates with resonant frequency of 65.5 kHz at nominal input voltage 31V and full-load, while operates with PFM+PS-PWM mode, and $f_s = 83.8$ kHz, $D \approx 0.2$ at input voltage of 39V and full-load. The switching frequency ranges from 47.3 kHz to 111 kHz. The MOSFETs operate with ZVS under full input voltage and loads. It should be noted that ZCS for the diodes, which is in favor of suppressing voltage spike, is also achieved even when f_s is higher than f_r . The reason is that the diodes current lead because of phase shift. This ZCS behavior is great different with that under single PFM control. The simulation has good agreement with the design.

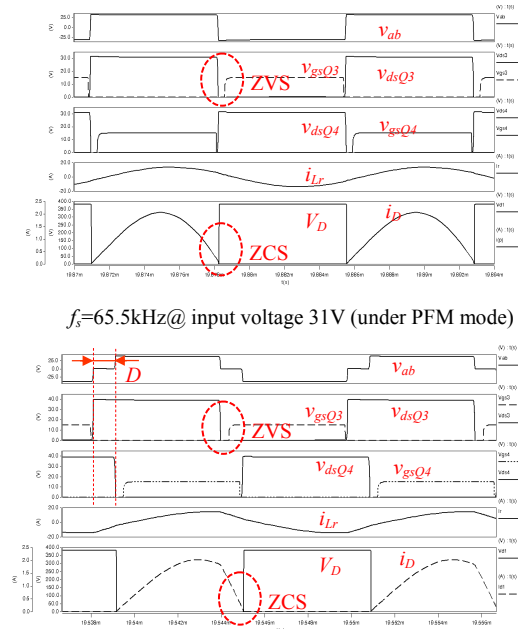
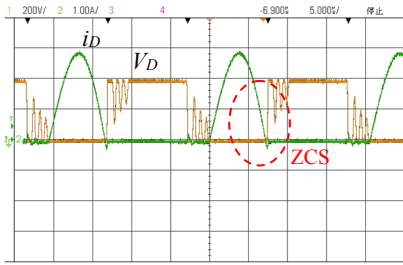
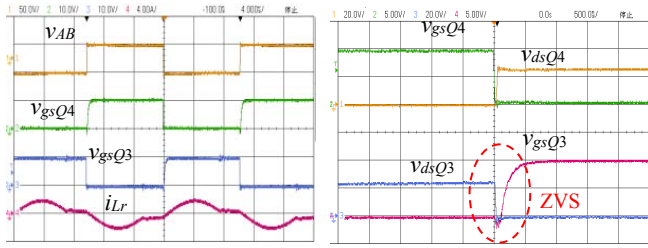


Fig.8.Key waveforms of LLC converter vs. input voltage

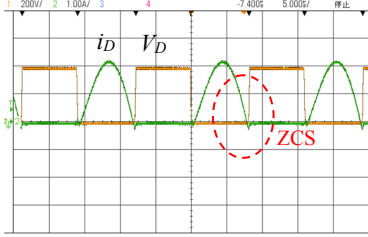
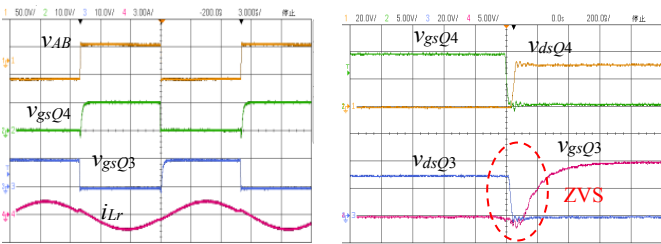
IV. Experimental results

4.1 Full-bridge LLC resonant converter

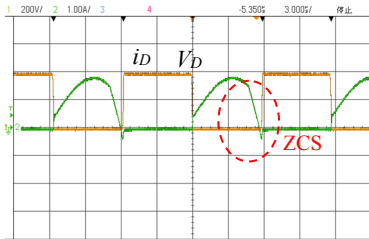
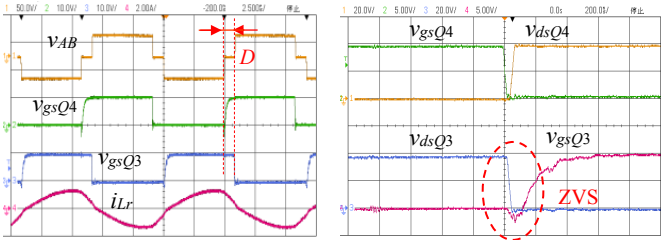
With the design parameters given in the "section 3.1", the inverter prototype was constructed. Moreover the inverter Q1-Q4 selects Infineon BSC016N06NS and T1-T4 selects IPD60R600C6. And D1, D2 selects STTH5L06. The key waveforms of the full-bridge LLC resonant converter vs. input voltage are given in Fig.9. It is shown that the converter operates with $f_s = f_r$ at nominal input voltage 31V and full load.



$f_s = 49.1\text{kHz}$ @ input voltage $V_{pv} = 25\text{V}$, output power $P_o = 250\text{W}$



$f_s = 63.6\text{kHz}$ @ input voltage $V_{pv} = 31\text{V}$, output power $P_o = 250\text{W}$



$f_s = 87.1\text{kHz}$ @ input voltage $V_{in} = 39\text{V}$, output power $P_o = 250\text{W}$

Fig.9 Key waveform of full-bridge LLC converter vs. V_{in}

And the converter operates under PFM control mode when f_s is lower than f_r , while operates at PFM+PS-PWM control mode

As shown in Fig.10, efficiency of the full bridge LLC converter is above 96% within MPPT voltage range from 25 to 39V and peak efficiency is high up to 97.7%.

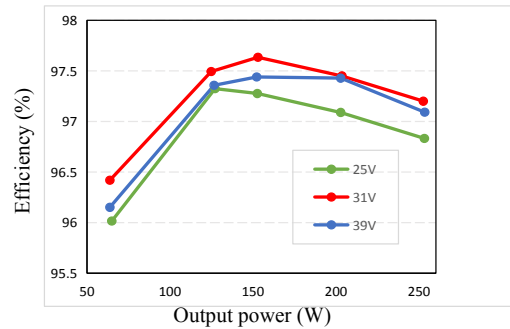
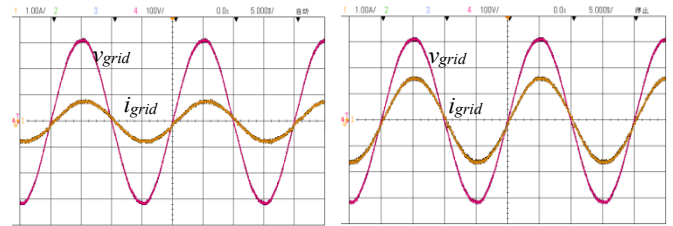


Fig.10 Efficiency of the LLC converter vs. V_{pv} and P_o

4.2 PVMI prototype

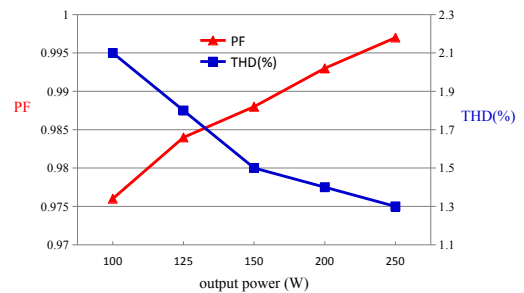
The grid-connected current waveform and its THD, PF are given in Fig.11. The THD is equal to 1.5% at full-load, and equal to 2% at half-load. The efficiency is shown in Fig.12, and the peak efficiency is up to 95.5% at about half-load. The test shows that the prototype has good performance in injected current and efficiency. It should be noted that power consumption 1.45W of auxiliary power supply for the control and drive in the inverter was also included in the test, and this consumption takes up more than 1% of the half-load with peak-efficiency occurring



@ Output power half-load 125W

@ Output power full-load 250W

(a) Current waveform



(b) THD and PF of injected current

Fig.11 Injected current @ $V_{pv} = 31\text{V}$, $v_{grid} = 220\text{Vac}$

The MPPT key waveforms are given in Fig.13. Under the test, the Regatron TCP.16.800.400.PV is used as PV simulator. The maximum power point is set to $P_{MP} = 250\text{W}$ and $V_{MP} = 36\text{V}$. v_{pv} , i_{pv} , p_{pv} is output voltage, current and power of the

simulator, and i_{grid} is the injected current of the inverter. The test shows that the inverter can search to the maximum power point quickly, and has good MPPT accuracy up to 99.3%.

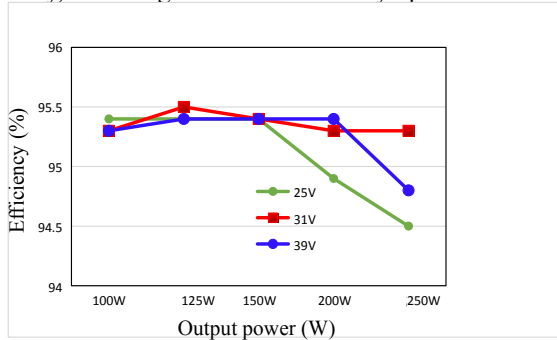


Fig. 12 efficiency of the PVMI vs. V_{pv} @ $v_{grid}=220Vac$

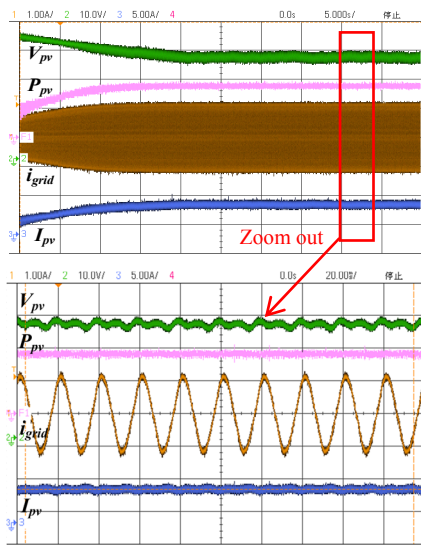


Fig. 13 MPPT key waveform @ $P_{MP}=250W$, $V_{MP}=36V$

V. Conclusion

The PVMI will meet more challenge of achieving high efficiency because of wide-range input voltage, A PVMI with two high-frequency power stages is proposed, and the full bridge LLC resonant converter with hybrid control is used for the first power stage to meet PV wide range input voltage. The LLC resonant converter operates PFM mode when switching frequency is lower than the resonant frequency; otherwise, it

operates with combining PFM and phase-shift PWM mode instead. Therefore, not only switching frequency range is limited, but also circumfluent current loss is decreased due to the reductions of phase-shift duty cycle under this proposed hybrid control. MPPT control is implemented on the second stage in order to avoid large amount of control variables existing in the LLC converter. Efficiency optimal design is implemented through the proposed design procedure for the resonant tank. Finally the experimental results verify the validity of the proposed scheme, design and control method.

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