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Low-loss high-confinement waveguides and microring resonators in AlGaAs-on-insulator

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AlGaAs is a promising material for integrated nonlinear photonics due to its intrinsic high nonlinearity. However, the challenging fabrication of deep etched AlGaAs devices makes it difficult to realize high performance devices such as low-loss dispersion engineered waveguides and high quality microring-resonators. Here, we reported a process to make high quality AlGaAs-on-insulator wafers where high confinement waveguides can be realized. Using optimized patterning processes, we fabricated AlGaAs-on-insulator waveguides with propagation losses as low as 1 dB/cm and microring resonators with quality factors up to 350,000 at telecom wavelengths. Our demonstration opens new prospects for AlGaAs devices in integrated nonlinear photonics. © 2016 Optical Society of America

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Integrated nonlinear photonics has recently drawn increasing research interests as it provides compact solutions compared with conventional fiber devices. Silicon-on-insulator (SOI) has been proposed as a promising platform due to its high material nonlinearity and its compatibility with the CMOS industry [1]. The enhanced device effective nonlinearity induced by the strong optical field confinement and the low linear losses ensured by the mature silicon fabrication enable various nonlinear applications such as optical signal processing [2], parametric amplification [3], supercontinuum generation [4] and frequency comb generation [5]. However, silicon suffers from two-photon absorption (TPA) when operated below 2.2 μm , which hampers its applications at e.g. telecom wavelengths (around 1.55 μm).

Aluminum gallium arsenide ($\text{Al}_x\text{Ga}_{1-x}\text{As}$) is also believed to be a promising nonlinear material [6,7]. It has a large transparency window and its intrinsic material Kerr ($\chi^{(3)}$) nonlinearity (on the order of $10^{-17} \text{ m}^2\text{W}^{-1}$) [8-10] is larger than that of silicon. Due to its non-centrosymmetric crystal structure, AlGaAs also exhibit strong $\chi^{(2)}$ effects [11,12]. Moreover, the bandgap of the AlGaAs material can be engineered by altering the aluminum composition (x) to

avoid TPA at telecom wavelengths. In addition, the linear refractive index of AlGaAs (around 3.3) is also similar to silicon, which makes it potentially suitable for high-index contrast waveguides. Conventional AlGaAs waveguides are realized by deep-etching a thick layer stack epitaxial grown on a GaAs substrate. The layer stack typically consists of $\text{Al}_x\text{Ga}_{1-x}\text{As}$ layers with different aluminum composition (x) where the index of the waveguiding layer is slightly larger than that of its adjacent top and bottom layers [8-10]. The fabrication of such waveguides with very high and narrow mesa structures is challenging, and increased sidewall roughness as the etch depth is increased is typically seen which severely affects the linear loss and thus the nonlinear efficiency. Significant efforts have been taken to improve the fabrication process over past years [6-12]. Recently, Porkolab et al. demonstrated low loss deep-etched AlGaAs waveguide by using a resist reflow technique [13]. However, more advanced designs than simple waveguide such as microring resonators with integrated waveguides are still difficult to realize with high performances. For instance, the achieved quality factors (Q) of AlGaAs microring resonators [15-17] are much lower than those of silicon devices [5]. Therefore, most of the nonlinear processes such as second harmonic generation (SHG) [11,12] and four-wave mixing (FWM) [8-10] are demonstrated in straight AlGaAs waveguides. In addition, the low vertical index-contrast of such AlGaAs waveguides limits its effective nonlinearity in spite of high intrinsic nonlinearity.

We propose an AlGaAs-on-insulator (AlGaAsOI) platform [18,19] where a thin AlGaAs layer on top of an insulator layer resides on a semiconductor substrate allowing device patterning using an easy shallow etching process. In addition, thanks to the high index contrast in AlGaAsOI, strong light confinement waveguides can be realized to enhance device effective nonlinearities. However, compared with the conventional low-index contrast waveguides, the surface roughness control become more critical for high index contrast waveguides due to the stronger light fields at the waveguide-cladding interfaces.

In this letter, we report a newly developed wafer bonding and substrate removal process to fabricate AlGaAsOI wafers with high quality surfaces. We also optimize patterning processes such as electron-beam lithography (EBL) and dry etching to get smooth device sidewalls. We fabricated sub-micron cross-sectional dimension waveguides with a linear loss as low as 1 dB/cm. We

also fabricated microring resonators with integrated bus waveguides and obtained an intrinsic Q of 350,000 for a 17- μm radius microring resonator.

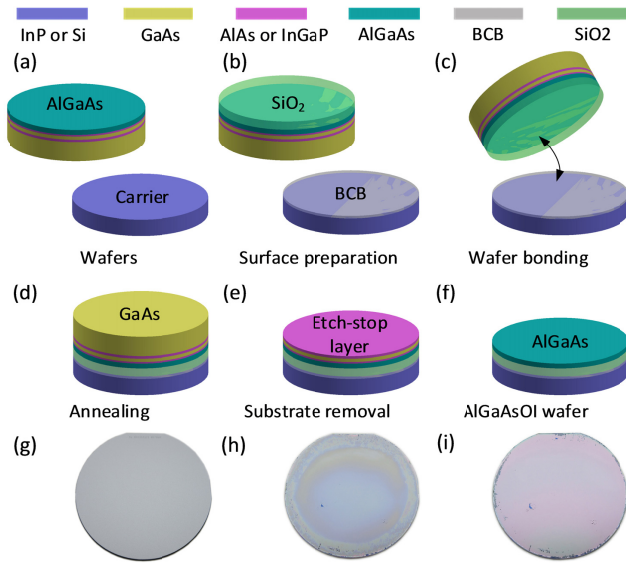


Fig. 1 Fabrication process for AlGaAs-on-insulator wafer starts with a GaAs wafer with epitaxial grown AlGaAs top layer and a semiconductor carrier wafer (a). A 3- μm SiO₂ layer is deposited on the AlGaAs wafer and a thin BCB layer is spun on top of the carrier wafer (b). The two wafers are then bonded (c) and annealed at 250 °C (d). The thick GaAs substrate is wet-etched with the AlGaAs layer being protected by the etch-stop layer (e). After wet-etching the etch-stop layer, AlGaAsOI wafers are ready for device fabrication (f). Photos of a bonded wafer with InGaP as the etch-stop layer before (g) and after (h) the GaAs substrate is removed and after all etch-stop layer removal (i).

The fabrication process of the AlGaAsOI wafer is shown in Fig. 1. Firstly, an epitaxial stack including Al_xGa_{1-x}As and etch-stop layers was grown in a low-pressure Metalorganic vapour phase epitaxy (MOVPE) reactor Emcore D125 on (100) GaAs substrates. The wafer temperature was regulated using an emissivity corrected pyrometer (RealTemp®) and the AlGaAs layer was deposited at 610°C. High quality GaAs substrates are essential in the MOVPE growth to obtain an atomic smooth surface for the AlGaAs layer. At all interfaces where group V gas was changed a 2-second growth interruption was introduced to prevent intermixing. A 3- μm thick SiO₂ layer is deposited by plasma enhanced chemical vapor deposition (PECVD) on the AlGaAs layer to become the buried oxide (BOX) layer. A 90 nm-thick Benzocyclobutene (BCB) layer is used as the adhesive layer for bonding between the AlGaAs wafer and another semiconductor carrier wafer (InP in this case to facilitate precise cleaving). The BCB thickness is also critical since a too thick BCB layer (e.g. >1 μm) results in difficulties in sample cleaving and inefficient thermal dissipation for the final devices while a too thin BCB layer (e.g. <50 nm) makes the wafer bonding extremely sensitive to the cleanliness of the wafer surface and thus adversely affect the yield of the wafer bonding and substrate removal processes. A thin SiO₂ layer and AP3000 adhesion promotor® was also deposited on the carrier wafer to enhance the BCB adhesion. The bonding process is performed under vacuum ($\sim 3 \times 10^{-4}$ mbar) in an EVG NIL bonding machine at 250°C for 1 hour, while a force of ~ 750 N is applied to the bonded wafers. Subsequently, the GaAs substrate is removed in two wet etching steps including an one-hour fast etch step with a sulfuric

acid/hydrogen peroxide solution (H₂SiO₄:H₂O₂=5:4) and a seven-hour slow etch step in a selective citric acid/hydrogen peroxide solution (C₆H₈O₇:H₂O₂=4:1) [20]. After the etch-stop layers (AlAs or InGaP) were wet-etched, the AlGaAsOI wafer is ready for device fabrication. Fig. 1(g-i) show photos for a bonded wafer before and after different wet etching steps. Only a few defects are observed at the edge of the AlGaAsOI wafer as shown in Fig. 1(i).

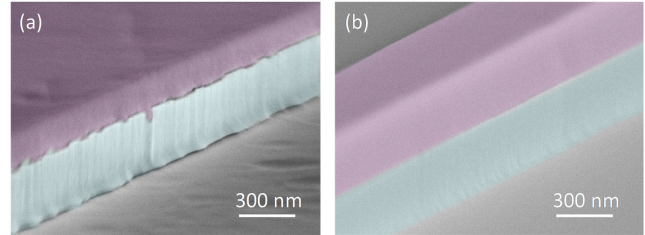


Fig. 2. Scanning electron microscopy (SEM) pictures of etched patterns on AlGaAsOI wafers fabricated by using different etch-stop layers: (a) AlAs (b) InGaP. Etch mask hydrogen silsesquioxane (HSQ) hard mask and AlGaAs material are denoted by the artificial purple and blue colour in both pictures.

A critical element in the substrate removal process is the choice of etch-stop layer as the high-index contrast device performance is very sensitive to surface roughness. We tested different etch-stop layers including AlAs and InGaP. Both AlAs and InGaP exhibit high resistance to the citric acid/hydrogen peroxide solution. Diluted hydrogen fluoride (HF) and hydrogen chloride (HCl) are used to wet-etch the AlAs and InGaP layers, respectively. Fig. 2 shows the comparison of patterns fabricated using AlGaAsOI wafers prepared using AlAs and InGaP as the etch-stop layer. It is found that the top surface of the AlGaAs layer is rough in the case of an AlAs etch-stop layer while a smooth top AlGaAs surface is observed in the case of an InGaP etch-stop layer. This large surface difference may due to byproducts in these two etching processes. In the AlAs etching process, the solid byproducts such as AlF₃ and [AlF_n(H₂O)_{6-n}]⁽³⁻ⁿ⁾⁺ are hard to dissolve into the solution [21], and As₂O₃ can also be generated on the AlGaAs surface with a high oxygen concentration in the HF etchant [22]. Whereas, in the InGaP etching process, the etchant HCl creates highly soluble byproducts such as AlCl₃, GaCl₃, which makes it easier to obtain a smooth surface. Therefore, HCl-based wet-etching is preferred for this purpose. Alternatively InAlP can be used as the etch-stop layer with HCl being the etchant [21].

In the substrate removal process, we introduce a dual-etch-stop layer consisting of two InGaP layers with a GaAs layer in between. The etching of the thick GaAs substrate (especially in the H₂SO₄/H₂O₂ etching) is not uniform and leaves a very rough InGaP surface. As shown in atomic force microscopy (AFM) image in Fig. 3(a), a root mean square (RMS) roughness of 1.8 nm is present on the top surface after removing InGaP layer. In order to reduce the surface roughness to a sub-nanometer level as the as-grown surface, an extra GaAs/InGaP layer stack is necessary. Due to the high etching selectivity of C₆H₈O₇:H₂O₂ and HCl etchant for GaAs/InGaP and InGaP/AlGaAs layer stacks respectively, the RMS roughness of the top surface after removing the second InGaP layer is estimated to be 0.5 nm as shown in Fig. 3(b). This low roughness has insignificant influence on waveguide propagation losses compared with a typical sidewall roughness induced by dry-etching processes [23].

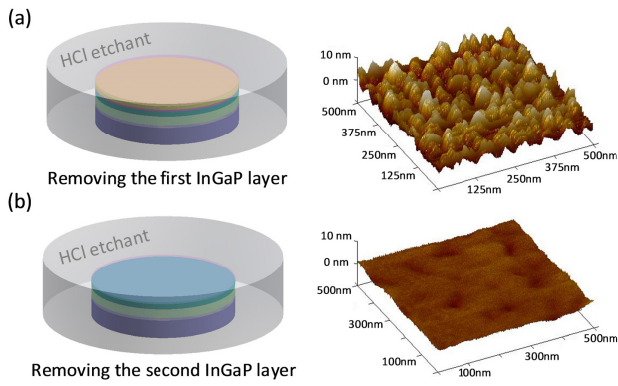


Fig. 3. Schematic illustration of wet etching of InGaP layer as the first etch-stop layer (a) and the second etch-stop layer (b) and three-dimensional atomic force microscopy (AFM) pictures for the wafer surface after the corresponding InGaP layer is removed.

For device patterning, we use hydrogen silsesquioxane (HSQ, Dow Corning FOX-15) as electron-beam resist. A 350 nm-thick HSQ layer is spun on the top of the AlGaAsOI wafer. EBL (JEOL JBX-9500FS) is used to define the patterns in HSQ with an average dosage $\sim 10,000 \mu\text{C}/\text{cm}^2$ and a KOH-based developer (AZ 400K:H₂O=1:3) is used for development of the HSQ. In EBL, the device pattern is fractured by the EBL software into small segments to fit the sub-field writing size ($4 \times 4 \mu\text{m}^2$) determined by the sub-deflector in the EBL system. We observed small stitching between those segments after HSQ developing, which will introduce extra roughness on the sidewalls especially for bending patterns as shown in Fig. 4(a). The stitching effect may be due to resist/BOX charging [24] since an ultra-high electron beam dosage is required for the low-sensitivity HSQ. A multi-pass (exposure) EBL process can be applied to mitigate the stitching effect. In such a process, the dose for each pass is much lower than that in the single-pass EBL case, which induces less resist charging. In addition, the device pattern is also fractured differently for each pass in such a way that most of the edges of the fractured pattern segments do not overlap with those in another EBL pass. Therefore, any small stitching occurred in one pass will be smeared out by other passes. In this way, smooth curved waveguide patterning can be realized as shown in Fig. 4(b).

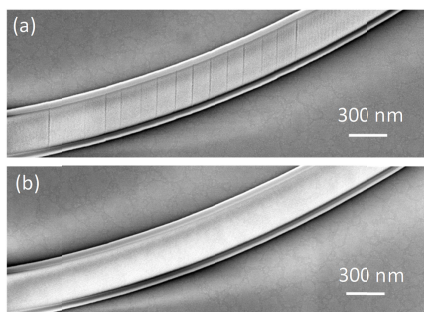


Fig. 4. EBL patterning comparison. Top-view SEM pictures of curved pattern in developed e-beam resist HSQ with different EBL processes: (a) standard single-pass EBL process, (b) multi-pass EBL process.

The device pattern was then transferred into the AlGaAs layer using a boron trichloride (BCl₃)-based dry etching process [25] in an inductive coupled plasma reactive ion etching (ICP-RIE) machine. To ensure a good selectivity towards the HSQ mask, straight and smooth waveguide sidewalls, critical etching

parameters including chamber pressure, coil and platen power are optimized (pressure: 10 mTorr, coil/platen power: 300W/50W) for our specific SPTS ICP tool. As shown in Fig. 2(b), the fabricated sidewall surface is smooth with only small roughness at bottom of the waveguide. Finally, a 3- μm thick SiO₂ cladding was deposited using PECVD, and the chip was cleaved to form the input and output facets where tapers or inverse nano-tapers [26] enable efficient chip-to-fibre coupling for characterization. The masking HSQ is kept on top of the AlGaAsOI devices since the dielectric properties of HSQ is very similar to SiO₂, and will not adversely affect device performances [27]. In addition, HF based wet etching will also attack the SiO₂ under the AlGaAs devices.

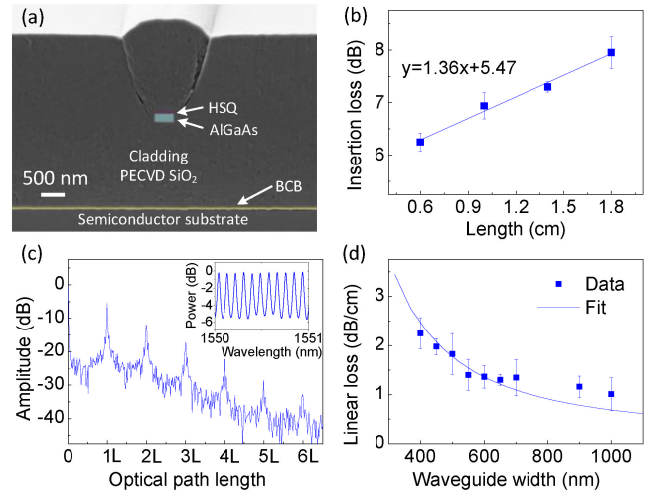


Fig. 5. (a) SEM picture showing the cross-section of an AlGaAsOI waveguide (denoted in artificial blue colour) cladded in SiO₂. (b) Measured insertion loss of waveguides with the same cross-sectional dimension (300 nm \times 600 nm) but different lengths. (c) Fourier transform of a measured transmission spectrum (inset) for a 3-mm long waveguide (300 nm \times 600 nm) with strong facet reflections. (d) The measured propagation loss of 300 nm-high AlGaAsOI waveguides with different widths (from 400 nm to 1 μm). The Payne-Lacey waveguide loss theory [28] is used for the fitting ($\sigma=2.8 \text{ nm}$, $L_c=50 \text{ nm}$).

To test the performance of our AlGaAsOI devices, we fabricated waveguides and microring resonators. Fig. 5(a) shows the cross-section of a fabricated AlGaAsOI nano-waveguide (300 \times 600 nm²) cladded in SiO₂ and Fig. 5(b) shows the measured insertion losses for the fundamental TE mode for waveguides with such a cross-sectional dimension but with different lengths. The extracted propagation loss is around 1.36 dB/cm. We also investigated the influence of waveguide width on propagation loss. Surface scattering is normally the main source for propagation loss due to roughness [28]. For high-index contrast AlGaAsOI waveguides, the propagation loss is dependent on the waveguide width since the strength of light field at the AlGaAs-SiO₂ interface varies resulting in a different scattering intensity as the waveguide dimension changes. Here, we utilized Fourier analysis for the loss characterization [29]. All the waveguides used for Fourier analysis are tapered to 4 μm at the chip facets to get a strong reflection and thus Fabry-Pérot fringes in the transmission spectra. The inset of Fig. 5(c) shows such a transmission for a 3-mm long waveguide (300 nm \times 600 nm). The Fourier transform of the spectrum in Fig. 5(c) shows high order harmonics with constant separations (corresponds to a round-trip length L) and a constant harmonic amplitude ratio (HAR) (corresponds to the round-trip loss

including propagation and reflection losses) for adjacent harmonics. For the waveguides with different widths, the propagation loss difference with respect to the 600 nm-wide waveguide is then extracted by comparing the HARs for different waveguides as they all have the same chip facet reflections. As shown in Fig. 5(d), the propagation loss decreased from 2.3 dB/cm to 1 dB/cm when the width for 300 nm-thick waveguides is increased from 400 nm to 1 μm . The limit at 1 dB/cm may be attributed to top and bottom roughness or surface absorption [30].

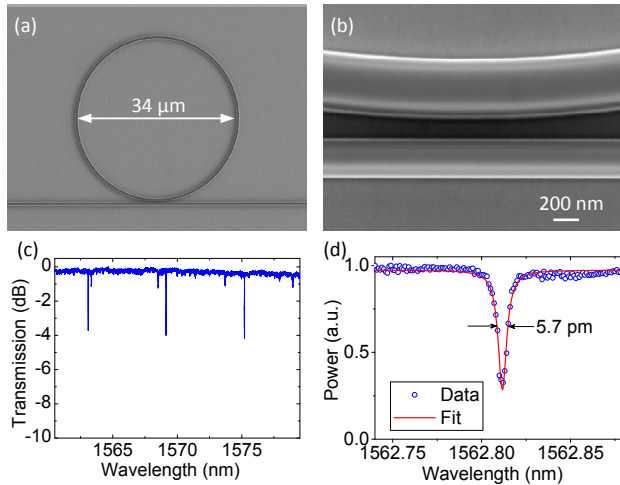


Fig. 6. (a) Top-view SEM image of the AlGaAsOI microring-resonator with an integrated bus waveguide. (b) Zoomed SEM image of the coupling region of this microring resonator. Measured (normalized) transmission spectrum of the microring resonator over a 20 nm wavelength range (c) and around the resonance at 1590 nm (d) with a loaded quality-factor (Q) of $\sim 269,500$.

Fig. 6(a) shows the SEM image of a fabricated 17- μm radius microring resonator. The waveguide widths for the bus and microring are 430 nm and 700 nm, respectively. The bus-to-ring coupling gap is 240 nm as shown in Fig. 6(b). Fig. 6(c) shows the transmission spectrum of this device for the TE polarization where two mode families were observed. Fig. 6(d) shows the resonance for fundamental TE mode at 1562.8 nm, which has a line-width about 5.7 pm corresponding to a loaded Q of $\sim 269,500$. In the characterization, the power coupled onto chip is kept at microwatt level to avoid thermal effects. The microring resonator is operated in the under-coupling condition and the intrinsic Q can be calculated by $Q_{int} = Q_{load} / (1 - \sqrt{T_0})$ [31], where T_0 is the fraction of transmitted optical power measured at the resonance. The intrinsic Q for the fundamental TE mode of our device is then estimated to be $\sim 3.5 \times 10^5$, which is the highest reported Q for microring resonators fabricated using III-V materials.

In summary, we realized an integrated AlGaAsOI platform by using a newly developed process. We fabricated low-loss sub-micron waveguides and high quality factor microring resonators in this platform. The high confinement induced by the high-index contrast makes the AlGaAsOI platform very promising in various nonlinear applications such as optical signal processing [18] and frequency comb generation [19].

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