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# Preselection Algorithm Based on Predictive Control for Direct Matrix Converter

**Abstract:** This paper presents an enhanced predictive control strategy to reduce the calculation effort for direct matrix converters. The main idea is to preselect the switching states to decrease the calculation effort during each sample period. The proposed preselection algorithm enables a predefined cost function to consider only the preselected switching states to perform the expected control. On the basis of the preselection of switching states at each sample period, the proposed method can effectively reduce the calculation effort as well as show a good performance. The proposed predictive control scheme using only preselected switching states needed to generate the desired source/load current waveforms and control the input power factor. The feasibility of the proposed method is experimentally verified and results are presented in the paper.

**Key words:** Direct matrix converter, preselection algorithm, predictive control.

#### 1. Introduction

Power converters are widely used for control in industrial applications including motor drives, energy conversion and power generation. The control of power converters has attracted much attention and several control schemes have been considered. Hysteresis and linear controls coupled with pulse-width modulation (PWM), which includes carrier-based modulation and space vector modulation (SVM), are the most mature techniques [1, 2]. Furthermore, some new and complex control schemes have been proposed and implemented due to the development of more advanced microprocessors. These new techniques include fuzzy logic, sliding mode control and predictive control.

The advantages of predictive control present great potential in the control of power converters [3]:

- (i) The concept is comprehensible and the control is easily implemented.
- (ii) The constraints and nonlinearities of different systems can be easily satisfied.
- (iii) Multiobjective problems can be simultaneously considered.

The current types of predictive control can be classified into four groups: deadbeat control, hysteresis-based predictive control, trajectory-based predictive control and model predictive control (MPC). MPC includes MPC with a continuous control set and MPC with a finite control set.

Compared with MPC with continuous control sets, MPC with finite control sets directly generates the switching signals of variable frequency without a modulator. Constraints of power converter can be included in predictive control and the methods generally have low implementation complexity. Considering the discrete nature of power converters and the finite set of possible switching states, the optimization problems of MPC is reduced to the evaluation of all possible switching states and the minimizing

of the given cost function. When the calculation horizon decreases the calculation of MPC with a finite control set is easier to implement. Hence, the predictive control method based on finite control set has been proposed as a simple and effective control method for power converters [4, 5]. The MPC with a finite control set has been applied for some application including current control [6, 7, 8], torque and flux control [9], power control [10], control of flying capacitor converter [10] and open-switch fault tolerant [11].

The standard Matrix converter (MC) [12] with nine bidirectional switches was first proposed by Gyugyi and Pelly in 1970 [13]. The MC has no DC-link energy storage elements, which makes it more compact and reliable compared with the back-to-back converter [14]. Due to these advantages, it is expected that the MC can be applied to ac-ac conversion applications, such as integrated motor drives, flexible ac transmission system, and wind energy conversion system [15, 16, 17]. Many conventional modulation methods [18], such as carrier-based modulation method, space vector modulation (SVM) method and modulation strategy based on mathematical constructions have been proposed for the MC. The model predictive control has recently been introduced to simplify the complexity of MC control [19, 20]. It has several advantages such as having a very intuitive approach, no need for linear controllers and modulators, and easy inclusion of nonlinearities [21]. This work establishes the models of the converter and load to predict the future values of load current and reactive power. These models are used to decide which switching state is the most suitable to apply for minimizing the cost function. Generally, according to the above control schemes, it is possible to summarize the principle of predictive control for MC as follows: (i) All possible switching states are substituted into the discrete models to calculate the future values of source current and load current in the next sampling time, and each predicted value corresponds to a value of cost function. (ii) The switching state producing the minimum value of cost function is selected to apply in the next modulation period. (iii) The performance of MC can be regulated by changing the weighting factors of different terms in the cost function.

In most predictive control algorithms, all possible control actions are evaluated by the cost function and then the optimum control can be taken by using the minimization of the cost function. Consequently, a certain predictive horizon will be formed by the system's reaction to these control actions. A higher prediction horizon theoretically leads to a better control performance, but the calculation effort rises exponentially. Among most of the predictive control methods for the MC, the easiest way to realize the minimization of cost function is an evaluation of all the possible switching states [22]. The disadvantage of this method is obvious that the calculation effort rises with the prediction horizon. Hence, an optimal algorithm is needed which will reduce the calculation effort and make possible higher predictive horizons.

In [23] a predictive control algorithm with three or four prediction steps is presented for two-level VSI. This algorithm uses a heuristic method to reduce the calculation effort and makes higher predictive horizons feasible in real time. Only the three switching states closet to the continuous-valued optimum solution are evaluated for the first two prediction steps, and only the two

closet ones for the third and fourth prediction step. The combinations of switching states decrease from  $7^3 = 343$  to 18 for three prediction steps and from  $7^4 = 2401$  to 36 for four prediction steps. Then the optimization becomes a search for the right region and the closet switching states. The region selection is completed by a binary search tree, which is effective and time-saving for a higher number of regions. This proposed method significantly reduces the calculation effort for the model predictive control (MPC), and has been applied to reduce the calculation effort for the induction motor (IM) fed by a two-level three-phase VSI in [24].

For a cascaded H-bridge inverter a large number of available voltage vectors make it difficult to implement the MPC algorithm in a standard control platform. In [25] a method is proposed to reduce the set of voltage vectors without degrading the system's performance by two steps. First, the voltage vectors generating the minimum common-mode voltage will be selected. Then, a subset of possible voltage vectors will be selected to drive the inverter by considering information about the previously applied voltage vector. In [26] a distributed model predictive control strategy was proposed, which is suitable for back-to-back converters and multi-level converters. The controller computational burden is approximately one fourth of classical requirement for FCS-MPC. In [27], a simplified FCS-MPC method solves the "required voltage" firstly, which makes the current in the next sampling period equal to its reference. The switching state, which is the closest to the required voltage, is applied to the power converter at the time instant k. The closest switching state is selected by a specialized sector distribution method. Compared with the conventional FCS-MPC using the cost function to select the optimal switching state, the computational complexity of simplified FCS-MPC is greatly reduced and the performance of the simplified FCS-MPC is the same as that of the conventional FCS-MPC.

This paper proposes an enhanced predictive control strategy to reduce the calculation effort for the direct matrix converter (DMC). According to the sector of input current vector and output voltage vector, the switching states at the next sample period will be preselected. In conventional predictive control scheme for DMC, 27 switching states are considered for the prediction. The proposed preselection algorithm firstly excludes the impossible switching states and uses 11 preselected switching states to generate the expected source/load current. This method is based on the idea of switching states preselection, and it can generate good source/load current waveforms and take full control of input power factor. The feasibility of the proposed method is validated using experiment results.

#### 2. The Direct Matrix Converter

The DMC consists of 3\*3 matrix bidirectional power semiconductors as shown in Fig.1. An input filter is used to avoid overvoltage and reduce input current distortion. The following equations explain the modulation principle of the DMC remembering that an open circuit is prohibited across the load connections and a short circuit is prohibited between the input lines.

$$\mathbf{u}_{o} = \begin{bmatrix} S_{Aa} & S_{Ab} & S_{Ac} \\ S_{Ba} & S_{Bb} & S_{Bc} \\ S_{Ca} & S_{Cb} & S_{Cc} \end{bmatrix} \mathbf{u}_{e}$$

$$(1)$$

$$\mathbf{i}_{e} = \begin{bmatrix} S_{Aa} & S_{Ab} & S_{Ac} \\ S_{Ba} & S_{Bb} & S_{Bc} \\ S_{Ca} & S_{Cb} & S_{Cc} \end{bmatrix}^{T} \mathbf{i}_{o}$$

$$(2)$$

$$\begin{cases} S_{Aa} + S_{Ab} + S_{Ac} = 1 \\ S_{Ba} + S_{Bb} + S_{Bc} = 1 \\ S_{Ca} + S_{Cb} + S_{Cc} = 1 \end{cases}$$
(3)

where  $\mathbf{u}_e$ ,  $\mathbf{i}_e$ ,  $\mathbf{u}_o$  and  $\mathbf{i}_o$  present the input voltage, the input current, the output voltage and the load current of the DMC, respectively.  $S_{XY}$  ( $X \in \{A, B, C\}$ ,  $Y \in \{a, b, c\}$ ) equals to '1' when  $S_{XY}$  is turned on and equals to '0' when  $S_{XY}$  is turned off, respectively.

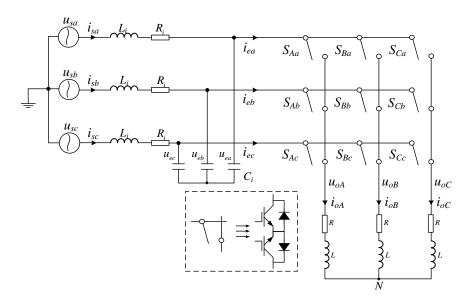


Fig. 1 The topology of the direct matrix converter

# 3. Principle of predictive control

In the following section all the three phase quantities are assumed to be symmetrical and these can therefore be transformed from the static three-phase coordinates to the static two-phase coordinates. For example, certain three phase quantities  $X_a$ ,  $X_b$  and  $X_c$  are re-expressed by the complex space vector

$$X = X_{\alpha} + j \cdot X_{\beta} \tag{4}$$

which is defined as

$$\begin{cases} X_{\alpha} = \frac{1}{3} (2X_a - X_b - X_c) \\ X_{\beta} = \frac{1}{\sqrt{3}} (X_b - X_c) \end{cases}$$
 (5)

Predictive control is utilized to select the optimal switching state that makes the controlled variables follow the respective reference during one sample period. For DMC, two main conditions must be satisfied to properly operate: unity power factor and satisfactory steady state performance. The predictive values of the source and load currents are calculated for each possible switching state by measuring the source voltage, the source current, the capacitor voltages and the load current to meet the mentioned objectives. First, the objective of unity power factor can be reduced to keep the source voltage and current in the same phase, as follow

$$g_{1} = \left(i_{s\alpha}^{*} - i_{s\alpha}^{P}\right)^{2} + \left(i_{s\beta}^{*} - i_{s\beta}^{P}\right)^{2} \tag{6}$$

where  $i_{s\alpha}^P$  and  $i_{s\beta}^P$  denote the predictive source current in the next sample period and  $i_{s\alpha}^*$  and  $i_{s\beta}^*$  denote the respective references. The phase of source current reference is equal to the phase of source voltage, and the amplitude of source current reference is determined as follow [11]

$$I_{sm}^{*} = \frac{\eta U_{sm} \pm \sqrt{(\eta U_{sm})^{2} - 4\eta R_{i} R I_{om}^{*2}}}{2\eta R_{i}}$$
(7)

where  $I_{om}^*$  and  $U_{sm}$  are the amplitude of the reference load current and the source voltage, respectively.  $\eta$  means the efficiency of DMC.

Second, the objective of satisfactory steady state performance can be reduced to minimize the error between the load current prediction and reference

$$g_2 = (i_{o\alpha}^* - i_{o\alpha}^P)^2 + (i_{o\beta}^* - i_{o\beta}^P)^2$$
(8)

where  $i_{o\alpha}^P$  and  $i_{o\beta}^P$  denote the predictive load current in the next sample period and  $i_{o\alpha}^*$  and  $i_{o\beta}^*$  denote the respective references. Equations (6) and (8) are merged into a cost function

$$g = \lambda_1 \cdot g_1 + \lambda_2 \cdot g_2 \tag{9}$$

where  $\lambda_I$ ,  $\lambda_2$  are the weighing factors deciding the priority of corresponding control variable, which are flexibly changed due to different control requirements. At each sample period, all possible switching states are substituted into (9) to calculate g. The switching state generating the minimum value of g is selected to be implemented for the next sample period.

# 4. Calculation of predictive values

The predictions of source and load currents, which are necessary for evaluating the cost function g, can be derived from the

mathematical models of input filter and load.

The mathematical model of the input filter is related to the source voltage, input voltage, source current and input current, and the following state-space system describe the input filter model

$$\begin{bmatrix} du_e/dt \\ di_s/dt \end{bmatrix} = A \begin{bmatrix} u_e \\ i_s \end{bmatrix} + B \begin{bmatrix} u_s \\ i_e \end{bmatrix}$$
 (10)

$$A = \begin{bmatrix} 0 & 1/C_i \\ -1/L_i & -R_i/L_i \end{bmatrix}, B = \begin{bmatrix} 0 & -1/C_i \\ 1/L_i & 0 \end{bmatrix}$$
 (11)

where the  $u_s$ ,  $u_e$ ,  $i_s$ ,  $i_e$  represent the source voltage, input voltage, source current and input current, respectively.  $R_i$ ,  $L_i$ ,  $C_i$  represent the resistances, inductances and capacitance of the mains and filter, respectively. Further, assuming the sample period is  $T_s$ , the state-space system is discretized by Forward Euler Approximation and the predictions of capacitor voltage and source current are obtained as follow

$$\begin{bmatrix} u_e^{k+1} \\ i_s^{k+1} \end{bmatrix} = e^{AT_s} \begin{bmatrix} u_e^k \\ i_s^k \end{bmatrix} + A^{-1} (e^{AT_s} - I) B \begin{bmatrix} u_s^k \\ i_e^k \end{bmatrix}$$

$$(12)$$

The load model is related to the output voltage and load current, and the following state-space system describes the load model

$$L\frac{di_o}{dt} = u_o - Ri_o \tag{13}$$

which is discretized in the same way as follow

$$i_o^{k+1} = u_o^k T_s / L + (1 - T_s R / L) i_o^k$$
(14)

Above all, all the prediction equations are rewritten as follow

$$\begin{cases} u_e^{k+1} = L_{11}u_e^k + L_{12}i_s^k + M_{11}u_s^k + M_{12}i_e^k \\ i_s^{k+1} = L_{21}u_e^k + L_{22}i_s^k + M_{21}u_s^k + M_{22}i_e^k \\ i_o^{k+1} = N_1u_o^k + N_2i_o^k \end{cases}$$
(15)

where 
$$\begin{bmatrix} L_{11} & L_{12} \\ L_{21} & L_{22} \end{bmatrix} = e^{AT_s}$$
,  $\begin{bmatrix} M_{11} & M_{12} \\ M_{21} & M_{22} \end{bmatrix} = A^{-1}(e^{AT_s} - I)B$ ,  $\begin{cases} N_1 = T_s/L \\ N_2 = 1 - T_sR/L \end{cases}$ .

# 5. Proposed Predictive Control with Preselection Algorithm

For the conventional predictive method, all these 27 switching states will be substituted into the cost function for optimum selection. A complete enumeration of all possible switching states leads to more online calculation time. In order to reduce the necessary online computation time, an optimized predictive control method with the vector preselection is proposed to narrow down the range of possible switching states, instead of numerating all switching states. The proposed predictive control method is based on the instantaneous space vector representation of input currents and output voltages. Different switching states will be

substituted into the cost function for optimum selection when the input current vector and the output voltage vector lie in different sectors respectively.

According to the output voltage vector and input current vector that each switching state generates, the 27 switching states are classified into three groups as shown in Table 1, where  $u_{eil}$  ( $i, l \in \{a, b, c\}, i \neq l$ ) represents the line-to-line input voltage,  $I_{om}$ ,  $U_{em}$  are the amplitude of the load current and the input voltage respectively.

Group I: Two output phases are connected to a common input phase, and the third is connected to a different input phase. This group includes 18 switching states which represent the active vectors and determine the output voltage vector and input current vector.

Group II: All three output phases are connected to the same input phase. This group includes three switching states which determine zero input current and output voltage vectors.

Table. 1 Possible switching states and their space vectors

Group NO.	Switching States	A B C	$U_{om}$	$\alpha_{_{o}}$	$I_{im}$	$oldsymbol{eta}_i$
I	+1	a b b	$2u_{eab}/3$	0	$2i_A/\sqrt{3}$	<b>-</b> π/6
I	-1	b a a	$-2u_{eab}/3$	0	$-2i_A/\sqrt{3}$	$-\pi/6$
I	+2	bсс	$2u_{ebc}/3$	0	$2i_A/\sqrt{3}$	$\pi/2$
I	-2	c b b	$-2u_{ebc}/3$	0	$-2i_A/\sqrt{3}$	$\pi/2$
I	+3	caa	$2u_{eca}/3$	0	$2i_A/\sqrt{3}$	$7\pi/6$
I	-3	асс	$-2u_{eca}/3$	0	$-2i_A/\sqrt{3}$	$7\pi/6$
I	+4	b a b	$2u_{eab}/3$	$2\pi/3$	$2i_B/\sqrt{3}$	$-\pi/6$
I	-4	a b a	$-2u_{eab}/3$	$2\pi/3$	$-2i_B/\sqrt{3}$	$-\pi/6$
I	+5	сьс	$2u_{ebc}/3$	$2\pi/3$	$2i_B/\sqrt{3}$	$\pi/2$
I	-5	b c b	$-2u_{ebc}/3$	$2\pi/3$	$-2i_B/\sqrt{3}$	$\pi/2$
I	+6	a c a	$2u_{eca}/3$	$2\pi/3$	$2i_B/\sqrt{3}$	$7\pi/6$
I	-6	сас	$-2u_{eca}/3$	$2\pi/3$	$-2i_B/\sqrt{3}$	$7\pi/6$
I	+7	b b a	$2u_{eab}/3$	$4\pi/3$	$2i_C/\sqrt{3}$	$-\pi/6$
I	-7	a a b	$-2u_{eab}/3$	$4\pi/3$	$-2i_C/\sqrt{3}$	$-\pi/6$
I	+8	c c b	$2u_{ebc}/3$	$4\pi/3$	$2i_C/\sqrt{3}$	$\pi/2$
I	-8	bbc	$-2u_{ebc}/3$	$4\pi/3$	$-2i_C/\sqrt{3}$	$\pi/2$
I	+9	a a c	$2u_{eca}/3$	$4\pi/3$	$2i_C/\sqrt{3}$	$7\pi/6$
I	-9	сса	$-2u_{eca}/3$	$4\pi/3$	$-2i_C/\sqrt{3}$	$7\pi/6$
II	0	a a a	0	_	0	_
II	0	b b b	0	_	0	_
II	0	ссс	0	_	0	
III		a b c	$U_{em}$	$\alpha_{i}$	$I_{om}$	$\beta_{o}$
III	_	a c b	- $U_{em}$	$-\alpha_{\rm i}$	$I_{om}$	$-\beta_{o}$
III	_	b a c	- $U_{em}$	$-\alpha_i + 4\pi/3$	$I_{om}$	$-\beta_0 + 2\pi/3$
III	_	b c a	$U_{em}$	$\alpha_i + 4\pi/3$	$I_{om}$	$\beta_o + 2\pi/3$
III	<del></del>	c a b	$U_{em}$	$\alpha_i + 2\pi/3$	$I_{om}$	$\beta_0 + 4\pi/3$
III		c b a	- $U_{em}$	$-\alpha_i+2\pi/3$	$I_{om}$	$-\beta_0+4\pi/3$

Group III: Each output phase is connected to different input phase respectively. In this group, the output voltage and input

current vectors have variable directions and amplitudes. Thus, the remaining six switching states are difficult to synthesize the reference vectors.

As shown in Fig. 2, the complex plane is divided into six sectors by six active voltage vectors. At any sample period, the reference output voltage  $u_o^*$  can be calculated from the reference load current  $i_o^*$  and the resistance-inductance load. The reference input current  $i_e^*$  can be obtained from the source voltage  $u_s$  and the reference source current  $i_s^*$ . The following equations describe the relationship among the quantities above.

$$u_o^* = (R + j\omega_o L)i_o^* \tag{16}$$

$$\begin{cases} u_s = u_e + (R_i + j\omega_i L_i) i_s^* \\ i_s^* = i_c + i_e^* \\ i_c = j\omega_i C_i u_e \end{cases}$$
(17)

where R and L are the load resistance and inductance respectively.  $L_i$  comprises the mains and filter inductances,  $R_i$  represents the mains and filter resistances and  $C_i$  is the filter capacitance. For the preselection of the finite control set, it is necessary to judge the sector of the vector  $u_o^*$  and  $i_e^*$ . Assume the input power factor is unity, the angle of the vector  $u_o^*$  and  $i_e^*$  can be calculated as follows.

$$\alpha_{uo} = \alpha_{io} + \arctan \frac{\omega_o L}{R}$$
 (18)

$$\beta_{ie} = \beta_{is} + \arctan \frac{-\omega_i C_i (u_s - R_i i_s^*)}{(1 - \omega_i^2 C_i^2) i_s^*}$$
(19)

where  $\alpha_{uo}$  is the angle of the vector  $u_o^*$ ,  $\alpha_{io}$  is the angle of the vector  $i_o^*$ .  $\beta_{ie}$  is the angle of the vector  $i_e^*$  and  $\beta_{is}$  is the angle of the source  $i_s^*$ .

In order to clearly explain the method of preselecting the finite control set, the reference vectors  $u_o^*$  and  $i_e^*$  are both assumed to be located in sector I as shown in Fig. 2 (a) and (b). The reference voltage vector  $u_o^*$  is resolved into two adjacent vectors  $u_o^{*'}$  and  $u_o^{*''}$ . The vector  $u_o^{*'}$  can be synthesized with the voltage vectors which correspond to the pairs of switching configurations  $\pm 1$ ,  $\pm 2$ ,  $\pm 3$ . These voltage vectors have the same direction with  $u_o^{*'}$ . At the same time, the six switching states of  $u_o^{*''}$  are  $\pm 7$ ,  $\pm 8$ ,  $\pm 9$ . Similarly, the reference input current vector  $i_e^*$  is obtained from two adjacent vectors  $i_e^{*'}$  and  $i_e^{*''}$  which are generated by switching configurations  $\pm 3$ ,  $\pm 6$ ,  $\pm 9$  and  $\pm 1$ ,  $\pm 4$ ,  $\pm 7$ . But only the common switching states between the output voltage and input current vectors will be considered in the finite control set because they are synthesized at the same sample period. As a result, the switching states  $\pm 2$  and  $\pm 8$  are eliminated and the switching configurations  $\pm 1$ ,  $\pm 3$ ,  $\pm 7$ ,  $\pm 9$  will be selected in the finite control set.

Table. 2 Selection of the switching configurations for each combination of output voltage and input current sectors

Preselected switching states		Sector of the output voltage vector			
		1 or 4	2 or 5	3 or 6	
the	1 or 4	0,±1, ±3, ±7, ±9	$0,\pm 4,\pm 6,\pm 7,\pm 9$	$0,\pm 1,\pm 3,\pm 4,\pm 6$	
Sector of the input current vector	2 or 5	$0,\pm 2,\pm 3,\pm 8,\pm 9$	$0,\pm 5,\pm 6,\pm 8,\pm 9$	$0,\pm 2,\pm 3,\pm 5,\pm 6$	
Secinpu	3 or 6	$0, \pm 1, \pm 2, \pm 7, \pm 8$	$0,\pm 4,\pm 5,\pm 7,\pm 8$	$0,\pm 1,\pm 2,\pm 4,\pm 5$	

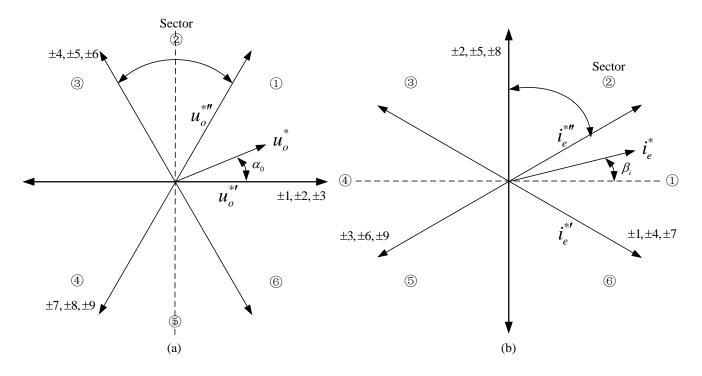


Fig. 2 (a) Ouput voltage vectors generated by the active switching states

(b) Input current vectors generated by the active switching states

In the same way, it is possible to determine the eight switching states related to any possible combination of output voltage and input current sectors. On the other hand, the three switching states from group II are chosen in each finite control set because the zero input current and output voltage vectors are useful for the proposed predictive control method. The preselected switching states are summarized in Table 2. In Fig. 3, the block diagram of the predictive control strategy applied on DMC is described. At the  $k^{th}$  sample period  $T^k$ , the angles of reference input current vector and the reference output voltage vector are computed from (18) and (19). With the angle of the vector  $i_e^*$  and  $u_o^*$ , it is easy to judge the sectors of  $i_e^*$  and  $u_o^*$ . And there are eleven switching states chosen in the preselected finite control set. At the same time, the variables  $u_s^k$ ,  $i_s^k$ ,  $u_e^k$  and  $i_o^k$  in  $T^k$  are obtained from the sensor circuits, and the switching states  $S^k$  in  $T^k$  is obtained from the previous sample period. The variables  $i_e^k$  and  $u_o^k$  are calculated according to the MC model. Thus,  $i_o^{k+1}$ ,  $i_o^{k+1}$  and  $u_e^{k+1}$  at the  $(k+1)^{th}$  sample period  $T^{k+1}$  can be derived according to the load model and the input filter model respectively.  $u_s^{k+1}$  is considered equal to  $u_s^k$  ignoring the change of the source voltage in a small

sample period. In the same way, predictive values of  $i_o^{k+2}$  and  $i_s^{k+2}$  at the  $(k+2)^{th}$  sample period  $T^{k+2}$  can be obtained for each valid switching state. Substituting each predictive value into  $g^{k+2}$ , the switching state making  $g^{k+2}$  minimal will be applied in the  $(k+1)^{th}$  sample period  $T^{k+1}$ .

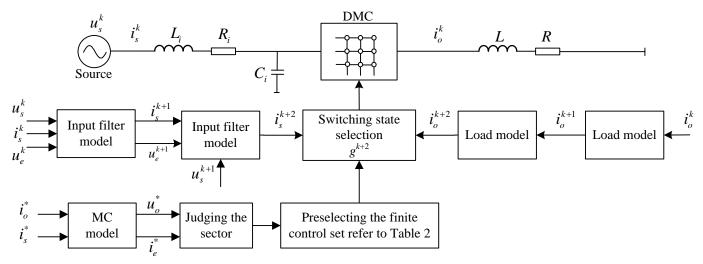


Fig. 3 Block diagram of the predictive control strategy

# 6. Experimental Results

The operation of the DMC using model predictive control with the preselection algorithm has been experimental validated using a prototype converter. Using the full schematic model of the system, all the experiments are performed with a symmetrical three phase source. The setup is fed at 50Hz and the output fundamental frequency was chosen as 40Hz or 60Hz in order to ensure the universality of the experiments. Relevant parameters of the experimental converter are presented in Table 3. The power source isn't perfect and hence the AC source voltages contain some undesirable fifth-order and seventh-order harmonics. Thus, the value of  $R_p$  is chosen as  $9\Omega$ , slightly higher than the ideal value. The bi-directional switches are implemented using IGBT modules, FF200R12KT3\_E. Sensor circuits are equipped to provide the information of the source voltage, the source current, the capacitor voltage and the load current. A floating-point digital signal processor (DSP, TMS320F28335) is used to select the optimal switching state while a field programmable gate array (FPGA, EP2C8T144C8N) is used for generating a set of impulses to control the switches. The floating-point DSP can also show the computation time of the system.

Table. 3 Parameters of the low-voltage experimental prototype

Parameters	Value
Source phase voltage ( $V_{RMS}$ )	60V
Source voltage frequency( $f_{in}$ )	50Hz
Input mains and filter inductor ( $L_i$ )	0.6 mH
Input filter capacitor ( $C_i$ )	66uF
Input mains and filter resistor ( $R_i$ )	$0.1\Omega$
Input passive damping resistor ( $R_p$ )	$9\Omega$
Resistor of load (R)	$4.4\Omega$
Inductor of load ( L )	6mH

The experiment results comparing the conventional and proposed method are shown in Fig. 4 when the amplitude of the reference load current change.  $u_{sa}$ ,  $i_{sa}$ ,  $u_{oAB}$  and  $i_{oA}$  represent the source voltage, the source current, the output line-to-line voltage and the load current, respectively. The enlarged drawings of the red dotted box in Fig. 4(a), (c) are shown in Fig. 4(b), (d), respectively. The transient response time of the proposed method in Fig. 4(b), (d) is about 0.4ms, which is similar to the conventional method in Fig. 4(a), (c).

The experiment results of the proposed method with different reference load current are shown in Fig. 5. At the same time, the conventional method is also experimented to make a comparison. Furthermore, experiments are performed to show a good result with the unit power factor and the load current following the reference accurately. Because 27 switching states are considered for the conventional method while only 11 switching states are considered for the preselection algorithm. The computation time of the conventional method for DSP (TMS320F28335) is about 64.1us while the less computation time 33.73us is taken by the preselection algorithm. The detailed comparison between the running time required by the conventional method and the proposed method are shown in Table. 4. Experimental results with the same  $I_{om}^*/f_o$  value are performed. The THD values of the source current and the load current with conventional method ( $T_s$ =70us) and proposed method ( $T_s$ =70us) are described in Table 5. The THD values of the proposed method with  $T_s$ =70us are a little larger than those of the conventional method with  $T_s$ =70us.

The experimental waveforms of the preselection algorithm show almost the same performance as that of the conventional method. Because of the time the preselection algorithm saves, it is possible to implement the total algorithm on microcontroller, which is cheaper and has lower calculating speed. In other words, these time savings allow the microcontroller to implement some other operations in only one sample period. Certainly, if high performance is required and the cost of microcontroller is negligible, the conventional method should be considered.

Table. 4 Comparison between the running time required by the conventional method and the proposed method

Case	Time (us)					
$(T_s = 70 us)$	A/D conversion	FCS-MPC algorithm	Other algorithms	Free		
Conventional method	2.63	55.25	4.42	5.90		
Proposed method	2.63	26.50	4.60	36.27		

Table. 5 THD values of the source current and the load current

	Cases		The source current THD	The load current THD	
Desired load current	Desired load current Sample period N		The source current TTD	The four current TTD	
8A, 40Hz	70us	Conventional	18.04%	3.51%	
	70us	Proposed	18.97%	4.58%	
10A, 50Hz	70us	Conventional	12.16%	3.13%	
	70us	Proposed	13.22%	4.13%	
12A, 60Hz	70us	Conventional	8.44%	2.17%	
	70us	Proposed	10.04%	3.44%	
14A, 70Hz	70us	Conventional	7.47%	2.07%	

70us Proposed 9.35% 3.17%

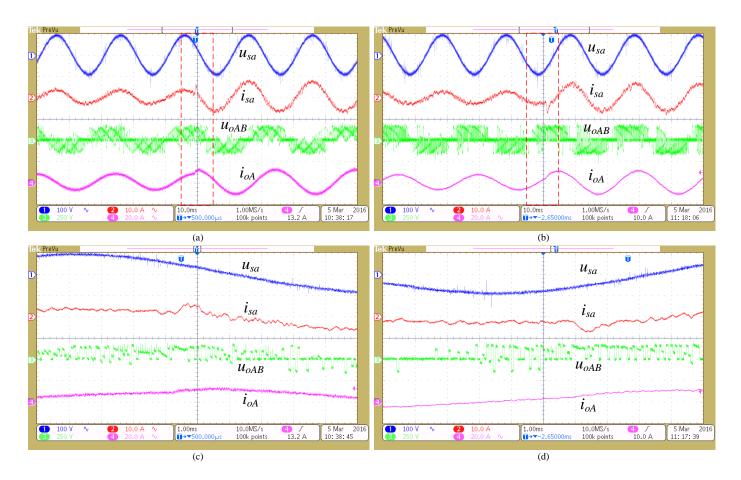


Fig. 4 Experiment results with  $T_s$  =70us when the reference load current changes from  $I_{om}^*$  = 8A,  $f_o$  = 40Hz to  $I_{om}^*$  = 12A,  $f_o$  = 40Hz (a) Conventional method. (b) Proposed method. (c) The enlarged drawing of the red dotted box in (a). (d) The enlarged drawing of the red dotted box in (b).

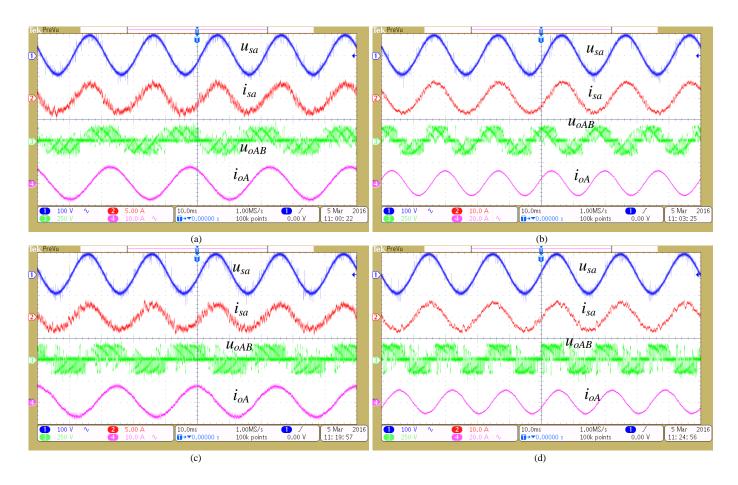


Fig. 5 Experiment results with  $T_s$  =70us. Conventional method: (a)  $I_{om}^* = 8A$ ,  $f_o = 40$ Hz. (b)  $I_{om}^* = 12A$ ,  $f_o = 60$ Hz. Proposed method: (c)  $I_{om}^* = 8A$ ,  $f_o = 40$ Hz. (d)  $I_{om}^* = 12A$ ,  $f_o = 60$ Hz.

## 7. Conclusion

In this paper a predictive control method with a preselection algorithm for a Matrix Converter is proposed. On the premise of satisfying the conditions of unity input power factor and accurately following the output current reference value, the proposed control method can reduce the calculation effort for the model predictive controller. The proposed method could also be used with a shorter sample period because of lower computation time. The proposed method preselects the finite control set by judging the sectors of the input current vector and the output voltage vector; only the switching states in the preselected finite control set are considered while the conventional predictive control method enumerates all the switching states satisfying the restriction of the matrix converter topology. The proposed method allows the regulation of the input power factor by controlling the phase shift between the source current and the source voltage. The experiment results validate the feasibility of the method.

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