Reduction of Noise Spikes in Touch Screen Systems by Low Pass Spatial Filtering

Shuo Gao, Student Member, IEEE, David McLean, Jackson Lai, Charles Micou and Arokia Nathan, Fellow, IEEE

Abstract—This paper reports on a low-pass spatial filtering technique for reduction of noise spikes in capacitive touch screen panels (TSPs). Filter bandwidth is adjusted by dynamically evaluating attenuation of signal and noise spikes. Based on the experimental results, we boost the signal-to-noise ratio (SNR) by 15.6dB and attenuate noise spikes by 19.25dB. The processed signal yields higher detection accuracy and lower power consumption.

Index Terms—capacitance touch screen panels, low-pass spatial filtering, noise spike reduction and signal attenuation.

I. INTRODUCTION

OUCH based human-machine interactivity has become a L ubiquitous technology used in displays [1], [2]. Among different sensing techniques, capacitive touch screen panels (TSPs) are widely used in mobile devices, where high touch accuracy is a fundamental requirement for good user experience. Besides, the power consumption is another key parameter for long battery life time. A factor directly associated with these two parameters is the noise, which adversely affects the detection accuracy hereby increasing power consumption to reach/maintain a desired performance. Hence, the noise reduction is one of the main considerations in TSP design. Many noise reduction techniques are presented [3]-[10]. In this context, noise refers to any unwanted signals that may lead to detection errors. Noise conventionally includes deterministic (e.g. display noise) and stochastic (e.g. thermal noise) components. Traditionally offset on a single electrode is not considered as this can be cancelled by deducting a DC value. However, in capacitance TSPs, the global multi-valued offset gives rise to potential errors [11], which ought to be accounted for as a noise component. In our previous work [11], the global multi-valued offset is removed along with the common-mode noise by correlated double sampling (CDS) [12]-[14]. However, when the correlation between the touch frames and the noise

reference frame becomes weak, noise spikes still remain, giving rise to detection errors. By analyzing the characteristics of the touch signal and noise spikes, we learn that the touch signal is normally of a low spatial frequency compared to the surrounding noise spikes [15]-[35]. Therefore spatial low-pass spatial filters can be used for the reduction of noise spikes. As depicted in Fig. 1, the noise spike is averaged by adjacent pixel values. However, the touch signal is reduced by the low pass spatial filtering as well, called smoothing effect, which may decrease signal-to-noise ratio (SNR), depending on the bandwidth and the mask size of the spatial filter. The induced smoothing effect may also result in signal distortion in terms of touch presence and position, leading to detection errors, thus requiring further research.

In this paper, we investigate the induced smoothing effect on touch detection. In particular, we analyze the following aspects: SNR, signal distortion (in terms of the change of touch position and the attenuation) and noise spike attenuation. The optimization of the above parameters can be achieved by tuning the mask size and bandwidth of the spatial filter. Based on the analysis, we present an adaptive bandwidth tuning algorithm for the dynamic optimization of spatial filter when the signal and noise conditions are changed. In the algorithm, we evaluate the attenuation of the signal and noise spike, and select a suitable bandwidth to maintain the desired performance.

This paper is structured as follows. Section II covers a review of the main noise sources and low pass spatial filters for TSPs. A theoretical analysis of the relationship between the mask size and registered touch position is provided in Section III. Section IV summarizes the parameters of the test bed and describes the low pass spatial filtering based adaptive bandwidth tuning algorithm. Section V contains experimental/simulation results and discussions. Finally, conclusions are presented in section VI.

II. THE MAIN NOISE SOURCES IN CAPACITIVE TOUCH SCREEN PANELS AND SPATIAL LOW PASS FILTERS

Charger Noise

Ideally, the output from a charger would be constant. However, in reality, the noise produced gives rise to common-mode fluctuation of the output, as shown in Fig. 2. An outstanding charger design can restrict common-mode noise below 3V, whereas a poorly designed charger can sometimes introduce peak common-mode noise higher than 40V [36]. A TSP can work smoothly with common-mode noise when it is

Manuscript received xx x, 2015.

S. Gao, D. McLean, C. Micou and A. Nathan are with the Hetero-Genesys Laboratory, Department of Engineering, University of Cambridge, Cambridge CB3 0FA, U.K. (e-mail: sg690@cam.ac.uk; drm57@cam.ac.uk; cm704@cam.ac.uk; an299@cam.ac.uk).

J. Lai is with Research and Development Department, Ecmictics Biotech, Vaughan, ON, L4L 4V8, Canada (e-mail: jlai@ecmictics.ca).



Fig. 1. Noise in touch screen systems; the CDS based technique for common-mode noise and global multi-valued offset cancellation; noise spikes attenuation by a spatial LPF and the corresponding smoothing effect.



Fig. 2. The yellow signal (top) and the green signal (middle) are the trigger signals with common-mode noise introduced from the charger. The pink signal (bottom) is the trigger signal after common-mode noise reduction.

not being touched, as the difference of the charger output is maintained. There are two scenarios for touch events: the machine may be held by a hand, or placed on a table. In the former case, the machine may be sufficiently grounded to the earth thus no crucial common-mode noise charge escapes to the earth [36]. However, if the TSP is placed on a table, the touch panel and human body don't share the same ground, leading to potential touch mis-registrations.

Display Panel Noise

Mainstream displays in today's portable digital assistants (PDAs) are active-matrix organic light-emitting diode (AMOLED) [37], [38] and liquid crystal (LC) [39] architectures. The former one possesses excellent viewing quality and less noise for touch screen sensors. However it requires higher cost, hence, the popularity of LCDs is maintained. Fig. 3 depicts a typical stack-up of an LCD, showing that two capacitors are formed. The first is constituted of the sub-pixel electrode and the V_{COM} layer, and shields the display noise since the V_{COM} layer (e.g. made from ITO) has considerable resistance. The second one is constituted of the V_{COM} layer and the sensing electrode, and couples the LCD noise to touch sensors. The coupled noise can be measured by detecting the voltage in a copper strip stacked directly on the



Fig. 3. Typical stack-up of a LCD pixel, modified from [37].



Fig. 4. LCD noise from the Dell e198wfp monitor (copper strip size was $3mm \times 3mm$).

LCD screen. The LCD noise from a commercial screen (Dell e198wfp) was measured using an oscilloscope (Agilent DSO-X 2024A). The area of the copper strip was $3mm \times 3mm$, which is in the same vein as that of a sensing electrode. Fig. 4 shows that the LCD noise involves a strong deterministic component, which mainly depends on the design of the product and does not significantly change after the product is fabricated [36]. Several methods are widely used to attenuate display noise: shielding, use of a different sensing frequency, and analog/digital filters.





Neighbourhood Pixel



Fig. 5. Algorithm description of a spatially based average low-pass filter.

Low-pass Spatial Filters and Corresponding Effect on Noise Reduction

Spatial LPFs are divided into two categories: linear (e.g. average filter) and non-linear types (e.g. median filter) [40]-[44]. A mask/kernel with size $m \times n$ (m and n are normally odd positive integers to ensure that there is only one pixel in the center of the mask) is employed in the spatial filter, using the same or different coefficients to control the bandwidth. Each pixel in the original image is computed along with its neighboring pixels, and the outcome is produced in a new image at the same position.

Both linear and non-linear structures offer benefits and drawbacks to noise spikes reduction for different types of touch based interactivities (e.g. finger touch). For example, good noise spike attenuation is offered by non-linear LPFs, which unfortunately may remove a stylus touch completely and cannot effectively remove the noise on the electrode. Thus, linear filters (average and Gaussian filter) are employed and analyzed in this paper. An example of an average filter is described in Fig. 5, whose mask size is 3×3 and the coefficients equal to 1. Each pixel is added to the surrounding 8 pixels. The sum is then divided by the scaling constant 9 to generate a new pixel value.

In practice, this algorithm can be implemented by constructing an intermediate image in which each pixel contains the sum of 3 pixels in the x-direction. The final filtered image is obtained by performing the same process in the y-direction on the intermediate image, and then dividing by the scaling constant 9. This process is equally valid for Gaussian-weighted filters. This is especially useful for higher resolution devices, which would require larger masks than 3x3, as this implementation scales linearly with mask size rather than quadratically.

The presented algorithm in this paper focuses on smoothing noise spikes to boost SNR while maintaining a desired signal strength level in order to avoid detection errors.



rig. 6: Conceptual top-view of a finger toter of a finder tow-and-column capacitance TSP. A, B_1 to B_4 , C_1 , to C_4 and N_1 to N_{40} indicate the values over the electrodes intersections. The average spatial filter is applied to the touch position pixel A (left) and the adjacent pixel B_1 (right).

III. THEORETICAL ANALYSIS

Mask Size and Touch Position

To avoid the mis-registration at a wrong position and the interference from other touches, the mask size of the average filter is determined to be 3×3 . This is because the touch event is limited in a certain region (e.g. 3×3 electrodes for a finger touch). If a large the mask size is used, noise in the non-touch area weakens the strength of the touch signal, resulting in a high probability of touch mis-registration. Below we mathematically analyze the relationship between the mask sizes and touch position registration. Fig. 6 shows that a perpendicular finger touch occurring in the red region only affects the adjacent 8 pixels (yellow and green regions). A represents the value at the touch point, B₁ to B₄ and C₁ to C₄ are the adjacent region values, and N₁ to N₄₀ indicate the surrounding noise values. When a 3×3 average filter is applied, the output at the same position in the new image is:

$$A' = (A + B_1 + B_2 + B_3 + B_4 + C_1 + C_2 + C_3 + C_4) / 9;$$
(1)

If we assume that pixels at equal distance from the touch point have similar values, then Eq. 1 is simplified as:

$$A' = (A + 4B_1 + 4C_1) / 9;$$
⁽²⁾

 B_1 and C_1 can be represented by A with scaling down factors (b and c), expressed as:

$$B_{1} = A / b, C_{1} = A / c, (b > 1, c > 1);$$
(3)

thus A' is:

$$A' = (A + 4A/b + 4A/c)/9 = A(bc + 4b + 4c)/9bc;$$
(4)

If the registered touch position in the new image is shifted, this most likely happens within the yellow regions which are geographically closest to the red. The yellow region with value B_1 is analyzed to explain when the mis-registration in terms of position takes place. The filtered value B_1 is expressed as:

$$B_1' = (A + B_1 + B_2 + B_4 + C_1 + C_2 + N_{10} + N_{11} + N_{12}) / 9$$

$$\approx (A + 3B_1 + 2C_1 + 3N_{11}) / 9$$

$$= (A(bc + 3c + 2b) + 3bcN_{11}) / 9bc ; (5)$$

To ensure that $A' > B_1'$, the following condition must be satisfied:

$$\frac{A}{N_{11}} > \frac{3bc}{2b+c} \quad ; \tag{6}$$

Normally N_{11} is about 2 orders smaller than A; and b and c lie between 1 to 3. Hence, the condition expressed in Eq. 6 is satisfied. When the mask size increases to 5×5, the condition of $A'>B_1'$ is completely determined by the surrounding noise values, resulting in a high probability of mis-registration.

Spatial Frequency Properties of Signal and Noise Spikes

Touch signal is normally low spatial frequency compared to the noise spikes. This is the fundamental assumption of low pass spatial filtering technique. However, in some cases the touch signal can offer high spatial frequency as well. For example, only one electrode may be affected when a stylus touch is applied. In contrast, when a noise spike happens within the touch region, it may have low spatial frequency property. The low pass spatial filtering cannot remove noise spikes and may result in the decrement of SNR when the spatial frequencies of touch signal and noise spikes are very close or overlapped. A possible solution of this is the time domain low pass filtering at pixel level.

In this paper, finger touch is used and analyzed, as this is the most preferred touch activity for PDAs. Finger touch is of low spatial frequency, and the bandwidth relies on the contact area, which is highly individual dependent. Even for the same user, the touch property can be changed when different applications are used. Hence, the filter bandwidth should be dynamically adjusted to keep the desired touch information while maximally reducing noise spikes.

IV. EXPERIMENTAL TEST BED AND ALGORITHM DESCRIPTION

The experiments were carried out on an 80×80 touch screen panel, and the details are provided in Table I.

To reduce the noise spikes in the frame after CDS [11], a spatial LPF with an initial bandwidth is used. Since the touch and noise properties are dynamically changed, the bandwidth of the spatial LPF is required to be adjusted in order to optimize the performance. As described in Fig. 7, the frame after CDS (denoted as f_{CDS}) is send to the spatial LPF for the noise spikes reduction. Then the filtered frame (denoted as f_{LPF}) enters into the touch decision function to determine if a touch happens or not. If a touch is registered, then the frame (denoted as f_{Touch}) is evaluated in terms of the signal and noise spike attenuation by the bandwidth decision function, to analyze if the bandwidth of the spatial LPF needs to be changed. If no touch is registered, the scanned frame (f_{scan}) is updated as a new reference noise pattern, which will be used by CDS algorithm, as shown in Fig. 7.

V. RESULTS AND DISCUSSION

 TABLE I

 PARAMETERS OF TEST BED

 Parameter
 Unit
 Value

 Diagonal
 Inch
 10.1

 Aspect Ratio
 None
 16/9

 Excitation Voltage
 Volt
 10

Display Pixel Size	(Micrometer) ²	56×56	
TX Electrode Size	Millimeter	3	
RX Electrode Size	Micrometer	449	
Refreshing rate	Hertz	60	
Sensing Array Size	(Millimeter) ²	3×3	

Millimeter

*Note:	TΥ	and	RΧ	represent	transmitter	and	eceiver 1	espectively
- INCHE	1 / \	ann	Λ	LEDLESEIII	HAUSHUEL		ELEIVEI I	ESDEL HVELV

Sensing Array Spacing



Fig. 7. Flowchart of the CDS and spatial LPF based noise reduction algorithm.

To evaluate the smoothing effect, three factors are considered here: SNR, signal and noise attenuation. The normalized output (no touch event) PSD plots (Fig. 8) are used to analyze the noise behavior after the average LPF. It can be observed that the high frequency noise generated by the CDS is suppressed. The SNR is boosted by 15.6dB. The signal and noise spike are attenuated by 4.51dB and 19.25dB respectively. The attenuation of the signal strength is undesirable, thus different coefficients are used for the bandwidth adjustment to retain more signal information. The coefficients of the filter mask follow Gaussian distribution with different standard deviations (σ) , representing various bandwidths of the LPF. Four Gaussian distribution based filter masks are illustrated in Fig. 9. Here small standard deviation indicates that more information about the pixel itself is maintained. In contrast, large standard deviation implies that the pixel is strongly affected by the adjacent neighborhoods. The simulation results of applying spatial filter with different bandwidths are illustrated in Fig. 10. Due to the spatial high frequency property, noise spike suffers severer attenuation compared to the touch signal. From Fig. 10



Fig. 8. Normalized PSD plots of the original output, CDS output with sampling frequency at 60Hz and spatial average LPF output with mask size of 3×3 .



Fig. 9. Four Gaussian distribution based masks with different bandwidths (represented by standard deviations). (a) $\sigma = 0.5$; (b) $\sigma = 1$; (c) $\sigma = 2$; (d) $\sigma = 4$.

(a), the signal and noise spike are attenuated dramatically when σ is within the range of 0.5 to 1. After $\sigma > 1.5$, the trends of attenuation are reaching saturation. Thus the range of $0.5 < \sigma < 1$ is further investigated, and the results are illustrated in Fig. 10 (b). By evaluating the signal and noise spike attenuation dynamically, the filter bandwidth is updated. For example, if the accepted signal attenuation is 2dB, then the initial Gaussian distribution based mask with standard deviation at 0.6 would be used to maximally attenuate the noise spike. Later, when the signal attenuation is found to be 1dB, then a bigger standard deviation can be selected to further smooth noise spikes and boost SNR.

It is also important to investigate the computational time and energy consumption of the presented algorithm. Our system's scanning rate is 60Hz, thus the computational time of the algorithm should be much lower than 16.7ms. The required computational time depends on the complexity of the algorithm and the performance of the processor. The complexity of the spatially based algorithms used in this research is around O(3N), where N is equal to 6400. Current commercial processors in mobile phones can operate in the range of GHz. Hence, a computational time of approximately 19.2µs is



Fig. 10. Results of signal and noise spike attenuation by applying spatial LPFs with different standard deviations. (b) is the dashed part of (a).

required when a 1GHz processor is equipped. Commercial processors can work at 20MIPS/mW [45], therefore the power consumption of the algorithm is 21.6nW which is negligible compared to that of scanning the whole panel.

VI. CONCLUSION

The noise spikes in TSPs give rise to "fake" touch, resulting in the high power consumption. In this paper, we present a low pass spatial filtering based technique for the noise spike reduction. By the approach of evaluating the spatial LPF induced smoothing effect, the filter bandwidth is dynamically adjusted to optimize the performance. Based on the experiment, a SNR enhancement of 15.6dB and a noise spike attenuation of 19.25dB are obtained. The filtered signal improves detection accuracy, thus less energy is required to maintain a desired performance.

REFERENCES

 L. Zhang, J. Saboune and A. E. Saddik, "Transforming a regular screen into a touch screen using a single webcam", *IEEE J. Display Technol.*, vol.10, no. 8, Aug. 2014.

- [2] A. Nathan, J. C. S. Lai, S. Cha, and C. Church, "Pressure sensing display device", U.S. Patent US20140008203 A1, Jan. 2014.
- [3] K. Kim, S. Byun, Y. Choi, J. Baek, H. Cho, J. Park, H. Ahn, C. Lee, M. Cho, J. Lee, S. Kin, H. Kwon, Y. Choi, H. Na, J. Park, Y. Shin, K. Jang, G. Hwang and M. Lee, "A capacitive touch controller robust to display noise for ultrathin touch screen displays" *IEEE ISSCC, Medical, Displays and Imagers*, pp. 116 117, Feb. 2012.
- [4] J. E. Park, D. H. Lin, and D. K. Jeong, "A reconfigurable 40-to-67 dB, 50-to-6400 Hz frame-rate, column-parallel readout IC for capacitive touch-screen panels", *IEEE J. Solid-State Circuits*, vol. 49, no. 10, Oct 2014.
- [5] S. Ko, H. Shin, H. Jang, L. Yun, and K. Lee, "A 70 dB SNR capacitive touch screen panel readout IC using capacitor-less trans-impedance amplifier and coded Orthogonal Frequency-Division Multiple Sensing scheme", *IEEE Symposium on VLSI*, pp. C216-C217, 2013.
- [6] S. Ko, H. Shin, J. Lee, H. Jang, B. C. So, I. Yun, and K. Lee, "Low noise capacitive sensor for multi-touch mobile handset's applications", *IEEE ASSCC*, pp. 1-4, Nov. 2010.
- [7] H. Jang, H. Shin, S. Ko, I. Yun, and K. Lee, "2D coded-aperture-based ultra-compact capacitive touch-screen controller with 40 reconfigurable channels", *IEEE ISSCC Dig. Tech. Papers*, pp.218-219 Feb. 2014.
- [8] J. Yang, S. Park, J. Choi, H. Kim, C. B. Park, S. Ryu and G. Cho, "A highly noise-immune touch controller using Filtered –Delta-Integration and a charge-interpolation technique for 10.1-inch capacitive touch-screen panels", IEEE ISSCC Dig. Tech. Papers, pp. 390-391, Feb. 2013.
- [9] N. Miura, S. Dosho, S. Takay, D. Fujimoto, T. Kiriyama, H. Tezuka, T. Miki, H. Yanagawa, and M. Nagata, "A 1mm-pitch 80×80-channel 322Hz-frame-rate touch sensor with two-step dual-mode capacitance scan", ISSCC Sensors, MEMs, and Displays, pp. 216-217, 2014.
- [10] H. Shin, S. Ko, H. Jang, I. Yun, and K. Lee, "A 55dB SNR with 240Hz frame scan rate mutual capacitor 30×24 touch-screen panel read-out IC using code-division multiple sensing technique", IEEE ISSCC Dig. Tech. Papers, pp. 388-389, 2013.
- [11] S. Gao, J. Lai, C. Micou and A. Nathan, "Reduction of Common-mode Noise and Global Offsets in Touch Screens by Correlated Double Sampling", *IEEE J. Display Technol.* DOI 10.1109/JDT.2016.2515847.
- [12] M. H. White, and D. R. Lampe, "Characterization of surface channel CCD image arrays at low light levels", in *IEEE J. Solid-State Circuits*, vol. sc-9, pp. 1-13, Feb. 1974.
- [13] C. Enz, G. Temes, "Circuit Techniques for reducing the effects of op-amp imperfections: autozeroing, correlated double sampling, and chopper stabilization", in *Proc. IEEE*, vol. 84, no. 11, Nov. 1996.
- [14] J. Ohta, "Smart CMOS image sensors and applications", CRC Press, 2007.
- [15] W. C. Westerman, S. Hotelling, "Perpheral pixel noise reduction", U.S. Patent US7643010 B2, Jan 2010.
- [16] S. Hotelling, J. Strickon, B. Huppi, I. Chaudhri, G. Christie, B. Ording, D. Kerr and H. Ive, "Gestures for touch sensitive input devices", U.S. Patent US20060026521 A1, Feb. 2006.
- [17] B. R. Land and S. P. Hotelling, "Error compensation for multi-touch surfaces", U.S. Patent US7692638 B2, Apr. 2010.
- [18] Z. Baharav and R. Kakarala, "Capacitive touch sensing: Signal and image algorithms", Proc. SPIE Conf. on Comput. Imaging, Feb. 2011.
- [19] H. Akhtar and R. Kakarala, "A methodology for evaluating accuracy of capacitive touch sensing grid patterns", *IEEE J. Display Technol.*, vol.10, no. 8, Aug. 2014.
- [20] T. Wang, Y. Ko, C. Lai, "Gesture detecting method capable of filtering panel mistouch", U.S. Patent US20130093692, Apr. 2013.
- [21] S. Hagermoser, "Harnessing the power of projected capacitive technology using unfiltered touchscreen data", 3M Touch Systems, 2014.
- [22] J. Jordan, "Sensing and defining an input object", U.S. Patent US20110057670 A1, Mar. 2011.
- [23] I. Guarneri, A. Capra, G. M. Farinella and S. Battiato, "Shape recognition for capacitive touch display", SPIE Electronic Imaging, Mar. 2013.
- [24] W. C. Westerman, "Image jaggedness filter for determining whether to perform baseline calculations", U.S. Patent, US20090174688 A1, Jul. 2009.
- [25] W. C. Westerman, "Multi-touch input discrimination", U.S. Patent, US20080158185 A1.
- [26] Guarneri, I., Capra, C., Castorina, A., Battiato, S., Farinella, M.,G., "PCA based shape recognition for capacitive touch display", *IEEE ICCE*, pp. 1-2, 2013.
- [27] J. Yang, S. Park, J. Choi, H. Kim, C. B. Park, S. Ryu and G. Cho, "A noise-immune high-speed readout circuit for in-cell touch screen panels",

IEEE Trans. Circuits Syst. I, Reg. Papers, vol. 60, no. 7, pp. 1800-1809, Jul. 2013.

- [28] S. P. Hotelling, D. T. Amm, M. Lammers, J. Orchard, B. M. King, O. S. Leung, D. Teoman, "Ground detection for touch sensitive device", U.S. Patent WO2011008610 A1, Jan. 2011.
- [29] H. S. Lee, T. J. Park and K. J. Kim, "Touch sensing method and touch sensing device", U.S. Patent US20140139478 A1, May 2014.
- [30] J. Zhang, Y. Guo and L. Mo, "Multi-touch detection method for capacitive touch screens", U.S. Patent, US20110221701 A1, Sep. 2011.
- [31] B. R. Land, M. Yousefpor and S. P. Hotelling, "Negative pixel compensation", U.S. Patent, US8570301 B2, Oct. 2013.
- [32] J. Liu, L. Liu, C. Chang and S. Tai, "Touch device and driving method of touch panel thereof", U.S. Patent US20140145965 A1, May 2014.
- [33] H. Philipp, "Capacitive sensor and array", U.S. Patent US6452514 B1, Sep. 2002.
- [34] T. Hwang, W. Cui, I. Yang and O. Kwon, "A highly area-efficient controller for capacitive touch screen panel systems", *IEEE Trans. Consum. Electron.*, vol. 56, issue 2, pp. 1115-1122, May 2010.
- [35] J. H. Yang, J. M. Choi, Y. S. Kim and H. S. Oh, "Control circuit of touch screen and noise removing method", U.S. Patent US20130300690 A1, Nov. 2013.
- [36] H. W. Klein, "Noise immunity of touchscreen devices", White paper, Cypress Semiconductor Corp., Feb. 2013.
- [37] C. M. Lai, Y. H. Yeh and Y. H. Huang, "Pixel structure of active matrix organic light-emitting diode and method for fabricating the same", U.S. Patent US20070152217 A1, Jun. 2007.
- [38] E. Anderson, "Reduce display noise in capacitive touchscreens", White paper, Cypress Semiconductor Corp., Jan 2013.
- [39] S. Kokawa, M. Nishihara, and Y. Sato, "Liquid crystal display", U.S. Patent US5467208 A, Nov. 14, 1995.
- [40] A. McAndrew, "An introduction to digital image processing with MATLAB", Lecture note for SCM2511 Image Processing, Victoria University of Technology, Sep. 2004.
- [41] J. R. Jensen and K. Lulla, "Introductory digital image processing: A remote sensing perspective", Geocarto International, vol. 2, issue. 1, 1987.
- [42] J. C. Russ, "The image processing handbook", J. Comput. Assist. Tomogr., vol. 19, issue 6, pp. 979-981, 1995.
- [43] J. S. Lim, "Two-dimensional signal and image processing", Englewood Cliffs, Prentice Hall, 1990.
- [44] J. S. Lee, "Digital image enhancement and noise filtering by use of local statistics", *IEEE Trans. Pattern Anal. Mach. Intell.*, vol. PAMI-2, issue 2, pp. 165-168, 1980.
- [45] ARM, "Cortex-a5 processor performance," [Online]. Available: http://www.arm.com/products/processors/cortex-a/cortex-a5.php



Shuo Gao received the M.Sc. in electrical engineering from the University of Ottawa, Canada, 2013. He is currently a Ph.D. student in the Hetero-Genesys Laboratory, University of Cambridge. Previously he worked as an optical fiber system engineer at Ciena Corporation, Ottawa, Canada. His research interests include touch interactivity and RF system for flexible electronics.



David McLean received the M.Eng. degree in Engineering from the University of Cambridge, U.K, in 2014 specialising in Information and Data Processing. His areas of interest include Image Processing, Computer Vision, Machine Learning, and Speech and Language Processing. He was a technology Scholar at Cambridge Consultants Ltd and is a member of the IET (Institution of Engineering and Technology).



Jackson Lai received the Ph.D. in electrical engineering from the University of Waterloo, Canada, in 2007. He is currently a Senior Display Technology Developer at Blackberry. Previously, he has held positions such as Device and Circuit Engineer in Carestream Health, as well as Lead Display Circuit Designer in Ignis Innovation Inc. His research interests include imaging circuits, large area electronics, as well as system on panel design.



Charles Micou is an Engineering student at the University of Cambridge, U.K., in the final year of his M. Eng degree. After two years of general engineering, he specialised into Electrical and Information Sciences. His areas of interest include Computer Vision, Machine Learning, Image Processing, and Embedded Systems. He has worked as a software engineer on projects ranging from embedded telecommunications devices at Eseye, to cloud-based distributed simulations at Improbable.



Arokia Nathan (S'84–M'87–SM'99–F'10) received the Ph.D. degree in electrical engineering from the University of Alberta. Following post-doctoral years at LSI Logic Corp., USA, and ETH Zurich, Switzerland, he joined the University of Waterloo, Canada, where he held the DALSA/NSERC Industrial Research Chair in sensor technology and subsequently the Canada Research Chair in nano-scale flexible circuits. He was a recipient of the 2001 NSERC E.W.R. Steacie Fellowship. In 2006, he moved to the U.K.

to take up the Sumitomo Chair of Nanotechnology at the London Centre for Nanotechnology, University College London, where he received the Royal Society Wolfson Research Merit Award. He has held Visiting Professor appointments at the Physical Electronics Laboratory, ETH Zürich and the Engineering Department, Cambridge University, U.K. He holds the Chair of Photonic Systems and Displays in the Department of Engineering, Cambridge University. He has published over 500 papers in the field of sensor technology, CAD, thin film transistor electronics, and is a co-author of four books. He has over 50 patents filed/awarded and has founded/co-founded four spin-off companies. He serves on technical committees and editorial boards in various capacities. Dr. Nathan is a Chartered Engineer (U.K.), Fellow of the Institution of Engineering and Technology (UK), Fellow of IEEE (USA), and an IEEE/EDS Distinguished Lecture.