# A RANKING METHOD FOR RATING THE PERFORMANCES OF PERMUTATION CODES 

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#### Abstract

Minimum Hamming distance, $d_{m}$, has been widely used as the yardstick for the performance of permutation codes (PCs). However, a number of PCs with the same $d_{m}$ and cardinality can have different performances, even if they have the same distance optimality. Since PC is a robust channel coding scheme in power line communications applications, we present a simple and fast ranking method that predicts the relative performance of PCs, by using the information extracted from their Hamming distance distributions. This tool is useful for selecting an efficient PC codebook out of a number of similar ones.


Key words: Channel coding, Hamming distance distribution, Permutation coding, Power line communications, Ranking method.

## 1 INTRODUCTION

Power line communications (PLC), which involves information transmission through the existing power network, is now a popular technology that may be used for home internet access, data distribution, networking, smart metering and electric vehicle-to-charging stations. The major setback of this technology is the performance degradation posed by the various noise types present in the communication channel (i.e. power line). These noise types include background noise, impulsive noise and narrowband interference [1,2]. These noise types are inherent in the channel, because the main purpose of the power line network is not for communication applications. In order to make the channel conducive for communications, it may be helpful to use higher transmission power or distortion-free frequencies in the communication system. There are, however PLC communication standards that enforce constraints on the range of power and frequencies that are useable [3-5]. As such, in order to achieve effective PLC systems, a robust channel coding scheme is a crucial component to consider.

The channel coding scheme of interest in this work is permutation coding (PC). This is motivated based on the foundational work on PCs reported by Vinck, who was the first to suggest its usage for PLC applications [1]. According to Vinck, PCs in combination with MFSK are capable of handling the types of noise associated with narrowband PLC channels. This then gave inspiration for more research works on PCs, some of which are reported in [6-16].

There have been some approaches to the generation of PC codebooks with different minimum Hamming distances and cardinalities. The term $d_{m}$ refers to the minimum Hamming distance between any two distinct codewords in a codebook, $\mathcal{C}$, while $|\mathcal{C}|$ is the cardinality, which is the total number of possible codewords in the codebook. However, how one selects the best codebook out of
a number of competitive PC codebooks with similar characteristics, is a challenging problem that we have attempted to solve in this study. The quality of a communication system can be greatly impaired if a poor code design is used.

In coding, $d_{m}$ is usually used to determine the strength of a code in handling errors. According to [2], a PC is able to detect $t$ symbol errors provided that:

$$
\begin{equation*}
t \leq\left(d_{m}-1\right) . \tag{1}
\end{equation*}
$$

Moreover, in terms of error correcting capability, a PC is able to correct $t / 2$ number of errors. However, apart from the $d_{m}$ in a given codebook, other distances greater than $d_{m}$ also feature, when the distance relationships between every distinct codeword are computed. All of these distances have a cummulative contribution in the performance of any given codebook. This notion was used by Viterbi in [19], when he demonstrated that various possible distances, resulting from every remerging path in the trellis-code representation of a convolutional code, contribute to its error probability. A similar approach was used in [20-22] to determine the distance spectrum used for comparing the performances of convolutional codes with similar constraint lengths and decoding complexities. We thus employ a similar approach in the ranking method presented in this study. However, instead of using it in the context of convolutional codes, it is used in the context of non-binary PC schemes. Also, Hamming distance distribution is what we have used in this study, instead of the distance spectrum used in convolutional code, which is a binary code. As such, this study is centered on determining the cumulative effects of all the possible distances, including $d_{m}$, on the error detecting capability of any given PC.

Other work done in repect of PCs’ performance tools include the one reported in [23], where a probabilistic approach was used to determine PC decoders' performances. However, this is not related to the work presented here, in
that our approach is not decoder-dependent, as opposed to the work in [23], which is based on the type of decoding algorithm used in the design.

In addition, instead of using cardinality and Hamming weight distribution, Hamming distance distribution (HDD), which takes into consideration the contribution of every possible distance, is employed in our work. The reason for this is discussed later in this paper. To the best of our knowledge, this is the first time a method such as this is employed for PCs.

It is however worth noting that the ranking method presented here is only evaluated based on combined background noise (modeled as AWGN) and impulsive noise channel conditions. As such, a representation of AWGN+IN channel model is involved in this work. In order to study the performance of data transmission over a PLC channel, the authors in [24] used Middleton's model to model the transmission channel using a binary symmetric channel, whose transmission error probability $\varepsilon$ is dependent on the impulsive noise variance. In our proposed ranking method, we represent the channel model as a composite channel, comprising of the modulator, demodulator and the PLC channel. This composite setup is appropriate, in order to be able to analyse channel codings [25]. The $\varepsilon$ of this channel setup is derived from Middleton's noise model for impulsive noise.

Our contribution in this work therefore further promotes the use of PCs in PLC-related applications, by providing a good ranking tool that can be used to rate the performances of PC schemes of similar characteristics, without the need to perform performance simulations. Having to perform performance simulations for a multitude of codebooks, where each simulation could take a considerable time to complete, would mean that the process of choosing the best codebook will be extremely time consuming. In practical terms, this tool can be handy, when the best codebook is to be chosen out of a number of competitive codebooks. In [17] and [18], various PC codebooks of the same $|\mathcal{C}|$ and $d_{m}$ were presented which had the same distance optimality, but their performances are seen to be slightly different when simulated. The method proposed here is therefore able to determine the best PC codebook in such situations. Hence, this work can be viewed as an extension of the work reported in [17] and [18].

We briefly describe what PC entails in Section 2. Section 3 gives a brief background to the HDD of a codebook, which is then used to propose the PC ranking method in Section 4. In Section 5, we present the HDDs of some known PCs and use the proposed method to analyse and predict their performances. Some simulation results are presented in Section 6 to validate the analysis performed in Section 5, after which a concluding note is given in Section 7.

## 2 INTRODUCTION TO PERMUTATION CODING

As stated in [9], a PC mapping maps binary data sequences to codewords of non-binary permutation sequences. A PC codebook is usually denoted as $\mathcal{C}(n, M, \eta)$, where $n$ is the
number of bits being mapped onto $M$ symbols, while $\eta$ determines the mapping type. Each codeword is of length $M$, and its symbols are chosen from $\{0,1, \ldots, M-1\}$. An example of a PC, with codeword length $M=4$, where $n=2$ bits are mapped to the PC symbols [9], is:

$$
\begin{equation*}
\{00,01,10,11\} \rightarrow\{2130,2103,3120,0213\} \tag{2}
\end{equation*}
$$

Another example with $M=4$ and $n=4$ is [18]:

$$
\left\{\begin{array}{l}
0000,0001,0010,0011,  \tag{3}\\
0100,0101,0110,0111, \\
1000,1001,1010,1011, \\
1100,1101,1110,1111
\end{array}\right\} \rightarrow\left\{\begin{array}{l}
1230,1203,1320,1302, \\
2130,2103,3120,3102, \\
0231,0213,0321,0312, \\
2031,2013,3021,3012
\end{array}\right\} .
$$

If the distances between the binary and permutation sequences are compared, three types of PC mappings can be defined, namely distance-conserving mappings (DCMs), distance-increasing mappings (DIMs) and distance-reducing mappings (DRMs). For instance, the binary sequences in (2) have $d_{m}=1$, while their corresponding permutation sequences have $d_{m}=2$. With this, $\eta=1$. Hence, such a PC mapping is a DIM. By definition, a DIM ensures that the distances of the permutation sequences have some increase above the those of the binary sequences; a DCM ensures that the distances of the binary sequences are at least maintained in their corresponding permutation sequences; and a DRM ensures a controlled distance loss between distances. These three mapping types are collectively referred to as distance-preserving mappings [17,26].

A number of PC mapping and decoding algorithms are available in the literature. However, since we are only focusing on analysing PCs' relative performance, the simulations done in this work have considered the simple PC mapping and decoding algorithm presented in [9]. Therein, information bits are grouped into $n$ bits, and these grouped bits are in turn mapped to corresponding codewords of length $M$, based on the grouped bit sequences. On the decoding side, a codeword having the closest Hamming distance to the received codeword is selected as the decoded codeword.

## 3 HAMMING DISTANCE DISTRIBUTION

We define HDD as the amount of contribution each possible Hamming distance has in the performance of a given PC codebook. Let Hamming distance $k$ be represented by $d_{k}$. Given a codebook $\mathcal{C}$, with $M$ and $d_{m}$, all the possible distances $d_{k}$ in $\mathcal{C}$ are given by:

$$
\begin{equation*}
k \in\{0,1, \ldots, M\} . \tag{4}
\end{equation*}
$$

If the minimum distance is $d_{m}$, (4) therefore reduces to

$$
\begin{equation*}
k \in\left\{d_{m}, d_{m}+1, \ldots, d_{M}\right\} \tag{5}
\end{equation*}
$$

If the distance relationship between every distinct
codeword in $\mathcal{C}$ is computed, a $|\mathcal{C}| \times|\mathcal{C}|$ dimensional distance matrix $\mathbf{E}$ is generated. This matrix $\mathbf{E}$ consists of elements $e_{i, j}$ that represent the distance between every pair of distinct codewords $x_{i}$ and $x_{j}$ in the codebook, where $i, j=1,2, \ldots,|\mathcal{C}|$ and is defined as:

$$
\mathbf{E}=\begin{align*}
&  \tag{6}\\
& \begin{array}{l}
x_{1} \\
x_{2} \\
\cdot \\
\cdot \\
\cdot \\
x_{|\mathcal{C}|}
\end{array}\left(\begin{array}{cccccc}
x_{1} & x_{2} & \cdot & \cdot & \cdot & x_{|\mathcal{C}|} \\
e_{1,1} & e_{1,2} & \cdot & \cdot & \cdot & e_{1,|\mathcal{C}|} \\
e_{2,1} & e_{2,2} & \cdot & \cdot & \cdot & e_{2,|\mathcal{C}|} \\
\cdot & \cdot & \cdot & & \cdot \\
\cdot & \cdot & & \cdot & & \cdot \\
\cdot & \cdot & & & \cdot & \cdot \\
e_{|\mathcal{C}|, 1} & e_{|\mathcal{C}|, 2} & \cdot & \cdot & \cdot & e_{|\mathcal{C}|,|\mathcal{C}|}
\end{array}\right)
\end{align*}
$$

where $e_{i, j}=0$, for $i=j$.
The total number of occurrences of each $d_{k}$ can then be determined from the elements in matrix E. For instance, the distance matrix for the codebook in (2) is given by:

$$
\left.\mathbf{E}=\begin{array}{c} 
 \tag{7}\\
2130 \\
2103 \\
3120 \\
0213
\end{array} \begin{array}{cccc}
2130 & 2103 & 3120 & 0213 \\
0 & 2 & 2 & 4 \\
2 & 0 & 3 & 3 \\
2 & 3 & 0 & 4 \\
4 & 3 & 4 & 0
\end{array}\right)
$$

Let $\delta_{k}$ denote the number of times that $d_{k}$ appears in the distance matrix. We can then express the HDD, $\Delta$, of all the possible $d_{k}$ as

$$
\begin{align*}
& \Delta=\left\{\delta_{1}, \ldots, \delta_{m-1}, \delta_{m}, \delta_{m+1}, \ldots, \delta_{M-1}, \delta_{M}\right\}  \tag{8}\\
& \text { with } \delta_{1}, \ldots, \delta_{m-1}=0
\end{align*}
$$

Using a probabilistic approach, the contribution of each $d_{k}$, given its $\delta_{k}$, is given by:

$$
\begin{equation*}
P_{k}=\frac{\delta_{k}}{\sum_{k=m}^{M} \delta_{k}}, m \leq k \leq M \tag{9}
\end{equation*}
$$

To illustrate this, the HDD and distance probabilities of the example codebook given in (3) are respectively given by:

$$
\begin{equation*}
\Delta=\{0,64,64,112\} \tag{10}
\end{equation*}
$$

and

$$
\begin{equation*}
P_{2}=P_{3}=0.2667 \text { and } P_{4}=0.4667 \tag{11}
\end{equation*}
$$

According to (11), it is clear that the codebook's performance mainly depends on $d_{4}$, since it has the greatest probability. The significance of the $\delta_{k}$ values (i.e., the HDD) in determining the strength of a PC can be understood by considering Viterbi's findings in [19]. In the trellis diagram of a convolutional code, any arbitrary path leading to a given node is associated with a distance,
which is computed by determining the bitwise differences between the code sequences along such a path and the expected code sequence. In order to detect the optimum path, all the distance information, which are dependent on the channel transition probability, need to be computed [19].

Similar to convolutional codes where numerous different codes, with varying performance, can be obtained for the same parameters, various mappings from binary sequences to permutation sequences can be obtained, all having the same $d_{m}$ and satisfying the distance preserving property. Thus a method was needed to determine which of these mappings would perform the best. The work in [17] and [18] addressed this by introducing the concept of distance optimality derived from distance matrix $\mathbf{E}$ like the one defined in (6). By denoting the sum of all distances in matrix $\mathbf{E}$ by $|\mathbf{E}|$, a PC is considered distance optimal if $|\mathbf{E}|$ is maximised. It was shown that an upper bound on $|\mathbf{E}|$ exists, and that any PC codebook attaining the bound would perform better than those that do not.

This clearly showed that all the distance contributions affect the performance of a given PC codebook. However, this approach is unable to distinguish between some codebooks with the same $|\mathcal{C}|, d_{m}$ and maximised $|\mathbf{E}|$. Hence, we investigate the proposed PC ranking method that goes further in analysing the HDD, to enable us to distinguish between such codebooks.

In HDD, the diagonal elements of $\mathbf{E}$ is not considered, because this would amount to comparing a codeword with itself. Hamming weight distribution on the other hand, is the number of codewords with Hamming distance $k$ from an all 0's reference codeword. This however holds for binary codes. For non-binary codes such as PCs, a reference origin codeword, which may or may not be in $\mathcal{C}$, is compared with each codeword, in order to determine the weight distribution [23]. One could state that HDD and weight distribution are the same, if the comparison is done with reference to an origin codeword. However, this is only valid for a linear code. Since PC is a non-linear code, to get accurate results every distinct codeword must be compared with all other codewords.

## 4 RANKING METHOD

In the context of this work, the term ranking refers to the order of performances of a given set of codewords, when being compared. The ranking method proposed in this study entails using the HDD discussed above to determine the probability of undetected symbol errors of a given codebook $C$. Since the PCs considered in this study are non-binary codes, this necessitates the use of non-binary modulation, such as phase shift keying (PSK) modulation. In order to evaluate the error probability of a PC system using such modulations, we employ the composite channel setup in Fig. 1 as the transmission channel.

The PLC channel block is a combiation of impulsive noise and additive white Gaussian noise (AWGN). Since the


Figure 1: Channel model for ranking method.
modulator employs non-binary waveforms and the detector makes hard decisions, we can say that the composite channel has a discrete-time non-binary input sequence $X=\left\{0,1, \ldots, M_{\mathrm{DP}}-1\right\}$, where $M_{\mathrm{DP}}$ is a modulation order whose value is a power of 2 , and a discrete-time non-binary output sequence. Each input symbol has a probability $1 / M_{\mathrm{DP}}$ of being received at the output. The probability of receiving an incorrect symbol is $\xi=\varepsilon /\left(M_{\mathrm{DP}}-1\right)$, while $(1-\varepsilon)$ is the probability of correctly receiving a symbol at the detector's output, where $\varepsilon$ is the transmission error probability.

If $\varphi$ and $\alpha$ respectively denote a modulated and a demodulated symbol from the system, where $\varphi \in$ $(0,1, \ldots, M-1)$ and $\alpha \in\left(0,1, \ldots, M_{\mathrm{DP}}-1\right)$, the following expression therefore holds, based on the above composite channel setup:

$$
P(Y=\alpha \mid X=\varphi)= \begin{cases}1-\varepsilon, & \text { if } \varphi=\alpha  \tag{12}\\ \xi, & \text { if } \varphi \neq \alpha\end{cases}
$$

Also, as stated in (1), a PC has a symbol error detecting capability of $\left(d_{m}-1\right)$, which can be generalised for each $d_{k}$ as

$$
\begin{equation*}
t_{k}=\left(d_{k}-1\right)=(k-1) \tag{13}
\end{equation*}
$$

Since each $d_{k}$ has its number of appearances $\delta_{k}$, the probability of a PC detecting $t_{k}$ number of errors is dependent on the value of $\delta_{k}$. The contribution of $d_{k}$ in the probabilty $P_{u}$ of undetected errors is based on the cummulative error probabilities of other distances $>d_{k}$ to yield a correct symbol and on the probabilities of $d_{k}$ and those of other distances $<d_{k}$ to yield an incorrect symbol (as defined in (12)). This is mathematically expressed as:

$$
\begin{align*}
& P_{d_{d_{k}}}=(\xi)_{\left.\right|_{d_{M}}} \times\left.(\xi)\right|_{d_{M-1}} \times \cdots \times(\xi)_{\left.\right|_{d_{k+1}}} \\
& \times \delta_{\left.\right|_{d_{k}}}(1-\varepsilon)_{\left.\right|_{d_{k-1}}} \times \cdots \times(1-\varepsilon)_{\left.\right|_{d_{m}}} \tag{14}
\end{align*}
$$

where (. $)_{\left.\right|_{d_{k}}}$ is the computation of (.) due to distance $d_{k}$.
By computing all the $P_{\left.\right|_{d_{k}}}$ values for every possible $d_{k}$ defined in (5), the expression in (14) reduces to a generalised expression for the probability of undetected errors $P_{u}$ in a PC system as:

$$
\begin{equation*}
P_{u}=\sum_{k=m}^{M} \delta_{k} \xi^{k}(1-\varepsilon)^{M-k} \tag{15}
\end{equation*}
$$

The expression in (15) can work for every type of channel, provided that $\varepsilon$, associated with such a channel, can be computed. The computation of $\varepsilon$ is what gives the characteristics of the communication channel. If $\varepsilon$ for a PLC channel can be obtained, it can be substituted into (15), thereby providing a means of analysing the performance of any PC codebook $\mathcal{C}(n, M, \eta)$ over a PLC channel, provided its HDD is known. This shall therefore lead us to the computation of $\varepsilon$ for a PLC channel impaired with impulsive noise, which is one of the most notorious noise types in PLC.

Impulsive noise has a broadband power spectral density (PSD), which sometimes affects frequency components of the transmitted data for a particular length of time. According to the approximated form of Middleton's class A noise model, it has a strength $\Gamma$, which is the relationship between its variance $\sigma_{I}^{2}$ and that of additive white Gausian noise (AWGN) $\sigma_{g}^{2}$, given by $\Gamma=\sigma_{g}^{2} / \sigma_{I}^{2}[6,27]$. The smaller $\Gamma$ is, the more severe the impulsive noise effect becomes. If $A$ is the probability of impulsive noise affecting a symbol, the general form of error probability for an MPSK system is modified to account for the combination of AWGN and impulsive noise effects as follows:

$$
\begin{align*}
\varepsilon_{\mathrm{MPSK}} & =\frac{(1-A)}{\beta} Q\left(\sqrt{\frac{\beta E_{b}}{\sigma_{g}^{2}}} \sin \left(\pi / M_{\mathrm{DP}}\right)\right) \\
& +\frac{A}{\beta} Q\left(\sqrt{\frac{\beta E_{b}}{\sigma_{g}^{2}+\sigma_{g}^{2} /(A \Gamma)}} \sin \left(\pi / M_{\mathrm{DP}}\right)\right), \tag{16}
\end{align*}
$$

where $\beta=\gamma\left(\log _{2}(M)\right)$ and $Q($.$) is a Q$ function.
For a PLC system impaired with impulsive and background noise, $\varepsilon$ in (15) is substituted with $\varepsilon_{\text {MPSK }}$. The value of $\gamma$, given by $\gamma=n / M$ is for coding rate compensation.

## 5 HDDs OF SOME KNOWN PCs AND THEIR PERFORMANCE RANKINGS

In order to validate the authenticity of the proposed ranking method, we adopt a number of codebooks from the available literature on PCs. As a form of evaluation, the expression in (16) was used to generate transmission errors for the expression in (15), which in turn, was used to simulate performance ranking curves for each codebook evaluated. An impulsive noise probability $A=0.01$ was used, with varying strength $\Gamma$ from 0 to 0.3 .

According to [18], (3) and two other codebooks were proven to be distance optimal. We therefore consider

Table 1: Comparison of $M=4 \mathrm{PC}$ mappings

| Code | $\delta_{2}$ | $\delta_{3}$ | $\delta_{4}$ | Source |
| :---: | :---: | :---: | :---: | :---: |
| $\mathcal{H}_{\mathrm{A}}$ | 64 | 64 | 112 | $(3),[18,(9)]$ |
| $\mathcal{H}_{\mathrm{B}}$ | 64 | 64 | 112 | $[18,(8)]$ |
| $\mathcal{H}_{\mathrm{C}}$ | 64 | 64 | 112 | $[18,(10)]$ |
| $\mathcal{H}_{\mathrm{D}}$ | 72 | 84 | 84 | $[18,(11)]$ |



Figure 2: Performance ranking curves for Schemes $\mathcal{H}_{\mathrm{A}}$ to $\mathcal{H}_{\mathrm{D}}$.
another non-optimal PC of $M=4$ and $|\mathcal{C}|=16$ (i.e., (8) from [18]), in comparison with the optimal ones. Table 1 gives their HDDs, while their performance ranking curves, simulated from (15) and (16), are as shown in Figure 2.

According to Figure 2, code $\mathcal{H}_{D}$, has the worst performance. Of course, $\mathcal{H}_{\mathrm{A}}, \mathcal{H}_{\mathrm{B}}$ and $\mathcal{H}_{\mathrm{C}}$ are expected to have overlapping performances, as their ranking curves clearly show, although their codewords are different. This agrees with the findings in [18]. This shall also be validated in our simulation section.

We proceed to consider PCs of $M=5$. Here we adopt the codebook in (12) of [18] and two other similar codebooks $M(5,5,0)$ and $M(5,5,0)^{*}$ from [17, Appendix B.1] and denote these three codebooks as $\mathcal{I}_{\mathrm{A}}, \mathcal{I}_{\mathrm{B}}$ and $I_{\mathrm{C}}$. These codebooks all have $M=5$ and $|\mathcal{C}|=32$. According to the distance optimality approach in [17] and [18], none of these codebooks are optimal, but $I_{\mathrm{C}}$ is near optimal. However, what makes $I_{\mathrm{B}}$ and $\mathcal{I}_{\mathrm{C}}$ different from $\mathcal{I}_{\mathrm{A}}$ is the fact that they have some repeating permutation symbols in some of their codewords. The HDDs of these codebooks are as shown in Table 2, with their ranking curves in Figure 3.

The curves in Figure 3 show that $J_{\mathrm{A}}$ should have the worst

Table 2: Comparison of $M=5$ PC mappings

| Code | $\delta_{2}$ | $\delta_{3}$ | $\delta_{4}$ | $\delta_{5}$ | Source |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $g_{\mathrm{A}}$ | 176 | 224 | 368 | 224 | $[18,(12)]$ |
| $g_{\mathrm{B}}$ | 160 | 192 | 384 | 256 | $[17, M(5,5,0)]$ |
| $g_{\mathrm{C}}$ | 160 | 176 | 352 | 304 | $\left[17, M(5,5,0)^{*}\right]$ |



Figure 3: Performance ranking curves for Schemes $g_{\mathrm{A}}$ to $I_{\mathrm{C}}$.

Table 3: Comparison of $M=6$ PC mappings

| Code | $\delta_{2}$ | $\delta_{3}$ | $\delta_{4}$ | $\delta_{5}$ | $\delta_{6}$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathcal{K}_{\mathrm{A}}$ | 320 | 256 | 896 | 896 | 1664 |
| $\mathcal{K}_{\mathrm{B}}$ | 320 | 320 | 736 | 1024 | 1632 |
| $\mathcal{K}_{\mathrm{C}}$ | 320 | 256 | 960 | 768 | 1728 |



Figure 4: Performance ranking curves for Schemes $\mathcal{K}_{\mathrm{A}}$ to $\mathcal{K}_{\mathrm{C}}$.
performance compared to the others, which agrees with the claims in [17] and [18]. Although the findings in [17] show that $I_{\mathrm{C}}$ is closer to optimal than $I_{\mathrm{B}}$, this analysis however shows that $I_{\mathrm{B}}$ and $\mathscr{I}_{\mathrm{C}}$ should have overlapping behaviours, which shall later be validated in the simulation section.

Next, we consider the three near-optimal codebooks, with $M=6, d_{m}=2$ and $|C|=64$, presented in [17]. As observed in [17], these codebooks have the same level of optimality. Their HDDs are presented in Table 3, as schemes $\mathcal{K}_{A}, \mathcal{K}_{\mathrm{B}}$ and $\mathcal{K}_{\mathrm{C}}$, while Figure 4 presents their ranking curves.

Based on the curves in Figure 4, all the three codebooks $\mathcal{K}_{\mathrm{A}}, \mathcal{K}_{\mathrm{B}}$ and $\mathcal{K}_{C}$ should have relatively overlapping performances. This is because their $P_{u}$ values in Figure 4 are the same. This finding actually corroborates the notion of the optimum distance approach presented in [17].

Another set of codebooks to be considered are the four optimal codebooks, with $M=8, d_{m}=2$ and $|C|=256$

Table 4: Comparison of $M=8$ PC mappings

| Code | $\delta_{2}$ | $\delta_{3}$ | $\delta_{4}$ | $\delta_{5}$ | $\delta_{6}$ | $\delta_{7}$ | $\delta_{8}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathcal{L}_{\mathrm{A}}$ | 1536 | 1024 | 3584 | 4096 | 7680 | 7168 | 40192 |
| $\mathcal{L}_{\mathrm{B}}$ | 1024 | 0 | 5632 | 0 | 17408 | 0 | 41216 |
| $\mathcal{L}_{\mathrm{C}}$ | 1024 | 512 | 3072 | 3072 | 10240 | 12800 | 34560 |
| $\mathcal{L}_{\mathrm{D}}$ | 1536 | 1024 | 4096 | 4096 | 6656 | 7168 | 40704 |



Figure 5: Performance ranking curves for Schemes $\mathcal{L}_{\mathrm{A}}$ to $\mathcal{L}_{\mathrm{D}}$.

Table 5: HDDs for other mappings from [17]

| Code | $\delta_{2}$ | $\delta_{3}$ | $\delta_{4}$ | $\delta_{5}$ | $\delta_{6}$ | $\delta_{7}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathcal{M}_{\mathrm{A}}$ | 768 | 640 | 1920 | 1536 | 4352 | 7040 |
| $\mathcal{M}_{\mathrm{B}}$ | 768 | 640 | 2432 | 3072 | 5888 | 3456 |
| $\mathcal{M}_{\mathrm{C}}$ | 896 | 1280 | 3456 | 4864 | 4224 | 1536 |



Figure 6: Performance ranking curves for Schemes $\mathcal{M}_{\mathrm{A}}$ to $\mathcal{M}_{\mathrm{C}}$.
in [17]. Their HDDs and ranking curves are presented in Table 4 and Figure 5, respectively.

According to the ranking curves of these codebooks, we see that $\mathcal{L}_{\mathrm{A}}$ and $\mathcal{L}_{\mathrm{D}}$ have overlapping curves, hence their real performances should follow a similar trend. Also, for $\mathcal{L}_{\mathrm{B}}$ and $\mathcal{L}_{\mathrm{C}}$, the same applies because of their overlapping performance ranking curves. Despite the fact that the four codebooks are declared optimal, our ranking method is able to determine the best from these sets of codebooks of the same class.

Further analyses carried out on some other mappings adopted from [17], are presented in Table 5 and Figure 6, based on the proposed ranking method.

Scheme $\mathcal{M}_{\mathrm{A}}$ is the $M(7,7,0)$ DPM with $M=7, d_{m}=2$ and $|\mathcal{C}|=128$ from [17, Appendix B.1]. Schemes $\mathscr{M}_{B}$ and $\mathcal{M}_{\mathrm{C}}$ from [17, Appendix C.1] are also of the same class with this $M(7,7,0)$ mapping. According to [17], $\mathcal{M}_{\mathrm{A}}$ is more optimal than the rest of these mappings, while $\mathcal{M}_{\mathrm{C}}$ is the least optimal. However, according to their ranking curves presented in Figure $6, \mathscr{M}_{\mathrm{A}}$ and $\mathscr{M}_{\mathrm{B}}$ should have overlapping performances in reality, because their curves overlap, while $\mathcal{M}_{\mathrm{C}}$ should perform worse than the rest.

## 6 SIMULATION RESULTS AND DISCUSSION

With a view to validating the proposed ranking method, all the schemes whose HDDs and ranking curves have been presented in Tables 1 to 5 and Figures 2 to 6 , were simulated under combined AWGN and impulsive noise associated with PLC channels. The input data is composed of 9600 random bits. In order to model the effect of impulsive noise on the transmitted information, we assumed a Gilbert-Elliot model, where probabilities of moving from the bad state to good state and from the good state to bad state are defined. The probability that a transmitted symbol is hit by impulsive noise, in a bad state was assumed to be $I N=1 / 32$, while a good state was assumed to be free of impulsive noise. Details about this model is available in [6]. The $E_{b} / N_{o}$ considered in the channel ranges from 0 to 25 dB .

The results shown in Figures 7 to 10 are for all the codebooks considered in our evaluations. As displayed in these figures, all the simulation results agree with the predicted performance rankings presented in Section 5. As predicted in Figure 2, Scheme $\mathcal{H}_{D}$ has the worst curve, which is the reason why it has the worst performance according to Figure 7. Also, the performance curves of Schemes $\mathcal{H}_{\mathrm{A}}, \mathcal{H}_{\mathrm{B}}$ and $\mathcal{H}_{\mathrm{C}}$ are seen to overlap, which is in line with the prediction of the proposed ranking method. The fact that these four schemes have the same $d_{m}$ does not mean they have the same performance. With the use of the proposed ranking method, we are able to know that Scheme $\mathcal{H}_{\mathrm{D}}$ is not to be used in a transceiver design when codes such as $\mathcal{H}_{\mathrm{A}}, \mathcal{H}_{\mathrm{B}}$ and $\mathcal{H}_{\mathrm{C}}$ are available.

The behaviours of Schemes $g_{\mathrm{A}}$ to $I_{\mathrm{C}}$ are also in the order of their ranking curves, with $\mathcal{I}_{\mathrm{C}}$ and $\mathcal{I}_{\mathrm{B}}$ being the best, while $g_{\mathrm{A}}$ is the worst, as the results in Figure 8 show. More so, as shown in Figure 9 the similarities in the performances of Schemes $\mathcal{K}_{\mathrm{A}}$ to $\mathcal{K}_{\mathrm{C}}$ also obey the predicted rankings presented in Figure 4.

In line with the predicted rankings in Figure 5, Schemes $\mathcal{L}_{\mathrm{A}}$ and $\mathcal{L}_{\mathrm{D}}$ are seen to have closely overlapping curves, especially at $E_{b} / N_{o}>15 \mathrm{~dB}$, as shown in Figure 10. Schemes $\mathcal{L}_{\mathrm{B}}$ and $\mathcal{L}_{\mathrm{C}}$ also have similar patterns of behaviour, as predicted by the ranking method.

The results of the simulations done on Schemes $\mathcal{M}_{\mathrm{A}}, \mathcal{M}_{\mathrm{B}}$ and $\mathcal{M}_{\mathrm{C}}$ are presented in Figure 11. According to this


Figure 7: Bit error rate curves for Schemes $\mathcal{H}_{\mathrm{A}}$ to $\mathcal{H}_{\mathrm{D}}$, under AWGN+impulsive noise channel.


Figure 8: Bit error rate curves for Schemes $I_{\mathrm{A}}$ to $\mathcal{I}_{\mathrm{C}}$, under AWGN+impulsive noise channel.


Figure 9: Bit error rate curves for Schemes $\mathcal{K}_{\mathrm{A}}$ to $\mathcal{K}_{\mathrm{C}}$, under AWGN+impulsive noise channel.
figure, $\mathcal{M}_{\mathrm{A}}$ is seen to overlap with $\mathscr{M}_{\mathrm{B}}$, but at high $E_{b} / N_{o}$ values, while $\mathcal{M}_{\mathrm{C}}$ is seen to be the worst performing scheme. This is in agreement with the predicted rankings presented in Figure 6, based on the proposed ranking method.

In the above simulation results, it is worth noting that the probability $I N$ used in the impulsive noise model is considered very high. That is why the performances of all the codebooks presented are relatively poor. With the aid of interleaving and concatenated outer codes, such as Reed-Solomon codes, the performances will definitely improve. Although the codebooks evaluated have a $d_{m}=$


Figure 10: Bit error rate curves for Schemes $\mathcal{L}_{\mathrm{A}}$ to $\mathcal{L}_{\mathrm{D}}$, under AWGN+impulsive noise channel.


Figure 11: Bit error rate curves for Schemes $\mathcal{M}_{\mathrm{A}}$ to $\mathcal{M}_{\mathrm{C}}$, under AWGN+impulsive noise channel.

2 , it does not mean that the proposed ranking method is limited to such cases. The method can be applied to any codebook category. Also, the significance of the different behaviours of all the schemes considered, are best observed at high $E_{b} / N_{o}$ values.

## 7 CONCLUSION

Permutation coding has emerged as a promising channel coding scheme in PLC related applications. With a view to contributing to this field, we have presented a fast and simple way of ranking the relative performance of PC codebooks with similar properties. This method computes the Hamming distance distribution of the PC codebook and uses the information therein to compute the probability $P_{u}$ of undetected error, which can, in turn, be used to compare the codebook to other codebooks with similar properties. A codebook with lower $P_{u}$ values has better performance than any codebook with higher $P_{u}$ values.

The method was validated using various known PC codebooks adopted from literature. The results of the simulations carried out agree with our claims, when the proposed method was used as a performance ranking tool. This tool is useful, when an efficient codebook is to be selected out of a number of similar codebooks. More so, it can be incorporated when exhaustive search for codebooks is done. It should however be noted that the computation of $P_{u}$ considers only AWGN and impulsive noise effects.

An extended work in this area of study can be centered on including the effects of other types of PLC noise, such as narrowband noise in the computation of probability $P_{u}$. This, however remains a challenge as there is no link found between its PSD and that of AWGN.

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## APPENDIX

Codes for Schemes $\mathcal{H}_{A}$ to $\mathcal{H}_{D}$ :
$\mathcal{H}_{\mathrm{A}}=\left\{\begin{array}{l}1230,1203,1320,1302,2130,2103,3120,3102 \\ 0231,0213,0321,0312,2031,2013,3021,3012\end{array}\right\}$
$\mathcal{H}_{\mathrm{B}}=\left\{\begin{array}{l}1230,1203,1320,1302,0231,0213,0321,0312 \\ 2130,2103,3120,3102,2031,2013,3021,3012\end{array}\right\}$
$\mathcal{H}_{\mathrm{C}}=\left\{\begin{array}{l}1230,1203,2130,2103,1320,1302,3120,3102 \\ 0231,0213,2031,2013,0321,0312,3021,3012\end{array}\right\}$
$\mathcal{H}_{\mathrm{D}}=\left\{\begin{array}{l}1230,1203,1320,1302,1023,1032,2130,2103 \\ 3210,3201,2310,2301,3021,3012,3120,3102\end{array}\right\}$

Codes for Schemes $I_{A}$ to $I_{C}$ :
$g_{\mathrm{A}}=\left\{\begin{array}{c}01234,01243,01324,01342,01423,01432 \\ 02134,02143,03214,03241,02314,02341 \\ 03421,03412,03124,03142,41230,41203 \\ 41320,41302,41023,41032,42130,42103 \\ 43210,43201,42310,42301,43021,43012 \\ 43120,43102\end{array}\right\}$
$J_{\mathrm{B}}=\left\{\begin{array}{c}12340,21340,12430,21430,32140,31240 \\ 42130,41230,14320,24310,13420,23410 \\ 34120,34210,43120,43210,02341,01342 \\ 02431,01432,02143,01243,02134,01234 \\ 04321,04312,03421,03412,04123,04213 \\ 03124,03214\end{array}\right\}$
$J_{\mathrm{C}}=\left\{\begin{array}{c}04321,03421,04312,03412,40321,30421 \\ 40312,30412,02341,02431,01342,01432 \\ 42301,32401,41302,31402,04123,03124 \\ 04213,03214,40123,30124,40213,30214 \\ 02143,02134,01243,01234,42103,32104 \\ 41203,31204\end{array}\right\}$
Codes for Schemes $\mathcal{K}_{A}$ to $\mathcal{K}_{C}$ :
$\mathcal{K}_{\mathrm{A}}=\left\{\begin{array}{c}123450,213450,124350,214350,123405 \\ 213405,124305,214305,321450,312450 \\ 421350,412350,321405,312405,421305 \\ 412305,143250,243150,134250,234150 \\ 143205,243105,134205,234105,341250 \\ 342150,431250,432150,341205,342105 \\ 431205,432105,503412,503421,504312 \\ 504321,053412,053421,054312,054321 \\ 501432,502431,501342,502341,051432 \\ 052431,051342,052341,503214,503124 \\ 504213,504123,053214,053124,054213 \\ 054123,501234,502134,501243,502143 \\ 051234,052134,051243,052143\end{array}\right)$
$\mathcal{K}_{B}=\left\{\begin{array}{c}123450,213450,124305,214305,321450 \\ 312450,421305,412305,143250,243150 \\ 134205,234105,341250,342150,431205 \\ 432105,523410,513420,024315,014325 \\ 521430,512430,021345,012345,543210 \\ 543120,034215,034125,541230,542130 \\ 031245,032145,103452,203451,154302 \\ 254301,301452,302451,451302,452301 \\ 103254,203154,154203,254103,301254 \\ 302154,451203,452103,503412,503421 \\ 054312,054321,501432,502431,051342 \\ 052341,503214,503124,054213,054123 \\ 501234,502134,051243,052143\end{array}\right\}$
123450,213450, 124350,214350, 123405 $213405,124305,214305,341250,342150$ 431250, 432150, 341205, 342105, 431205 432105,523410, 513420, 524310, 514320 023415, 013425, 024315, 014325,541230 542130,531240, 532140, 041235, 042135
$\mathcal{K}_{\mathrm{C}}=$ 031245,032145, 103452, 203451, 104352 204351, 153402, 253401, 154302, 254301 301254, 302154,401253,402153, 351204 352104,451203,452103, 503412, 503421 504312,504321, 053412, 053421, 054312 054321, 501234, 502134, 501243, 502143 051234, 052134,051243, 052143

Codes for Schemes $\mathcal{L}_{A}$ to $\mathcal{L}_{D}$ :
$12345670,12345607,12346570,12346507,12435670,12435607,12436570,12436507,21345670$ $21345607,21346570,21346507,21435670,21435607,21436570,21436507,12345076,12345706$ $12346075,12346705,12435076,12435706,12436075,12436705,21345076,21345706,21346075$ $21346705,21435076,21435706,21436075,21436705,14325670,14325607,14326570,14326507$ $13425670,13425607,13426570,13426507,24315670,24315607,24316570,24316507,23415670$ $23415607,23416570,23416507,14325076,14325706,14326075,14326705,13425076,13425706$ $13426075,13426705,24315076,24315706,24316075,24316705,23415076,23415706,23416075$ $23416705,32147650,32140657,32147560,32140567,42137650,42130657,42137560,42130567$ $31247650,31240657,31247560,31240567,41237650,41230657,41237560,41230567,32147056$ $32140756,32147065,32140765,42137056,42130756,42137065,42130765,31247056,31240756$ $31247065,31240765,41237056,41230756,41237065,41230765,34127650,34120657,34127560$ $34120567,43127650,43120657,43127560,43120567,34217650,34210657,34217560,34210567$ $43217650,43210657,43217560,43210567,34127056,34120756,34127065,34120765,43127056$ $43120756,43127065,43120765,34217056,34210756,34217065,34210765,43217056,43210756$ $43217065,43210765,56701234,56071234,65701234,65071234,56701243,56071243,65701243$ $65071243,56702134,56072134,65702134,65072134,56702143,56072143,65702143,65072143$ $50761234,57061234,60751234,67051234,50761243,57061243,60751243,67051243,50762134$ $57062134,60752134,67052134,50762143,57062143,60752143,67052143,56701432,56071432$ $65701432,65071432,56701342,56071342,65701342,65071342,56702431,56072431,65702431$ $65072431,56702341,56072341,65702341,65072341,50761432,57061432,60751432,67051432$ $50761342,57061342,60751342,67051342,50762431,57062431,60752431,67052431,50762341$ $57062341,60752341,67052341,76503214,06573214,75603214,05673214,76504213,06574213$ $75604213,05674213,76503124,06573124,75603124,05673124,76504123,06574123,75604123$ $05674123,70563214,07563214,70653214,07653214,70564213,07564213,70654213,07654213$ $70563124,07563124,70653124,07653124,70564123,07564123,70654123,07654123,76503412$ $06573412,75603412,05673412,76504312,06574312,75604312,05674312,76503421,06573421$ $75603421,05673421,76504321,06574321,75604321,05674321,70563412,07563412,70653412$ $07653412,70564312,07564312,70654312,07654312,70563421,07563421,70653421,07653421$ $70564321,07564321,70654321,07654321$
( $12345670,12345607,12346570,12346507,12435670,12435607,12436570,12436507,21345670$ $21345607,21346570,21346507,21435670,21435607,21436570,21436507,12347056,12340756$ $12347065,12340765,12437056,12430756,12437065,12430765,21347056,21340756,21347065$ $21340765,21437056,21430756,21437065,21430765,34125670,34125607,34126570,34126507$ $43125670,43125607,43126570,43126507,34215670,34215607,34216570,34216507,43215670$ $43215607,43216570,43216507,34127056,34120756,34127065,34120765,43127056,43120756$ $43127065,43120765,34217056,34210756,34217065,34210765,43217056,43210756,43217065$ $43210765,12705634,12075634,12706534,12076534,12705643,12075643,12706543,12076543$ $21705634,21075634,21706534,21076534,21705643,21075643,21706543,21076543,12567034$ $12560734,12657034,12650734,12567043,12560743,12657043,12650743,21567034,21560734$ $21657034,21650734,21567043,21560743,21657043,21650743,34705612,34075612,34706512$ $34076512,43705612,43075612,43706512,43076512,34705621,34075621,34706521,34076521$ $43705621,43075621,43706521,43076521,34567012,34560712,34657012,34650712,43567012$ $43560712,43657012,43650712,34567021,34560721,34657021,34650721,43567021,43560721$
$\mathcal{L}_{\mathrm{B}}=$
$43657021,43650721,56341270,56341207,65341270,65341207,56431270,56431207,65431270$ $65431207,56342170,56342107,65342170,65342107,56432170,56432107,65432170,65432107$ $70341256,07341256,70341265,07341265,70431256,07431256,70431265,07431265,70342156$ $07342156,70342165,07342165,70432156,07432156,70432165,07432165,56123470,56123407$ $65123470,65123407,56124370,56124307,65124370,65124307,56213470,56213407,65213470$ $65213407,56214370,56214307,65214370,65214307,70123456,07123456,70123465,07123465$ $70124356,07124356,70124365,07124365,70213456,07213456,70213465,07213465,70214356$ $07214356,70214365,07214365,56701234,56071234,65701234,65071234,56701243,56071243$ $65701243,65071243,56702134,56072134,65702134,65072134,56702143,56072143,65702143$ $65072143,70561234,07561234,70651234,07651234,70561243,07561243,70651243,07651243$ $70562134,07562134,70652134,07652134,70562143,07562143,70652143,07652143,56703412$ $56073412,65703412,65073412,56704312,56074312,65704312,65074312,56703421,56073421$ $65703421,65073421,56704321,56074321,65704321,65074321,70563412,07563412,70653412$ $07653412,70564312,07564312,70654312,07654312,70563421,07563421,70653421,07653421$ $70564321,07564321,70654321,07654321$
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Codes for Schemes $\mathcal{M}_{A}$ to $\mathcal{M}_{D}$ :
$\mathcal{M}_{\mathrm{A}}=\left\{\begin{array}{c}1234567,2134567,1243567,2143567,1234657,2134657,1243657,2143657,3214567,3124567 \\ 4213567,4123567,3214657,3124657,4213657,4123657,1432567,2431567,1342567,2341567 \\ 1432657,2431657,1342657,2341657,3412567,3421567,4312567,4321567,3412657,3421657 \\ 4312657,4321657,1234765,2134765,1243765,2143765,1234756,2134756,1243756,2143756 \\ 3214765,3124765,4213765,4123765,3214756,3124756,4213756,4123756,1432765,2431765 \\ 1342765,2341765,1432756,2431756,1342756,2341756,3412765,3421765,4312765,4321765 \\ 3412756,3421756,4312756,4321756,5674123,5674213,5673124,5673214,6574123,6574213 \\ 6573124,6573214,5674321,5674312,5673421,5673412,6574321,6574312,6573421,6573412 \\ 5672143,5671243,5672134,5671234,6572143,6571243,6572134,6571234,5672341,5671342 \\ 5672431,5671432,6572341,6571342,6572431,6571432,7654123,7654213,7653124,7653214 \\ 7564123,7564213,7563124,7563214,7654321,7654312,7653421,7653412,7564321,7564312 \\ 7563421,7563412,7652143,7651243,7652134,7651234,7562143,7561243,7562134,7561234 \\ 7652341,7651342,7652431,7651432,7562341,7561342,7562431,7561432\end{array}\right\}$

1234566,2134566, 1243566, 2143566, 1234656,2134656, 1243656, 2143656, 3412566,3421566 4312566,4321566,3412656,3421656,4312656,4321656,5234166,5134266,5243166,5143266 6234156,6134256, 6243156,6143256,5412366,5421366,5312466,5321466, 6412356,6421356 6312456,6321456, 1634526,2634516, 1643526, 2643516, 1534626, 2534616, 1543626, 2543616 3612546,3621546,4612536,4621536,3512646,3521646,4512636,4521636,5634126,5634216 5643126,5643216,6534126,6534216,6543126,6543216,5612346,5621346,5612436,5621436 6512346, 6521346,6512436,6521436, 1264563, 2164563, 1263564, 2163564, 1264653, 2164653 1263654,2163654,3462561,3461562,4362561,4361562,3462651,3461652,4362651,4361652 $5264163,5164263,5263164,5163264,6264153,6164253,6263154,6163254,5462361,5461362$ 5362461,5361462,6462351, 6461352, 6362451,6361452, 1664523,2664513, 1663524,2663514 1564623, 2564613, 1563624, 2563614,3662541,3661542,4662531,4661532,3562641,3561642 4562631,4561632,5664123,5664213,5663124,5663214,6564123,6564213,6563124,6563214 5662341,5661342,5662431,5661432,6562341,6561342,6562431,6561432
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$\mathscr{M}_{\mathrm{C}}=\{\quad 5452351,5451352,5352451,5351452,1534525,2534515,1543525,2543515,1554523,2554513$ $1553524,2553514,3514525,3524515,4513525,4523515,3554521,3554512,4553521,4553512$ $1532545,2531545,1542535,2541535,1552543,2551543,1552534,2551534,3512545,3521545$ $4512535,4521535,3552541,3551542,4552531,4551532,5534125,5534215,5543125,5543215$ $5554123,5554213,5553124,5553214,5514325,5524315,5513425,5523415,5554321,5554312$ $5553421,5553412,5532145,5531245,5542135,5541235,5552143,5551243,5552134,5551234$ $5512345,5521345,5512435,5521435,5552341,5551342,5552431,5551432$

