# Uncertainty Quantification for Integrated Circuits: Stochastic Spectral Methods

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Zheng Zhang Research Lab of Electronics Massachusetts Institute of Technology Cambrige, MA 02139, USA E-mail: z zhang@mit.edu Ibrahim (Abe) M. Elfadel Microsystem Engineering Masdar Inst. of Science & Technology Abu Dhabi, United Arab Emirates E-mail: ielfadel@masdar.ac.ae Luca Daniel Research Lab of Electronics Massachusetts Institute of Technology Cambrige, MA 02139, USA E-mail: luca@mit.edu

Abstract—Due to significant manufacturing process variations, the performance of integrated circuits (ICs) has become increasingly uncertain. Such uncertainties must be carefully quantified with efficient stochastic circuit simulators. This paper discusses the recent advances of stochastic spectral circuit simulators based on generalized polynomial chaos (gPC). Such techniques can handle both Gaussian and non-Gaussian random parameters, showing remarkable speedup over Monte Carlo for circuits with a small or medium number of parameters. We focus on the recently developed stochastic testing and the application of conventional stochastic Galerkin and stochastic collocation schemes to nonlinear circuit problems. The uncertainty quantification algorithms for static, transient and periodic steady-state simulations are presented along with some practical simulation results. Some open problems in this field are discussed.

#### I. INTRODUCTION

Manufacturing process variations have led to significant performance uncertainties in submicron and nano-scale IC design [1], [2]. Many results have been reported on variationaware modeling for semiconductor devices [3]–[5], interconnects [6]–[13], and for analog/RF and digital ICs [14], [15]. However, few have focused on the uncertainty quantification aspect that analyzes the uncertainty propagation from the device level to the circuit level through SPICE simulation.

Monte Carlo (MC) [16] has been the mainstream uncertainty quantification technique in commercial circuit simulators for decades [17]–[20]. Recently, Singhee *et al.* improved MC-based simulation and applied it to the yield analysis of analog/RF and digital ICs [21], [22]. Despite its wide application, MC has a slow convergence rate proportional to  $\frac{1}{\sqrt{N_s}}$  (where  $N_s$  is the number of samples used in MC). Very often, one must run a huge number of SPICE simulations to achieve acceptable accuracy at a prohibitively high computational cost.

Stochastic spectral methods [23]–[29] have emerged as a promising solution to uncertainty quantification problems, showing significant speedup over MC (especially when the parameter dimensionality is small or medium). Such methods represent the parameter-dependent solutions by some properly constructed basis functions, such as polynomial chaos (PC, also called Hermite polynomial chaos) [30] or generalized polynomial chaos (gPC) [27]. Mainstream stochastic spectral solvers include the stochastic Galerkin (also called stochastic finite element method [24]) and stochastic collocation [31]– [34] methods. Stochastic Galerkin is an intrusive (or "nonsampling based") solver, since it directly computes the PC/gPC coefficients by solving a coupled equation resulting from Galerkin testing. Stochastic collocation is a non-intrusive (or "sampling based") method: it solves the deterministic equations at a set of sample points, followed by a numerical scheme to reconstruct the PC/gPC coefficients.

There is an increasing interest in applying stochastic spectral methods to circuit simulation. Most works use PC-based stochastic collocation or stochastic Galerkin methods to simulate on-chip and off-chip interconnects with Gaussian parameters [35]–[39]. Limited results have been reported on nonlinear circuit analysis. The PC-based stochastic circuit simulator proposed by Strunz [40] requires constructing the stochastic device models *a-priori*, thus it cannot be easily integrated with industrial semiconductor device models. In [41], stochastic collocation was combined with harmonic balance to simulate nonlinear RF circuits under Gaussian variations.

Practical ICs often contain also non-Gaussian parameters, and they cannot be effectively simulated by PC-based techniques. For such cases, gPC is more appealing since it can effectively handle non-Gaussian parameters. Motivated by this, Pulch applied gPC-based spectral methods to analyzing stochastic linear circuits [42]. Since almost all semiconductor devices are nonlinear, it is necessary to develop uncertainty quantification tools for nonlinear circuit simulation. Some progress has been reported along this line [43]-[46]. The RF circuit simulators in [45], [46] directly apply gPC and stochastic Galerkin, showing remarkable speedup over MC. In order to further reduce the computational cost, the authors of this paper have proposed to simulate nonlinear circuits using a stochastic testing scheme [43], [44]. Stochastic testing can be regarded as a hybrid version of stochastic collocation and stochastic Galerkin methods, and it proves more efficient for time-domain circuit simulation.

In this paper, we aim to review the fundamental ideas of gPC-based transistor-level simulation, and to summarize the recent progress on this topic. In Section II, we review some backgrounds on gPC and numerical quadrature. Section III discusses stochastic testing, stochastic Galerkin and stochastic

 TABLE I

 UNIVARIATE GPC POLYNOMIAL BASIS OF SOME TYPICAL RANDOM PARAMETERS [29].

Distribution of $\xi_k$	PDF of $\xi_k \ [\rho_k(\xi_k)]^1$	univariate gPC basis $\phi_{\nu}^{k}(\xi_{k})$	Support $\Omega_k$
Gaussian	$\frac{1}{\sqrt{2\pi}}\exp\left(\frac{-\xi_k^2}{2}\right)$	Hermite-chaos polynomial	$(-\infty, +\infty)$
Gamma	$\frac{\xi_k^{\gamma-1}\exp(-\xi_k)}{\Gamma(\gamma)}, \ \gamma > 0$	Laguerre-chaos polynomial	$[0, +\infty)$
Beta	$\frac{\xi_k^{\alpha-1}(1-\xi_k)^{\beta-1}}{\mathrm{B}(\alpha,\beta)}, \ \alpha,\beta>0$	Jacobi-chaos polynomial	[0, 1]
Uniform	$\frac{1}{2}$	Legendre-chaos polynomial	[-1, 1]
$\sim$	1		

 $^{1}\Gamma(\gamma) = \int_{0}^{\infty} t^{\gamma-1} \exp(-t) dt$  and  $B(\alpha, \beta) = \int_{0}^{1} t^{\alpha-1} (1-t)^{\beta-1} dt$  are the Gamma and Beta functions, respectively.

collocation techniques and compares their performances in circuit simulation. In Section IV, some stochastic periodic steady-state simulators based on intrusive solvers are discussed and compared. Section V discusses some open problems in this field, followed by the conclusion in Section IV.

### II. PRELIMINARIES

Consider the stochastic differential algebraic equation obtained from modified nodal analysis [47]:

$$\frac{d\vec{q}\left(\vec{x}(t,\vec{\xi}),\vec{\xi}\right)}{dt} + \vec{f}\left(\vec{x}(t,\vec{\xi}),\vec{\xi}\right) = B\vec{u}\left(t\right)$$
(1)

where  $\vec{u}(t)$  is the input;  $\vec{x} \in \mathbb{R}^n$  denotes nodal voltages and branch currents;  $\vec{q} \in \mathbb{R}^n$  and  $\vec{f} \in \mathbb{R}^n$  represent the charge/flux and current/voltage terms, respectively. Here  $\vec{\xi} = [\xi_1, \dots, \xi_d] \in$  $\Omega$  (with  $\Omega \subseteq \mathbb{R}^d$ ) denotes d Gaussian and/or non-Gaussian parameters describing the device-level variations. Assume that all random parameters are independent, i.e., their joint probability density function (PDF) can be expressed as

$$\rho(\vec{\xi}) = \prod_{k=1}^{d} \rho_k(\xi_k), \qquad (2)$$

with  $\rho_k(\xi_k)$  being the PDF of  $\xi_k \in \Omega_k$ . In this paper, we focus on how to solve (1) by stochastic spectral methods to extract the statistical information of the state vector  $\vec{x}(t, \vec{\xi})$ .

## A. Generalized Polynomial Chaos (gPC) Construction

Univariate gPC. For  $\xi_k \in \Omega_k \subseteq \mathbb{R}$ , one can construct a set of polynomial functions subject to the orthonormal condition:

$$\left\langle \phi_{\gamma}^{k}(\xi_{k}), \phi_{\nu}^{k}(\xi_{k}) \right\rangle = \int_{\Omega_{k}} \phi_{\gamma}^{k}(\xi_{k}) \phi_{\nu}^{k}(\xi_{k}) \rho_{k}(\xi_{k}) d\xi_{k} = \delta_{\gamma,\nu} \quad (3)$$

where  $\langle, \rangle$  denotes the inner product;  $\delta_{\gamma,\nu}$  is a Delta function; integers  $\gamma$  and  $\nu$  are the degrees of  $\xi_k$  in polynomials  $\phi_{\gamma}^k(\xi_k)$ and  $\phi_{\nu}^k(\xi_k)$ , respectively. Given  $\rho_k(\xi_k)$  and  $\Omega_k$ , one can utilize a three-term recurrence relation to construct such orthonormal polynomials [48]. Some univariate gPC basis functions are listed in Table I as a demonstration. It is worth noting that: 1) the univariate gPC basis functions are not limited to the cases listed in Table I; 2) when  $\xi_k$  is a Gaussian variable, its gPC simplifies to the Hermite polynomial chaos [30].

**Multivariate gPC.** When the components of  $\xi$  are assumed mutually independent, the multivariate gPC can be constructed

based on the univariate gPC of each  $\xi_k$ . Given an index vector  $\vec{\alpha} = [\alpha_1, \cdots, \alpha_d] \in \mathbb{N}^d$ , the corresponding multivariate gPC is constructed as

$$H_{\vec{\alpha}}(\vec{\xi}) = \prod_{k=1}^{a} \phi_{\alpha_k}^k(\xi_k).$$
(4)

The obtained multivariate gPC is orthonormal, i.e.,

$$\left\langle H_{\vec{\alpha}}(\vec{\xi}), H_{\vec{\beta}}(\vec{\xi}) \right\rangle = \int_{\Omega} H_{\vec{\alpha}}(\vec{\xi}) H_{\vec{\beta}}(\vec{\xi}) \rho(\vec{\xi}) d\vec{\xi} = \delta_{\vec{\alpha}, \vec{\beta}}$$

Note that  $H_{\vec{\alpha}}(\vec{\xi})$  is the product of different types of univariate gPC bases when  $\xi_k$ 's have different density functions.

# B. gPC Expansion

If  $\vec{x}(\vec{\xi}, t)$  is a 2nd-order stochastic process (i.e.,  $\vec{x}(\vec{\xi}, t)$  has a bounded 2nd-order moment), we can approximate it by a finite-term gPC expansion

$$\vec{x}(t,\vec{\xi}) \approx \tilde{x}(t,\vec{\xi}) = \sum_{\vec{\alpha}\in\mathcal{P}} \hat{x}_{\vec{\alpha}}(t) H_{\vec{\alpha}}(\vec{\xi})$$
(5)

where  $\hat{x}_{\vec{\alpha}}(t) \in \mathbb{R}^n$  denotes the gPC coefficient with index  $\vec{\alpha}$ , and  $\mathcal{P}$  is a set containing some properly selected index vectors.

Given  $p \in \mathbb{N}^+$ , there are two popular choices for  $\mathcal{P}$  [23]. In the tensor product method, one sets  $\mathcal{P} = \{\vec{\alpha} \mid 0 \le \alpha_k \le p\}$ , leading to a total of  $(p+1)^d$  gPC bases. In order to reduce the total number of basis functions, the total degree scheme sets  $\mathcal{P} = \{\vec{\alpha} \mid \alpha_k \in \mathbb{N}, 0 \le \alpha_1 + \cdots + \alpha_d \le p\}$ , leading to

$$K = \begin{pmatrix} p+d\\ p \end{pmatrix} = \frac{(p+d)!}{p!d!} \tag{6}$$

gPC bases in total. This total degree method is employed in our stochastic circuit simulator. There is a one-to-one correspondence between k (with  $1 \le k \le K$ ) and the index vector  $\vec{\alpha}$ , thus for simplicity (5) is normally rewritten as

$$\vec{x}(t,\vec{\xi}) \approx \tilde{x}(t,\vec{\xi}) = \sum_{k=1}^{K} \hat{x}^k(t) H_k(\vec{\xi}).$$
(7)

It is shown that gPC expansions converge exponentially for some analytical functions [25], [27], [29]. Such exponential convergence rates may not be observed in practical engineering problems, but gPC still converge very fast when the function of interest has a smooth dependence on  $\vec{\xi}$ . With gPC approximations, some statistical information (e.g., mean and variance) can be easily calculated due to the orthonormality of  $H_k(\vec{\xi})$ 's.  $\begin{cases} non-intrusive \begin{cases} MC: random samples from PDF \rightarrow solution at each sample \rightarrow distribution, moments, etc. \\ stochastic collocation: samples from TP, SP, etc. \rightarrow solution at each sample \rightarrow gPC coefficients \\ stochastic Galerkin: Galerkin testing \rightarrow directly compute gPC coefficients \end{cases}$ 

stochastic testing: collocation testing  $\rightarrow$  directly compute gPC coefficients

Fig. 1. Classification of various stochastic solvers [43]. "TP" and "SP" means the quadrature rules based on tensor product and sparse grids, respectively.

#### C. Numerical Quadrature

This section briefly reviews some numerical quadrature methods widely used in stochastic spectral methods.

intrusive

**1-D Case.** When computing an integral with a quadrature method one typically uses the expression

$$\int_{\Omega_k} g(\xi_k) \rho_k(\xi_k) d\xi_k \approx \sum_{j=1}^{\hat{n}} g(\xi_k^j) w_k^j \tag{8}$$

when  $g(\xi_k)$  is a smooth function. The quadrature points  $\xi_k^j$ 's and weights  $w_k^j$ 's are chosen according to  $\Omega_k$  and  $\rho_k(\xi_k)$ . Two kinds of quadrature rules are widely used: Gauss quadrature [49] and Clenshaw-Curtis rules [50], [51]. With  $\hat{n}$  points, Gauss quadrature rule produces exact results for all polynomials of degree  $\leq 2\hat{n} - 1$ , and Clenshaw-Curtis gets exact results when the degree of  $g(\xi_k)$  is  $\leq \hat{n} - 1$ . Clenshaw-Curtis scheme generates nested quadrature points and assumes that  $\xi_k$  is uniformly distributed in a bounded domain.

**Multi-dimensional Case.** One can also evaluate a multidimensional integral in  $\Omega \subseteq \mathbb{R}^d$  using the formula

$$\int_{\Omega} g(\vec{\xi})\rho(\vec{\xi})d\vec{\xi} \approx \sum_{j=1}^{\hat{N}} g(\vec{\xi}^j)w^j.$$
(9)

where  $\hat{N}$  is the total number of quadrature points, and  $w^j$  is the weight corresponding to quadrature point  $\vec{\xi}^j$ . Given the 1-D quadrature points for each  $\xi_k$ ,  $\vec{\xi}^j$ 's and  $w^j$ 's can be obtained for instance using a tensor-product rule or using sparse grids [52], [53]. With Smolyak's algorithm, sparse grid technique uses much fewer quadrature points than the tensor-product rule, thus it is widely used to solve stochastic PDEs [31]–[34]. In [31]–[34] Smolyak's algorithm produces nested sparse grids because all random parameters are assumed uniformly distributed (and thus Clenshaw-Curtis rule is used for all  $\xi_k$ 's). However, Smolyak's algorithm generates nonnested sparse grids when non-nested 1-D quadrature points are used for some parameters (since many random parameters with non-uniform distributions may not be effectively handled by the Clenshaw-Curtis rule).

#### **III. STOCHASTIC SPECTRAL METHODS**

## A. Classification of Stochastic Solvers

The main stochastic solvers are classified in Fig. 1. MC and stochastic collocation are both non-intrusive (or sampling-based) methods: they solve (1) as a deterministic problem at

# Algorithm 1 Testing Point Selection for ST [43].

1: construct  $\hat{N}$  *d*-D quadrature points and weights; 2: reorder the quadrature points such that  $|w^{j}| \ge |w^{j+1}|$ ; 3: set  $V = \vec{H}\left(\vec{\xi^{1}}\right)/||\vec{H}\left(\vec{\xi^{1}}\right)||, \vec{\xi_{1}} = \vec{\xi^{1}}, \text{ and } m = 1;$ 4: for  $j = 2, \dots, \hat{N}$  do 5:  $\vec{v} = \vec{H}\left(\vec{\xi^{j}}\right) - V\left(V^{T}\vec{H}\left(\vec{\xi^{j}}\right)\right);$ 6: if  $||\vec{v}||/||\vec{H}\left(\vec{\xi^{j}}\right)|| > \beta$ 7: set  $V = [V; \vec{v}/||\vec{v}||], m = m + 1, \vec{\xi_{m}} = \vec{\xi^{j}};$ 8: if  $m \ge K$ , break, end; 9: end if 10: end for

a set of samples. Their main difference lies in the sampling stage: MC randomly draws some samples based on  $\rho(\vec{\xi})$ , whereas stochastic collocation typically uses the points from a tensor-product or sparse-grid rule such that the gPC coefficients can be well reconstructed. Stochastic Galerkin and stochastic testing belong to the family of intrusive solvers: through solving a new coupled differential algebraic equation they directly compute the gPC coefficients. The former sets up the coupled equation by Galerkin testing, whereas the latter constructs a coupled equation via collocation testing.

#### B. Stochastic Testing (ST)

The stochastic testing method needs to select K testing points  $\vec{\xi_1}, \dots, \vec{\xi_K}$ . First, a quadrature scheme (e.g., tensorproduct or sparse grid rule in Section II-C) is applied to generate  $\hat{N}$  quadrature points  $\vec{\xi^j}$ 's in parameter space  $\Omega$ , which are called candidate nodes. Second, the K most important candidate nodes are selected such that the transformation matrix  $\mathbf{V} \in \mathbb{R}^{K \times K}$ , with its (i, j) entry being

$$\mathbf{V}_{i,j} = H_j(\vec{\xi}_i),\tag{10}$$

is invertible and well conditioned. Define a vector function  $\vec{H}(\vec{\xi}) := [H_1(\vec{\xi}); \cdots; H_K(\vec{\xi})]$ , then the testing points can be selected by Algorithm 1 [43]. Only a small portion of the candidate nodes are finally selected as the testing points.

Let  $\hat{\mathbf{x}}(t) = [\hat{x}^1(t); \cdots; \hat{x}^K(t)]$  denote the gPC coefficients,  $\tilde{q}(\hat{\mathbf{x}}(t), \vec{\xi}) = \vec{q}(\tilde{x}(t, \vec{\xi}), \vec{\xi})$  and  $\tilde{f}(\hat{\mathbf{x}}(t), \vec{\xi}) = \vec{f}(\tilde{x}(t, \vec{\xi}), \vec{\xi})$ . Substituting  $\tilde{x}(t, \vec{\xi})$  of (7) into (1) yields a residual function

$$\mathbf{R}(\hat{\mathbf{x}}(t),\vec{\xi}) = \frac{d\tilde{q}(\hat{\mathbf{x}}(t),\xi)}{dt} + \tilde{f}(\hat{\mathbf{x}}(t),\vec{\xi}) - B\vec{u}(t).$$
(11)

**Collocation Testing.** Enforcing the residual function to zero at all testing points, stochastic testing generates the following coupled differential algebraic equation:

$$\frac{d\mathbf{q}(\hat{\mathbf{x}}(t))}{dt} + \mathbf{f}(\hat{\mathbf{x}}(t)) = \mathbf{B}u(t), \tag{12}$$

where the k-th blocks of  $\mathbf{q}(\hat{\mathbf{x}}(t))$ ,  $\mathbf{f}(\hat{\mathbf{x}}(t))$  and **B** are  $\tilde{q}(\hat{\mathbf{x}}(t), \vec{\xi_k})$ ,  $\tilde{f}(\hat{\mathbf{x}}(t), \vec{\xi_k})$  and B, respectively.

Numerical Solver. Stochastic testing is an intrusive solver: the gPC coefficients  $\hat{\mathbf{x}}(t)$  are directly computed by simulating (12), then the parameter-dependent current/voltage variables are obtained by gPC approximations. In transient analysis, the time step sizes can be selected adaptively according to the local truncation error (LTE) of (12) as done in commercial deterministic circuit simulators [17], [18]. Another desirable feature of stochastic testing is the decoupling procedure inside the intrusive solver. Assume that **J** is the Jacobian inside the Newton's iteration when simulating (12) (as a DC problem or as a transient problem using numerical integration such as backward Euler). It is shown in [43] that **J** can be factored as

$$\mathbf{J} = \text{blkdiag}(J_1, \cdots, J_K)(\mathbf{V} \otimes \mathbf{I}_n)$$
(13)

where blkdiag is the block diagonal operator,  $\otimes$  is the Kronecker product operation, and  $\mathbf{I}_n \in \mathbb{R}^{n \times n}$  is an identity matrix. Matrix  $J_k \in \mathbb{R}^{n \times n}$  can be treated as a Jacobian corresponding to (1) with  $\vec{\xi} = \vec{\xi}_k$ . Since the Vandermonde-like matrix **V** [as defined in (10)] can be easily inverted [54], the linear system solution inside each Newton's iteration can be decoupled into K small-size problems. Consequently, the overall computational cost scales linearly with K [43].

## C. Stochastic Galerkin (SG)

Galerkin Testing. Applying Galerkin testing

$$\left\langle \mathbf{R}(\hat{\mathbf{x}}(t),\vec{\xi}), H_k(\vec{\xi}) \right\rangle = \int_{\Omega} \mathbf{R}(\hat{\mathbf{x}}(t),\vec{\xi}) H_k(\vec{\xi}) \rho(\vec{\xi}) d\vec{\xi} = 0 \quad (14)$$

for  $k = 1, \dots, K$ , stochastic Galerkin forms a coupled equation in the form of (12). Now the k-th blocks of  $\mathbf{q}(\hat{\mathbf{x}}(t))$ ,  $\mathbf{f}(\hat{\mathbf{x}}(t))$  and **B** are  $\langle \tilde{q}(\hat{\mathbf{x}}(t), \vec{\xi}), H_k(\vec{\xi}) \rangle$ ,  $\langle \tilde{f}(\hat{\mathbf{x}}(t), \vec{\xi}), H_k(\vec{\xi}) \rangle$ and  $\langle B, H_k(\vec{\xi}) \rangle$ , respectively. The inner products can be evaluated using the numerical quadrature rules described in Section II-C or by an MC integration (if d is large).

Numerical Solver. After (12) is formed by Galerkin testing,  $\hat{\mathbf{x}}(t)$  is also computed in an intrusive manner. In timedomain simulation, the time step sizes can also be controlled adaptively as in stochastic testing. Compared with stochastic testing, stochastic Galerkin has two drawbacks. First, the inner product evaluation needs  $\hat{N} > K$  quadrature points, and thus at each time point stochastic Galerkin requires more circuit/device evaluations. This can lead to remarkable time cost when complex semiconductor device models are employed. Second, the resulting Jacobian in a stochastic Galerkin-based simulator cannot be decoupled, although it can be decoupled for linear circuits if the gPC bases are chosen by the tensor product method [42]. This causes a significant computational overhead compared with stochastic testing.

#### D. Stochastic Collocation (SC)

In stochastic collocation, Eq. (1) is first is solved at  $\hat{N}$  sample points to obtain a set of deterministic solutions  $\vec{x}(t, \vec{\xi}^k)$ 's. After that the gPC coefficients are reconstructed by a post-processing numerical scheme. In the mainstream stochastic collocation schemes [31]–[34], the samples are selected by a tensor product or sparse-grid quadrature technique, and thus the *j*-th gPC coefficient vector can be estimated by

$$\hat{x}^{j}(t) = \left\langle \vec{x}(t,\vec{\xi}), H_{j}(\vec{\xi}) \right\rangle \approx \sum_{k=1}^{N} w^{k} H_{j}(\vec{\xi}^{k}) \vec{x}(t,\vec{\xi}^{k}).$$
(15)

In practical time-domain simulation, each  $x(t, \vec{\xi}^k)$  is computed at a set of discretized time points. Therefore, to reconstruct the gPC coefficients, the deterministic solutions for all samples should be located on the same time grid. Since it is difficult to preselect an adaptive time grid for the black-box deterministic solver, a small fixed step size is normally used, leading to excessive computational cost for stiff circuits.

The speedup factor of stochastic testing over stochastic collocation can be estimated as [43]

$$\kappa_{\text{overall}} = \kappa_{\text{samp}} \times \kappa_{\text{tctrl}}.$$
 (16)

Here  $\kappa_{\text{samp}} = \hat{N}/K > 1$  because stochastic testing uses fewer samples than stochastic collocation. If stochastic collocation uses tensor-product quadrature points,  $\kappa_{\text{samp}}$  gets extremely large as d increases. When stochastic collocation uses nested Smolyak sparse grids and the total degree of the gPC expansion is p,  $\kappa_{\text{samp}}$  is about  $2^p$  for  $d \gg 1$ . The second factor  $\kappa_{\text{tctrl}} > 1$  is caused by adaptive time stepping in stochastic testing, which is case dependent. In DC analysis,  $\kappa_{\text{tctrl}} = 1$ .

#### E. Performance Analysis

We have implemented stochastic testing, stochastic Galerkin and stochastic collocation in MATLAB and performed various simulations (DC, transient, AC) on several analog/RF and digital ICs [43]. For those benchmarks with several Gaussian and non-Gaussian random parameters, all stochastic spectral methods have shown  $10^2-10^3 \times$  speedup over MC due to the fast convergence of gPC expansions. The speedup factors of stochastic testing over stochastic Galerkin and stochastic collocation are on the level of O(1) to  $O(10^2)$ , which are more significant as the gPC order p increases.

The static analysis of a common-source amplifier (with four random parameters) shows that stochastic testing has slightly larger errors than stochastic Galerkin and stochastic collocation, but it uses the least amount of CPU time to achieve a similar level of accuracy. The results of a low-noise amplifier (LNA) with three random parameters (in Fig. 2) is plotted in Fig. 3. The  $L_2$ -norm errors of the computed gPC coefficients from all three methods are almost the same, and stochastic testing costs significantly less CPU time. Our experiments show that a 3rd-order gPC expansion (i.e., p = 3) is enough for most circuits.

Transient simulation of the common-source amplifier and LNA shows that the speedup factor of stochastic testing over



Fig. 2. Schematic of the LNA.



Fig. 3. Accuracy and efficiency of stochastic testing (ST), stochastic Galerkin (SG) and stochastic collocation (SC) for the DC analysis of LNA.

stochastic Galerkin and stochastic collocation is about  $O(10^1)$ to  $O(10^2)$ , which is more significant for large-size circuits. In analog circuits, the speedup factor caused by adaptive time stepping is about O(1) to  $O(10^1)$ . For digital (e.g., SRAM cell) and multi-rate RF (e.g., BJT mixer) circuits, stochastic testing can solve the problem with seconds or minutes of CPU time, whereas stochastic collocation may require > 1 hour due to the uniform time stepping. Fig. 4 shows a mixer with uncertainties at  $R_1$  and  $R_2$ . Stochastic testing produces the mean and standard-deviation waveforms (in Fig. 5) after 21 minutes, whereas stochastic Galerkin, stochastic collocation and MC are prohibitively expensive on the MATLAB platform.

# IV. UNCERTAINTY QUANTIFICATION FOR PERIODIC STEADY STATES

Analog/RF and power electronic circuit designers are interested in periodic steady-state analysis [55]–[60]. Using stochastic spectral methods, uncertainties of the periodic steady states can be analyzed more efficiently than using MC. This section summarizes the progress on stochastic timedomain periodic steady-state solvers [44]. Other solvers (e.g., harmonic balance) can also be easily implemented.

#### A. Forced Circuits

For many forced circuits (e.g., amplifiers and power converters), there exists a periodic steady-state solution  $\vec{x}(t, \vec{\xi}) = \vec{x}(t+T, \vec{\xi})$  when the input is a time-varying periodic signal  $\vec{u}(t) = \vec{u}(t+T)$ . The state vector  $\vec{x}(t, \vec{\xi})$  is periodic for any



Fig. 4. Schematic of the BJT double-balanced mixer.



Fig. 5. Uncertainties of  $V_{\text{out}} = V_{\text{out}1} - V_{\text{out}2}$  of the double-balanced mixer: (a) mean value, (b) standard deviation.

 $\vec{\xi} \in \Omega$  if and only if  $\hat{\mathbf{x}}(t)$  is periodic. Therefore, we can set up the following equation

$$\mathbf{g}(\hat{\mathbf{y}}) = \Phi(\hat{\mathbf{y}}, 0, T) - \hat{\mathbf{y}} = 0.$$
(17)

Here  $\hat{\mathbf{y}} = \hat{\mathbf{x}}(0)$ , and  $\hat{\mathbf{x}}(T) = \Phi(\hat{\mathbf{y}}, 0, T)$  is the state transition function of (12) formed by stochastic testing (c.f. Section III-B) or stochastic Galerkin (c.f. Section III-C).

Eq. (17) can be solved by the standard shooting Newton method [55]–[58]. When solving the linear equation inside each Newton's iteration, evaluating the right-hand side requires integrating (12) from t = 0 to t = T, and the Jacobian matrix can be obtained once a Monodromy matrix is computed (via a sensitivity analysis along the discretized trajectories). Directly solving (17) requires  $O(K^3n^3)$  cost if a direct matrix solver is employed. Fortunately, [44] shows that the linear equation solution can be easily decoupled into K small problems by a similarity transform, if (12) is formed by stochastic testing. The decoupled intrusive transient solver in Section III-B can be employed to evaluate the right-hand side of each linear equation inside Newton's iterations, thus the overall cost can be reduced to  $KO(n^3)$  in the stochastic testing formulation.

**Results.** The simulation result of the LNA (with  $V_{\rm in} = 0.1\sin(4\pi \times 10^8 t)$  V) is plotted in Fig. 6. With a 3rd-order total-degree gPC expansion, the stochastic testing-based and stochastic Galerkin-based solvers give the same results. Using a standard MC, 8000 samples are required to achieve a similar level of accuracy (<1% relative errors for the mean and



Fig. 6. Periodic steady-state waveforms for the LNA. (a) & (b): mean and s.t.d of  $V_{\rm out};$  (c) & (d): mean and s.t.d of  $I(V_{\rm dd}).$ 



Fig. 7. Probability density functions obtained by MC and stochastic testing (ST). (a) total harmonic distortion and (b) power dissipation.

standard deviation). Fig. 7 plots the density functions of the total harmonic distortion and power consumption extracted from the computed periodic steady-state solution, which are consistent with those from MC. The simulation cost of the decoupled stochastic testing solver is 3.4 seconds, which is  $42 \times$  faster over the coupled stochastic testing solver,  $71 \times$  faster over the stochastic Galerkin-based solver, and  $220 \times$  faster over MC. In [44] an  $O(K^2)$  speedup factor caused by decoupling is clearly observed for stochastic testing.

# B. Autonomous Circuits

For unforced cases (e.g., oscillators), the input signal  $\vec{u}(t) = \vec{u}$  is time-invariant, and the period is unknown. The periodicity constraint is  $\hat{\mathbf{x}}(t,\vec{\xi}) = \hat{\mathbf{x}}(t+T(\vec{\xi}),\vec{\xi})$ , where the period  $T(\vec{\xi})$  depends on  $\vec{\xi}$ . Choose a constant  $T_0$  and assume that  $a(\vec{\xi})$  is a scaling factor such that  $T(\vec{\xi}) = T_0 a(\vec{\xi})$ , then we obtain a scaled time variable  $\tau = t/a(\vec{\xi})$  [60]. Let  $\vec{z}(\tau,\vec{\xi}) := \vec{x}(t,\vec{\xi})$ , then  $\vec{z}(\tau,\vec{\xi})$  has a constant period  $T_0$  on the scaled time axis  $\tau$ . Both  $a(\vec{\xi})$  and  $\vec{z}(\tau,\vec{\xi})$  can be approximated by gPC expansions

$$a(\vec{\xi}) \approx \tilde{a}(\vec{\xi}) = \sum_{k=1}^{K} \hat{a}^k H_k(\vec{\xi}),$$
  
$$\vec{z}(\tau, \vec{\xi}) \approx \tilde{z}(\tau, \vec{\xi}) = \sum_{k=1}^{K} \hat{z}^k(\tau) H_k(\vec{\xi}).$$
 (18)

Substituting the above approximation into (1) and changing the time variable, we obtain a new residual function

$$\mathbf{R}(\hat{\mathbf{z}}(\tau), \hat{\mathbf{a}}, \vec{\xi}) = \frac{d\tilde{q}(\hat{\mathbf{z}}(\tau), \xi)}{d\tau} + \tilde{a}(\vec{\xi})\tilde{f}(\hat{\mathbf{z}}(\tau), \vec{\xi}) - \tilde{a}(\vec{\xi})B\vec{u}.$$



Fig. 8. Schematic of the BJT Colpitts oscillator.



Fig. 9. Realizations of  $V_{\rm out}$  for the Colpitts oscillator. (a) on the scaled time axis, (b) on the original time axis.

Here  $\tilde{q}(\hat{\mathbf{z}}(\tau), \vec{\xi}) = \vec{q}(\tilde{z}(\tau, \vec{\xi}), \vec{\xi}), \quad \tilde{f}(\hat{\mathbf{z}}(\tau), \vec{\xi}) = \vec{f}(\tilde{z}(\tau, \vec{\xi}), \vec{\xi});$  $\hat{\mathbf{a}}$  and  $\hat{\mathbf{z}}(\tau)$  collect the gPC coefficients of  $\tilde{a}(\vec{\xi})$  and  $\tilde{z}(\tau, \vec{\xi})$ , respectively. The following coupled differential equation

$$\frac{d\mathbf{q}\left(\hat{\mathbf{z}}(\tau)\right)}{d\tau} + \mathbf{f}\left(\hat{\mathbf{z}}(\tau), \hat{\mathbf{a}}\right) = \mathbf{B}(\hat{\mathbf{a}})\vec{u}$$
(19)

can be constructed by either stochastic testing [44] or stochastic Galerkin [45]. In stochastic testing we perform collocation testing (c.f. Section III-B) on  $R(\hat{z}(\tau), \hat{a}, \vec{\xi})$ , whereas in stochastic Galerkin one applies Galerkin testing (c.f. Section III-C).

Based on (19), an algebraic equation can be set up to solve for the gPC coefficients of  $\tilde{z}(0, \vec{\xi})$  and  $\tilde{a}(\vec{\xi})$ . Let  $\hat{\mathbf{y}} := [\hat{\mathbf{z}}(0); \hat{\mathbf{a}}]$ and fix the *j*-th component of  $\vec{z}(0)$  at  $\lambda$ , then we have

$$\mathbf{g}(\hat{\mathbf{y}}) = \begin{bmatrix} \Psi(\hat{\mathbf{z}}(0), \hat{\mathbf{a}}) \\ \chi(\hat{\mathbf{z}}(0)) \end{bmatrix} = \begin{bmatrix} \Phi(\hat{\mathbf{z}}(0), 0, T_0, \hat{\mathbf{a}}) - \hat{\mathbf{z}}(0) \\ \chi(\hat{\mathbf{z}}(0)) \end{bmatrix} = 0.$$
(20)

Here  $\Phi(\hat{\mathbf{z}}(0), 0, T_0, \hat{\mathbf{a}})$  is the state transition function of (19), which depends on  $\hat{\mathbf{a}}$ . The phase constraint  $\chi(\hat{\mathbf{z}}(0)) = 0 \in \mathbb{R}^K$ 

$$\chi(\hat{\mathbf{z}}(0)) = \left[\hat{\mathbf{z}}_j(0) - \lambda; \, \hat{\mathbf{z}}_{j+n}(0); \, \cdots; \, \hat{\mathbf{z}}_{j+(K-1)n}(0)\right] = 0$$

is added to make (20) a determined equation.

When solving (20) by Newton's iterations, the Jacobian evaluation is more involved than that in forced circuits. Besides the Monodromy matrix, the sensitivity matrix of  $\mathbf{g}(\hat{\mathbf{y}})$  w.r.t  $\hat{\mathbf{a}}$  is also required, which can be obtained in an iterative way [44]. Similar to the forced circuits, decoupling leads to an  $O(K^2)$  speedup if the stochastic testing formulation is employed [44].

**Results.** The gPC-based periodic steady-state solvers are applied to analyze the BJT Colpitts oscillator in Fig. 8. The oscillation frequency is influenced by the Gaussian variation of  $L_1$  and non-Gaussian variation of  $C_1$ . With a 3rd-order gPC expansion, the stochastic testing-based [44] and stochastic Galerkin-based [45] solvers produce the same results. Fig. 9 shows some realizations of  $V_{out}$ . The variation looks small on the scaled time axis  $\tau$ , but it is significant on the original time axis due to the uncertainties of the frequency. The CPU time of the decoupled stochastic testing-based solver is 4.9 seconds, which is  $2 \times$  and  $5 \times$  faster over the coupled stochastic testingbased solver and the stochastic Galerkin-based solver [45], respectively. To achieve the similar level of accuracy (< 1%) errors for the mean and standard deviation of the frequency), MC must use 5000 samples, which is about  $507 \times$  slower than the stochastic testing-based simulator with decoupling.

#### C. Other Related Work

An intrusive simulator has been proposed to analyze the uncertainties of RF circuits with multi-rate input signals [46]. It uses the multi-time PDE technique [61] to solve a coupled differential equation formed by stochastic Galerkin, generating stochastic quasi-periodic steady-state solutions. The stochastic testing-based formulation can be easily extended to this case to further reduce the computational cost.

Non-intrusive periodic steady-state solvers are not discussed in this paper due to their ease of implementation.

#### V. OPEN PROBLEMS

Although stochastic spectral methods seem promising for stochastic circuit simulation, there still exist many open problems, some of which are summarized below.

**High Dimensionality.** The number of total gPC bases increases very fast as the parameter dimensionality d increases. Consequently, the computational cost becomes prohibitively expensive when d is large. It is worth exploiting the sparsity of the gPC coefficients to reduce the complexity. Compressed sensing [62] seems effective for behavior modeling [14], but its efficiency can degrade for simulation problems (since the gPC coefficients of different nodal voltages and/or branch currents have different sparsity pattens). A dominant singular vector method has been proposed for high-dimensional linear stochastic problems [6], yet solving the non-convex optimization is challenging for nonlinear problems.

**Correlated Non-Gaussian Parameters.** In existing literatures, the parameters are typically assumed mutually independent, which is not valid for many practical circuits. Unlike Gaussian variables, correlated non-Gaussian parameters cannot be easily transformed to independent ones, making the gPC basis construction challenging. A theoretical method has been proposed to deal with parameters with arbitrary density functions [63], but its numerical implementation is non-trivial.

**Long-Term Integration.** In digital IC simulation, normally designers have to perform a long-time transient simulation. In the applied math community, it is well known that PC/gPC approximation can be inaccurate for a tong-time integration, despite some improvements [64].

# VI. CONCLUSION

Stochastic spectral methods have emerged as a promising technique for the uncertainty quantification of integrated circuits. After reviewing some key concepts about gPC, this paper has discussed stochastic testing, stochastic Galerkin and stochastic collocation methods, as well as their implementation and performance in nonlinear transistor circuit analysis. Some recent progress on stochastic periodic steady-state analysis has been summarized. Among these techniques, stochastic testing has shown higher efficiency in time-domain IC simulation. Some important problems, such as how to deal with high parameter dimensionality, correlated non-Gaussian parameters and long-term integration errors, have not been solved.

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#### REFERENCES

- D. S. Boning, "Variation," *IEEE Trans. Semiconductor Manufacturing*, vol. 21, no. 1, pp. 63–71, Feb 2008.
- [2] N. Tega, H. Miki, F. Pagette, D. J. Frank, A. Ray, M. J. Rooks, W. Haensch, and K. Torii, "Increasing threshold voltage variation due to random telegraph noise in FETs as gate lengths scale to 20 nm," in *Proc. Intl. Symp. VLSI Technology*, Jun. 2009, pp. 50–51.
- [3] S. R. Nassif, "Modeling and analysis of manufacturing variations," in Proc. Intl. Conf. Custom Integrated Circuits, Sept. 2001, pp. 223 – 228.
- [4] D. S. Boning and S. Nassif, "Models of process variations in device and interconnect," in *Design of High Performance Microprocessor Circuits*. IEEE Press, 2000.
- [5] P. A. Stolk, F. P. Widdershoven, and D. B. M. Klaassen, "Modeling statistical dopant fluctuations in MOS transistors," *IEEE Trans. Electron Devices*, vol. 45, no. 9, pp. 1960 – 1971, Sept. 1998.
- [6] T. Moselhy and L. Daniel, "Stochastic integral equation solver for efficient variation aware interconnect extraction," in *Proc. Design Auto. Conf.*, Jun. 2008, pp. 415–420.
- [7] —, "Stochastic dominant singular vectors method for variation-aware extraction," in *Proc. Design Auto. Conf.*, Jun. 2010, pp. 667–672.
- [8] —, "Variation-aware stochastic extraction with large parameter dimensionality: Review and comparison of state of the art intrusive and nonintrusive techniques," in *Proc. Intl. Symp. Quality Electronic Design*, Mar. 2011, pp. 14–16.
- [9] Z. Zhang, I. M. Elfadel, and L. Daniel, "Model order reduction of fully parameterized systems by recursive least square optimization," in *Proc. Intl. Conf. Computer-Aided Design*, Nov. 2011, pp. 523–530.
- [10] R. Shen, S. X.-D. Tan, J. Cui, W. Yu, Y. Cai, and G.-S. Chen, "Variational capacitance extraction and modeling based on orthogonal polynomial method," *IEEE Trans. Very Large Scale Integr. Syst.*, vol. 18, no. 11, pp. 1556 –1565, Nov. 2010.
- [11] W. Yu, C. Hu, and W. Zhang, "Variational capacitance extraction of onchip interconnects based on continuous surface model," in *Proc. Design Auto. Conf.*, Jun. 2009, pp. 758–763.
- [12] F. Gong, H. Yu, L. Wang, and L. He, "A parallel and incremental extraction of variational capacitance with stochastic geometric moments," *IEEE Trans. VLSI*, vol. 22, no. 9, pp. 1729 –1737, Sept. 2012.
- [13] H. Zhu, X. Zeng, W. Cai, J. Xue, and D. Zhou, "A sparse grid based spectral stochastic collocation method for variations-aware capacitance extraction of interconnects under nanometer process technology," in *Proc. Design Auto. Test in Europe*, Apr. 2007, pp. 1–6.
- [14] X. Li, "Finding deterministic solution from underdetermined equation: large-scale performance modeling of analog/RF circuits," *IEEE Trans. Computer-Aided Design of Integrated Circuits and Systems*, vol. 29, no. 11, pp. 1661–1668, Nov 2011.

- [15] C. Visweswariah, K. Ravindran, K. Kalafala, S. G. Walker, S. Narayan, D. K. Beece, J. Piaget, N. Venkateswaran, and J. G. Hemmett, "Firstorder incremental block-based statistical timing analysis," *IEEE Trans. Computer-Aided Design of Integrated Circuits and Systems*, vol. 25, no. 10, pp. 2170 – 2180, Oct 2006,.
- [16] S. Weinzierl, "Introduction to Monte Carlo methods," NIKHEF, Theory Group, The Netherlands, Tech. Rep. NIKHEF-00-012, 2000.
- [17] K. S. Kundert, *The Designers Guide to SPICE and Spectre*. Boston, MA: Kluwer Academic Publishers, 1995.
- [18] P. W. Tuinenga, Spice: A Guide to Circuit Simulation and Analysis Using PSpice, 3rd ed. Upper Saddle River, NJ: Prentice Hall PTR, 1995.
- [19] http://www.cadence.com.[20] http://www.synopsys.com/Tools/Verification.
- [21] A. Singhee and R. A. Rutenbar, "Statistical blockade: Very fast statistical simulation and modeling of rare circuit events and its application to memory design," *IEEE Trans. on CAD of Integrated Circuits and* systems, vol. 28, no. 8, pp. 1176–1189, Aug. 2009.
- [22] —, "Why Quasi-Monte Carlo is better than Monte Carlo or latin hypercube sampling for statistical circuit analysis," *IEEE Trans. CAD* of Integr. Circuits and Syst., vol. 29, no. 11, pp. 1763–1776, Nov. 2010.
- [23] J. Bäck, F. Nobile, L. Tamellini, and R. Tempone, "Spectral and high order methods for partial differential equations," *Lecture Notes in Computational Science and Engineering*, vol. 76, pp. 43–62, 2011.
- [24] R. Ghanem and P. Spanos, Stochastic finite elements: a spectral approach. Springer-Verlag, 1991.
- [25] D. Xiu, Numerical Methods for Stochastic Computations: A Spectral Method Approach. Princeton University Press, 2010.
- [26] O. Le Maitre and O. Knio, Spectral methods for uncertainty quantification: with application to computational fluid dynamics. Springer, 2010.
- [27] D. Xiu and G. E. Karniadakis, "The Wiener-Askey polynomial chaos for stochastic differential equations," *SIAM J. Sci. Comp.*, vol. 24, no. 2, pp. 619–644, Feb 2002.
- [28] —, "Modeling uncertainty in flow simulations via generalized polynomial chaos," J. Comp. Phys., vol. 187, no. 1, pp. 137–167, May 2003.
- [29] D. Xiu, "Fast numerical methods for stochastic computations: A review," *Comm. in Comput. Physics*, vol. 5, no. 2-4, pp. 242–272, Feb. 2009.
- [30] N. Wiener, "The homogeneous chaos," American Journal of Mathematics, vol. 60, no. 4, pp. 897–936, Oct 1938.
- [31] D. Xiu and J. S. Hesthaven, "High-order collocation methods for differential equations with random inputs," *SIAM J. Sci. Comp.*, vol. 27, no. 3, pp. 1118–1139, Mar 2005.
- [32] I. Babuška, F. Nobile, and R. Tempone, "A stochastic collocation method for elliptic partial differential equations with random input data," *SIAM J. Numer. Anal.*, vol. 45, no. 3, pp. 1005–1034, Mar 2007.
- [33] F. Nobile, R. Tempone, and C. G. Webster, "A sparse grid stochastic collocation method for partial differential equations with random input data," *SIAM J. Numer. Anal.*, vol. 46, no. 5, pp. 2309–2345, May 2008.
- [34] —, "An anisotropic sparse grid stochastic collocation method for partial differential equations with random input data," *SIAM J. Numer. Anal.*, vol. 46, no. 5, pp. 2411–2442, May 2008.
- [35] I. S. Stievano, P. Manfredi, and F. G. Canavero, "Carbon nanotube interconnects: Process variation via polynomial chaos," *IEEE Trans. Electromagnetic Compatibility*, vol. 54, no. 1, pp. 140–148, Feb. 2012.
- [36] —, "Parameters variability effects on multiconductor interconnects via hermite polynomial chaos," *IEEE Trans. Compon., Packag., Manufacut. Tech.*, vol. 1, no. 8, pp. 1234–1239, Aug. 2011.
- [37] J. Fan, M. Ni, S. X.-D. Tan, Y. Cai, and X. Hong, "Statistical model order reduction for interconnect circuits considering spatial correlations," in *Proc. Design Auto. Test in Europe*, Apr. 2007, pp. 1508–1513.
- [38] J. Wang, P. Ghanta, and S. Vrudhula, "Stochastic analysis of interconnect performance in the presence of process variations," in *Proc. Design Auto Conf.*, 2004, pp. 880–886.
- [39] Y. Zou, Y. Cai, Q. Zhou, X. Hong, S. X.-D. Tan, and L. Kang, "Practical implementation of stochastic parameterized model order reduction via Hermite polynomial chaos," in *Proc. Asia South Pacific Design Auto. Conf.*, 2007, pp. 367 – 372.
- [40] K. Strunz and Q. Su, "Stochastic formulation of SPICE-type electronic circuit simulation with polynomial chaos," ACM Trans. Modeling and Computer Simulation, vol. 18, no. 4, pp. 15:1–15:23, Sep 2008.
- [41] J. Tao, X. Zeng, W. Cai, Y. Su, D. Zhou, and C. Chiang, "Stochastic sparse-grid collocation algorithm (SSCA) for periodic steady-state analysis of nonlinear system with process variations," in *Porc. Asia and South Pacific Design Automation Conference*, 2007, pp. 474–479.

- [42] R. Pulch, "Polynomial chaos for linear differential algebraic equations with random parameters," *Int. J. Uncertainty Quantification*, vol. 1, no. 3, pp. 223–240, 2011.
- [43] Z. Zhang, T. A. El-Moselhy, I. A. M. Elfadel, and L. Daniel, "Stochastic testing method for transistor-level uncertainty quantification based on generalized polynomial chaos," *IEEE Trans. Computer-Aided Design Integr. Circuits Syst.*, vol. 32, no. 10, Oct. 2013.
- [44] Z. Zhang, T. A. El-Moselhy, P. Maffezzoni, I. A. M. Elfadel, and L. Daniel, "Efficient uncertainty quantification for the periodic steady state of forced and autonomous circuits," *IEEE Trans. Circuits Syst. II: Exp. Briefs*, vol. 60, no. 10, Oct. 2013.
- [45] R. Pulch, "Modelling and simulation of autonomous oscillators with random parameters," *Mathematics and Computers in Simulation*, vol. 81, no. 6, pp. 1128–1143, Feb 2011.
- [46] —, "Polynomial chaos for multirate partial differential algebraic equations with random parameters," *Applied Numerical Mathematics*, vol. 59, no. 10, pp. 2610–2624, Oct 2009.
- [47] C.-W. Ho, A. Ruehli, and P. Brennan, "The modified nodal approach to network analysis," *IEEE Trans. Circuits Syst.*, vol. CAS-22, no. 6, pp. 504–509, Jun. 1975.
- [48] W. Gautschi, "On generating orthogonal polynomials," SIAM J. Sci. Stat. Comput., vol. 3, no. 3, pp. 289–317, Sept. 1982.
- [49] G. H. Golub and J. H. Welsch, "Calculation of gauss quadrature rules," *Math. Comp.*, vol. 23, pp. 221–230, 1969.
- [50] C. W. Clenshaw and A. R. Curtis, "A method for numerical integration on an automatic computer," *Numer. Math.*, vol. 2, pp. 197–205, 1960.
- [51] L. N. Trefethen, "Is Gauss quadrature better than Clenshaw-Curtis?" SIAM Review, vol. 50, no. 1, pp. 67–87, Feb 2008.
- [52] V. Barthelmann, E. Novak, and K. Ritter, "High dimensional polynomial interpolation on sparse grids," *Adv. Comput. Math.*, vol. 12, no. 4, pp. 273–288, Mar. 2000.
- [53] T. Gerstner and M. Griebel, "Numerical integration using sparse grids," *Numer. Algor.*, vol. 18, pp. 209–232, Mar. 1998.
- [54] D. Calvetti and L. Reichel, "Fast inversion of Vandermonde-like matrices involving orthogonal polynomials," *BIT Numerical Mathematics*, vol. 33, no. 3, pp. 473–484, 1994.
- [55] K. S. Kundert, "Introduction to RF simulation and its application," *IEEE Journal Solid-State Circuits*, vol. 34, no. 9, pp. 1298–1319, Sept. 1999.
- [56] O. Nastov, R. Telichevesky, K. Kundert, and J. White, "Fundamentals of fast simulation algorithms for RF circuits," *IEEE Proc.*, vol. 95, no. 3, pp. 600–621, March 2007.
- [57] R. Telichevesky, K. S. Kundert, and J. K. White, "Efficient steadystate analysis based on matrix-free Krylov-subspace methods," in *Proc. Design Automation Conf.* New York, NY, Jun 1995, pp. 480–484.
- [58] T. Aprille and T. Trick, "Steady-state analysis of nonlinear circuits with periodic inputs," *IEEE Proc.*, vol. 60, no. 1, pp. 108–114, Jan. 1972.
- [59] —, "A computer algorithm to determine the steady-state response of nonlinear oscillators," *IEEE Trans. Circuit Theory*, vol. CT-19, no. 4, pp. 354–360, July 1972.
- [60] I. Vytyaz, D. C. Lee, P. K. Hanumolu, U.-K. Moon, and K. Mayaram, "Sensitivity analysis for oscillators," *IEEE Trans. Computer-Aided Design*, vol. 27, no. 9, pp. 1521–1534, Sept. 2008.
- [61] J. Roychowdhury, "Analyzing circuits with widely separated time scales using numerical PDE methods," *IEEE Trans. Circuits Syst. I: Fundamental Theory Appl.*, vol. 27, no. 9, pp. 578 –594, May 2001.
  [62] D. L. Donoho, "Compressed sensing," *IEEE Trans. Informa. Theory*,
- [62] D. L. Donoho, "Compressed sensing," IEEE Trans. Informa. Theory, vol. 52, no. 4, pp. 578 –594, April 2006.
- [63] C. Soize and R. Ghanem, "Physical systems with random uncertainties: Chaos representations with arbitrary probability measure," *SIAM J. Sci. Comput.*, vol. 26, no. 2, p. 395410, Feb 2004.
- [64] X. Wan and G. E. Karniadakis, "Long-term behavior of polynomial chaos in stochastic flow simulations," *Comput. Methods Appl. Mech. Eng.*, vol. 195, no. 41-43, p. 55825596, Aug. 2006.