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Implementation of Sensor RFID: Carrying Sensor Information in the Modulation Frequency

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Abstract—An approach that can be used for exploiting the sensing capabilities of radio-frequency identification (RFID) is presented and formulated. In this approach, sensor information is carried through the modulation frequency of RFID. The aim of this work is to investigate the sensor concept and to characterize the sensor performance both theoretically and experimentally. Furthermore, the operation of the sensor radio-frequency (RF) parts and oscillator are described analytically, and the equations are verified by simulations and measurements. The concept is experimentally demonstrated at a single carrier frequency to test its suitability for ultra-high frequency (UHF) RFID applications, and shown to be feasible for implementing sensors that can be read across distances up to 14 meters.

Index Terms—Diode, mixer, modulation, oscillator, radio frequency identification (RFID), rectifier, wireless sensors.

I. INTRODUCTION

ULTRA-high frequency (UHF) and microwave radio-frequency Identification (RFID) enables identifying distant objects equipped with an RFID tag [1]. The technology has become increasingly ubiquitous, including applications such as asset tracking and inventory in logistics, access control, and road toll collection. In addition to existing applications, there is a huge need for an inexpensive technology capable of both identification and of sensing some external parameter, such as pressure, temperature or strain. Battery-powered active or semi-passive wireless sensors are often available, but they may be too expensive or bulky for many applications. In addition, their life-time or environmental operating conditions can be limited by the battery. These limitations can be avoided by using passive wireless sensors.

Communication methods used by passive wireless sensors can be divided into analog and digital ones. Digital sensors are also known as RFID and can further be divided into two sub-categories, depending on whether the sensor read-out is analog or digital [2].

RFID tags equipped with integrated circuits (IC) enable several sophisticated features such as identification, rewritable memory and anti-collision protocol. UHF RFID has been used for sensing by adding an A/D converter, a microprocessor or other digital logic and a sensing element to the tag [3]–[9]. In this digital sensing approach, an analog sensor value is transformed into digital format and communicated to the reader device using the digitally modulated backscattering

principle. The advantage of this approach is that it facilitates a generic sensor element and is thus well suited for a very broad range of applications. However, increased power consumption due to the additional electronics reduces the read-out range significantly. As an example, sensors presented in [9] provide a read-out range of 1.5 m while RFID tags without a digital sensor provide a range in the order of 15 m. Therefore, a long read-out distance of UHF RFID sensor tags necessitates an on-board battery or other energy source.

Examples of RFIDs equipped with analog sensors are presented in [10]–[14]. In this approach, the RFID antenna impedance (and thus the matching frequency) is made sensitive to a measured quantity, such as moisture, by loading the antenna with a moisture-sensitive element. The sensed quantity can be obtained in two ways. In one, the reader interrogates the sensor at different frequencies and records either relative sensitivity or modulated radar cross-section of the tag as a function of frequency. The sensed quantity can be related to these responses through antenna matching. The second way of obtaining the sensed quantity is to add a tuning circuit to the RFID chip. This tuning circuit is used to find the input impedance that maximizes the power transfer between the reader and the tag.

The advantage of using the antenna as a sensor is a relatively long read-out distance because the sensing element does not necessarily increase the power consumption. However, the concept requires a sensing element operating at microwave frequencies. Typical off-the-shelf sensors are not feasible because they are often designed to operate at frequencies below a few MHz. Furthermore, the antenna impedance also changes due to proximity to conductive or dielectric objects, which can falsify the sensor read-out [15].

Sensors utilizing analog communication are surface acoustic wave (SAW) RFID, intermodulation sensors and resonance sensors. SAW sensors [16]–[18] utilize an interdigital transducer patterned on a piezoelectric substrate to convert the electromagnetic energy into a surface-acoustic wave, which is then manipulated with acoustic reflectors, transformed back to electromagnetic energy, and radiated back to the reader device. The measured quantity affects the propagation properties of the SAW on the piezoelectric substrate. The need for a piezoelectric material for the sensing element limits possible applications. In addition, SAW tags only enable hard-coded identification (ID).

Resonance sensors (e.g., [19]–[21]) consist of a simple resonance circuit whose resonance is sensitive to a measured quantity. These sensors require near-field coupling to the reader, which limits their read-out distance to a few centimeters. Another hindrance is that their resonance may

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TABLE I
 SUMMARY OF PASSIVE WIRELESS SENSORS

	Resonance sensor	SAW RFID	Intermod. sensor	RFID + sensitive antenna	RFID + digital sensor	This work
Communication	Analog	Analog	Analog	Digital	Digital	Digital
Sensing	Analog	Analog	Analog	Analog	Digital	Analog
ID	No	Yes	Yes	Yes	Yes	Yes
Memory	No	No	No	Yes	Yes	Yes
Anti-collision	No	No	No	Yes	Yes	Yes
Environment insensitive	No	Yes	Yes	No	Yes	Yes
Read-out distance	Small	Large	Large	Large	Small	Large
Sensing element	Generic	Special	Generic	Special	Generic	Generic
Reader device	Special	Special	Special	Standard	Standard	Standard

be affected by proximity to conductive or dielectric objects.

Recently developed intermodulation sensors utilize frequency conversion for sensor read-out [22] – [26]. In this principle, the sensor is interrogated by two closely located frequencies, and the sensor responds at an intermodulation frequency. This approach enables wireless read-out of a generic sensor element, such as a Microelectromechanical systems (MEMS) sensor across a very large distance (even tens of meters). The sensors can also be equipped with a hard-coded ID. Intermodulation sensors can be made insensitive to antenna proximity effects.

The properties of passive wireless sensors are summarized in Table I. No single technology can simultaneously provide identification, anti-collision protocol, memory, insensitivity to antenna proximity effects, and large interrogation distance. In this paper, we present a passive wireless sensor principle potentially capable of offering all these benefits. The sensor utilizes the digital communication principle of RFID, and the sensor information is coded into the digital modulation frequency of the backscattered signal. It complies with the frequency regulations set for UHF RFID in the ISO/IEC18000-6C standard and can be compatible with existing high-end readers capable of recording the modulation frequency of the tag with sufficient precision (e.g., Infinity 510 manufactured by Sirit or Tagformance Lite by Voyantic). Compatibility with some existing RFID hardware offers substantial cost benefits.

Previously, the idea of converting sensor information to the modulation frequency of a backscatter transponder has been presented in [27] – [29], but the concept is not analyzed in detail. Moreover, analytical equations for predicting the sensor performance have not been developed yet. Thus, the aim of the current work is to analyze the sensor principle and to develop analytical equations for predicting the sensor performance. In addition, equations for the sensor radio-frequency (RF) parts and oscillator are derived and demonstrated in practice. Finally, the analytically predicted sensor performance is compared with measurement results. Digital circuitry of RFID (such as memory, analog-to-digital converter, or envelope detector) is beyond the scope of the current work. However,

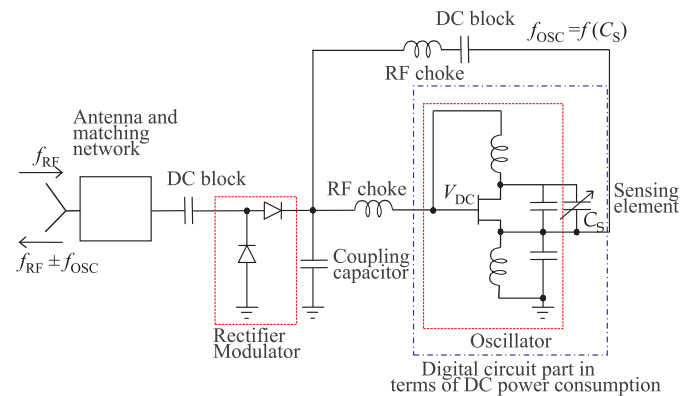


Fig. 1. Simplified RF circuit of the sensor.

the oscillator and sensing element described in this paper could replace the local oscillator in the digital circuitry of a conventional RFID tag. As a consequence, the remaining digital part would consume less power.

II. ANALYTICAL MODEL FOR THE SENSOR

A. Operation Principle of the Sensor

An RFID reader communicates with the tag using a binary amplitude modulation. The tag rectifies the required energy for its operation from the interrogation signal of the reader, and replies to the reader by reflecting a portion of the signal carrying a binary modulation back to the reader. The sensor proposed in this paper operates in a similar way as a normal UHF RFID tag, except that the modulation frequency of the tag carries sensor information. According to the ISO/IEC 18000-6C standard, the modulation frequency of the tag is set by the reader device at the beginning of the communication, and it can be from 40 kHz to 640 kHz [30]. The modulation frequency of the tag can vary $\pm 4\% \dots 22\%$ from the initially set frequency depending on the frequency and operating conditions. This enables coding sensor information to the modulation frequency.

The sensor can be realized by modifying the internal oscillator of an RFID chip in such a way that its oscillation

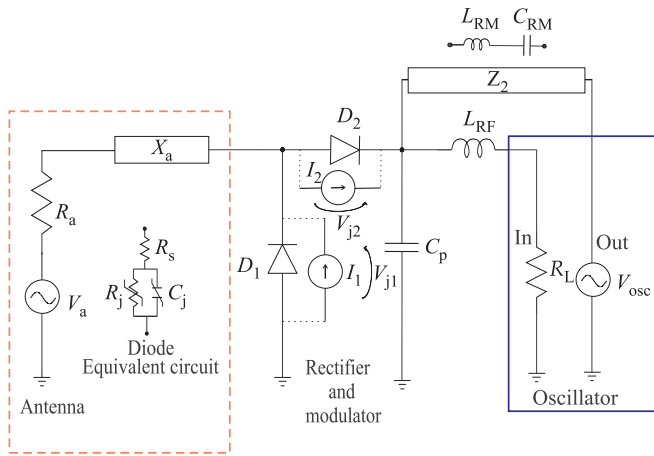


Fig. 2. Electrical equivalent circuit of the antenna, rectifier, and modulator. The oscillator is represented with a resistor at its dc input and an ac voltage source at its output.

frequency tunes with the measured quantity. This can be done by loading the oscillator with an inductive, capacitive or resistive sensor element. An advantage of this principle is that generic sensor elements, such as MEMS sensors, can be used because the sensor is read out at low kHz frequencies. The sensing element does not necessarily increase the power consumption of the tag, and the principle therefore offers a long read-out distance. The sensor circuitry is also fully isolated from the antenna, making the concept insensitive to antenna proximity effects. This concept can potentially be made compatible with the existing RFID systems, and many sensing elements can be incorporated into one RFID tag with digitally controlled switches. The sensing elements can be arranged so that the oscillation frequency is affected by only one element at a time [15].

The main RF parts of the sensor (excluding the antenna) are voltage rectifier, oscillator, and modulator. The rectifier produces dc voltage for the oscillator from the received interrogation signal. When the oscillator turns on, it produces an ac output, which drives the modulator. As a consequence, a modulated signal is scattered back. In the studied case, the rectifier and modulator are the same element. Fig. 1 shows a simplified RF circuit of the sensor. In the following, we present different parts of the sensor in more detail and derive analytical equations to describe their electrical behavior.

B. Voltage Rectifier

The rectifier is based on two Schottky diodes and a capacitor. This rectifier topology is also known as a voltage doubler [31]. A voltage doubler circuit is selected instead of a voltage quadrupler or $2N$ -fold voltage multiplier because it is relatively simple to realize in practice with discrete components. The antenna is matched to the rectifier at the carrier frequency, and RF chokes are used to prevent RF signal from dissipating in the oscillator ports. A dc block capacitor (C_b) is placed between the diode and antenna to prevent rectified voltage from grounding through the antenna, and a coupling capacitor (C_p) is used to ground the RF signal. Fig. 2

shows the electrical equivalent circuit of the rectifier. Antenna and matching circuit with dc block capacitor are represented as a voltage source with an internal impedance of $Z_a = R_a + jX_a$, and the dc load of the oscillator is R_L .

In the following, we derive an equation for the rectified dc voltage across the load R_L under small-signal conditions. The rectified voltage is later needed to predict the sensitivity of the sensor (i.e., the lowest RF power that turns the sensor on) and to predict the modulated radar cross-section of the sensor.

When the sensor is illuminated by the reader device, the antenna produces a voltage

$$V_a = 2\sqrt{2P_{in}R_a} \cos(\omega_0 t) \quad (1)$$

where P_{in} is the received power, R_a is the antenna resistance, and ω_0 is the angular frequency. Assume that dissipations in the series resistance R_s of the diodes are small compared to those in the junction resistance R_j . The Schottky diode is zero-biased (possible self-biasing is neglected for simplicity), and its junction resistance is given as $R_j = 1/(\alpha I_s)$ where I_s is the saturation current and $\alpha = q/nkT$ where q is the elementary charge, n is an ideality factor, k is the Boltzmann constant and T is temperature. Fig. 2 depicts that at the time of rectification, two diodes (D_1 and D_2) produce two different junction voltage V_{j1} and V_{j2} , respectively. For the sake of simplicity we calculate the rectifier dc voltage for each diode one at a time and sum the results later to obtain the total rectified dc voltage. In dc analysis, the junction capacitance can be neglected and the diode is represented as R_j . The junction voltage across diode 1 is

$$V_{j1} = 2\sqrt{2P_{in}R_a} S_{ja1} \cos(\omega_0 t) \quad (2)$$

where $S_{ja1} = V_{j1}/V_a$ is the voltage transfer function from the antenna to diode 1. The small-signal approximation for junction currents under zero bias is

$$I(V_j) \approx I_s + I_s \alpha V_j + \frac{I_s \alpha^2}{2} V_j^2. \quad (3)$$

The linear term of V_j represents the linear resistance and the square term of V_j contributes to rectification. Let us substitute (2) into the last term of (3). The dc current is

$$I_{1(j,DC)} = 2I_s \alpha^2 P_{in} R_a S_{ja1}^2. \quad (4)$$

This current can be represented by an equivalent current generator in parallel with the junction. The dc voltage produced by diode 1 across the load R_L (i.e., the supply voltage for the oscillator) can be expressed as

$$V_{1(DC)} = I_{1(j,DC)} \left(\frac{R_j R_L}{2R_j + R_L} \right). \quad (5)$$

Following the same procedure for diode 2 and adding the contribution from both diodes, the overall rectified dc voltage is given as

$$V_{DC} = \left(I_{1(j,DC)} + I_{2(j,DC)} \right) \left(\frac{R_j R_L}{2R_j + R_L} \right). \quad (6)$$

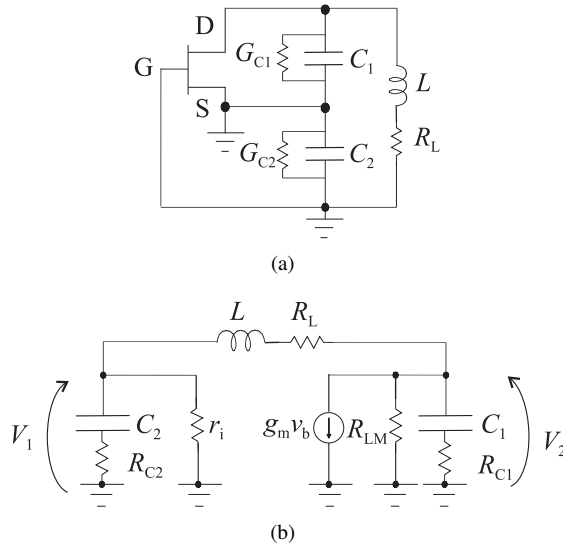


Fig. 3. (a) Colpitts oscillator circuit without bias network, (b) Small signal electrical equivalent circuit of Colpitts oscillator.

C. Oscillator

A Colpitts oscillator topology is selected because it is relatively simple to analyze and realize in practice with discrete components. However, other classical oscillator topologies could be used as well. Fig. 3(a) shows a Colpitts oscillator circuit without biasing network. In the proposed concept, sensing is realized in two ways: either replacing C_1 or C_2 with a capacitive sensor, or by connecting a capacitive sensitive element in parallel or in series with either of the capacitors. Possible losses of the sensing element can be embedded into G_{C1} or G_{C2} , depending on the chosen configuration.

Small-signal analysis is used to evaluate the startup condition and to calculate the oscillation frequency. Let us represent the Colpitts oscillator with the small-signal equivalent circuit shown in Fig. 3(b). In the following, we derive equations for the oscillation frequency, turn-on dc voltage, and output ac voltage.

1) *Oscillation Frequency and Condition:* The Kirchhoff equations for the two voltage nodes of the circuit in Fig. 3(b) can be expressed in matrix format as

$$[\mathbf{Y}][\mathbf{V}] = 0$$

in which

$$[\mathbf{Y}] = \begin{bmatrix} j\omega C_2 + G_{C2} + G_i + Y_L & -Y_L \\ g_m - Y_L & j\omega C_1 + G_{C1} + Y_L + G_{R_{LM}} \end{bmatrix}$$

$$[\mathbf{V}] = \begin{bmatrix} V_1 \\ V_2 \end{bmatrix}$$

where $G_i = r_i^{-1}$ is the transistor input admittance, g_m is the transconductance; and G_{C1} and G_{C2} represent the conductance of capacitors C_1 and C_2 respectively. The feedback inductor admittance is $Y_L = (j\omega L_1 + G_{R_L})^{-1}$, $G_{R_{LM}} = R_{LM}^{-1}$, where R_{LM} is the load resistance. Oscillation is possible when the determinant of the admittance matrix equals zero. By requiring this condition, we get an equation for the oscillation frequency

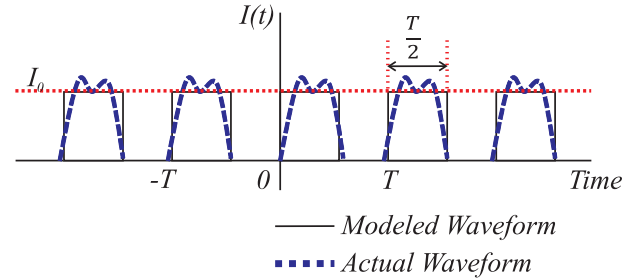


Fig. 4. Actual and modeled drain current waveform of the transistor [32].

$$f = \frac{1}{2\pi} \sqrt{\frac{G_i g_m G_{R_L} G_{R_{LM}} G_{C1} G_{C2}}{C_1 C_2 + C_1 L_1 + C_2 L_1}} \quad (7)$$

and an equation for the oscillation condition

$$\left[\frac{C_1 (G_i + G_{R_L} + G_{C2}) + C_2 (G_{R_{LM}} + G_{R_L} + G_{C2})}{(G_i + G_{R_{LM}} + g_m + G_{C1} + G_{C2})} \right] \geq L_1. \quad (8)$$

2) *Oscillator Output Voltage:* In the oscillator circuit, a capacitive feedback is formed by capacitors C_1 and C_2 . The output voltage of the oscillator is derived from the time-domain waveform of the drain current $I(t)$, as shown in Fig. 4. For output voltage calculation, the periodic drain current is modeled by a square wave with a period of $T_0 = 2\pi/\omega_0$ and amplitude I_0 . The maximum drain current occurs when the gate voltage reaches its peak value.

Assuming that the amplitude of the output oscillating signal is A , the maximum drain current $I_1(t)$ can be approximated when the transistor operates in the nonsaturated region [32]

$$I_0 = \mu_n C_{ox} \frac{W}{L} \left[(V_{DC} + A + nA - V_t)(V_{DC} - A) - \frac{1}{2}(V_{DC} - A)^2 \right] \quad (9)$$

where $n = (C_1 + C_2)/C_1$, μ_n is the mobility of electrons in the channel and the capacitance per unit area is denoted by C_{ox} . The MOSFET channel width, length and threshold voltage are expressed as W , L and V_t , respectively. From the Fourier series $I_1(t)$, the fundamental current component is given by

$$I_1(t)|_{fund} \approx \frac{2}{\pi} I_0 \sin(\omega_0 t) \quad (10)$$

and the fundamental voltage amplitude can be expressed as

$$A \approx \frac{2}{\pi} I_0 R_{LM}. \quad (11)$$

From (10) and (11), a simplified expression of the oscillator output voltage is given by

$$V_0 = \frac{V_{DC}}{2\pi} \left(\frac{1}{2} + \frac{C_2}{C_1} \right). \quad (12)$$

D. Modulator

The sensor utilizes the modulated backscattering principle for communication. Modulated backscattering is realized by applying the oscillator output voltage across the rectifier, which is also used as the modulator. The ac output voltage of the oscillator modulates the RF impedance of the rectifier. As a consequence, there are sidebands in the signal reflected from the sensor. Fig. 2 shows the electrical equivalent circuit that is used to derive an equation for the signal reflected from the sensor at one modulation sideband.

Unlike in rectification, non-linear junction capacitance contributes to the mixing due to higher frequencies considered, and it therefore needs to be taken into account. The voltage-dependent junction capacitance of the diode is given as

$$C_j(V_j) = \frac{C_{j0}}{\left(1 - \frac{V_j}{\Phi}\right)^\gamma} \quad (13)$$

where γ is the profile parameter for the depletion capacitance ($\gamma = 0.5$ for a uniformly doped junction), C_{j0} is junction capacitance at zero bias, V_j is the voltage across the junction, and Φ is junction potential. The small-signal approximation for the junction currents at zero bias is given as [23]

$$I(V_j) \approx 2 \left(\frac{1}{R_j} + j\omega C_{j0} \right) V_j + \left(\frac{\alpha}{R_j} + \frac{j\omega C_{j0} \gamma}{\Phi} \right) V_j^2. \quad (14)$$

The first term represents the current of a normal (voltage-independent) capacitor which does not produce any modulated voltages, whereas the second term generates mixing products. Hence, the modulated current (at the first side band) of the equivalent current generator for the Schottky diode is

$$I_{dM} = 2\sqrt{2P_{in}R_a}S_{ja}S_{jm}V_0 \left(\frac{\alpha}{R_j} + \frac{j\omega C_{j0}}{\Phi} \right) \cdot \left(\cos(\omega_0 - \omega_{osc})t + \cos(\omega_0 + \omega_{osc})t \right) \quad (15)$$

where $S_{jm} = (V_{jm1} + V_{jm2})/V_{osc}$, with V_{jm1} and V_{jm2} being the generated junction voltages due to the oscillator across diodes 1 and 2, respectively. For simplicity, we assume that $V_{jm1} = V_{jm2}$. Thus, I_{dM} is the non-linear term of (14). The oscillator output voltage is represented by a voltage source $V_{osc} = V_0 \cos(\omega_{osc}t)$, where V_0 is the oscillator ac output voltage amplitude and ω_{osc} is oscillator angular frequency. As can be seen from Fig. 2, Z_2 consists of an RF choke and a dc block capacitor. The voltage at $f = f_{RF} - f_{osc}$ across the radiation resistance (the backscattered signal at the lower side band) is

$$V_{dM} = 2\sqrt{2P_{in}R_a}S_{ja}S_{jm}V_0Z_N \cdot \left[\left(\frac{\alpha}{R_j} + \frac{j\omega C_{j0}}{\Phi} \right) \left(\cos(\omega_0 - \omega_{osc})t \right) \right] \quad (16)$$

where the impedance of the equivalent Norton current source is Z_N , shown in (17), at the top of the next page. The voltage reflected by the antenna can be expressed as

$$V_{ma} = S_{aM}V_{dM} \quad (18)$$

where S_{aM} is the voltage transfer function (equivalent to transmission coefficient S_{21} of a two-port device) between the equivalent source and the antenna.

E. Sensor Response at the Modulation Frequency

By substituting (12) and (16) into (18) we get an equation for the backscattered voltage of the sensor $V_{a,s}$ (across the antenna) at the lower side band as a function of input power

$$V_{a,s} = \frac{1}{\pi} \sqrt{2P_{in}R_a}S_{ja}S_{jm}Z_N I_{DC} \cdot \left[\left(I_{1(j,DC)} + I_{2(j,DC)} \right) \left(\frac{R_j R_L(V_{DC})}{2R_j + R_L(V_{DC})} \right) \right] \cdot \left[\left(\frac{1}{2} + \frac{C_2}{C_1 + C_S} \right) \left(\frac{\alpha}{R_j} + \frac{j\omega C_{j0}}{\Phi} \right) \left(\cos(\omega_0 - \omega_{osc})t \right) \right] \quad (19)$$

where R_L is the input resistance of the oscillator and C_S is the capacitance of the sensing element, which in this case is connected parallel to C_1 . Moreover, the resistance is assumed constant although in practice it depends on the voltage [33]. This result can be used to describe the backscattered power of the sensor and also to predict the sensor backward read-out distance.

F. Link Budget Calculation for Communication

Let us derive the read-out distance of the sensor. The power received by the sensor located at a distance r from the reader is

$$P_s = P_t G_r G_s \left(\frac{\lambda}{4\pi r} \right)^2 \quad (20)$$

where P_t is the power transmitted by the reader, G_r is the reader antenna gain (to the direction of the sensor), G_s is sensor antenna gain, and λ is the wavelength.

There are two conditions that must be fulfilled in order to read the sensor. First, the sensor must receive enough power to turn on its oscillator. Second, the response signal received by the reader must be above the noise floor of the receiver. These two conditions give two read-out ranges that can be distinct, forward (r_f) and backward (r_b) read-out ranges. The forward read-out range can be obtained by requiring that the power received by the sensor (as given in (20)) exceeds the threshold power (P_{th}) required to turn on the oscillator. Similarly, the backward read-out range is obtained by requiring that the signal received by the reader is above the noise floor. The read-out distance of the sensor is limited by the smaller one of the forward and backward distances and is given as

$$r = \min_{\substack{P_{th}=P_s \\ r_f=r_b}} \begin{cases} r_f = \frac{\lambda}{4\pi} \sqrt{\frac{P_t G_r G_s}{P_{th}}} & \text{(forward)} \\ r_b = \frac{\lambda}{4\pi} \sqrt{\frac{\Gamma_m P_{th} G_r G_s}{P_N}} & \text{(backward)} \end{cases} \quad (21)$$

$$Z_N = \frac{1}{j\omega C_j + \frac{1}{R_j} + \frac{1}{\frac{1}{j\omega C_j + \frac{1}{R_j}} + \frac{1}{j\omega C_p + \frac{1}{Z_2}} + \frac{1}{Z_a + \frac{1}{j\omega C_b}}} \quad (17)$$

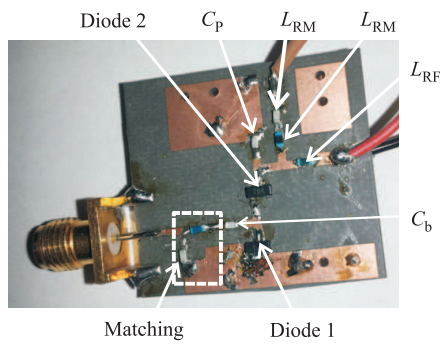


Fig. 5. Photograph of the prototype rectifier and modulator implemented with lumped elements soldered on the PCB.

where P_N is the receiver sensitivity, Γ_m is the modulation factor, and r_f and r_b represents the sensor forward and backward read-out distance, respectively.

III. EXPERIMENT AND SIMULATION

UHF RFID frequencies vary in each country depending on particular frequency regulations. The worldwide UHF RFID band is 860–960 MHz. In order to validate the theoretical model in practice, an example sensor design is fabricated and measured. The sensor is designed to operate at 844.5 MHz, and the purpose of the design is to validate the model used rather than to design a sensor for any particular application. Moreover, Schottky diodes are used as rectifier and modulator, and frequency has little influence on their rectification and mixing characteristics. Therefore, our experiment demonstrates the concept also in frequencies allocated to UHF RFID in different countries.

A. Rectifier and Modulator

The rectifier and modulator prototype is implemented with lumped circuit elements soldered on a printed circuit board made from Rogers RT/duroid 5870 substrate ($\epsilon_r = 2.33, \tan \delta = 0.0012$). Lumped capacitor and inductor are used as matching elements and two zero-bias Schottky diodes as the rectifier and modulator. Fig. 5 shows a photograph of the prototype.

The rectifier and modulator are simulated with ADS (ADS Design Environment, Santa Clara, CA., USA. [Online]. Available: <http://www.agilent.com/>) using the harmonic balance simulation. Fig. 6 shows the rectifier and modulator simulation model. In simulation, discrete passives are modeled with S-parameters provided by the component manufacturers. The component values used in the calculations, simulations and measurements are listed in Table II.

The rectified voltage is measured with a digital multimeter (HP 3478A) and the modulated voltage at one sideband

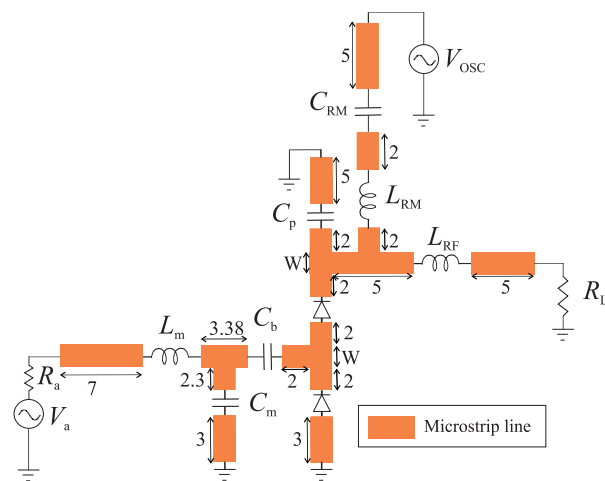


Fig. 6. Schematic circuit of the rectifier and modulator simulation model with microstrip lines. All microstrips have the same width, $W = 1$ and all dimensions are in millimeters.

TABLE II
PARAMETERS OF THE RECTIFIER AND MODULATOR USED IN CALCULATIONS, SIMULATIONS AND MEASUREMENTS

Antenna resistance	$R_a = 50 \Omega$
Matching inductor	$L_m = 27 \text{ nH}$
RF-block inductor	$L_{RF} = L_{RM} = 270 \text{ nH}$
Matching capacitor	$C_m = 6.8 \text{ pF}$
Coupling capacitor	$C_p = 68 \text{ pF}$
Blocking capacitor	$C_b = C_{RM} = 68 \text{ pF}$
Junction capacitance	$C_{j0} = 0.18 \text{ pF}$
Saturation current	$I_s = 50 \text{ nA}$
Ideality factor	$n = 1.08$
Series resistance	$R_s = 6 \Omega$
Junction grading coefficient	$M = 0.5 \Omega$
Load resistance	$R_L = 0.5 \text{ M}\Omega$
Continuous wave frequency	$f_1 = 844.5 \text{ MHz}$

is measured with a spectrum analyzer (Anritsu MS2683A). A continuous wave (CW) is generated by a signal generator (Rohde & Schwarz SML-03) and fed to the rectifier through a circulator (Voyantic V2.0). For testing purposes, the modulation voltage is generated with a signal generator (Rohde & Schwarz SML-06) at 150 kHz and fed to the oscillation port. The signal reflected from the modulator is distinguished with a circulator and detected by the spectrum analyzer receiver.

The simulated and measured rectified voltage as a function of input frequency is shown in Fig. 7. The input power is kept constant (19 dBm) and the load resistance is 240 k Ω . The simulated and measured curves align almost perfectly at frequencies below 840 MHz, but at higher frequencies the simulated voltage is approximately 10 μV higher than the

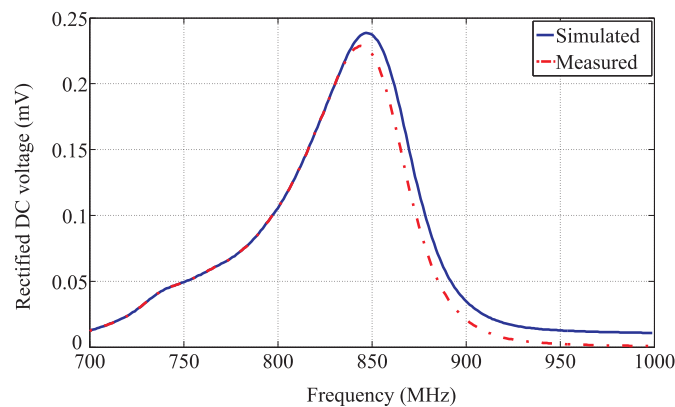


Fig. 7. Rectified DC voltage as a function of frequency. Input power to the rectifier is $P_{in} = -19$ dBm.

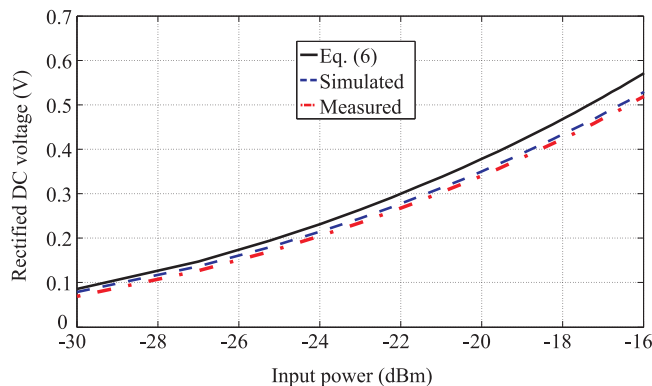


Fig. 8. Rectified DC voltage as a function of input power. The CW frequency is $f_{CW} = 844.5$ MHz.

measured one. The offset between the simulated and measured curves can be caused by, e.g., the accuracy of the printing and etching process, and component variations.

As can be seen in Fig. 7, the circuit exhibits the maximum dc voltage at the input frequency of 844.5 MHz. In general, non-linear properties of diodes change slowly with the frequency, but the peak is due to the matching between the diodes and antenna.

Fig. 8 shows the calculated, simulated, and measured dc voltage of the rectifier at different input power levels. The CW frequency is 844.5 MHz and the output load is $R_L = 0.5$ M Ω , which is the approximate input impedance of the oscillator obtained from the simulation. Fig. 8 exhibits a good agreement between the analytical, simulated and calculated curves at small power levels. The calculated curve would naturally fail at high power levels as the model is derived assuming small-signal analysis.

The backscattered power of the modulator is studied by sweeping the oscillation output voltage and keeping the oscillation frequency and input power constant. Fig. 9 shows the analytical, simulated and measured backscattered power of the modulator as a function of output voltage of the oscillator. The RF input power is -19 dBm and the oscillation frequency is 150 KHz. The simulated and measured curves agree well and they correspond to the analytical results. Note that small deviation in analytical results is due to component losses that

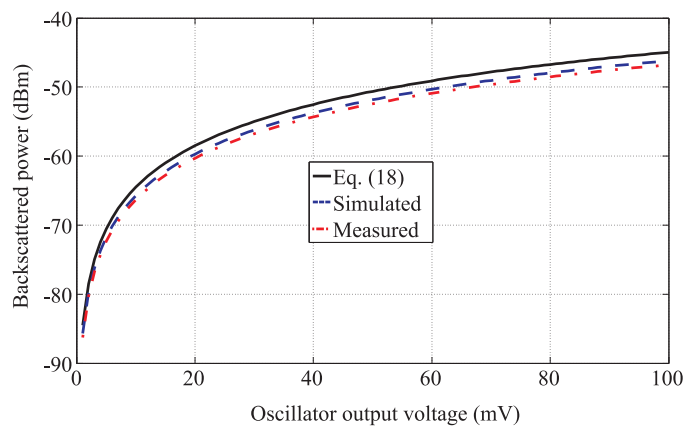


Fig. 9. Backscatter power as a function of oscillator output voltage at input power level of -19 dBm.

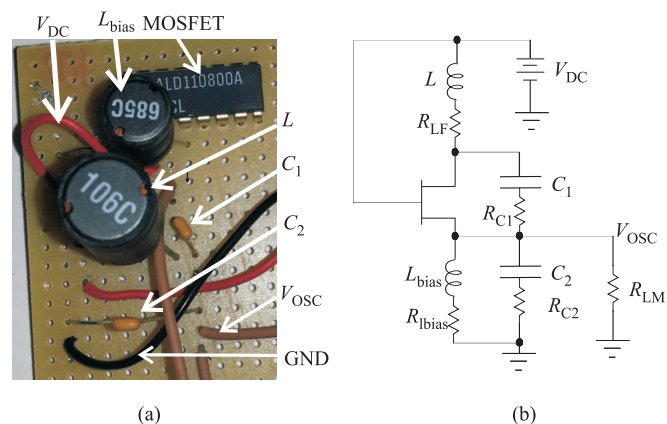


Fig. 10. (a) Photograph of the prototype oscillator implemented by using the BPS stripboard-3u. (b) Schematic circuit of the oscillator simulation model.

are neglected when deriving (18).

B. Oscillator

The oscillator circuit is implemented using the BPS stripboard-3u. An ALD110800A matched pair MOSFET transistor is used in the oscillator, whose prototype is shown in Fig. 10 (a).

Fig. 10 (b) represents a schematic circuit of the oscillator simulation model used in ADS. A SPICE (LEVEL 2) model is used for the transistor in the simulation. The component values used in the simulation, calculation and measurement are listed in Table III.

The oscillator is tested by measuring its output ac voltage and oscillation frequency as a function of input dc voltage supplied by an Agilent E3631A dc voltage supply, and the waveform is captured with an oscilloscope (Tektronix TDS 1002).

The calculated, simulated and measured oscillation frequency as a function of the input dc voltage is shown in Fig. 11. Calculations based on (7) agree well with the simulation, while the measured frequency is approximately 19% smaller. We assume that the deviation between the measured and simulated curves occurs because of the measurement probe used to measure the oscillation frequency. The probe is a

TABLE III
 PARAMETERS OF THE OSCILLATOR USED IN CALCULATIONS,
 SIMULATIONS AND MEASUREMENTS

Inductor	$L_1 = 10 \text{ mH}$
Series resistance of L_1	$R_{L1} = 7.3 \Omega$
Biassing inductor	$L_{\text{bias}} = 6.8 \text{ mH}$
Series resistance of L_{bias}	$R_{L_{\text{bias}}} = 13.5 \Omega$
Capacitor	$C_1 = 180 \text{ pF}$
Estimated series resistance of C_1	$R_{C1} = 136 \Omega$
Capacitor	$C_2 = 390 \text{ pF}$
Estimated series resistance of C_2	$R_{C2} = 182 \Omega$
Load resistance of	$R_{LM} = 0.5 \text{ M}\Omega$

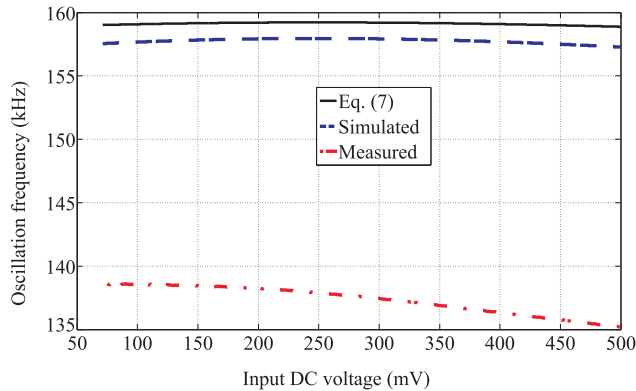


Fig. 11. The measured, simulated and calculated oscillation frequency as a function of input DC supply voltage.

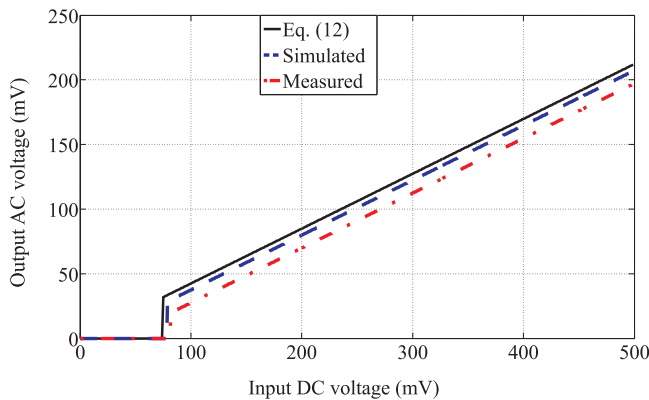


Fig. 12. The measured, simulated and calculated oscillator output voltage as a function of input DC supply voltage.

capacitive load to the oscillator, lowering its frequency. Note that the small deviation between the calculated and simulated frequency is due to parasitic capacitance of the MOSFET. An ideal MOSFET is assumed in the calculated curve.

Fig. 12 shows the measured, simulated, and calculated output voltage of the oscillator at different input dc voltages. According to analytical and simulated results, oscillation is sustained when the input dc voltage is above 69.3 mV and 73.7 mV, respectively, whereas the measured turn-on voltage is 82.6 mV. Thus, the oscillator turn-on voltage calculated according to (8) corresponds well to the simulated and measured turn-on voltages. Later, oscillator turn-on voltage will

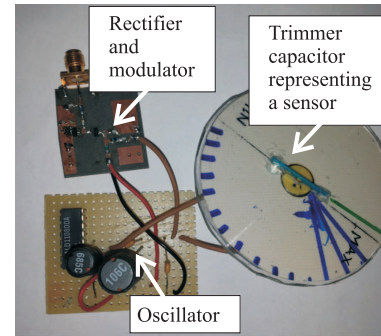


Fig. 13. Photograph of the sensor prototype where a trimmer capacitor is used to represent a sensor.

be considered as the threshold voltage of the sensor.

C. Sensor

The sensor prototype is implemented by integrating all three parts together. A photograph of the sensor prototype is shown in Fig. 13. We demonstrate passive wireless sensor functionality by adding in parallel to capacitor C_1 a trimmer capacitor, to represent a capacitive sensor element. The capacitance of the trimmer capacitor for different rotation angles is measured with an HP 4192A LF impedance analyzer .

In the wired measurement, the CW is generated with a signal generator and fed through a circulator to the sensor, like in the rectifier and modulator measurement. The reflected signal is separated with a circulator and fed to the spectrum analyzer.

In addition to the wired measurement, the sensor response is measured wirelessly. The wireless measurement is carried out with the Voyantic measurement system (Hand carry kit, Voyantic Ltd. [online]. <http://www.voyantic.com/>) and the sensor is connected to an ultra-wideband patch antenna (FXUB.66 maximus ultra-wide band antenna, Taoglas antenna solution. [Online]. Available: <http://www.taoglas.com/>). In the measurement, the reader antenna and sensor are placed 8 m away from each other. The Voyantic measurement system was controlled with a LabVIEW-based software.

As can be seen from (19), an analytical equation for the backscattered voltage is derived. However, the sensor read-out distance is always measured from the backscattered power. Thus, the backscattered voltage is converted into backscattered power using the well-known formula $P = V^2/R$. In this case, antenna impedance is matched to 50Ω . Measured and calculated backscattered power as a function of input power level are shown in Fig. 14. According to the calculation, the threshold power of the sensor is -28 dBm , whereas it is -22 dBm in the measurement. There is an offset between the measured and calculated curves, but they exhibit similar shape. This offset can be due to the input resistance of the oscillator. In the calculation, a $0.5 \text{ M}\Omega$ input resistance was assumed, although it is in reality voltage-dependent and may be different from the assumed value. In addition, the oscillator and modulator input impedances consist of capacitance, inductance and resistance. Complex impedance could be used as a load for better fit.

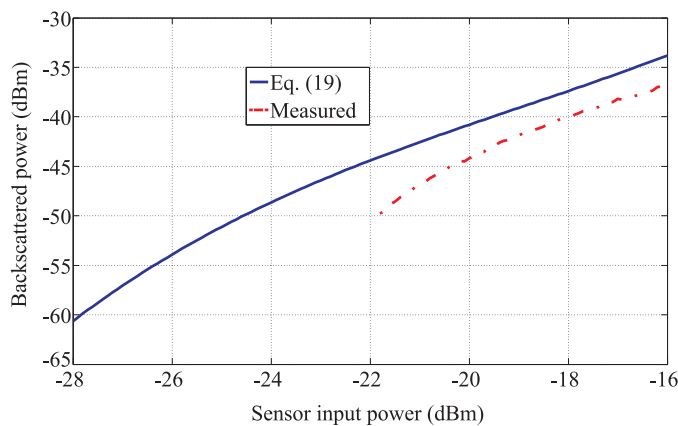


Fig. 14. Backscatter power responses of the sensor as a function of sensor input power levels.

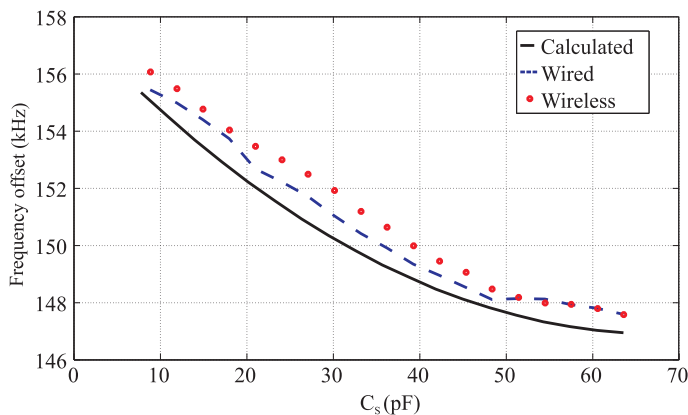


Fig. 15. Sensor oscillation frequencies of the sensor as a function of sensor capacitance.

The wired, wireless and calculated frequency offset is shown in Fig. 15. The slope of the calculated frequency offset slightly differs from the wired and wireless frequency offsets. The deviation is likely caused by the fact that the capacitance value of the trimmer capacitor may change when it is connected to the sensor circuit due to the other sensor components and parasitic effects. Note that the small deviation between wired and wireless measurement is due to the power level received by the sensor. In the wired measurement, the input power level is kept constant at -19 dBm, but in the wireless measurement, power received by the sensor varies due to interference and multipath propagation. In the present case, the corresponding power transmitted by the reader is approximately 27 dBm and the backscattered power received by the reader is -73 dBm. The read-out distance in the wireless experiment was 12 m. The corresponding link budget calculation is presented in Table IV.

Fig. 16 shows the received power in the sensor and in the reader. Sensor sensitivity or power-up threshold of -22 dBm and reader sensitivity of -75 dBm are also shown. In this measurement, sensor response is measured by sweeping the transmitted power of the reader antenna. During the measurement, the sensor is placed 30 cm away from the reader antenna in the anechoic chamber. Finally, sensor forward and

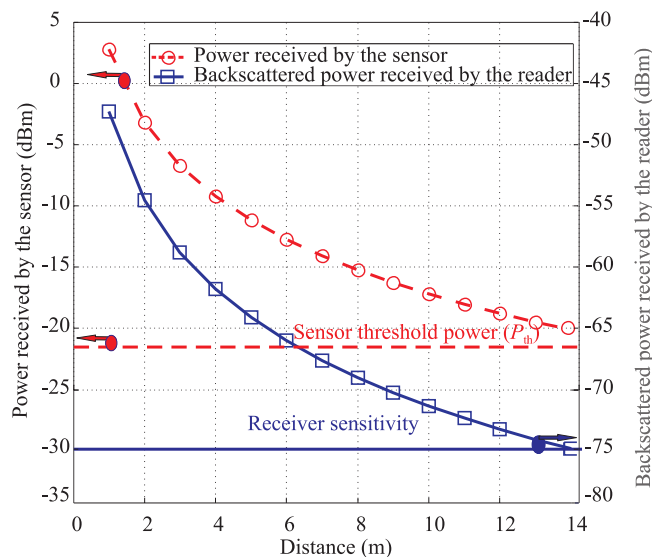


Fig. 16. Measured received power in the sensor and reader as a function of distance. The transmitted power of the reader antenna was 27 dBm.

TABLE IV
THE CALCULATED LINK BUDGET OF A WIRELESS RFID SENSOR INTERROGATED WITH A READER DEVICE ($f = 844.5$ MHz)

Transmitted power	$P_t = 27$ dBm
Reader antenna gain	$G_r = 7.7$ dB
Free space loss ($r = 12$ m)	$\left(\frac{\lambda}{4\pi r}\right)^2 = 52$ dB
Received power by the sensor	-19 dBm
(Threshold power for the sensor	$P_{th} = -22$ dBm)
Modulation loss	8.4 dB
Sensor antenna gain	$G_s = 2.7$ dBi
Received backscatter power by the reader	-73 dBm
(Receiver noise	-75 dBm)

backward read-out distance is calculated accordingly to (21). As can be seen from Fig. 16, when the prototype sensor is interrogated with a reader with 27 dBm transmit power, 7.7 dB antenna gain, and -75 dBm sensitivity, and when the sensor is equipped with an antenna whose gain is 2.7 dBi, the detection distance of the sensor would be 14 m at 844.5 MHz.

IV. CONCLUSION

This paper has discussed an approach that can be used to harness the sensing capabilities of RFID. The presented concept has been analyzed theoretically and verified with simulations and measurements. Moreover, theoretical equations for various properties of the sensor RF parts and oscillator are derived and realized in practice.

A passive wireless sensor using a trimmer capacitor as a sensing element is demonstrated to have a read-out distance of at least 14 m at 844.5 MHz. The experiments demonstrate a very good agreement between the measured and theoretically derived performance of the sensor. The given theoretical models can be applied to predict the sensor performance without carrying out any particular measurements.

Future work includes measuring the oscillator and modulator input impedance for optimizing the sensor performance. In addition, a sensor element will be attached to the oscillator circuit for measuring an external quantity and it will also be integrated into an existing RFID tag in order to enable the features of identification and the possibility to measure an external quantity with larger read-out distance.

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