Modeling of anisotropic two-dimensional materials monolayer HfS₂ and phosphorene metal-oxide semiconductor field effect transistors

[Jiwon Chang](http://aip.scitation.org/author/Chang%2C+Jiwon)

Citation: [J. Appl. Phys.](/loi/jap) **117**, 214502 (2015); doi: 10.1063/1.4921806 View online: <http://dx.doi.org/10.1063/1.4921806> View Table of Contents: <http://aip.scitation.org/toc/jap/117/21> Published by the [American Institute of Physics](http://aip.scitation.org/publisher/)

Modeling of anisotropic two-dimensional materials monolayer HfS₂ [and phosphorene metal-oxide semiconductor field effect transistors](http://dx.doi.org/10.1063/1.4921806)

Jiwon Chang

SEMATECH, 257 Fuller Rd #2200, Albany, New York 12203, USA

(Received 25 February 2015; accepted 16 May 2015; published online 1 June 2015)

Ballistic transport characteristics of metal-oxide semiconductor field effect transistors (MOSFETs) based on anisotropic two-dimensional materials monolayer HfS₂ and phosphorene are explored through quantum transport simulations. We focus on the effects of the channel crystal orientation and the channel length scaling on device performances. Especially, the role of degenerate conduction band (CB) valleys in monolayer $HfS₂$ is comprehensively analyzed. Benchmarking monolayer HfS2 with phosphorene MOSFETs, we predict that the effect of channel orientation on device performances is much weaker in monolayer $HfS₂$ than in phosphorene due to the degenerate CB valleys of monolayer HfS2. Our simulations also reveal that at 10 nm channel length scale, phosphorene MOSFETs outperform monolayer HfS₂ MOSFETs in terms of the on-state current. However, it is observed that monolayer HfS_2 MOSFETs may offer comparable, but a little bit degraded, device performances as compared with phosphorene MOSFETs at 5 nm channel length. © 2015 AIP Publishing LLC. [[http://dx.doi.org/10.1063/1.4921806\]](http://dx.doi.org/10.1063/1.4921806)

I. INTRODUCTION

Recent years have witnessed an upsurge of interest in two-dimensional (2-D) layered materials, particularly transition metal dichalcogenides (TMDs). Since the thickness of TMDs can be pushed down to less than a few nanometers, TMDs are promising alternative channel materials for realizing ultra-thin body metal-oxide-semiconductor field effect transistors (MOSFETs) which are robust to short channel effects. First experimental demonstration of monolayer $MoS₂ MOSFETs$ suggested the possibility of mono-layer TMDs for the electronic device application^{[1](#page-6-0)} and ignited extensive following researches on Mo- and W-based TMDs, such as MoS_2 , $MoSe_2$, $MoTe_2$, WS_2 , and WSe_2 .²⁻⁵ Continuous efforts to seek for other 2-D materials lead to the discovery of anisotropic 2-D material phosphorene.^{[6](#page-6-0)} Unlike Mo- and W-based TMDs, phosphorene is characterized with its highly anisotropic band structures. $6-8$ Theoretical investigations of phosphorene MOSFETs have been performed^{[9,10](#page-6-0)} and reported that the unique anisotropy of phosphorene band structures is advantageous to improve ballistic device performances over the nearly isotropic 2-D material monolayer $MoS₂ MOSFETs$. Very recently, anisotropic 2-D materials other than phosphorene, Hf- and Zrbased TMDs have been explored by density functional theory (DFT) calculations.^{[11,12](#page-6-0)} Monolayers of Hf- and Zrbased TMDs, such as HfS_2 , $HfSe_2$, ZrS_2 , and $ZrSe_2$, are quite different with those of Mo- and W- based TMDs because of different crystal symmetries and atomic bond-ings.^{[11](#page-6-0),[12](#page-6-0)} They have indirect band gaps with three-fold degenerate conduction band (CB) valleys whose dispersions around the minimum of CB are highly anisotropic. Among Hf- and Zr-based TMDs, monolayer $HfS₂$ and monolayer $ZrS₂$ exhibit sizable band gaps, larger than 1 eV, suitable for MOSFETs applications. $11,12$ In this work, we present a comprehensive computational study of monolayer $HfS₂$

MOSFETs through the ballistic quantum transport simulations. We discuss the influence of band structure anisotropy of monolayer $HfS₂$ on device performances as well as the scaling behavior of monolayer HfS₂ MOSFETs. Simulations of phosphorene MOSFETs with the same device geometry and biasing conditions are also carried out to benchmark key performance metrics with monolayer $HfS₂ MOSFETs.$

II. COMPUTATIONAL APPROACH

Figure $1(a)$ shows the top view of monolayer HfS₂ atomic structure and the corresponding 1st Brillouin Zone (BZ). Similar to MoS_2 , HfS_2 is a layered material composed of vertically stacked S-Hf-S layers through van der Waals forces. Each single S-Hf-S layer consists of two hexagonal planes of S atoms and an intermediated hexagonal plane of Hf atoms interacting through ionic-covalent interactions. Difference between $MoS₂$ and $HfS₂$ is that hexagonal planes of S are A-A stacked in MoS_2 , while A-B stacked in HfS_2 . Electronic structure calculations and structure optimization of monolayer HfS_2 were performed through DFT calculations by \widehat{OPENMX}^{13} using the linear combination of pseudoatomic orbital method with the local density approximation (LDA) to describe the exchange-correla-tion.^{[14](#page-6-0)} We constructed monolayer HfS₂ structure by using the in-plain lattice parameter $(a = 3.622 \text{ Å})$ from experi-ments^{[15](#page-6-0)} since it was reported that 2-D materials remain very close to their $3-D$ parents.^{[16](#page-6-0)} We fixed the in-plane lattice value and relaxed atoms with a force tolerance of 0.001 Hartree/Bohr. The cutoff energy of 300 Ry and $7 \times 7 \times 1$ kmesh for the BZ integrations were used for the structure optimization as well as for band structure calculations. Similar to the other plane wave based DFT calculation, $11,12$ we found the highly anisotropic CB with its minimum located at the M-point as in Figure $1(d)$. The calculated indirect band gap is 1.13 eV in close agreement with the previous theoretical

FIG. 1. (a) Top view of monolayer HfS_2 showing a primitive hexagonal unit cell and corresponding 1st BZ with high symmetric points. Three lowest conduction band valleys are located at three M points. (b) Electron effective masses along two transport directions for each valley in (a). (c) Schematic of simulated device structure. The nominal device parameters are as follows: HfO₂ (κ = 25) gate oxide thickness = 3 nm; channel length L_{Ch} = 5, 7, and 10 nm; n-type doping density of source and drain = 2×10^{13} cm⁻²; and SiO₂ oxide thickness = 10 nm. Band structures of (d) monolayer HfS_2 and (e) phosphorene from DFT (solid lines) and from TB Hamiltonian (squares) along the high symmetric points in the hexagonal BZ and rectangular BZ, respectively.

prediction.^{[11](#page-6-0)} Experimental verification of band gap has not been demonstrated yet. However, a band gap size has limited effects on the MOSFETs simulation since both CB and valence band (VB) are considered simultaneously only in the calculation of gate-induced drain leakage (GIDL) current which has not been observed in the simulated range of V_{GS} in this work. There are three CB valleys in the 1st BZ of monolayer HfS₂ as seen in Figure $1(a)$. We extracted electron effective masses of three valleys in two directions M $(\Gamma \rightarrow M)$ and K $(\Gamma \rightarrow K)$ by parabolic fitting of the band structure and listed them in Figure $1(b)$. In the direction M, valley 1 has a very heavy effective mass $m_e^* = 3.05 \times m_e$ with a light transverse effective mass leading to a low density of states (DOS), while the other two valleys have light ones $m_e^* = 0.35 \times m_e$. In the other direction K, valley 2 exhibits a light effective mass $m_e^* = 0.25 \times m_e$ with a heavy transverse effective mass for a high DOS and the other two valleys show heavier effective masses $m_e^* = 0.81 \times m_e$.

In the simulated device structure shown in Figure $1(c)$, monolayer HfS₂ with a dielectric constant $\kappa = 6.2^{17}$ was used as a channel material with the 3 nm HfO₂ ($\kappa = 25$) gate oxide and the 10 nm SiO_2 ($\kappa = 3.9$) substrate. Semi-infinite source and drain were n-type doped with a doping concentration of 2×10^{13} /cm². We varied the channel length L_{Ch} from 10 to 5 nm to examine scaling behavior. To describe electronic transport through monolayer HfS_2 , we performed selfconsistent ballistic quantum transport simulations using tightbinding (TB) hopping potentials. TB potentials were obtained through maximally localized Wannier functions (MLWFs) calculated directly from the DFT Kohn-Sham orbitals and potential using OPENMX.¹⁸ As in Figure 1(d), TB potentials accurately reproduce original DFT band structures. We used the scattering matrix approach to propagate injected eigenmo-des from semi-infinite source and drain through the device.^{[19](#page-6-0)} Transport equations were solved iteratively together with Poisson's equation for the self-consistency between the charge density and the electrostatic potential. The total current was calculated within the Landauer–Büttiker formalism. Two transport directions $M (\Gamma \rightarrow M)$ and $K (\Gamma \rightarrow K)$ of monolayer $HfS₂$ in Figure 1(a) were investigated in the quantum transport simulations. We benchmarked performances of monolayer HfS₂ MOSFETs with those of phosphorene MOSFETs in the same device structure and biasing conditions. TB Hamiltonian of phosphorene used in our previous modeling work²⁰ was adopted for quantum transport calculations. We considered two transport directions X ($\Gamma \rightarrow X$) and Y ($\Gamma \rightarrow Y$) for light and heavy effective masses $m_e^* = 0.115 \times m_e$ and m_e^* $= 1.17 \times m_e$ in phosphorene, respectively, in Figure 1(e).

III. RESULTS AND DISCUSSION

We analyze simulation results in terms of effective masses in both transport and transverse directions. Even if the effective mass becomes irrelevant to describe the ballistic conductance between two terminals within the Landauer-Büttiker formalism, understanding MOSFETs performances with the effective mass is still valid as the current is controlled by the gate via self-consistent electrostatic field. Simulation results of 10, 7, and 5 nm channel length monolayer $HfS₂$ and phosphorene MOSFETs are presented in Figures [2](#page-3-0) and [3](#page-3-0). Figure [2\(a\)](#page-3-0) compares transfer characteristics of monolayer $HfS₂$ and phosphorene MOSFETs with 10 nm channel length in different transport directions at $V_{DS} = 0.5$ V. We adjusted V_{GS} such that the off-state ($V_{DS} = 0.5$ V and $V_{GS} = 0$ V) current I_{OFF} is 100 nA/ μ m according to the ITRS requirement for high performance logic devices. 21 For both monolayer HfS₂ and phosphorene MOSFETs, irrespective of transport directions, good subthreshold behavior and limited shortchannel effects are observed in 10 nm channel length device. Subthreshold slope (SS) and drain-induced barrier lowering (DIBL) are estimated to be \sim 70 mV/dec and \sim 50 mV/V, respectively, in Figures $3(a)$ and $3(b)$. As V_{GS} increases, transfer characteristics start to show dependency on the channel material and the transport direction. At the on-state $(V_{DS} = V_{GS} = 0.5 \text{ V})$, phosphorene MOSFETs offer the largest on-state current $I_{ON} \sim 3000 \mu A/\mu m$ in X-direction due to the light transport effective mass and the heavy transverse effective mass while the smallest $I_{ON} \sim 1200 \,\mu\text{A}/\mu\text{m}$ in Y-direction because of the heavy transport and light transverse effective masses as similar to the previous reports. $9,10$ In monolayer $HfS₂ MOSFETs, both transport directions *M* and *K* result in$ the quite similar level of I_{ON} . I_{ON} in *M*-direction is slightly higher (\sim 1820 μ A/ μ m) than in K-direction (\sim 1730 μ A/ μ m). Compared with phosphorene MOSFETs, a very narrow range of I_{ON} (1730 \sim 1820 μ A/ μ m) depending on the channel crystal orientation is obtainable in monolayer $HfS₂$, while a much wider range $(1200 \sim 3000 \,\mu\text{A}/\mu\text{m})$ in phosphorene. I_{ON} in monolayer HfS_2 reaches roughly 57–60% of the maximum I_{ON} in phosphorene MOSFETs. Therefore, as shown from the plot of I_{ON} as a function of $I_{\text{ON}}/I_{\text{OFF}}$ ratio in Figure [3\(c\),](#page-3-0) we can achieve a larger I_{ON} in phosphorene than in monolayer

FIG. 2. Transfer characteristics of (a) 10 nm and (b) 5 nm channel length monolayer HfS₂ and phosphorene MOSFETs in different transport directions at $V_{GS} = 0.5$ V.

 $HfS₂$ with the same I_{ON}/I_{OFF} ratio and similar subthreshold characteristics ((Figures $3(a)$ and $3(b)$) by properly adjusting the channel crystal orientation.

For deeper understanding of the current transport, we plot CB edge profiles and corresponding energy resolved current densities for 10 nm channel length MOSFETs at the on-state in Figure [4.](#page-4-0) In the plots, the source and drain Fermi levels are indicated by E_{FS} and E_{FD} , respectively. For the given source and drain doping concentration of 2×10^{13} /cm², positions of E_{FS} and E_{FD} relative to CB edge are higher in phosphorene than in monolayer HfS_2 since monolayer HfS_2 has three degenerate valleys with the heavier effective masses compared to only one valley with the relatively lighter effective masses in phosphorene. From

FIG. 3. (a) SS, (b) DIBL, and (c) I_{ON} vs. I_{ON}/I_{OFF} ratio of monolayer HfS₂ and phosphorene MOSFETs for different channel lengths in different transport directions.

Figures $4(a)$ and $4(b)$, phosphorene MOSFETs in X-direction provide improved I_{ON} because the light effective mass enhances the injection velocity and the heavy transverse mass leads to the high DOS as discussed above. $9,10$ For monolayer $HfS₂$, the current density originating from each valley in Figure $1(a)$ is shown separately in Figures $4(c)$ and [4\(d\).](#page-4-0) We consider current up to 500 meV above E_{FS} because current becomes negligibly small and it becomes difficult to discriminate one valley to the others above that energy level. From Figure $4(c)$, current from valley 1 is relatively small compared to those from the other valleys due to its heavy effective mass $m_e^* = 3.05 \times m_e$ (Figure [1\(b\)](#page-2-0)). The contribution of each valley current to I_{ON} is more clearly seen in Figure [5](#page-4-0). Only about 13.7% of I_{ON} comes from valley 1 and the rest originates from valleys 2 and 3. As we change the transport direction from M to K , the transport effective mass for valley 1 is significantly reduced, but still heavy (m_e^*) $= 0.81 \times m_e$, while that for valley 3 increases and eventually becomes same with the value of valley 1 as in Figure $1(b)$. For valley 2 in K-direction, on the other hand, it is aligned to have the lightest transport and the heaviest transverse effective masses in monolayer $HfS₂$ CB as explained in Figure $1(a)$. Therefore, valley 2 contributes the most among three valleys to I_{ON} in the current density plots of Figure $4(d)$. As observed in Figure [5,](#page-4-0) the valley 2 current accounts for the biggest portion of I_{ON} (more than 58%), while valleys 1 and 3 provide about 21% of I_{ON} for each. From comparing components of I_{ON} between transport directions M and K , the valley 3 current in M -direction is lowered by more than half in K -direction owing to increasing transport and decreasing transverse effective masses. On the other hand, the valleys 1 and 2 current boost more than 50% and 36%, respectively, in K-direction because of lighter transport and heavier transverse effective masses. As a result, even though the current from valley 3 decreases, the amount of current increase from the other valleys, particularly valley 2, compensates the decrease, thereby resulting in the slight increase in overall I_{ON} in the transport direction K. Unlike phosphorene, even if monolayer $HfS₂$ has anisotropic CB, it does not exhibit high dependency of device performances on the orientation of channel material in 10 nm channel length scale due to its degenerate CB valleys.

FIG. 4. CB edge profiles and corresponding energy resolved current densities for 10 nm channel length phosphorene MOSFETs in transport directions (a) $\Gamma \rightarrow X$ and (b) $\Gamma \rightarrow Y$ and for 10 nm channel length monolayer HfS2 MOSFETs in transport directions (c) $\Gamma \rightarrow M$ and (d) $\Gamma \rightarrow K$ at $V_{GS} = 0.5$ V. E_{FS} and E_{FD} represent Fermi levels in the source and drain, respectively. Current density up to 500 meV above the source Fermi level from each valley of monolayer $HfS₂$ in Figure $1(a)$ is plotted in log (left) and linear (right) scales in (c) and (d).

To examine the scaling limit of monolayer $HfS₂$ and phosphorene MOSFETs, we simulated 7 and 5 nm channel length devices with the same other device parameters under the same biasing conditions. Figures $3(a)$ and $3(b)$ show the scaling behavior of SS and DIBL for monolayer $HfS₂$ and phosphorene MOSFETs. As the channel length scales down, subthreshold characteristics are substantially degraded due to the short channel effects as well as the source-to-drain direct tunneling. Below 10 nm channel length scale, the choice of channel material and the transport direction affects subthreshold characteristics as summarized in Figures $3(a)$ and $3(b)$. In phosphorene, at 5 nm, SS and DIBL increase up to 125 mV/dec and 206 mV/V in X-direction while up to 82 mV/dec and 127 mV/V in Y-direction. Degradation in the transport direction Y is suppressed due to the heavy effective mass that effectively blocks the source-to-drain direct tun-neling as discussed in the previous work.^{[10](#page-6-0)} Monolayer HfS_2 MOSFETs also exhibit significant degradations of SS and DIBL. At 5 nm channel length scale, SS of 87–94 mV/dec

FIG. 5. Components of current in 10 nm channel length monolayer $HfS₂$ MOSFETs at $V_{GS} = 0.5$ V for different transport directions. Red, green, and blue bars represent the portions of total current for three valleys in Figure [1\(a\),](#page-2-0) respectively.

and DIBL of $165 \sim 172$ mV/V are observed, depending on the transport direction. Degradation is slightly more severe in the transport direction K than in M . In comparison with phosphorene MOSFETs, the transport directional dependency of subthreshold characteristics is not substantial in monolayer HfS_2 MOSFETs because of degenerate CB valleys as indicated from Figures [3\(b\)](#page-3-0) and [3\(c\).](#page-3-0) DIBL and SS of monolayer HfS₂ MOSFETs lie somewhere between the maximum and minimum values of phosphorene MOSFETs. SS is managed better in monolayer HfS_2 than in phosphorene aligned in X-direction, suggesting the reduced source-todirect tunneling. Transfer characteristics of 5 nm channel length devices at $V_{DS} = 0.5 \text{ V}$ are plotted in Figure [2\(b\)](#page-3-0). Here, V_{GS} is not adjusted to yield I_{OFF} of 100 nA/ μ m at $V_{GS} = 0$ V. Instead, we used the same V_{GS} range applied in 10 nm channel length device to check the threshold voltage V_T roll-off. As discussed above, the highest subthreshold current and the most severe V_T roll-off are exhibited for phosphorene in the transport direction X because of the largest SS in Figure $3(a)$. Similar to 10 nm channel length device, a wide range of I_{ON} is achievable in phosphorene via tuning the transport direction, while changing transport direction does not have a critical influence on I_{ON} in monolayer HfS₂. Compared with 10 nm device, I_{ON} improves by nearly 60% and 35% in X- and Y-directions, respectively, in phosphorene and reaches \sim 4800 μ A/ μ m and 1615 μ A/ μ m. In monolayer HfS₂, roughly 40% more I_{ON} is obtained for both M- and Kdirections at the on-state. This enhancement of I_{ON} in 5 nm channel length device is attributed not only to the electrostatic short channel effects but also to the source-to-drain direct tunneling. Especially for phosphorene MOSFETs in X-direction, the source-to-drain tunneling current becomes considerable because of the light transport effective mass, thereby leading to the biggest increase in I_{ON} .^{[10](#page-6-0)} Even though monolayer $HfS₂$ has at least one valley with a light effective mass in both M - and K -directions (Figure [1\(b\)](#page-2-0)), valley with the light effective mass does not result in the significant degradation of device performances in overall as for phosphorene in X-direction.

FIG. 6. CB edge profiles and corresponding energy resolved current densities for 5 nm channel length phosphorene MOSFETs at $V_{GS} = 0$ V in transport directions (a) $\Gamma \rightarrow X$ and (b) $\Gamma \rightarrow Y$. CB edge profiles and corresponding energy resolved current densities for 5 nm channel length monolayer HfS₂ MOSFETs at $V_{GS} = 0$ V in transport directions (c) $\Gamma \rightarrow M$ and (d) $\Gamma \rightarrow K$ and at $V_{GS} = -0.2 \text{ V}$ in transport directions (e) $\Gamma \rightarrow M$ and (f) $\Gamma \rightarrow K$. E_{FS} and E_{FD} represent Fermi levels in the source and drain, respectively. source and drain, respectively. Current density up to 500 meV above the source Fermi Level from each valley of monolayer HfS_2 in Figure [1\(a\)](#page-2-0) is plotted in log (left) and linear $(right)$ scales in (c) – (f) .

To further evaluate subthreshold characteristics, CB edge profiles and corresponding energy resolved current densities for 5 nm channel length devices at the off-state $(V_{GS} = 0 V)$ and at $V_{GS} = -0.2 V$ are shown in Figure 6. Figures $6(a)$ and $6(b)$ explicitly present the difference of source-to-drain tunneling current between X- and Y-directions of phosphorene MOSFETs at the off-state. Phosphorene directed in Y-direction suppresses the tunneling current more efficiently than in X-direction, offering the best subthreshold device performances in Figures [2\(b\),](#page-3-0) [3\(a\),](#page-3-0) and [3\(b\)](#page-3-0). I_{OFF} of monolayer $HfS₂$ is investigated through calculating the current density from each valley in Figures $6(c)$ and $6(d)$. Figure $7(a)$ shows the portions of I_{OFF} for source-to-drain tunneling (TN) and thermionic emission (TE) currents as well as the contribution of each valley to TN and TE currents. From Figure $7(a)$, TN (grey solid) and TE (grey striped) currents supply about 60% and 40% of I_{OFF} in *M*-direction, respectively. Valley 1 (red) contributes the smallest amount of both TN and TE currents among three valleys because its heavy effective mass $m_e^* = 3.05 \times m_e$ in Figure [1\(b\)](#page-2-0) lowers the tunneling efficiency and the injection velocity as well. If we change the transport direction from M to K , TN current increases by roughly 20%, while only about 8% boost in TE current is observed as seen in Figure $7(a)$. As a result, overall 15% more I_{OFF} is observed in K-direction. As discussed in 10 nm device, aligning monolayer HfS_2 in K-direction puts valley 2 in the optimal orientation for the maximum current (the lightest transport and heaviest transverse effective masses). Therefore, the major contributor to I_{OFF} in K-direction is from valley 2 (green) whose components of TN and TE currents are as high as 87% and 55%, respectively, in Figure $7(a)$. Figure $6(d)$ also confirms that the valley 2 current is dominant, especially for TN current below the top of the potential barrier. The valley 1 current (red) also increases, but remains relatively small because the transport effective mass is still heavy ($m_e^* = 0.81 \times m_e$) in K-direction. On the other hand, valley 3 (blue) provides less current in K-direction due to the heavier transport effective mass than in M-direction. Essentially, the same as the discussion of the onstate current in 10 nm channel length HfS₂ MOSFETs, the three-fold valley degeneracy of CB in monolayer $HfS₂$ diminishes the impact of channel orientation on device

FIG. 7. Components of current in 5 nm channel length monolayer HfS_2 MOSFETs at $V_{GS} = (a) 0.0 V$ and $(b)-0.2 V$ for different transport directions. Grey solid and striped bars correspond to the portions of total current for TN and TE currents, respectively. Red, green, and blue bars represent the portions of TN current or TE current for each valley in Figure $1(a)$.

performances in the subthreshold regime. However, at 5 nm scale, where the quantum mechanical source-to-drain tunneling occurs, changing transport direction starts to make a slight difference. From Figure $7(a)$, I_{OFF} is 15% more in the transport direction K mainly due to the increase in TN current from valley 2. This distinction between M - and K -directions becomes clearer at lower V_{GS} , where TN current takes up the subthreshold current more dominantly. Figures $6(e)$ and [6\(f\)](#page-5-0) are CB edges and current densities of monolayer $HfS₂$ MOSFETs for M - and K-directions, respectively, at $V_{GS} = -0.2$ V. TN currents from valley 1 for the transport direction M and valleys 1 and 3 for the transport direction K are well suppressed because of the heavy transport effective masses. On the other hand, valleys 2 and 3 constitute most TN current in Figure $6(e)$, while only valley 1 supplies most of it as in Figure [6\(f\).](#page-5-0) However, the amount of TN current from valley1 in K-direction exceeds the sum of current from valleys 2 and 3 in M-direction. Components of current at $V_{GS} = -0.2$ V are further analyzed in Figure [7\(b\)](#page-5-0), indicating a bigger relative difference of current between M- and Kdirections than at the off-state (V_{GS} = 0 V) in Figure [7\(a\).](#page-5-0) Comparing Figure $7(a)$ with $7(b)$ reveals that changing transport direction from M to K enhances TN current by about 82% at $V_{GS} = -0.2$ V compared with 20% at $V_{GS} = 0$ V, and TE current by about 20% at $V_{GS} = -0.2$ V compared with 8% at $V_{GS} = 0$ V. This significant increase in TN current in Kdirection at $V_{GS} = -0.2 V$ is mainly attributed to the huge boost of TN current from valley 2 as shown in Figure [7\(b\).](#page-5-0) Monolayer HfS_2 directed in K-direction becomes more vulnerable to the source-to-drain direct tunneling since valley 2 aligned for the lightest transport and the heaviest transverse masses maximizes the tunneling efficiency. Therefore, subthreshold device performances of monolayer $HfS₂$ are further degraded when monolayer HfS_2 is oriented to K-direction, hence creating a difference in the subthreshold current between two transport directions. We can expect an even stronger dependency of device performances of monolayer $HfS₂ MOSFETs$ on the transport direction below 5 nm channel length scale where the source-to-drain direct tunneling has more sizable impact.

IV. CONCLUSION

We examined device performances of MOSFETs based on the anisotropic 2-D material monolayer $HfS₂$ through the ballistic quantum transport simulations. The dependency of device performances on the transport direction and the scaling behavior were assessed and benchmarked with phosphorene MOSFETs. At 10 nm channel length scale, both monolayer HfS₂ and phosphorene MOSFETs offer excellent subthreshold characteristics regardless of the transport direction. At the on-state, however, phosphorene can provide much higher level of I_{ON} than monolayer HfS₂ when the light effective mass direction is used for the transport direction in phosphorene MOSFETs. Even though both monolayer $HfS₂$ and phosphorene have anisotropic band structures, improving device performances through tuning the channel crystal orientation is expected only in phosphorene because three-fold degenerate CB valleys in monolayer $HfS₂$ reduce the effect of channel orientation. As the channel length is scaled down, the most substantial degradation of subthreshold characteristics is observed in phosphorene MOSFETs aligned in the light transport effective mass direction since it suffers from the severe source-to-drain direct tunneling. On the other hand, directing phosphorene to the heavy effective direction yields the best subthreshold characteristics, and comparable, but slightly more degraded, subthreshold characteristics are achievable with monolayer $HfS₂$. Below 5 nm channel length, in the presence of sourceto-drain direct tunneling, monolayer $HfS₂$ starts to exhibit the channel orientation dependency in the subthreshold regime because the source-to-drain tunneling drastically increases or decreases, depending on the transport effective mass of each valley in monolayer $HfS₂$.

- ¹B. Radisavljevic, A. Radenovic, J. Brivio, V. Giacometti, and A. Kis, [Nat.](http://dx.doi.org/10.1038/nnano.2010.279) [Nanotechnol.](http://dx.doi.org/10.1038/nnano.2010.279) 6, 147-150 (2011).
- ²S. Larentis, B. Fallahazad, and E. Tutuc, [Appl. Phys. Lett.](http://dx.doi.org/10.1063/1.4768218) 101, 223104 (2012).
- 3 N. R. Pradhan, D. Rhodes, S. Feng, Y. Xin, S. Memaran, B.-H. Moon, H. Terrones, M. Terrones, and L. Balicas, [ACS Nano](http://dx.doi.org/10.1021/nn501013c) ⁸, 5911 (2014). ⁴
- ⁴W. S. Hwang, M. Remskar, R. Yan, V. Protasenko, K. Tahy, S. D. Chae, P. Zhao, A. Konar, H. G. Xing, A. Seabaugh, and D. Jena, [Appl. Phys.](http://dx.doi.org/10.1063/1.4732522) [Lett.](http://dx.doi.org/10.1063/1.4732522) 101, 013107 (2012).
- ⁵H. Fang, S. Chuang, T. C. Chang, K. Takei, T. Takahashi, and A. Javey, [Nano Lett.](http://dx.doi.org/10.1021/nl301702r) 12(7), 3788-3792 (2012).
- H. Liu, A. T. Neal, Z. Zhu, Z. Luo, X. Xu, D. Tománek, and P. D. Ye, [ACS Nano](http://dx.doi.org/10.1021/nn501226z) ⁸, 4033 (2014). ⁷
- $7Y.$ Cai, G. Zhang, and Y. Zhang, [Sci. Rep.](http://dx.doi.org/10.1038/srep06677) 4, 6677 (2014).
- ⁸J. Qiao, X. Kong, Z.-X. Hu, F. Yang, and W. Ji, [Nat. Commun.](http://dx.doi.org/10.1038/ncomms5475) 5, 4475 (2014).
- 9 K. Lam, Z. Dong, and J. Guo, IEEE Electron Device Lett. 35, 963 (2014).
- ¹⁰F. Liu, Y. Wang, X. Liu, J. Wang, and H. Guo, [IEEE Trans. Electron](http://dx.doi.org/10.1109/TED.2014.2353213)
- [Devices](http://dx.doi.org/10.1109/TED.2014.2353213) 61, 3871 (2014). 11C. Gong, H. Zhang, W. Wang, L. Colombo, R. M. Wallace, and K. Cho,
-
-
-
- [Appl. Phys. Lett.](http://dx.doi.org/10.1063/1.4817409) 103, 053513 (2013).
¹²W. Zhang, Z. Huang, W. Zhang, and Y. Li, [Nano Res.](http://dx.doi.org/10.1007/s12274-014-0532-x) 7, 1731 (2014).
¹³T. Ozaki and H. Kino, [Phys. Rev. B](http://dx.doi.org/10.1103/PhysRevB.45.13244) **72**, 045121 (2005).
¹⁴J. P. Perdew and Y. Wang, Phys. Rev. B **45**, 132 [Materials](http://dx.doi.org/10.1007/10681735_209), Semiconductors' of Landolt-Börnstein - Group III Condensed Matter Vol. 41, edited by O. Madelung, U. Rössler, and M. Schulz (Springer, Berlin, Heidelberg, 2000), Vol. 41D, pp. 1–6. ¹⁶K. Novoselov, D. Jiang, F. Schedin, T. Booth, V. Khotkevich, S.
- Morozov, and A. Geim, [Proc. Natl. Acad. Sci. U.S.A.](http://dx.doi.org/10.1073/pnas.0502848102) 102, 10451 (2005).
¹⁷G. Lucovsky and R. M. White, [Phys. Rev. B](http://dx.doi.org/10.1103/PhysRevB.79.235118) 7, 3859 (1973).
¹⁸H. Weng, T. Ozaki, and K. Terakura, [Phys.](http://dx.doi.org/10.1103/PhysRevB.52.8244) Rev. B 79, 235118 (2009).
¹⁹T. Usuki, M.
-
-
-
-
- [Rev. B](http://dx.doi.org/10.1103/PhysRevB.52.8244) 52, 8244 (1995).
²⁰J. Chang and C. Hobbs, [Appl. Phys. Lett.](http://dx.doi.org/10.1063/1.4913842) **106**, 083509 (2015).
²¹See <http://www.itrs.net/> for Process Integration, Devices, and Structures (PIDS), International Technology Roadmap for Semiconductors (ITRS).