

# Mechanisms and solutions to the brittle solder joint in electroless Ni plating

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## ABSTRACT

Brittle solder joints in Electroless Ni electroless Pd immersion Au (ENEPIG) surface finishes are one of the key reliability issues in electronics assembly. Previous characterization of the reflow process has indicated that interfacial voids formed after solder reflow are responsible for the decreases in solder joint strength. However, the mechanisms behind the formation of these voids in the ENEPIG process remain unclear. In this paper, the interaction between various aspects of the ENEPIG process and solder joint strength were investigated. Surface roughness, morphology, and nano-pitting at the interface between electroless Pd and Ni-P were characterized. The size and density of nano voids inside Ni<sub>2</sub>SnP were measured after the specimens were reflowed with Sn4Ag0.5Cu solder ball. Additionally, high speed shear solder joint strength measurements were made. The results indicated that anion adhesion induced nano-pitting at the interface between the Ni<sub>2</sub>SnP intermetallic and Pd, resulting in the formation of a nano void layer during reflow. These interfacial voids lead to lower solder joint strength. Based on the results, a solution to prevent the brittle solder joint failures is suggested.

## 1. Introduction

Electroless Ni immersion Au (ENIG) and electroless Ni electroless Pd immersion Au (ENEPIG) surface finishes have been used extensively in high density interconnects as the surface finish for substrates or printed circuit boards. These surface finishes provide better dimensional control, surface flatness, and solder joint strength, all of which are desirable for fine pitch flip chip interconnects [1]. In comparison to electrolytic plating, electroless Ni (EN) plating is denser, more uniform, and has superior corrosion and wear resistance [2,3]. In passivated EN surface such as ENIG, it has been found that occasionally the brittle fracture of the solder joint can occur during thermal-mechanical testing, revealing a clean, flat Ni<sub>3</sub>P surface [4]. Analysis of the solder joint after reflow showed multiple Ni-Sn-P intermetallic compound layers at the interface between Ni<sub>3</sub>Sn<sub>4</sub> and Ni-P [5–7]. Earlier studies suggested attributed the brittle fractures to the crystallization of the Ni-P layer [8,9]. Some researchers also proposed the hyperactive corrosion-induced black pad or mud cracking on the EN surface finish as possible reasons for degraded solder joint strength [10]. More recently, researches have determined that the fracture occurs at the interface between the Ni<sub>2</sub>SnP and Ni<sub>3</sub>P layers [4,11]. The susceptibility to brittle fracture at this interface has been hypothesized to be caused by series of nano voids inside the Ni<sub>2</sub>SnP intermetallic compound. These voids grow and coalesce after multiple reflows and establish a continuous voiding line inside Ni<sub>2</sub>SnP [4]. Once this weakened solder joint is

subsequently loaded, the characteristic brittle fracture occurs. One of the contributing factors is the inherent porosity found in the immersion Au layer that allows oxygen atoms to diffuse through to the Ni and cause oxidation [12]. As with ENEPIG, introducing an amorphous Pd barrier layer prevents this diffusion, effectively alleviating the Ni oxidation [13]. However, the nano voids inside the Ni<sub>2</sub>SnP intermetallic are still formed due to a hyperactive corrosion process. The purpose of this paper is to understand the origin and evolution of these nano voids. A complete characterization of the nano pitting during the surface finish process up to the solder reflow step to form the joint has been conducted. Finally, solutions to prevent the brittle solder joint failures are suggested.

## 2. Experimental procedures

The ENEPIG process consists of the following steps: Cu substrate cleaning, electroless Ni plating (5 μm thickness), electroless Pd formation, and immersion Au deposition (50 nm thickness). The ENEPIG plating solutions used in this paper were supplied by Uyemura Corp., Japan. To provide a clean surface suitable for plating, the polished copper substrate was acid-rinsed by 10% H<sub>2</sub>SO<sub>4</sub> for 2 mins to remove any remaining oxides. The substrate was then micro-etched with a solution composed of Na<sub>2</sub>S<sub>2</sub>O<sub>8</sub> 100 g/L and H<sub>2</sub>SO<sub>4</sub> 40 g/L for 1 min and rinsed with de-ionized (DI) water. After rinsing, the substrates were subjected to a 26 °C palladium activation solution for 3 min. Electroless nickel in the form of a nickel-phosphorus alloy was deposited onto the palladium-activated copper by immersing the substrates into an 80 °C, pH 4.6 electroless nickel bath for 30 min. Upon removal from the bath, the Ni-P specimen was rinsed in three DI water tanks sequentially. To verify the cleanliness of the

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substrate, the water conductivity of the third rinse tank was measured. To form the Pd layer, the nickel plated sample was placed in an 80 °C electroless bath of TPD-30 (Uyemura Corp., Japan) for 15 min. After plating, the specimens were rinsed with DI water. To form the final Au layer, the Pd-plated substrates were dipped into an immersion bath consisting of 1 g/L Au with the stabilizers TSB72M (Uyemura Corp., Japan) at 100 g/L; TSB72R (Uyemura Corp., Japan) at 7 mL/L and KCN 5 ppm. This caused a galvanic reaction to occur between the Au solution and Pd-plated surface, resulting in the deposition of the final solid Au layer. As a final step, the specimens were rinsed by DI water and air dried. The final EN layer consisted of 3–9 wt% phosphorous and was between 5 and 9 μm thick. Compositions and preparation conditions of the ENEPIG surface finishes can be found in our previous publication [13]. The surface roughness of the plated specimens was measured using Atomic Force Microscopy (AFM). The surface morphology of the Ni was analyzed with high-resolution field emission scanning electron microscopy (FE-SEM). The Ni-P weight percentage were measured using energy-dispersive X-ray spectroscopy (EDX). The plated ENEPIG was then cross-sectioned using a focus ion beam (FIB) and analyzed with a JEOL JEM-2100F transmission electron microscope (TEM). Details of a specimen's preparation for TEM analysis can be found in our earlier publication [4]. The density of the nano pitting was recorded as the number of voids per micron length. After the specimens were coated with a layer of flux, 0.5 mm diameter Sn4Ag0.5Cu solder balls were placed onto the surface, followed by a 250 °C reflow for 50 s. The interface between the solder and Ni layer was cross sectioned with a FIB and analyzed with TEM. The density of the nano voids was measured and recorded. The solder joint strength was characterized by high-speed shear (HSS) at 1500 mm/s at zero degrees.

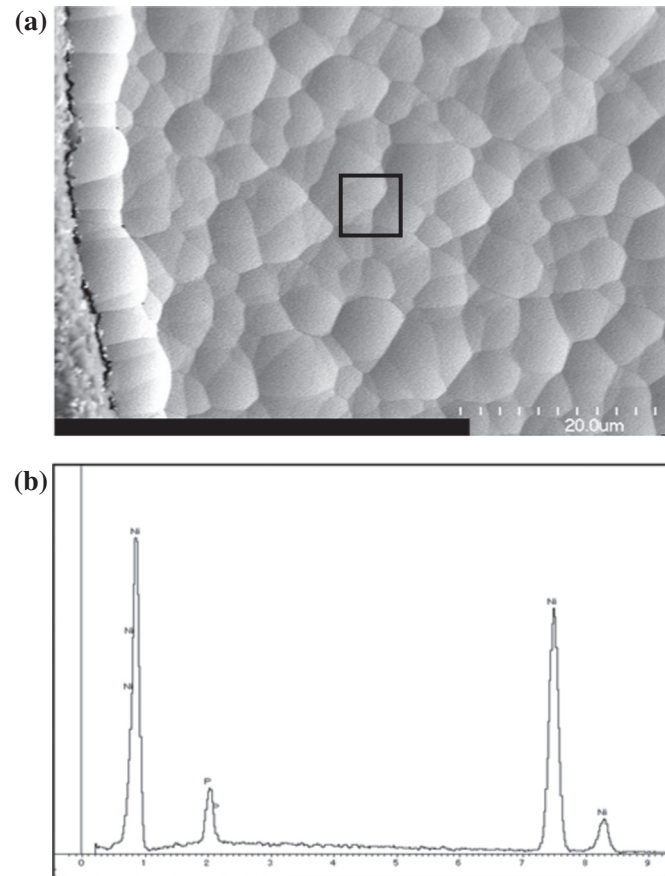


Fig. 1. SEM micrographs of (a) Ni-P nodules. (b) EDX spectrum shows P contents in the range of 6–9 wt%.

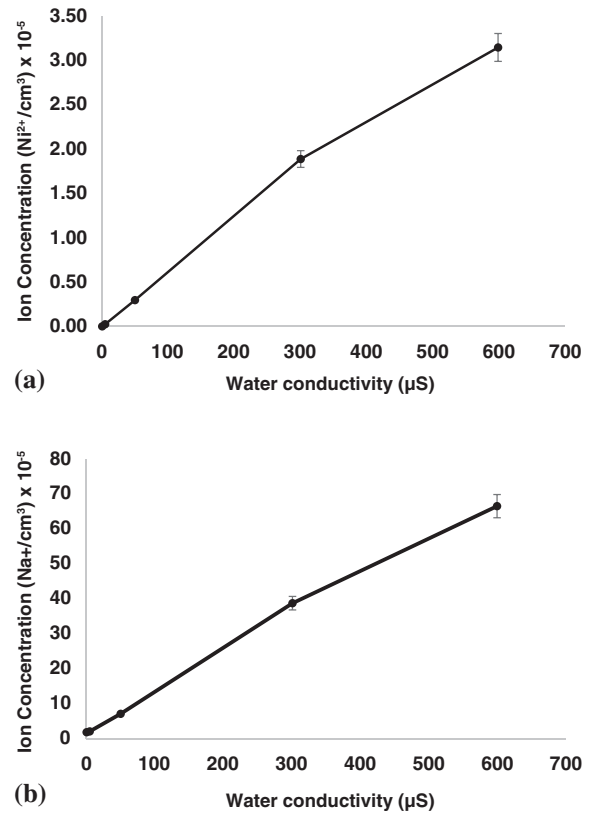


Fig. 2. Effects of ions [Ni<sup>2+</sup>] and [Na<sup>+</sup>] concentration on water conductivity.

### 3. Results

#### 3.1. Characterization of electroless Ni surface

As mentioned in the introduction, hyper corrosion of Ni can cause nano pitting. Fig. 1 is a representative FE-SEM micrograph of the nodular structure of the EN surface. EDX analysis of the Ni(P) surface returned a phosphorous composition of 6–9 wt%. The purpose of the post Ni deposition DI water cleaning is to clean the surface of the Ni from previous plating steps and remove all residual solutions. To analyze the ion concentration in the residual solutions, the water conductivity of the final rinse tank post Ni deposition was measured. Fig. 2 shows the relationship between the water conductivity and the Ni<sup>2+</sup> and Na<sup>+</sup> ionic concentration in the cleaning tank. The water conductivity increases as the concentration of both ions inside the DI water-rinsing bath increase. Fig. 3 shows the plot of the EN surface roughness versus water

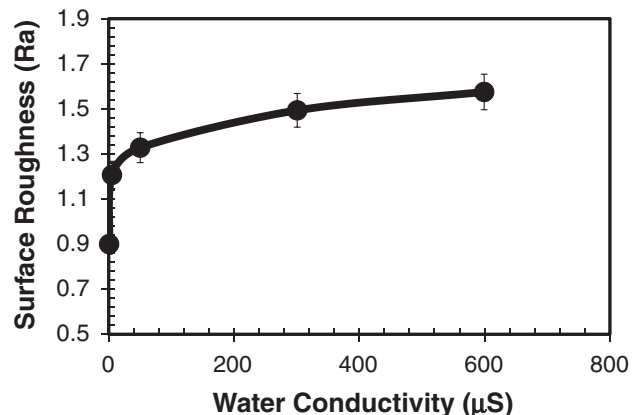
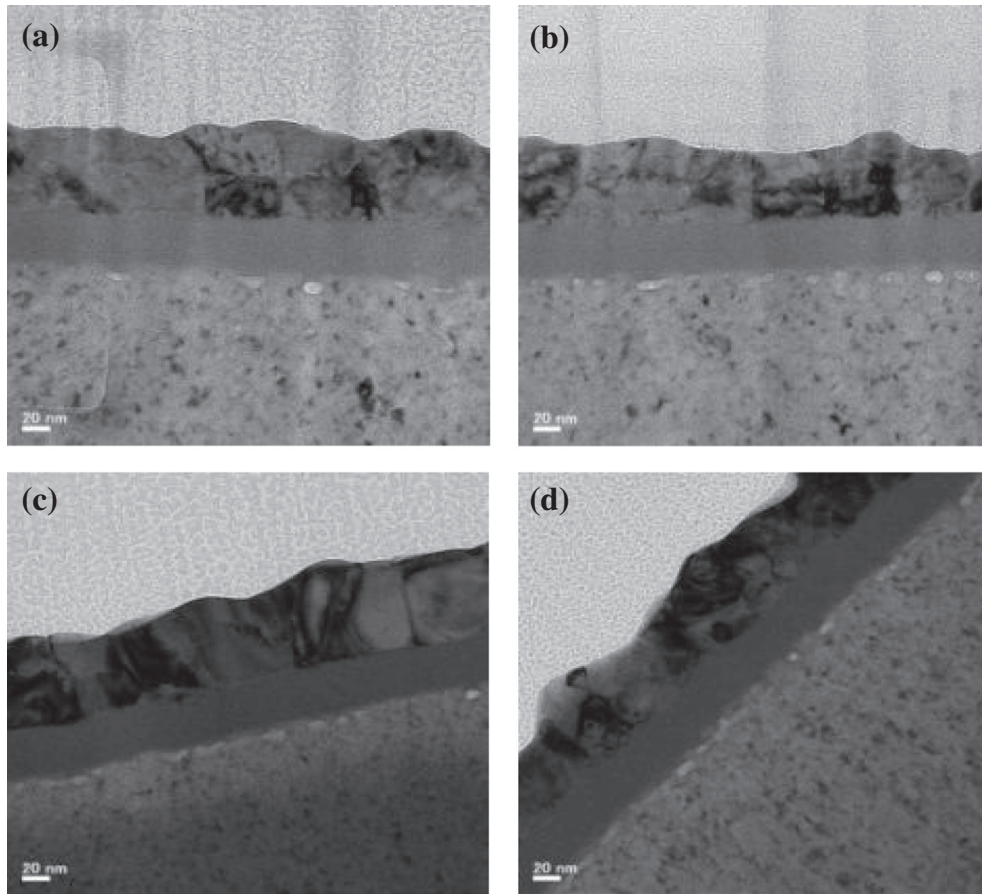


Fig. 3. Effects of water conductivity on surface roughness.



**Fig. 4.** TEM micrographs showing interfacial white spots at different water conductivities ( $\mu\text{S}$ ): (a) 5.2; (b) 50.3; (c) 300.9; (d) 599.3.

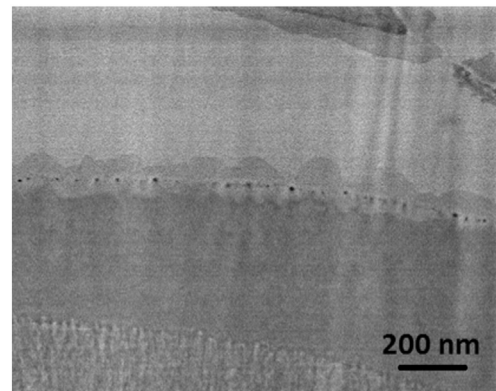
conductivity; the roughness increases exponentially with increasing water conductivity. The FIB cross sectioned samples were examined under bright-field TEM (Fig. 4a–d). White spots at the interface between the electroless Pd and the electroless Ni layers were found. Table 1 shows the measured densities of the white spots per micron length. Laurila and Vourinen [14] reported similar white spots and suggested they are voids. The sizes of the white spots are in the range of 5–20 nm and the density of these white spots increase with both increasing surface roughness and water conductivity.

### 3.2. Characterization of nano voids after reflow

Fig. 5 is the FE-SEM depicting a micrograph of the solder joint after reflow. The micrograph shows connected interfacial nano voids inside the  $\text{Ni}_2\text{SnP}$  layer. Fig. 6a–d show the high resolution images of various FE-SEM micrographs; Table 2 lists the density of the nano voids based on analysis of the micrographs. The size of the nano voids is from 4 to 20 nm. As the water conductivity increases, the nano void density increases as well. The sizes nano voids are very similar to the size of the

white spots at the interface between electroless Pd and Ni-P before reflow as depicted in Fig. 4.

However, the density of the nano voids per micron after reflow is lower than the density before reflow. On the specimen with the lowest water conductivity, no interfacial nano voids were observed. Fig. 7 shows the HSS energy across different interfacial nano void densities; as the interfacial nano void density increases, the energy drops significantly. At the density of about 9 nano voids per micron, further increases in the interfacial nano void density does not result in further degradation of the HSS energy. Above this nano void density, the solder joints exhibit brittle fracture failures.



**Fig. 5.** High resolution image of a FE-SEM micrograph depicting interfacial nano voids at the  $\text{Ni}_2\text{SnP}$  after reflow.

**Table 1**  
The measured density of interfacial voids and water conductivity before solder reflow.

TEM micrographs	Water conductivity ( $\mu\text{S}$ )	Size of interfacial white spot (nm)	Density of interfacial white spot (spots/ $\mu\text{m}$ )
(a)	5.2	8–14	19
(b)	50.3	5–17	30
(c)	300.9	6–16	44
(d)	599.3	8–20	52

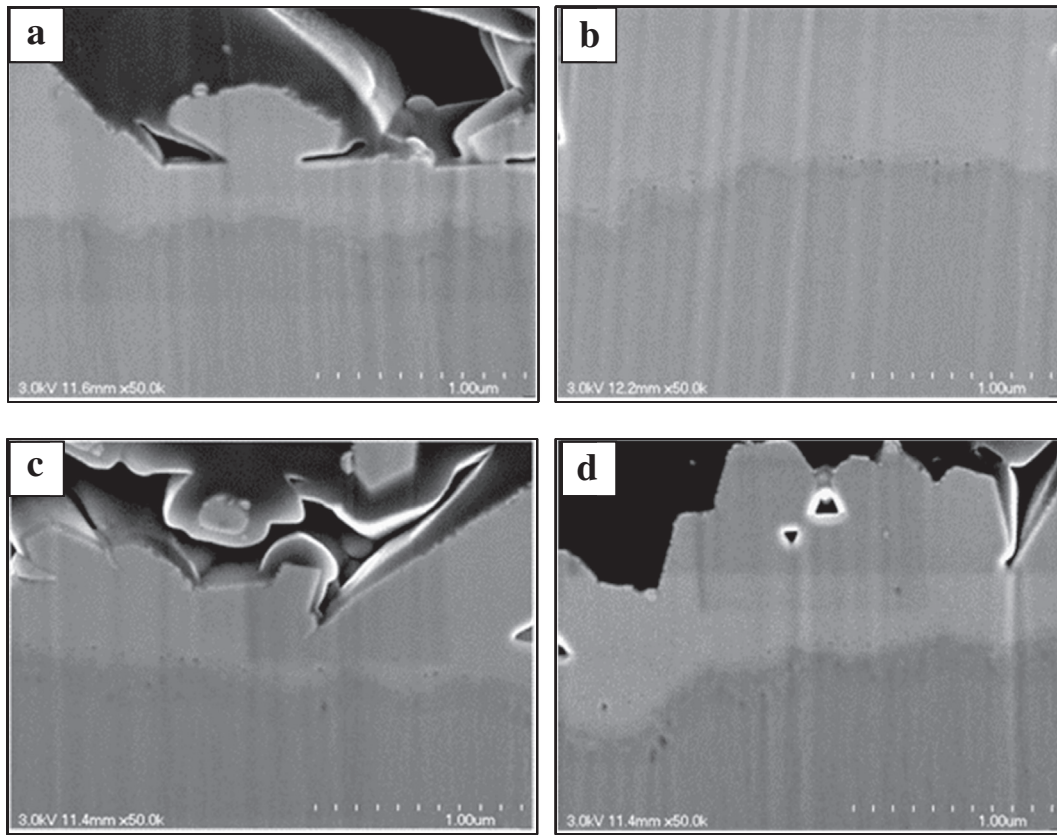


Fig. 6. High resolution images of FE-SEM micrographs showing interfacial voids found inside the  $\text{Ni}_2\text{SnP}$  at different water conductivities ( $\mu\text{S}$ ): (a) 5.2; (b) 50.3; (c) 300.9; (d) 599.3.

## 4. Discussions

### 4.1. Effects of residual ion concentration on nano pitting

Fig. 2a and b show that as the  $\text{Ni}^{2+}$  ion/ $\text{cm}^{-3}$  concentration increases, the water conductivity of the third rinse tank after EN increases in accordance with standard ohmic law [15]. The  $\text{Na}^+$  ions are possibly from the previous EN plating bath; the presence of  $\text{Ni}^{2+}$  suggests that the  $\text{Ni}^{2+}$  originates from the dissolution of Ni due to incomplete residual EN plating solution removal prior to the electroless Pd plating step. As a result, the dissolution of Ni and the residual plating solution in the DI water rinse manifests as an increase in water conductivity. The EN surface roughness shown in Fig. 3 also supports this hypothesis. Therefore, the corrosion of Ni is likely to happen during the water rinsing process. The poorer corrosion performance of EN specimens prepared can be attributed to the presence of the particle boundaries and the trace impurities adsorbed onto or included inside the forming the Ni-P layer [4]. Schlaup and Horch [16] analyzed the phosphate adsorption in-situ using scanning transmission microscopy. The size of the anions observed as white small patches was similar to our findings: approximately 5 nm. TEM images such as those depicted in Fig. 4 shows the white spots that were inferred to be nano pitting. During

the electroless Pd process, the Pd layer deposits on top of the nano pitting, which trapped the impurities and anions.

### 4.2. Evolution of nano pitting during reflow

During reflow, the dissolution of Au and Pd helps to close some of the voids. This can be observed by comparing the nano pitting before reflow and after reflow as shown in Tables 1 and 2. The nano pitting produced voids that were trapped at the interface within the  $\text{Ni}_2\text{SnP}$  layer, as studied by Kang et al. [17]. Our study found that flux can further mitigate nano pitting, albeit not entirely. This may be because the immersion Au and electroless Pd cover the top surface of the nano pitting, acting as an effective barrier of which the flux can clean [13]. As seen in Fig. 6, the size of the nano white spots is very similar to the interfacial nano voids after reflow. The number of nano voids per unit micron was

Table 2

The measured density of interfacial voids and water conductivity after solder reflow.

TEM micrographs	Water conductivity ( $\mu\text{S}$ )	Nano pitting size (nm)	Pitting density (spots/ $\mu\text{m}$ )
(a)	5.2	0	0
(b)	50.3	5–17	4.6
(c)	300.9	6–16	5.2
(d)	599.3	8–20	6.0

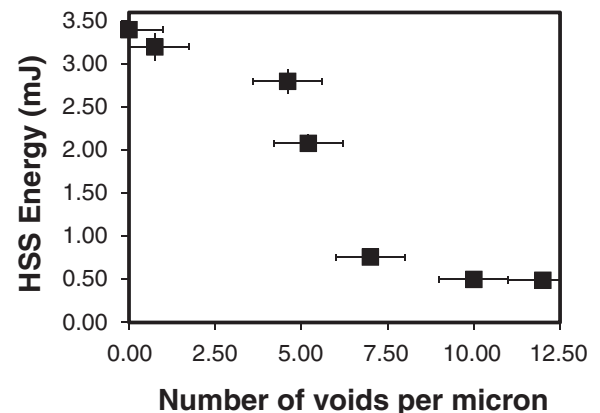


Fig. 7. Plot of solder joint strength (HSS energy) at different nano void densities.

reduced after reflow. One method to reduce the water conductivity is to increase the amount water rinsing. Thus, the purity, temperature, and air agitation are critical factors for the DI water used to remove the adhered anions. Fig. 7 shows that as the void density increases, the HSS energy decreases accordingly because of the decreasing effective area connecting the layers to the substrate [4]. It was determined that the presence of 9–12 nano voids was sufficient for the specimens to fail the shock tests.

## 5. Conclusions

The interfacial nano voids at the intermetallic Ni<sub>2</sub>SnP layer was confirmed to originate from existing nano pitting. The cause of the nano pitting was attributed to anion adhesion that resulted in Ni dissolution and subsequent rough surface formation. The level of Ni dissolution can be monitored analyzing the post Ni deposition cleaning water conductivity. The higher the water conductivity in the DI water, the rougher the Ni-P surface produced. During soldering, the nano pits became trapped at the Ni<sub>2</sub>SnP interface, forming a layer of voids and induced brittle solder joint failures. It was found that a higher nano void density is correlated to a higher water conductivity. Higher nano void density caused brittle solder failures joint due to the reduction of the effective connected area in the solder joint. By enhancing the water rinsing process after the Ni-P plating step, the anion adhesion prior to the Immersion Au plating can be alleviated. As a result, the pitting effect and the nano void density inside the Ni<sub>2</sub>SnP layer can be mitigated. Thus, it is important to monitor the water conductivity of the rinse after the Ni-P plating step and prior to electroless Pd step in the ENEPIG process.

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## References

- [1] G.O. Mallory, The fundamental aspects of electroless nickel plating, in: G.O. Mallory, J.B. Hajdu (Eds.), *Electroless Plating: Fundamental and Application*, Noyes Publications/William Andrew Publishing LLC, New York 1991, pp. 1–56.
- [2] R. George, S. Venkatachalam, K.N. Ninan, Electrochemical impedance measurements on Ni-P coated magnesium alloy, chromated magnesium alloy, and anodised aluminium alloys in aqueous salt solutions, *Br. Corros. J.* 37.1 (2002) 37–42.
- [3] W.-J. Cheong, B.L. Luan, D.W. Shoesmith, Protective coating on Mg AZ91D alloy—the effect of electroless nickel (EN) bath stabilizers on corrosion behavior of Ni-P deposit, *Corros. Sci.* 49 (2007) 1777–1798.
- [4] C.K. Chung, Y.J. Chen, W.M. Chen, C.R. Kao, Origin and evolution of voids in electroless Ni during soldering reaction, *Acta Mater.* 60 (2012) 4586–4593.
- [5] P.L. Liu, Z. Xu, J.K. Shang, Thermal stability of electroless-nickel/solder interface part a: interfacial chemistry and microstructure, *Metall. Mater. Trans.* 31A (2000) 2857–2866.
- [6] Y.-C. Lin, J.-G. Duh, Phase transformation of the phosphorus-rich layer in SnAgCu/Ni-P solder joints, *Scr. Mater.* 54 (2006) 1661–1665.
- [7] C.W. Hwang, K. Suganuma, Interface microstructures between Ni-P alloy plating and Sn-Ag-(Cu) lead-free solders, *J. Mater. Res.* 18 (2003) 2540–2543.
- [8] J.W. Jang, P.G. Kim, K.N. Tu, D.R. Frear, P. Thompson, Solder reaction-assisted crystallization of electroless Ni-P under bump metallization in low cost flip chip technology, *J. Appl. Phys.* 85 (1999) 8456–8463.
- [9] K.C. Hung, Y.C. Chan, C.W. Tang, H.C. Ong, Correlation between Ni<sub>3</sub>Sn<sub>4</sub> intermetallics and Ni<sub>3</sub>P due to solder reaction-assisted crystallization of electroless Ni-P metallization in advanced packages, *J. Mater. Res.* 15 (2000) 2534–2539.
- [10] K. Zeng, R. Stierman, D. Abbott, M. Murtuza, The root cause of black pad failure of solder joints with electroless Ni/immersion gold plating, *JOM* 58 (2006) 75–79.
- [11] H.-B. Kang, J.-H. Bae, J.-W. Yoon, S.-B. Jung, J. Park, C.-W. Yang, Characterization of ternary Ni<sub>2</sub>SnP layer in Sn-3.5Ag-0.7Cu/electroless Ni (P) solder joint, *Scr. Mater.* 63 (11) (2010) 1108–1111.
- [12] R. Ramanauskas, A. Selskis, J. Juodkazyte, V. Jasulaitiene, PCB failure analysis related to the ENIG black pad problem, *Circuit World* 39 (3) (2013) 124–132.
- [13] C.C. Li, W.L. Shih, C.K. Chung, C.R. Kao, Amorphous Pd layer as a highly effective oxidation barrier for surface finish of electronic terminals, *Corros. Sci.* 83 (2014) 419–422.
- [14] T. Laurila, V. Vuorinen, Combined thermodynamic-kinetic analysis of the interfacial reactions between Ni metallization and various lead-free solders, *Materials* 2.4 (2009) 1796–1834.
- [15] R.L. Miller, W.L. Bradford, N.E. Peters, Specific conductance: theory considerations and application to analytical quality control, US Department of Interior, Geol. Surv. Water Supply Pap. 2311 (1988) 1–16.
- [16] C. Schlaup, S. Horsch, In-situ STM study of phosphate adsorption on Cu(111), Au(111) and Cu/Au(111) electrodes, *Surf. Sci.* 608 (2013) 44–54.
- [17] H.-B. Kang, J.-H. Bae, J.-W. Lee, M.-H. Park, J.-W. Yoon, S.-B. Jung, C.-W. Yang, Characterization of interfacial reaction layers formed between Sn-3.5Ag solder and electroless Ni-immersion Au-plated Cu substrates, *J. Electron. Mater.* 37 (1) (2008) 84–89.