

Imperial College London  
Department of Computing

# Structural Separation Logic

Adam Douglas Wright

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Supervised by Professor Philippa Gardner

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# Declaration

I herewith certify that all material in this dissertation which is not my own work has been properly acknowledged.

Adam Douglas Wright

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# Abstract

This thesis presents structural separation logic, a novel program reasoning approach for software that manipulates both standard heaps and structured data such as lists and trees. Structural separation logic builds upon existing work in both separation logic and context logic. It considers data abstractly, much as it is exposed by library interfaces, ignoring implementation details.

We provide a programming language that works over structural heaps, which are similar to standard heaps but allow data to be stored in an abstract form. We introduce abstract heaps, which extend structural heaps to enable local reasoning about abstract data. Such data can be split up with structural addresses. Structural addresses allow sub-data (e.g. a sub-tree within a tree) to be abstractly allocated, promoting the sub-data to an abstract heap cell. This cell can be analysed in isolation, then re-joined with the original data. We show how the tight footprints this allows can be refined further with *promises*, which enable abstract heap cells to retain information about the context from which they were allocated. We prove that our approach is sound with respect to a standard Hoare logic.

We study two large examples. Firstly, we present an axiomatic semantics for the Document Object Model in structural separation logic. We demonstrate how structural separation logic allows abstract reasoning about the DOM tree using tighter footprints than were possible in previous work. Secondly, we give a novel presentation of the POSIX file system library. We identify a subset of the large POSIX standard that focuses on the file system, including commands that manipulate both the file heap and the directory structure. Axioms for this system are given using structural separation logic. As file system resources are typically identified by paths, we use promises to give tight footprints to commands, so that they do not require all the resource needed to explain paths being used. We demonstrate our reasoning using a software installer example.

For those who knew that I could when I thought that I couldn't.

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# 1. Introduction and background

*“The purpose of abstraction is not to be vague, but to create a new semantic level in which one can be absolutely precise”*

Edsger Dijkstra, *The Humble Programmer* [20]

In this thesis, we link local reasoning about heaps to local reasoning about rich data structures, and so create *structural separation logic*. Our aim is *library reasoning*: the ability to give clear and useful axiomatic specifications to programming libraries concerned with manipulating structured data. We focus on *data abstractions*, and so create axioms that capture the effect of commands in a natural manner. This leads to easier proof construction, and the ability to give specifications that match programmers’ intuitions. Just as libraries enhance programming languages and ease the development of programs, structural separation logic enhances separation logic [52] and eases the verification of programs.

Programming libraries are abstractions for sets of operations. They are the standard method for interacting with both data structures and system services. It is rare to see a programmer using, for example, a custom list implementation. Instead, they use a pre-existing list library, which abstracts away from the implementation details. This allows programmers to use lists without worrying about how they are implemented. An operation such as removing element  $e$  from a list referenced by  $l$  is not exposed as “*If you pass in a list reference  $l$  and element  $e$ , this command obtains an initial linked-list pointer from  $l$ , walks along a doubly-linked structure until the memory containing element  $e$  is encountered, swings the pointers around it, and deallocates that memory*”. Rather, it is exposed as “*If you pass in list reference  $l$  and element  $e$ , the list at  $l$  will be updated such that element  $e$  has been removed, and nothing else has changed*”. The first description is much too “low level”. It focuses on how an operation is achieved rather than what the operation does, and it forces the programmer to think in terms of machine memory. The second is “high level” (equally, at the “level of the abstraction”). It allows a programmer to focus on the *meaning* of the code they are writing.

Many libraries are exposed at a high level. From the programmer’s perspective, these libraries do not set out to abstract some fixed implementation, but instead implement a well-chosen abstraction. How the operations are performed is hidden, allowing the library to change implementation details without changing its interface. Moreover, and perhaps more importantly, this allows programmers to use the library with an mental model of what it does, rather than how it does it.

Often, libraries are defined only in these abstract terms and have no standard implementation. If you obtained this thesis from the Internet, you probably downloaded it from a web page that contained JavaScript [27]. Web browsers expose their internal web page representation to JavaScript via the *Document Object Model* [71], allowing script programs embedded within the page to manipulate the in-memory representation. The DOM, a standardised programming library based upon trees, is described only via an abstract specification. Browser authors must carefully read the specification, and provide an implementation that matches the operations described. However, script programmers can simply rely on the abstract description of the trees and operations upon them. Another ubiquitous system presented abstractly is the *Portable Operating Systems Interfaces for UNIX* standard, commonly abbreviated to POSIX [1]. POSIX is a library that all UNIX-like operating systems offer. By targeting the POSIX library rather than OS specific interfaces, programmers can create software that should compile and run on any operating system that provides POSIX. In this thesis, we will demonstrate how structural separation logic can provide natural axiomatic semantics for both DOM and a subset of POSIX focusing on file systems.

Libraries help programmers perform informal reasoning about their code. Leveraging good abstractions, programmers can quickly determine the expected effect of their work and track down bugs. The informal nature of this approach inevitably leads to problems, which *formal reasoning* can help eliminate. Ideally, formal reasoning should (as far as possible) follow the insights provided by informal reasoning. Unfortunately, this has rarely been the case, and formal reasoning remains out of reach for most programmers. It is generally adopted only by cost-critical systems such as hardware design and the finance industry, or safety-critical systems such as aircraft and national defense [72]. The majority of software is never subjected to mathematical rigour, relying instead on manual testing.

This is not terribly surprising. Formal methods have not generally kept pace with software development practices. One very common programming technique is the use of *heap memory*. Formal reasoning about heap manipulating software has, traditionally, been badly served. Fortunately, over the last decade, this has been changing, with some heap-using programs being statically analysed before they are given to customers. For

example, the SLAM project [6] from Microsoft forms the basis of several tools used to analyse parts of their Windows operating system before they are shipped. Their Static Driver Verifier [5], which uses the SLAM engine, checks that device drivers cannot cause system failures. Going even further than this is the L4.verified project [49], which provides formal verification for an entire operating system kernel.

One theoretical development that is enabling the verification of heap manipulating programs *and* capturing the spirit of informal program justification is *separation logic* [52]. Separation logic is a Hoare logic [42] for *local reasoning*. In essence, local reasoning means that verifying a program requires consideration of only the parts of memory touched by that program. This *footprint* concept very often matches the intuitions a programmer has when writing heap manipulating code. When, for example, implementing a linked list reversal, the core loop deals only with the current list element, the next, and the previous. Programmers focus only on this memory, knowing that all other list elements will not change during the current loop iteration. Separation logic reasoning considers the same memory as the programmer, in contrast to previous reasoning techniques which often required consideration of the entire list (or even the entire heap). This similarity can give the creation of a separation logic proof a similar flavour to writing the code in the first place.

Separation logic achieves local reasoning by focusing the descriptions of program heaps onto individual machine memory cells. These *heap cells* can be combined with a *separating conjunction*. Each cell combined with the separating conjunction is guaranteed to be disjoint from all others, allowing the effects of heap update to be tracked precisely to the region of memory affected. Commands are then specified with *small axioms*, which describe the effect of the command over the smallest possible number of heap cells. This technique sidesteps many of the problems that traditional Hoare reasoning about heaps typically suffers, and allows commands to be specified using only the memory they will actually access - the command *footprint* alluded to earlier. The footprint concept has made separation logic highly compositional, allowing proofs to behave modularly. In the past decade, the local reasoning philosophy, and separation logic itself, has flourished. Many research sites now work with local reasoning, with uses spanning the verification of complex concurrent algorithms [19] to automated reasoning tools that can prove large amounts of real world code free of memory errors<sup>1</sup>. Separation logic has been particularly successful in proving properties of *concurrent* programs. Separation fits extremely well with concurrent data update, as manipulating disjoint data can never result in *data races*, where multiple program threads simultaneously access the same data.

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<sup>1</sup>Such as over 50% of Apache, OpenSSH and Linux [24]

Separation logic’s representation of data is typically built upon the shape of machine memory. When dealing with structured data, such as lists or binary trees, a variety of *data predicates* are used. The simplest approach is *inductive predicates*, which encode an implementation of the structure into the heap. Whilst inductive predicates allow inductively defined structures to be represented, they do not provide good *abstractions* of the data structure. The implementation details leak through the predicates, making proofs dependent on them. Approaches such as *abstract predicates* [57] hide more of the implementation choices, representing data structures as “black-boxes”. These black-boxes can be manipulated via axioms, and only the library implementation can *unroll* the boxes to reveal implementation details.

We term these approaches “bottom-up”. The abstractions must exist alongside machine heap assertions, will be justified in terms of the heap, and are manipulated similarly to heap cells. Unfortunately the notion of separation, being designed to support heap memory, can struggle when trying to enable local reasoning about these abstractions, especially when they are abstracting highly structured data. The natural specifications for commands over, e.g. lists or n-ary trees, often want to work on single list elements or sub-trees. Separating these sub-data out from the abstractions can be hard when using a notion of separation born in an implementation-focused environment. The predicates, however abstract, must carry enough information to undo the separation. There is tension between providing abstractions that allow natural reasoning at the level of the data structure, and easy verification of data structure implementations.

To gain the benefits of local reasoning on abstract structured data, it is helpful to have an equally abstract notion of separation. This “abstract separation” can be designed to break the structure into its natural components without being concerned about implementations or machine heaps. One approach to this “top-down” reasoning is *context logic* [15]. Context logic, like separation logic, is a Hoare logic for local reasoning. However, where separation logic typically works with memory shaped like machine heaps, context logic works with memory shaped like the abstract data being used. For example, when reasoning with lists, abstract lists are stored in the memory directly. The standard technique of *contexts* is used to separate complex data into smaller sub-data, and a novel *separating application* rule allows these sub-data to be reasoned with locally. Importantly, the logic does not consider *how* data might be represented in heap memory. This representation allows the specification of libraries given entirely abstractly. For example, context logic was used to give the first axiomatic semantics to the DOM library [65].

Contexts enable a more abstract notion of locality for structured data. Unfortunately, they lose some of the benefits of separation logic. For one, contexts make little sense for

reasoning about flat heaps, as the machinery of contexts is too heavy handed for simple heap manipulation. This can make reasoning about programs that use both heap memory and abstract data more difficult than it need be. Moreover, the nature of contexts limits certain types of desirable locality. Tree commands such as `appendChild(p, c)` (which moves a tree node `c` to be the child of node `p`) cannot be given truly small axioms, as the contexts requires a larger footprint than the command needs. These larger axioms are both unintuitive and, by being far less local than truly needed, limit reasoning about concurrency.

This thesis seeks the best of both worlds, and arrives at **structural separation logic**. Structural separation logic is designed to mix separation-logic-style heap reasoning with context-logic-style reasoning about structured data. This removes many of the restrictions of context logic, and can obtain truly small footprints for commands like `appendChild`. These small footprints, alongside the restoration of a commutative `*` operation to reasoning with contexts, enable far more fine-grained *concurrent* reasoning. We also gain natural reasoning about combinations of standard heaps and abstract data. Structural separation logic can create axiomatic specifications for many libraries, including DOM and the POSIX file system.

To develop structural separation logic, we first introduce a standard concurrent programming language, but give it a memory model of *structured heaps*. Structured heaps are like normal machine heaps, but instead of storing heap cells representing the memory used by a specific implementation, store rich structured data directly (see figure 1.1, left). These heaps are not concerned with how a real machine might implement the structured data, instead working directly on abstract representations. They share a basic shape with normal separation logic heaps, allowing low-level implementation focused reasoning to be used alongside the structured data.

We accomplish local reasoning by introducing *abstract heaps*. Abstract heaps allow structured data to be cut-up into sub-data, and enable the sub-data to be “promoted” to the heap level via *abstract allocation*. Abstract allocation is analogous to allocation in imperative programs, but is managed entirely in the logic. It places sub-data in *abstract heap cells*, addressed by *abstract addresses* (figure 1.1, right). These abstract heap cells are disjoint, and so are separated via `*` in the reasoning. They behave like normal heap cells (albeit cells invisible to the program code), until they are no longer needed. At this point, *abstract deallocation* merges the data back into the value containing the matching *body address*. In this manner, we obtain the high-level reasoning enabled by contexts, and retain the low-level reasoning and natural separation that heaps provide.

We introduce structural separation logic with pedagogical examples, but our goal is



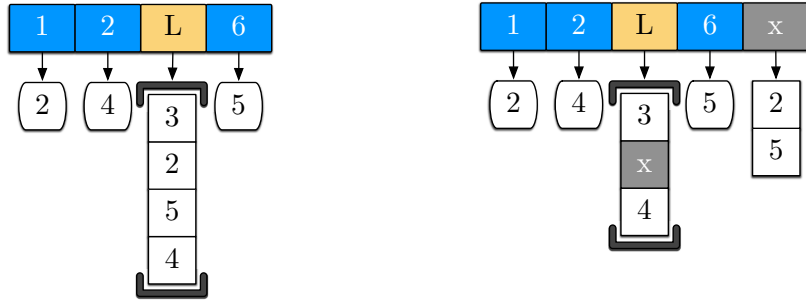


Figure 1.1.: On the left, a structured heap, storing an entire list at address L. On the right, an abstract heap, in which the sub-list  $2 \otimes 5$  has been *abstractly allocated* at *abstract address*  $x$ . Notice the same address then appears as a *body address* in the list at L.

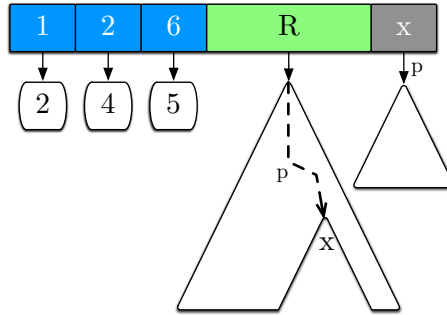


Figure 1.2.: An abstract heap in which abstract cell  $x$  has promise  $p$ .

to give concise axiomatic descriptions of real libraries. This thesis includes two such libraries, the first being the *Document Object Model*. DOM was originally standardised entirely with English prose [71], until the work of Gardner, Smith, Wheelhouse and Zarfaty gave an axiomatic specification using context logic [35, 65]. We revisit this specification in chapter 4 to demonstrate the techniques of structural separation logic, and recast the previous axiomatic semantics using the smaller footprints that our techniques allow. We demonstrate that our specifications for DOM are useful for verifying *client programs* that use the library. By developing a *photo library* program that manages encoded photographic data within an XML file, we show how our techniques can reason about typical uses of the DOM. Moreover, our example requires the use of normal heap memory, demonstrating that we can uniformly handle a mixture of flat heap and structured data reasoning.

Most DOM library commands might be termed “naturally local”. The commands work

on sub-trees identified via globally unique *node identifiers*. Each identifier gives a precise handle with which we can extract the data in question, and provides a natural focus for the update. Not all libraries behave this way. Some use global properties of the data structure to select the location of an update, even when that update is focused on a small region. For example, when analysing an element of a list locally, a command may want to know that the element was 5<sup>th</sup> in the list. The footprint of the command must then contain sufficient data to deduce this fact. Another common example is *paths*. Paths are used in tree manipulating libraries to identify sub-trees. Commands access resource by following the path, then acting on the data at the end of it. These paths can meander around the tree quite freely, but the resultant update is generally local to the sub-tree at the end of the path. Choosing footprints for such commands is thus difficult. The commands need the resource required to follow the path, but will only update a small fraction of it. Requiring that command axioms include all the path resource in addition to the structure needed for the update both complicates the specifications, and drastically limits the number of sub-tree resources that can be disjointly considered simultaneously.

To combat this, we add *promises* to structural separation logic. Promises allow abstract heap cells to retain some information about the structure from which they were allocated. In figure 1.2, there is sub-data at path  $p$  within cell  $R$ . The abstract cell  $x$  has retained this information, noting that it has been allocated from within a tree at path  $p$ . This information has been associated with the data at  $R$  as an *obligation* not to change the path to  $x$  until it has been deallocated.

These promises and obligations allow us to give an axiomatic semantics to our second major example, the POSIX file system library. From the large POSIX specification, we distill a core subset that describes the *file system tree*, and the commands that manipulate it. We create a novel model for this subset, and use structural separation logic with promises and obligations to give an axiomatisation. Commands are specified using resource associated with path promises, allowing intuitive axioms that focus on the resource at the *end* of the path. As in DOM, our axiomatisation is useful for proving properties of client programs. We use our logic to reason about an example *software installer*. This program demonstrates the key file system manipulations that software installers must perform. We show that it either completes the installation task correctly (with respect to the file system manipulations), or that no meaningful changes have been made to the file structure. We will also revisit the DOM specifications, and show how promises can further tighten the footprints of some commands.

That structural separation logic can reason about both DOM and POSIX is a strong statement of its usefulness. Both libraries were originally defined only via English speci-

fications, and both are implemented in a variety of ways. They are very widely used by client programs, with our photo library and software installer examples being two typical scenarios. They both use structured data, but the natural footprints of commands are quite different. In our examples, we see that despite the differences in resource usage, the work required to prove the client programs is very similar. Structural separation logic works at the level of the structured data abstraction, and so allow reasoning about structured data libraries alongside normal, implementation focused, heap reasoning. This is brought out especially in the photo library example, which naturally mixed abstraction layers.

Overall, our contributions can be seen in two parts. First, we offer formal machinery that enables local reasoning with abstract data at a natural level. Our techniques enhance existing methods and reasoning tools, and are built to co-exist with advances in other local Hoare logics. Second, we use our techniques to both specify libraries that use abstract data, and prove clients programs that use the libraries. We have not only enhanced on previous library specifications, but can now provide specifications for libraries not previously analysed in this way. With the techniques and examples of this thesis, we hope to provide library developers with a sound formal system for saying what their libraries do, and programmers with a wider range of feedback methods for software development.

## 1.1. Contributions

The contributions of this thesis are in five chapters.

### **Chapter 2 - A Reasoning Framework:** *A framework supporting our novel contributions.*

We introduce a reasoning framework, specialising the views system introduced by Dinsdale-Young, Birkedal, Gardner, Parkinson and Yang [22]. Our framework consists of *imperative machines*, described using a simple concurrent programming language, with operational semantics for programs using *structured heaps* as states. We introduce an assertion language that describes sets of structured heaps, but add *instrumentation* to the addresses and values. The choice of instrumentation is left to the user, but is added to enable reasoning when the underlying machine states contain insufficient facts for certain types of proofs (one example being fractional permissions for heap cells). We show how these sets can act as *views*, and so demonstrate a sound Hoare reasoning system.

**Chapter 3 - Structural Separation Logic:** *A uniform theory for reasoning about combinations of abstract structured data, which allows smaller command axioms than previously possible, as well as the easy mixing of high-level (abstract data) and low-level (standard heap) reasoning.*

Our core contribution, structural separation logic, is a novel program logic for specifying libraries that manipulate structured data, and for reasoning about the programs that use them. We achieve this by splitting data via *structural addresses*. Structural addresses allow data to be broken into smaller *sub-data*, with the address recording the relationships between the data. Sub-data can be *allocated* at a structural *body address*, promoted to a cell within an abstract heap addressed by a structural *abstract address*, treated much like a heap assertion in separation logic, and then *deallocated* to compose again with the body address. Abstract heap cells allow both smaller command axioms than previous work in this area [15], and naturally support a mix of abstract data and normal heap data. This allows hybrid reasoning, combining the abstract reasoning of context logic with the low-level reasoning of separation logic. We give a detailed treatment of reasoning with abstract addresses via simple examples of lists and trees.

**Chapter 4 - The Document Object Model:** *An case study that revisits previous axiomatic DOM specifications, showing that structural separation logic enables smaller axioms than previous techniques, along with a mix of high- and low-level reasoning.*

We give an axiomatic specification for a subset of the Document Object Model using structural separation logic, building upon previous work [35, 36, 65]. The DOM is a library for tree manipulation, most often used within web browsers as the programming interface between a web page, and script programs that manipulate it. We take the previously identified *featherweight DOM* fragment of the specification [35, 36], which was axiomatised with context logic, and show how structural separation logic can both provide tighter footprints than previous work and integrate reasoning about DOM manipulation with heap manipulation. Following the work of Smith in extending featherweight DOM to specify the entirety of the DOM standard [65], we expect our work on featherweight subset to extend easily to full DOM core level 1. We give an *photo library* example, that shows a typical usage of the DOM library alongside standard heap reasoning. This shows how structural separation logic can naturally handle both flat heap and structured data reasoning simultaneously.

**Chapter 5 - Promises:** *A general sharing model for abstract structured data, allowing*

*local views of abstract data to retain information about the larger environment.*

Structural addresses allow rich data to be split into the sub-data that a command requires to function correctly, and the rest of the data that a command does not. However, some libraries use *global* properties to find *local* data. For example, paths in trees are often used to identify the sub-tree on which a command will act. Including all the global data needed to locate sub-data increases the size of command axioms, both decreasing the comprehensibility of axioms and limiting concurrency. (as while data is being used in the footprint of a command, it cannot be used in the footprint of another thread). To combat these problems, we introduce promises. Promises allow sub-data that has been abstractly allocated to retain some information about the context from which it came. For example, an abstract heap cell containing a sub-tree might use a promise to indicate the path at which the sub-tree can be found in the complete tree. Here, we examine *naturally stable* promises, where the facts given by promises cannot be invalidated by any library command. We examine a richer notion in chapter 7.

**Chapter 6 - The POSIX File System** (*joint work with Gian Nitzk*): *A large case study of specifying and structured data library using structural separation logic and promises, using a novel sub-set of the POSIX standard.*

This chapter uses structural separation logic with promises and obligations to give a specification to the POSIX file system library. We identify a subset of POSIX concerned with representing the core *file system tree* and the commands over it. From this, we distill *Featherweight POSIXFS*: a subset of the POSIX file system specification about which we can formally reason. We design an assertion language for POSIXFS based upon trees and heaps, and use structural separation logic to provide axiomatic semantics for our commands. We expect the reasoning we used for this subset to extend naturally to the entire POSIX file systems specification.

Resources used by the file system library are identified with *paths*. We use naturally stable promises to describe sub-data located at the end of paths. This avoids breaking the intuitive notion that a command on file *f* acts only on file *f*. We illustrate our approach with an *installer example*. We prove that a simple software installation program either successfully installs a package onto a system, or leaves the file system untouched.

**Chapter 7 - Obligations:** *A richer sharing model for structured data with, allowing a wider range of libraries to be specified.*

Some useful promises are not naturally stable. For example, the promise that a given element exists at index  $n$  within a list is not naturally stable, as the elements in the list prefix up to  $n$  could be extended or deleted. We extend promises with a symmetric notion of *obligations*. Just as promises provide information about the wider context of some local data, obligations inhibit certain manipulations of local data to ensure that promises held by the wider context are true. This allows the library to include commands that *could* invalidate promises, as any use such a command will be guarded by the obligations. We use obligations to reduce the size of our DOM specifications.

Taken together, our contributions introduce *library reasoning* for structured data. Our design for structural separation logic allows it to be easily added to many existing - and still to be developed - local reasoning systems. Our goal is that that the data assertions and axioms for a library such as DOM can be easily added to a logic that focuses on a programming language such as C or Java, enriching it with reasoning for the library with minimal effort. We obtain a useful separation of concerns, with structural separation logic focusing on reasoning intuitively about structured data, supported by (and supporting) the broader program logic to which it has been added.

This thesis provides foundational concepts and examples. Building upon this work, we intend to give complete axiomatic semantics for the various levels of DOM, and extend our work on POSIX. We also hope to accelerate reasoning about libraries and languages that have strong relationships, such as joining our DOM reasoning with the JavaScript reasoning of Gardner, Maffeis and Smith [34]. This would allow formal reasoning about web programs in their entirety.

This work will be helped by building on existing separation logic tools. Our abstract heaps easily act as a superset of the symbolic heaps used by tools such as Verifast [45] and the line of automated separation logic tools initiated by Smallfoot [7] (such as SpaceInvader [74] and jStar [25]). We have carried out initial experiments that suggest we can easily integrate with these systems. Resource reasoning about language features can continue to be handled by a tool's normal proof theory. Structured or abstract cells can be handled by a custom engine for structural separation logic, tailored to the domain of the library being used. We can thus develop a system that can “plug-in” to existing tools for separation logic, providing them with easy abstract reasoning for libraries whilst in turn benefiting from their continued advancement.

## 1.2. Technical background

In 1969, Hoare introduced his eponymous **Hoare logic** [42] for reasoning about imperative programs. Building on previous work by Floyd [29], Hoare logic is based upon *Hoare triples* of the form  $\{P\} \mathbb{C} \{Q\}$ . A triple associates *assertions*  $P$  and  $Q$  with a program  $\mathbb{C}$ , where  $P$  describes the *pre-condition* and  $Q$  the *post-condition* of the program. These assertions are first-order logical formulæ that describe the states of the program before and after execution. The meaning of the triple is that, if program  $\mathbb{C}$  is run in a state described by  $P$  then, if it successfully terminates, the resulting state will be described by  $Q$ . This *partial correctness* interpretation says nothing about programs that do not terminate<sup>2</sup>. Hoare’s original work did not consider what happens if a program fails, a property of the reasoning he called “conditional correctness”.

However, using first-order logic as the assertion language proved inadequate for reasoning about programs that use *heap* (or *dynamically allocated*) memory. This is because programs with such memory are susceptible to *aliasing*, where several variables reference the same piece of memory. With first-order logic, it is difficult to express the effects of commands, as what might appear to be a simple update confined to one heap cell could have unbounded effects via aliasing. For example, the assertion  $\mathbf{x} \leftrightarrow 5 \wedge \mathbf{y} \leftrightarrow 6$  describes a global state in which the heap cell at  $\mathbf{x}$  references value 5, and the heap cell at  $\mathbf{y}$  references value 6. If a program performs an update to the cell at  $\mathbf{x}$ , the assertion does not tell us whether the cell at  $\mathbf{y}$  will also be updated, as we do not know if  $\mathbf{x}$  and  $\mathbf{y}$  are aliases.

A similar problem occurs when describing structures. If the assertion  $\mathit{list}(l, x)$  describes a linked-list  $l$  starting at memory address  $x$ , then the assertion  $\mathit{list}(l_1, x) \wedge \mathit{list}(l_2, y)$  should intuitively describe two disjoint lists. However, this is not the case. The assertion does not rule out *sharing* between  $l_1$  and  $l_2$  in memory. Updating list  $l_1$  may inadvertently update some of list  $l_2$ . Adding anti-aliasing and sharing information proved to be difficult, and ensured that Hoare-style reasoning about heap-manipulating programs languished for many years.

### 1.2.1. Separation logic

In 2001, O’Hearn, Reynolds and Yang introduced **separation logic** [52], whose assertion language builds on previous work with the logical of bunched implications [53]. They extended first-order logic with linear *heap cell* assertions: the assertion  $\mathbf{x} \mapsto 5$  describes a state in which the heap memory cell addressed by variable  $\mathbf{x}$  contains value 5. They also

---

<sup>2</sup>Hoare simultaneously introduced a *totally correct* interpretation of triples that guarantees termination, but we will not be using this.

added the *separating conjunction*,  $*$ , which combines heap memory in a disjoint manner. The assertion  $x \mapsto 5 * y \mapsto 6$  describes a state in which  $x$  references 5,  $y$  references 6, and in which  $x$  and  $y$  *do not refer to the same heap cell*. The  $*$  enforces this disjointness between heap resources. As such, a single heap cell assertion  $x \mapsto 5$  describes *partial* heaps, which may be extended via  $*$ . Note that the assertion  $x \mapsto 5 * x \mapsto 5$  describes no heaps at all, as it is impossible to have two disjoint heap cells that both have address  $x$ .

The original work on separation logic focused on the *flat heap* (or *RAM*) memory model, a finite partial function between positive integers (acting as “memory addresses”) and integers in general (acting as “memory values”). These mappings, known as *heaps*, are comprised of a set of *heap cells*. We will make extensive use of diagrams to demonstrate these heaps. Figure 1.3 shows the heap  $1 \mapsto 2 * 2 \mapsto 4 * 6 \mapsto 5$ , containing addresses 1, 2 and 6 with values 2, 4 and 5 respectively. Any heap can be *separated* into smaller heaps by considering it as the union of the smaller heaps. These small heaps can be composed with the  $*$  operator. In the original literature, heap union is described via disjoint function union notation,  $\sqcup$ . In keeping with the notation of [22], we overload  $*$  to act as function union at the model level, in addition to its behavior on assertions.

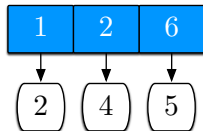


Figure 1.3.: A simple flat heap with three heap cells.

One typical problem in heap-manipulating programs is the access of cells that are not allocated. This causes a *memory fault*, resulting in undefined program behaviour. In focusing on reasoning about heaps, the authors of separation logic recast the interpretation of Hoare triples to ensure that any program proven with separation logic had no memory faults. This **fault-avoiding** interpretation for a triple  $\{P\} \mathbb{C} \{Q\}$  is that, if program  $\mathbb{C}$  is run in a state given by  $P$ , it will not encounter a memory fault and, if it terminates, it will do so in a state described by  $Q$ . Soundness of separation logic was originally proven by O’Hearn and Yang in [75], working with the flat heaps model and a small imperative language with standard operational semantics.

The combination of this interpretation and the resource disjointness given by  $*$  founded a school of *local reasoning* for programs. In local reasoning, program proofs are designed to use the minimum amount of resource needed to show correctness. The behaviours of primitive commands are given via axioms with pre-conditions describing only the resource



needed to show that the command does not fault, and its effect on the state. This concept is known as the *footprint* of the command, so called because it states what resource the command “treads on”. Two examples for flat heaps are the allocation of a new heap cell, and the update of a heap cell to a new value:

$$\begin{array}{l} \{\text{emp}\} \quad \mathbf{x} := \text{alloc} \quad \{\exists X.(X \mapsto 0 \wedge \mathbf{x} = X)\} \\ \{\mathbf{x} \mapsto X \wedge \mathbf{y} = Y\} \quad [\mathbf{x}] := \mathbf{y} \quad \{\mathbf{x} \mapsto Y\} \end{array}$$

Notice that the allocation pre-condition requires no resource, since creating a new cell does not alter any others. Changing the value of a heap cell affects only that cell, so the pre-condition for update does not mention any others. These *small axioms* can be extended to account for more memory by adding resource with  $*$ . As  $*$  ensures that the resource is disjoint, we know that the commands could not have accessed it. The *frame rule* exploits this knowledge to add resource to the pre- and post-condition of a triple.

$$\frac{\{P\} \mathbb{C} \{Q\}}{\{P * R\} \mathbb{C} \{Q * R\}} \quad \begin{array}{l} \text{If the variables modified by} \\ \mathbb{C} \text{ are not mentioned in } R \end{array}$$

The added resource  $R$  is known as the *frame*. Applying the rule is often known as “framing on” or “framing off” resource (depending on the context). The side-condition ensures that variables added by a frame cannot be altered by the program, which would be unsound as the  $R$  in the post-condition would not account for the changes. It is possible to pick  $R$  that describes resource already described by the pre-condition. The interpretation of triples ensures this is still meaningful, as the pre-condition becomes unsatisfiable by the definition of  $*$ , and the interpretation is given in terms of states described by the pre-condition. If the pre-condition describes no states, a Hoare triple is valid, but gives no information about the program behaviour.

The elegance of the separation logic frame rule and footprint concept has allowed a flourishing area of automated reasoning tools. By design, each axiom can affect only a small part of a heap. It is therefore often possible for computers to easily determine the effect a command has on an assertion, and so automatically either generate a separation logic proof, or verify an existing one. The first separation logic tool, Smallfoot [7], could prove properties about simple heap manipulating programs using data abstractions such as lists and binary trees. Smallfoot’s progeny, tools such as SpaceInvader [74] and jStar [25] are now capable of automatically verifying that large programs are free of memory faults, including the majority of the Linux kernel [24]. There has also been extensive work in proof assistants. One example is Verifast [45] which, when provided with intermediary “ghost” verification instructions, can prove detailed properties about complex programs,

such as implementations of electronic identity cards and device drivers.

## Concurrency

Concurrent programs contain more than one thread of execution. A typical problem in concurrent programming with heaps is *data races* where, for example, two threads access the same memory simultaneously. This can leave memory in an inconsistent state, or cause the threads to read invalid data. If we can prove that the code executed by two threads never accesses the same memory, the program can never encounter a data race. The separation afforded by  $*$  provides exactly the mechanism to do this, and allows the *parallel rule* of separation logic. This rule, given below, states that if two programs  $\mathbb{C}_1$  and  $\mathbb{C}_2$  can be proven correct via triples  $\{P_1\} \mathbb{C}_1 \{Q_1\}$  and  $\{P_2\} \mathbb{C}_2 \{Q_2\}$ , it is safe to concurrently execute  $\mathbb{C}_1$  and  $\mathbb{C}_2$  in states described by  $P_1 * P_2$ . This type of concurrency is known as *disjoint*, as the two threads are given resource separated via  $*$ .

$$\frac{\{P_1\} \mathbb{C}_1 \{Q_1\} \quad \{P_2\} \mathbb{C}_2 \{Q_2\}}{\{P_1 * P_2\} \mathbb{C}_1 \parallel \mathbb{C}_2 \{Q_1 * Q_2\}} \quad \text{If the variables modified by } \mathbb{C}_i \text{ are not} \\ \text{mentioned in } P_j \text{ or } Q_j \text{ for } \{i, j\} = \{1, 2\}$$

This rule was proven sound by Brooks in [12]. However, this method does not allow any resource sharing using the flat heap model, as we can never pass a heap cell to two threads. To combat this, Bornat, Calcagno, O’Hearn and Parkinson introduced *permission systems* [10]. The most well-known permission system associates a *fractional permission* with each heap cell. A permission is a rational number in the  $(0, 1]$  interval. Heap cells can then themselves be separated by dividing the permission into two, and associating each half with a copy of the cell. This is possible only if  $\pi_1, \pi_2 \in (0, 1]$ , and  $\pi_1 + \pi_2 \leq 1$ :

$$\mathbf{x} \xrightarrow{\pi_1 + \pi_2} v \iff \mathbf{x} \xrightarrow{\pi_1} v * \mathbf{x} \xrightarrow{\pi_2} v$$

Each of these fractional  $\mathbf{x}$  heap cells can be passed to a different thread. A thread can update a heap cell if it is contained in its footprint with permission 1. If it has less than permission 1 it can be read, but never written to. Notice this remains *disjoint* concurrency, as the resource passed to the threads is still separated via  $*$ .

## Abstract separation logic

A general framework for program reasoning with separation was not provided until *abstract separation logic* [17], introduced by Calcagno, O’Hearn and Yang. Resource models

in abstract separation logic are *separation algebras*: cancellative, commutative monoids  $(H, *, u)$  where  $H$  is interpreted as the set of resources and  $*$  joins resources. This definition induces a notion of disjointness between data, in that if  $h_1 * h_2$  is defined then  $h_1$  and  $h_2$  are disjoint. Abstract separation logic provides a generalised soundness proof for local reasoning with separation algebras, and also shows how *mixed* heaps can be constructed. By taking the cross-product of two separation algebras, one can create heaps involving more than one type of resource. The abstract separation logic approach was the ancestor of several modern formalisms for local reasoning, one of which we will use in this thesis [22].

One non-flat-heap model that has become common is **variables as resource**. Introduced by Bornat, Calcagno and Yang in [10], it treats program variables as linear resource (much like heap cells). This removes the need for the side-condition on the frame and parallel rules. It also improves the uniformity of models for the logic, which no longer need to have separate parts for the heap and variable store. However, it increases the complexity of any axiom that works with variables, and decouples variables from their natural syntactic scoping. Despite this, we will use this system to handle variables throughout this thesis, as the uniformity allows us to focus on library reasoning by implementing variables within our structured heaps.

### 1.2.2. Data abstraction

The assertions of separation logic with flat heaps describe individual allocated memory cells. Structures, such as lists or binary trees, can be described with *inductive predicates*. These predicates provide a representation of the structure using the separation logic memory model, and are somewhat analogous to “implementing” the structure in a language like C. One standard list representation is a singly-linked structure. The list predicate  $list(\alpha, x)$  is parameterised by an algebraic list  $\alpha$  and the memory location of the first heap cell of the linked list. Algebraic lists are of the form  $1 \otimes 2 \otimes 3$ , with empty lists denoted  $\emptyset$ . The standard definition is given in figure 1.4.

The  $list$  predicate describes complete lists (that is, those linked lists that are null terminated). To describe only part of a list, we can use a  $listseg$  predicate. The definition of  $listseg(\alpha, x, y)$  below describes part of a singly-linked list carrying the contents of algebraic list  $\alpha$ , starting at address  $x$  and ending at address  $y$ .

$$\begin{aligned}
list(\alpha, x) &\triangleq && \alpha = \emptyset \wedge x = 0 \\
&&& \vee \\
&&& \exists v, \beta, y. \alpha = v \otimes \beta \wedge x \mapsto v * (x + 1) \mapsto y * list(\beta, y)
\end{aligned}$$

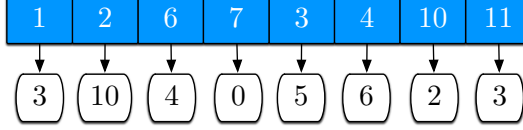


Figure 1.4.: The list predicate for a singly-linked list implementation using binary cells. A diagram of a heap satisfying the predicate  $list([3 \otimes 2 \otimes 5 \otimes 4], 1)$  is given below the predicate definition.

$$\begin{aligned}
listseg(\alpha, x, y) &\triangleq && \alpha = \emptyset \wedge x = y \\
&&& \vee \\
&&& \exists v, \beta, z. \alpha = v \otimes \beta \wedge x \mapsto v * (x + 1) \mapsto z * listseg(\beta, z, y)
\end{aligned}$$

Programs will often need to gain access to the contents of a structure, such as the individual heap cells that contain the elements inside a  $list(\alpha, x)$  instance. To achieve this with inductive predicates requires that they be *rolled* and *unrolled*, replacing the body with the name and vice versa. This rolling and unrolling allows inductive predicates to be separated into more primitive components. The equivalence  $\exists y. listseg(\alpha, x, y) * list(\beta, y) \iff list(\alpha \otimes \beta, x)$  is justified by unrolling the  $list$  instance of figure 1.4, then rolling it back into a  $listseg$  and a  $list$  instance. This would allow for example, two threads to process the same list concurrently (each thread taking half of the list).

Unfortunately, inductive predicates exhibit poor modularity. As the predicate definition is always available, proofs are tied to the choice of implementation given by the predicate body. Consider a *list library*, that provides client code with functions for interacting with a list. The memory representation of a list should be private to the implementation, so it can be changed. With inductive predicates, nothing prevents the proof of client code from unrolling a predicate and becoming dependent on specific library implementation choices.

To combat this loss of modularity, Parkinson and Bierman introduced *abstract predicates* [57]. Their theory allows the bodies of predicates to be hidden from parts of a proof. The Hoare judgement becomes  $\Delta \vdash \{P\} \mathbb{C} \{Q\}$ , where  $\Delta$  is a *predicate environment* containing a mapping from predicate names to definitions. When its definition is not in scope, an abstract predicate becomes a “black box”; essentially, just a name and some parameters.

The abstract predicate is part of an assertion, but can never be unrolled to give a body. Proof rules are provided to weaken and strengthen the predicate environment. By having client proofs operate on weak environments, and using strengthening to add predicate definitions when entering non-client code, modularity is restored. The library can replace the definition of the predicate at will and, as long as the predicate signature is unchanged, existing client proofs are still valid.

Unfortunately, this modularity comes at the price of locality. Without access to the implementation, an abstract predicate cannot be unrolled into separate components. Recall the *list* in figure 1.4, which we can use to obtain a *listseg* for some prefix of the list. Were *list* abstract, clients could not unroll it, and so could not split the list representation. Whilst it is possible to provide axioms regarding abstract predicates, such as  $\exists y. \text{listseg}([\alpha], x, y) * \text{list}([\beta], y) \iff \text{list}([\alpha \otimes \beta], x)$ , each must be justified by appeal to an implementation. The choice of predicate must have sufficient parameters to allow safe abstract joining and splitting, which can restrict implementation choices.

We can see an example of this problem by making the *list*( $\alpha, x$ ) and *listseg*( $\alpha, x, y$ ) predicates abstract. Without the predicate body, clients can only split lists into smaller *list* and *listseg* instances via axioms. The addresses  $x$  and  $y$  allow the reconnection of the split data, and work well for linked lists. However, we can conceive of implementations for which these parameters are insufficient to allow reconnection. For example, the list may be backed by a complex structure such as a B-tree. In this scenario, splitting a list involves splitting the tree at the implementation level. Rejoining two split trees would require significantly more parameters than are present on the predicates.

Fundamentally, this problem arises as the separation model is chosen for language memory, and not the data model of the abstraction. Facts about the memory model leak across the abstraction boundary, revealing or limiting implementation choices. Thus, the locality exhibited by the predicates is always an expression of the locality of the underlying heap. This problem can be partially mitigated by allowing sharing. The *concurrent abstract predicates* of Dinsdale-Young, Dodds, Gardner, Parkinson and Vafeiadis [23], although designed primarily for concurrent programming, enable such sharing. This system augments the logic with *permissions* and *tokens* over *shared regions*. Shared regions are intuitively groups of heap cells that are given a name and can be shared between threads. They behave additively with respect to separation, so that multiple copies of the same shared region can exist at once (although they all reflect the same underlying heap memory). To avoid inconsistencies, and ensure that concurrent uses of the regions are safe, access to shared regions is guarded via tokens. Updates to the region are performed by actions which are enabled by having certain tokens. The logic guarantees this enforces a sound

protocol for access to the regions.

Shared regions allow a form of reasoning where the separation at the abstract level does not match the separation of an implementation. The example given in [19] is a *set* module, where the abstract predicates are *in*(*i*) and *out*(*i*). These predicates allow sets to be described in terms of which elements are in the set and which are not. They are separate at the abstract level, allowing assertions of the form  $in(1) * out(2) * in(3)$ . This describes a set of numbers which certainly contains 1 and 3, certainly does not contain 2, and about which we have no other knowledge. The implementation of the predicates *in* and *out* can be, e.g. a linked list containing *all* the elements contained within the set. The linked list describing the overall set is placed in a shared region, and each predicate is defined in terms of a copy of it, along with tokens allowing the element corresponding to the predicate to be removed (in the *in* case) or added (in the *out* case). Thus, the predicate *in*(1) has a copy of the region and tokens permitting the entry 1 to be removed from the list. The apparent disjointness at the abstract level is not reflected in the implementation, something the authors termed a *fiction of disjointness*.

Even with concurrent abstract predicates, the theory of separation is still fixed. It must be explained by the underlying language memory model, with the richer separability being enabled by the sharing. *Fictional separation logic* by Jensen and Birkedal [46] attempts to solve this problem in a sequential setting. It allows the assertions of multiple separation algebras to co-exist in a single proof. The basic triples for fictional separation logic are of the form  $I. \{P\} \mathbb{C} \{Q\}$ , and carry an implicit parameter of  $\Sigma$ , a separation algebra. The  $I$  is an *interpretation map*, which takes assertions of the algebra  $\Sigma$  into normal heap assertions. Libraries expose triples to clients with an existentially quantified separation algebra and interpretation map. This gives the library author freedom to expose an interface using a notion of separation appropriate to the module.

This works very well for certain types of abstractions. However, even at the abstract level, the notion of separation is still a commutative operation. To enable fine-grained reasoning, the  $*$  operation must allow granular data to be isolated. For rich data, finding a separation algebra that is flexible enough to allow these sub-data to be accessed can be hard. For example, when working with n-ary trees, one often wants to access a specific sub-tree as a separate resource. Trying to find a separation algebra that allows this sub-tree to be separated elegantly, and without disturbing the rest of the tree structure, is difficult, and is one of the contributions of this thesis.

## Views

Fictional separation logic allows proofs that expose abstractions via a separation model that is entirely different to that of the standard heap. A similar approach was taken by Dinsdale-Young, Birkedal, Gardner, Parkinson and Yang in *Views* [22]. Sub-titled “compositional reasoning for concurrent programs”, the views framework provides a general Hoare-style reasoning system. The key idea of the framework is to split the data representation in two. The states of the underlying machines are described independently from the abstract states used for reasoning. These machine states need not be easily separable nor compositional. Instead, rather than reasoning with machine states, reasoning is performed on *views*. It is these views which are compositional, abstract representations of the states, typically including rich instrumentation.

A view model is a commutative semi-group  $(\text{VIEWS}, *)$ , in which every element of  $\text{VIEWS}$  is a view. Views compose with  $*$ , but this composition need only make sense with respect to the *instrumentation* being used. Many views represent no machine data at all, instead representing objects used to enable the reasoning (such as the tokens of CAP). The name *view* is apropos, as each object represents a “perspective” on the current states of the machine along with the current “state” of the program reasoning. A view is converted to a set of machine states via a *reification* operation, shown in figure 1.5. Reification transforms a view into the set of machine states it represents. The reification of view  $p$ ,  $[p]$ , denotes all the machine states described by the view  $p$ .

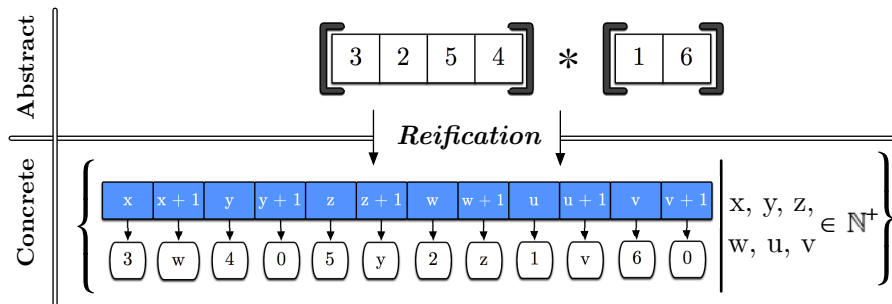


Figure 1.5.: Views uses a two tiered system. The set of views is used to give assertions describing machine states. In this example, each view is a complete list. Reification transforms them into sets of machine states, in this case choosing to represent the abstract lists as singly-linked-lists. Notice the generation of all possible machine states that represent the abstract structure.

Views are used as the pre- and post-conditions of Hoare triples. For each axiom, one must show that the effect of the command is described by the axiom triple, and that the axiom is *stable* (that is, does not disturb other resource). Let views  $p$  and  $q$  be a choice of pre- and post-condition of some command  $C$ ,  $\{p\} C \{q\}$ . The choice is good only if, for all possible views  $r$ , the effect of the command on all states in  $[p * r]$  is contained within  $[q * r]$ . The link between triples and commands is thus between the reifications of  $p$  and  $q$  when extended by *any possible frame*  $r$ . Locality and compositionality is “baked in” to the system at every level.

The framework provides a *semantic consequence rule* that allows a view to be altered. The semantic consequence  $p \preceq q$  is justified if, no matter what frame view  $r$  is chosen, all the machine states of  $[p * r]$  are contained within those of  $[q * r]$ . It is informally justified because, from the perspective of the machine, nothing important has changed; all that has occurred is a “view shift”. It can be seen as a “ghost update” step, which updates the in the instrumentation. The check that the containment works under all frames ensures that these changes do not conflict with instrumentation on possible frames. Semantic consequence will play a key role in justifying the formalism of abstract heaps in our structural separation logic.

The views framework can represent many disparate reasoning systems, from the original separation logic through to complex modern logics for concurrency such as CAP [23], traditional logics for concurrency such as Rely-Guarantee [47] or Owicki-Gries [55]. It provides a consistent logical framework in which to setup results and prove soundness. We will make extensive use of the views framework throughout this thesis.

### 1.2.3. Context logic

Meanwhile, in 2005<sup>3</sup>, an alternate local reasoning approach for abstract data was being developed. Encouraged by separation logic, but concerned about fine-grained reasoning for structured data, Calcagno, Gardner and Zarfaty introduced **context logic** [15]. Context logic enables local reasoning with data models that are abstractions of the data being manipulated. The memory models are not heaps, but rather representations of lists, trees, and similar structures.

Contexts are a well known tool in computing theory. There are a variety of types, several of which have been considered as models for context logics. The first presentation was *single-holed* contexts, with typical models being inductively defined data (such as the algebraic list used in figure 1.4), extended with a single *context hole*. The *separating*

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<sup>3</sup>Approximately the same time as abstract predicates were introduced.



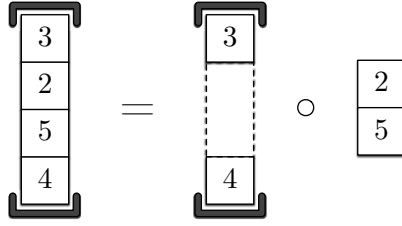


Figure 1.6.: An abstract list  $[3 \otimes 2 \otimes 5 \otimes 4]$  decomposed via separating application into the context  $[3 \otimes - \otimes 4]$  and sub-list  $2 \otimes 5$ , representing the entire list as  $[3 \otimes - \otimes 4] \circ (2 \otimes 5)$ .

*application* operator  $\circ$  allows data to be pulled apart, leaving a context with a hole and some *sub-data*, as in figure 1.6.

Using contexts to represent data enables different forms of locality than the separation concepts seen so far. Most importantly, it is *top-down*, allowing us to use a data model that matches the abstraction level of the data it is manipulating. Freed from the need to provide a  $*$ , the separating application can be non-commutative. There is a difference between a context (which has a hole) and data (which fits in a hole). The relationships between data and sub-data can be recorded via the context hole, without consideration for a representation within some machine memory. This is reflected in the frame rule of context logic, which just adds a context over an entire datum.

$$\frac{\{P\} \mathbb{C} \{Q\}}{\{R \circ P\} \mathbb{C} \{R \circ Q\}} \quad \begin{array}{l} \text{If the variables modified by} \\ \mathbb{C} \text{ are not mentioned in } R \end{array}$$

Context logic was used by Gardner, Smith, Wheelhouse and Zafarty to give an axiomatic specification to a subset of the *Document Object Model Library Core Level 1* standard [35, 36]. Smith later extended this subset to the entire standard. The commands of DOM manipulate a tree structure that is described abstractly. Contexts could give local footprints to many of the commands, but certain situations (such as `appendChild`) still required more resource than would be expected.

Moreover, using single-holed contexts also restricts some types of locality. Context applications can be nested in an associative fashion, but a single datum cannot contain more than one hole. The lack of commutativity means there is no natural analogue of the parallel rule from separation logic. The first of these problems was addressed by Calcagno, Dinsdale-Young and Gardner in [14]. They extended the single-holed models to support *multiple labeled holes*. These multi-holed contexts were developed for technical results

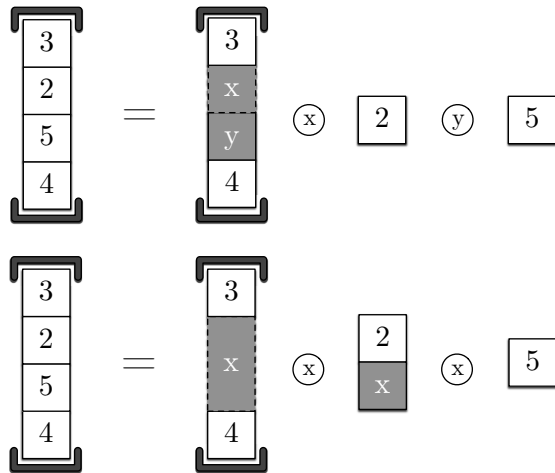


Figure 1.7.: Two multi-holed contexts that describe the same list. The separating applications bind the holes  $\mathbf{x}$  and  $\mathbf{y}$  within the lists. Two contexts may use the same hole as long as composition is unambiguous.

regarding adjoint elimination, but provide for richer splittings of data, as in figure 1.7.

By naming holes, composition gains *quasi-commutativity*, such that in cases like the top of figure 1.7 the order of composing 2 and 5 into the larger list is irrelevant. However, composition is still not commutative. There is a difference between  $P \otimes Q$  and  $Q \otimes P$ , as  $P$  *must* have an  $\mathbf{x}$  hole, but  $Q$  need not. Composition remains a notion of “filling holes”, with holes in contexts bound by the composition operation, so that the bottom result of figure 1.7 is allowable.

Multi-holed contexts allow data analysis at a finer level than single-holed. Consider a command `appendChild(p, c)`, acting on a tree. It moves the sub-tree with top node  $c$  to be the last child of node  $p$ .

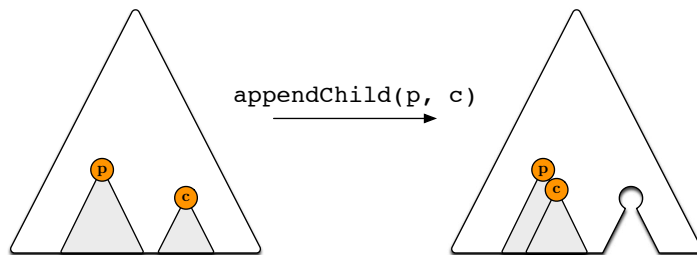


Figure 1.8.: The action of the command `appendChild(p, c)`.

This command can fail, in a manner analogous to a memory fault, if the node identified by  $p$  or  $c$  does not exist in a tree. However, even assuming that both nodes exist in a tree, this command can still fail if the node  $c$  is an ancestor of node  $p$ . Were this possible, it would create a “tree” where  $c$  is a sub-tree of itself.

To axiomatise this command, we need to describe the two safe cases: one where the node  $p$  is an ancestor of the node  $c$ , and one where the two nodes have no parent/child relationship. To capture the two cases using contexts, we must find a single splitting of the data that describes both. Such a case can be found by taking a *covering context*: one that captures the smallest tree containing both  $p$  and  $c$ . This allows the pre-condition to be expressed as  $(C \otimes_x p[t]) \otimes_y c[t']$ , one case of which is below. This would not be possible with single-holed contexts, as one possible splitting here is that  $C$  contains both the  $x$  and  $y$  hole.

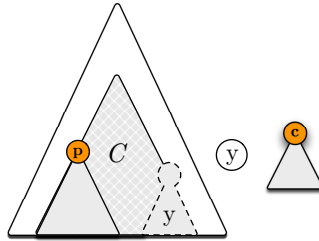


Figure 1.9.: To give a single description of the states safe for `appendChild(p, c)` with multi-holed contexts, a *covering context* must be used to capture the entire sub-tree containing both node  $p$  and  $c$ . In this case, the covering context  $C$  is for the case where  $c$  is not a child of  $p$ .

Multi-holed contexts still have limitations. The axiom for the pre-condition of `appendChild` would ideally be in terms of the node at  $p$  and the sub-tree at  $c$ . The covering context is not touched by the command, but must be in the footprint due to the nature of contexts. Moreover, the concurrency rule is still inapplicable. These problems occur because of the non-commutative nature of separating application, which acts as a binder to the multi-holed context to its left.

### 1.3. Introducing structural separation logic

We now give a high-level introduction to the concepts of **structural separation logic**, the main technical contribution of this thesis. Structural separation logic fuses the natural

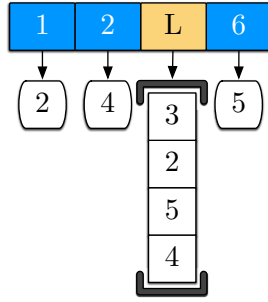


Figure 1.10.: A structured heap, with regular heap cells at addresses 1, 2 and 6, and a list heap cell at address  $L$  containing the list  $[3 \otimes 2 \otimes 5 \otimes 3]$ .

heap reasoning of separation logic with the natural abstract reasoning of contexts, and does so without complicating assertions with additional machinery. Contexts and heap cells are freely mixed in assertions, and are connected with a standard associative and commutative  $*$  conjunction. Abstract and flat heap data can be reasoned about using the same techniques. We can give tighter axioms to library specifications and more compact proofs of code that use them.

We achieve this by working with *structured heaps* at the operational level, and *abstract heaps* at the reasoning level. Structured heaps allow us to avoid considering the implementation of data structures. They are similar to the flat heaps of separation logic but, rather than consisting of heap cells with simple values like integers, are of a mix of cells storing flat and *rich* values. For example, alongside standard values, a cell can contain an entire list (see figure 1.10).

A model of *imperative machines* is given that treats these structured cells like any other memory. Commands reference the data by address, and perform imperative update within the structured value, changing it atomically from one structured datum to another. The use of rich values is optional, so that structured heaps behave as standard heaps if needed.

A key contribution is a reasoning model for structured data using *abstract heaps* and *structural addresses*. Abstract heaps are like structured heaps, but contain *abstract heap cells* storing sub-data from within some structure. Structural addresses enable this in two ways. Firstly, data can contain a structural *body address*, giving a location in which sub-data can be applied. Secondly, abstract heap cells are addressed via a structural *abstract address*, linking the sub-data to the body address from which it came. Abstract heap cells are created by *abstractly allocating* sub-data from within some structure into a freshly addressed abstract cell. They can be destroyed by *abstractly deallocating* an abstract cell

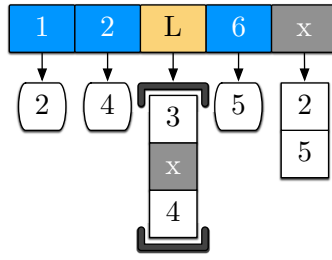


Figure 1.11.: An abstract heap, where the sub-list  $2 \otimes 5$  has been *abstractly allocated* at *structural address*  $\mathbf{x}$

back into the matching body address. In figure 1.11, the list of figure 1.10 on the preceding page has been split by abstract allocation to create a new abstract heap cell at address  $\mathbf{x}$ . When this heap cell is no longer needed, it will be deallocated, restoring the complete list.

In the reasoning, abstract heap cells allow sub-data to be treated exactly like any other heap cell. When needed, a rule within the proof theory performs the “allocation”. Axioms for commands on structured data then work with the smallest abstract heap cells that contain their footprint. Once the operation is complete, *abstract deallocation* removes the abstract cell, and recombines the data. Abstract allocation and deallocation are performed by directly updating the assertions, and do not requiring additional machinery in the assertion language.

Such updates to the model are enabled by our adoption of the *views framework* [22]. As discussed in section 1.2.2, views provides us with local and compositional reasoning. However, the key facility the framework gives us is *reification*, and with that, *semantic consequence*. Reification allows the abstract heaps we use for Hoare reasoning to be interpreted as structured heaps. Reification removes abstract heap cells and collapses the data they contain back into the corresponding body address. Moreover, it also handles *partial* abstract heaps. It is possible to construct abstract heaps that are missing the abstract heap cells needed to represent complete data. Reification provides these by *completing* the data in every possible way. Our use of reification is illustrated in figure 1.12.

The semantic consequence relation of views enables abstract allocation and deallocation. Note that our structural addresses are a form of instrumentation on heaps. By designing these addresses so that two abstract heaps reify to the same structured heaps if they differ only by uses of abstract allocation and deallocation, we can update the splitting of the heap using only semantic consequence steps.

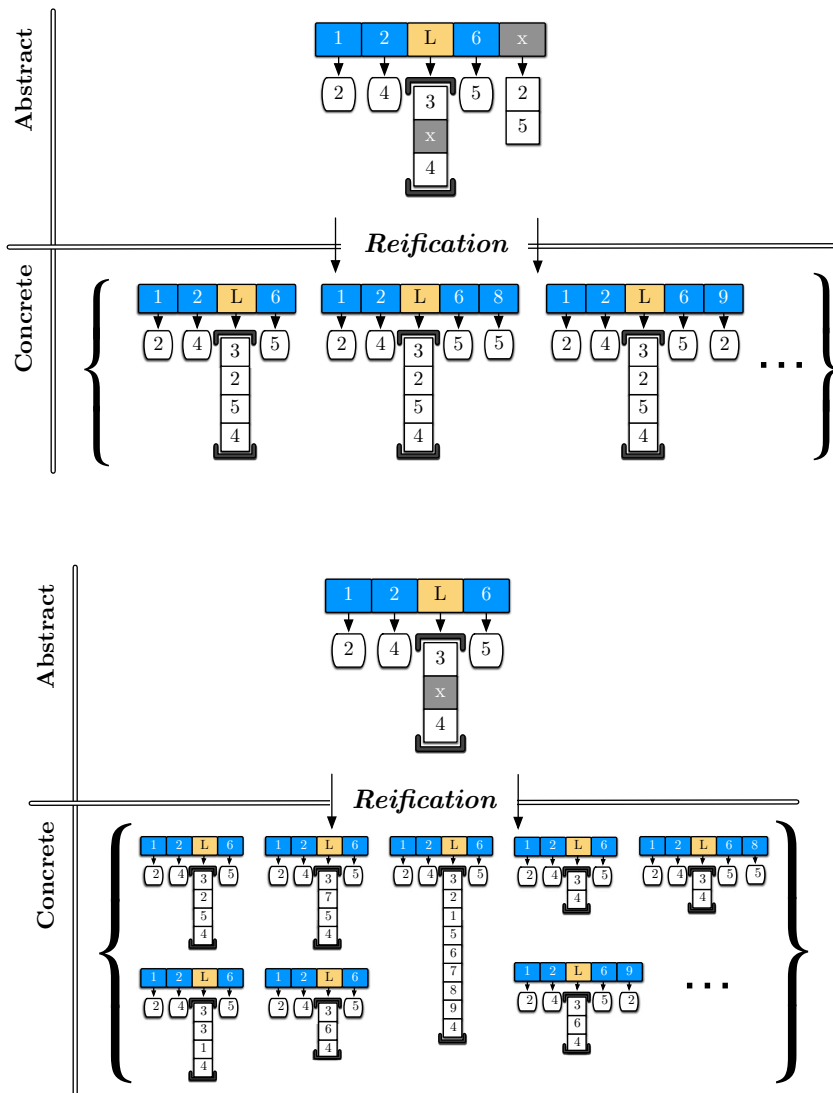


Figure 1.12.: If all abstract heap cells are present, reification rejoins them. Abstract heaps are also *completed* in all possible ways, to account for possible missing data. The results are thus sets of machine heaps.

There has been previous work with similar goals to structural separation logic. Wheelhouse’s *segment logic* [38, 70] restored an associative  $*$  operation to working with contexts. He achieved this by splitting data into a set of *segments*, where each segment was either *rooted* or labelled with a context hole. Models of assertions were sets of segments, which were separated by  $*$ . These models are somewhat similar to the abstract heaps of structural separation logic.

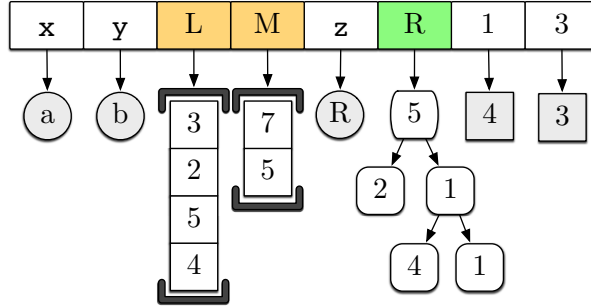
Unfortunately, reasoning with segment logic can be somewhat cumbersome. The structure of the models was not simple heaps, but rather *segment algebras*. To ensure  $*$  was associative, *binders* had to be introduced into assertions, so that a segment  $L \mapsto [3 \otimes 2 \otimes 5 \otimes 4]$  could not directly be represented as  $L \mapsto [3 \otimes \alpha \otimes 5 \otimes 4] * \alpha \mapsto 2$ . Instead, *revelation* (from the ambient calculus [18]) and *freshness quantification* was used, so that  $L \mapsto [3 \otimes 2 \otimes 5 \otimes 4] = \forall \alpha. \alpha \textcircled{R}(L \mapsto [3 \otimes \alpha \otimes 5 \otimes 4] * \alpha \mapsto 2)$ . By adding these to the assertion language, new rules were needed in the reasoning to manage them.

The combination of these bindings, and the non-heap model, means that the local reasoning provided by segment logic comes with rather a lot of baggage. With the advent of the views philosophy, the key ideas behind segment logic were refined, and formed the genesis of structural separation logic. The key advantages and disadvantages of each techniques reflect the technology available at their creation.

The design of structural and abstract heaps allows several data models to be combined together. Normal “low-level” separation logic reasoning can be performed alongside the use of a library representing, e.g. lists. Moreover, as the complexity of the reasoning is hidden behind reification operations, the assertion language remains simple. Abstract allocation and deallocation occur via a proof rule, justified by the properties of reification. These results mean that structural separation logic axioms for a library could easily be added to most other separation logic reasoning systems, from the original work of O’Hearn [63], through Parkinson’s separation logic for Java [56] and even Smith’s work on JavaScript [34]. The theory “bolts on” the side, giving extra expressively with minimal effort. We also expect that integration with tools such as Verifast [45] and JStar [25] will be straightforward. Our abstract heaps can exist alongside the symbolic heaps used by the tools, so as we develop automation for structural separation logic, we can extend and cooperate with the development of automation in separation logic.

## 2. A reasoning framework

This chapter presents a local reasoning framework for programs that manipulate *structured heaps*, providing a foundation for the rest of this thesis. Like a standard heap, a structured heap associates *addresses* with *values*. Unlike a standard heap, the values can have rich structure. In the below example, the *addresses*  $x, y, z, 1$  and  $3$  map to simple flat values, but the addresses  $L$  and  $M$  map to complete lists and  $R$  maps to a complete tree.



Our framework gives reasoning for *imperative machines*. Imperative machines provide a model for programming built from structured heaps, a concurrent programming language, and an operational semantics. They formalise structured heaps as sets of *addressed values*. Their definition is flexible, allowing many types of addresses, values and commands to be used. This enables the representation of programming *libraries*.

We give a program reasoning system based on *instrumented structured heaps*. Like structured heaps, these are formed of addresses and values, but are decorated with additional *instrumentation* to facilitate reasoning. Instrumented states are collected into sets called *views*, which can be converted into sets of structured heaps by a process of *reification*. This reification interprets the instrumentation, and allows views to represent possible states at each point in program execution. The views used by us are those of the Views framework [22], discussed in section 1.2.2. They allow us to build a *semantic reasoning system*, and construct Hoare triples that specify interesting program properties. We extend the work of [22] with a syntactic *assertion language* that describes views, and can be used as the pre- and post-conditions of a local Hoare calculus.



To aid understanding, the examples are designed to link our framework to well known local reasoning techniques. We show how to reason about programs manipulating *variables as resource*, *flat heaps*, and *resource with permissions*. Variables as resource is a commonly used technique for managing variables in separation logic style proofs [11]. Our term *flat heaps* refers to the original work on separation logic [63]. Augmenting resources with fractional permissions [10] has become a standard method for reasoning about shared resource. The full generality of our framework will be used to in upcoming chapters.

We will not use the full generality of structured heaps in this chapter, focusing on introducing the concepts of the framework via flat heaps. The purpose of this thesis is to extend the reasoning about these well understood heaps to reasoning about libraries that manipulate highly structured data. We will only hint at these uses here, but in future chapters, structured heaps will allow us to give imperative machines that directly manipulate this rich data. We will use them as the basis of structural separation logic in chapter 3, DOM in chapter 4 and POSIX file-systems in chapter 6. The reasoning system and soundness proofs presented here will be reused without change on these more complex values.

## 2.1. Imperative machines

Imperative machines are abstract computational systems consisting of three parts: a *state*, a *program*, and an *operational semantics*. Their definition is parametric in several places, allowing different types of addresses and values to be used.

### 2.1.1. Structured heaps

Our imperative machines have state. These states are *structured heaps*, in which data is represented by *addressed values*.

**Parameter 1** (Machine addresses and values). Assume a non-empty countable set of **machine addresses** MACHINEADDRS, ranged over by  $a, a_1, \dots, a_n$ . Assume also a non-empty countable set of **machine values** MACHINEVALS, ranged over by  $v, v_1, \dots, v_n$ .

Neither the set of addresses nor values need be infinite. There also need be no relationship between the sets MACHINEADDRS and MACHINEVALS.

**Example 1** (Machine addresses and values). The following are some example choices for machine addresses and values.

**Program variables:** Let  $\text{MACHINEADDRS}$  be a set of program variable names, and  $\text{MACHINEVALS}$  be the integers  $\mathbb{Z}$  and booleans  $\{\mathbf{true}, \mathbf{false}\}$ . This is one choice of addresses and values for *program variables* (e.g.  $\mathbf{x} = 5$ ). Notice that  $\text{MACHINEADDRS} \not\subset \text{MACHINEVALS}$ , as we do not store variable names within variables, nor do we consider integers or booleans as variable names. This relationship is different from separation logic, where addresses are contained within values.

**Flat heaps:** Let  $\text{MACHINEADDRS} = \mathbb{N}^+$ , and  $\text{MACHINEVALS} = \mathbb{N}$ . These choices are suitable for flat heaps, the style of model used in the original separation logic papers [63]. Notice both that  $\text{MACHINEADDRS}$  is infinite in size, and that  $\text{MACHINEVALS} \subset \text{MACHINEADDRS}$ .

**Trees:** Let  $\text{MACHINEADDRS} = \{\top\}$  (a single address), and  $\text{MACHINEVALS}$  be a set of trees. If the tree nodes have distinct identifiers, are given names, and have arbitrarily many children, then they are suitable for constructing *DOM trees*. If tree nodes have no identifiers, but have names distinct between siblings and arbitrarily many children, then these choices are suitable for a simple *file-system directory structure*. We will investigate systems of these types in chapters 4 and 6 respectively.

**Flat heap and variables:** We can combine multiple address/value choices together. For example,  $\text{MACHINEADDRS}$  could be variable names *and* the natural numbers. The set  $\text{MACHINEVALS}$  would then be  $\mathbb{Z} \cup \{\mathbf{true}, \mathbf{false}\}$ . This gives us a single heap which can store both variables and flat values. We refer to these as *primitive heaps*, and they will be our primary example in this chapter.

Addresses and values are paired together to form *heap cells*. The state of an imperative machine is a set of heap cells, where each address is unique. We call this a *structured heap*, or just *heap* if the context is clear. We will often give diagrams to structured heaps. An example for primitive heaps is given in figure 2.1.

It is sometimes convenient to prevent heaps from containing certain heap cells. One example of this is a type system for variables, where some variables must store values of a certain type. Another is when combining two sets of addresses, as in the primitive

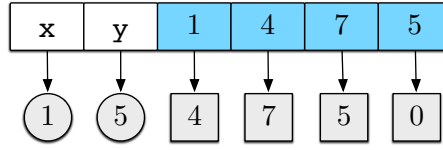


Figure 2.1.: Example rendering of a primitive heap, containing variable heap cells  $x \mapsto 1$  and  $y \mapsto 5$  and flat heap cells  $1 \mapsto 4$ ,  $4 \mapsto 7$ ,  $7 \mapsto 5$  and  $5 \mapsto 0$ .

heaps above, where we want to ensure that flat heap cells only contain values from  $\mathbb{N}$ . The definition of structured heaps accounts for these possibilities by being a subset of all definable structured heaps.

**Parameter 2** (Structured heaps). Given a set of machine addresses  $\text{MACHINEADDRS}$  and machine values  $\text{MACHINEVALS}$  (parameter 1), assume a set of **structured heaps**  $\text{STRUCTHEAPS}$ , ranged over by  $s_1, \dots, s_n$ , with type:

$$\text{STRUCTHEAPS} \subseteq \{s \mid s : \text{MACHINEADDRS} \xrightarrow{\text{fin}} \text{MACHINEVALS}\}$$

Imperative machines allow programs that cause machines to “go wrong”. The notion of “wrong” is situation specific, but is a common feature of languages and machines. For example, the commands of the flat heap example will “go wrong” when instructed to access an address not present in the structured heap. The singleton heap  $5 \mapsto 3$  contains no 4 address. When the command  $x := [4]$  is run, it will try to access address 4, and fail. Another example is the DOM tree library. Each DOM node in the tree has a unique identifier. Attempting to access an identifier not present in the tree will fail.

Whenever a machine attempts to perform an impossible operation, it enters a *fault state*. A choice of commands may have several ways of faulting. For example, DOM commands will also fail if instructed to create a node with a name containing certain reserved characters. We are not interested in the specific modes of failure, so there is only one fault state.

**Definition 1** (Fault state). Given a set of structured heaps  $\text{STRUCTHEAPS}$  (parameter 2), let  $\zeta$  be the single **fault state**, where  $\zeta \notin \text{STRUCTHEAPS}$ .

Structured heaps plus the fault state form the set of *outcome states*, representing the final result of a program.

**Definition 2** (Outcome states). Given a set of structured heaps  $\text{STRUCTHEAPS}$  (parameter 2) and a fault state  $\zeta$  (definition 1), the set of **outcome states** is defined as:

$$\text{OUTCOMES} \triangleq \text{STRUCTHEAPS} \cup \{\zeta\}$$

Our examples will almost all use program variables. We define a simple set of variables and values.

**Definition 3** (Program variables). Assume a set of program variables  $\text{PVARs} = \{x, y, z, a, i, \text{foo}, \dots\}$ . Assume also a set of program values  $\text{PVALs} = \mathbb{Z} \cup \{\mathbf{true}, \mathbf{false}\}$ .

**Example 2** (Variables as resource). By selecting the machine addresses and values (parameter 1) as  $\text{PVARs}$  and  $\text{PVALs}$  respectively, we can define the structured heaps (parameter 2) for variables as resource [11] as:

$$\text{STRUCTHEAPS} \triangleq \{s \mid s : \text{PVARs} \xrightarrow{\text{fin}} \text{PVALs}\}$$

**Example 3** (Flat heaps). To model flat heaps, select  $\text{MACHINEADDRS} = \mathbb{N}^+$  and  $\text{MACHINEVALS} = \mathbb{N}$ . The structured heaps are:

$$\text{STRUCTHEAPS} \triangleq \{s \mid s : \mathbb{N}^+ \xrightarrow{\text{fin}} \mathbb{N}\}$$

**Example 4** (Primitive heaps). Flat heaps alone are not useful, as programs cannot reference heap addresses. It is standard to use program variables to store these addresses. We thus combine the variable store and flat heap examples, calling the result **primitive heaps**.

We use program variables and values from definition 3, and so let  $\text{MACHINEADDRS} = \text{PVARs} \cup \mathbb{N}^+$ , and  $\text{MACHINEVALS} = \text{PVALs}$  (recalling that  $\mathbb{Z} \subset \text{PVALs}$ ). The heap definition is:

$$\text{STRUCTHEAPS} \triangleq \left\{ s \mid \begin{array}{l} s : \text{PVARs} \cup \mathbb{N}^+ \xrightarrow{\text{fin}} \text{PVALS}, \\ \forall a \in \text{dom}(s). a \in \mathbb{N}^+ \implies s(a) \in \mathbb{N} \end{array} \right\}$$

Notice that flat heap addresses are only associated with natural numbers, preventing heaps from mapping flat heap addresses to negative numbers or booleans.

**Comment 1.** In many presentations of similar work, machine states are often split in two, with the heap and variable store being different. Heaps are treated as linear resource, variables as “pure” facts. I will use only linear resource, adopting the variables as resource approach from [11]. This allows the imperative machine states to be of a uniform type, and so simplifies the reasoning. It is possible to use a separate variable store with the work in this thesis but, as handling variables is orthogonal to my library reasoning programme, I opt for presentational simplicity.

### 2.1.2. Programming language

Imperative machines programs are written using a simple concurrent WHILE language. Programs consist of basic operations (the atomic commands and skip), flow control constructs (sequencing, conditional choice and looping), and parallel composition.

**Definition 4** (Basic programming language). The **programs** of the basic programming language PROGRAMS, ranged over by  $\mathbb{C}, \mathbb{C}_1, \dots, \mathbb{C}_n$ , are defined by induction as follows: for all  $B$  in BOOLEXPRES and  $C \in \text{ATOMICCMDS}$  (to be given by parameters 3 and 5 respectively)

$\mathbb{C} ::=$	$\mathbb{C}$	Atomic Commands
	<b>skip</b>	No operation
	$\mathbb{C}_1; \mathbb{C}_2$	Sequencing
	<b>if</b> (B) $\mathbb{C}_1$ <b>else</b> $\mathbb{C}_2$	Conditional choice
	<b>while</b> (B) $\mathbb{C}$	Conditional loop
	$\mathbb{C}_1 \parallel \mathbb{C}_2$	Parallel composition

The **if** and **while** commands are parametrised by *boolean expressions*. These expressions are evaluated to determine which branch of a conditional choice to take, and to

guard loops. In most situations, the boolean expressions will be a subset of some richer expression language. However, we only mandate the existence of boolean expressions, as they are all we need to give an operational semantics to the language.

**Parameter 3** (Boolean expressions). Assume a set of **boolean expressions** `BOOLEXPRS`, ranged over by  $B_1, \dots, B_n$ .

A boolean expression is evaluated on a structured heap, resulting in a boolean truth value or a fault. Faults indicate a failure of evaluation. The heaps in which boolean expression evaluation faults are dependent on the choice of expression language. Typical reasons for faulting are an expression referencing an unassigned variable or type errors such as performing arithmetic on strings but, as discussed, we consider them all be a single fault type. We make the assumption that expression evaluation never alters the state, and is deterministic.

**Parameter 4** (Boolean expression evaluation). Given a set of boolean expressions `BOOLEXPRS` (parameter 3) and structured heaps `STRUCTHEAPS` (parameter 2), assume a **boolean expression evaluation** function  $\llbracket \cdot \rrbracket(\cdot) : \text{BOOLEXPRS} \rightarrow \text{STRUCTHEAPS} \rightarrow \{\mathbf{true}, \mathbf{false}, \zeta\}$ . The evaluation of boolean expression `B` in state  $s$  is written as  $\llbracket B \rrbracket(s)$ .

**Example 5** (Expressions for primitive heaps). We define boolean expressions for our primitive heap example in terms of **basic expressions**. Basic expressions are either a variable lookup (e.g. `x`), a literal value (e.g. `true` or `5`), or the sum of two other expressions. Formally, the **basic program expressions** `EXPRS`, ranged over by  $e, e_1, \dots, e_n$ , are defined inductively as: for all  $x \in \text{PVARs}, v \in \text{PVALs}$

$$\begin{array}{ll}
 e ::= & v \quad \text{Literal value} \\
 & | x \quad \text{Program variable} \\
 & | e_1 + e_2 \quad \text{Integer sum}
 \end{array}$$

Literal value expressions evaluate to the literal itself. The evaluation of a program variable expression just looks up the variable in the heap, and faults if it is not present. Integer sums evaluate to the summation of evaluating the operands (faulting if the results are not integers). Formally, using the expressions `EXPRS`, primitive heaps

STRUCTHEAPS (example 4) and program values PVALS (example 2), the evaluation function for basic expressions  $\llbracket \cdot \rrbracket(\cdot) : \text{EXPRS} \rightarrow \text{STRUCTHEAPS} \rightarrow \text{PVALS} \cup \{\zeta\}$  is defined as:

$$\begin{aligned} \llbracket v \rrbracket(s) &\triangleq v \\ \llbracket \mathbf{x} \rrbracket(s) &\triangleq \begin{cases} s(\mathbf{x}) & \text{if } \mathbf{x} \in \text{dom}(s) \\ \zeta & \text{otherwise} \end{cases} \\ \llbracket e_1 + e_2 \rrbracket(s) &\triangleq \begin{cases} \llbracket e_1 \rrbracket(s) + \llbracket e_2 \rrbracket(s) & \text{if } \llbracket e_1 \rrbracket(s), \llbracket e_2 \rrbracket(s) \in \mathbb{Z} \\ \zeta & \text{otherwise} \end{cases} \end{aligned}$$

Boolean expressions are then defined in terms of these basic expressions. The primitive boolean expression is just a basic expression, which will evaluate to fault unless the basic expression evaluates to a boolean value. The others are standard, but note that equality comparison is between boolean expressions rather than basic expressions. This allows programs to include idiomatic boolean expressions like  $\neg \mathbf{B} = \mathbf{true}$ . Formally, using the expressions EXPRS, the **boolean expressions** for primitive heaps, BOOLEXPRS, are defined inductively as: for all  $e, e_1, e_2 \in \text{EXPRS}$

$$\begin{array}{ll} \mathbf{B} ::= & | e \quad \text{Basic expression} \\ & | \neg \mathbf{B} \quad \text{Negation} \\ & | \mathbf{B}_1 = \mathbf{B}_2 \quad \text{Equality} \\ & | e_1 < e_2 \quad \text{Inequality} \end{array}$$

The evaluation for the boolean expressions is standard. Type checking is performed, with faults occurring when the types are incorrect. Formally, the evaluation function for boolean expressions  $\llbracket \cdot \rrbracket(\cdot) : \text{BOOLEXPRS} \rightarrow \text{STRUCTHEAPS} \rightarrow \{\mathbf{true}, \mathbf{false}, \zeta\}$  is defined as:

$$\begin{aligned}
\llbracket e \rrbracket(s) &\triangleq \begin{cases} \llbracket e \rrbracket(s) & \text{if } \llbracket e \rrbracket(s) \in \{\mathbf{true}, \mathbf{false}\} \\ \not\downarrow & \text{otherwise} \end{cases} \\
\llbracket \neg B \rrbracket(s) &\triangleq \begin{cases} \neg \llbracket B \rrbracket(s) & \text{if } \llbracket B \rrbracket(s) \in \{\mathbf{true}, \mathbf{false}\} \\ \not\downarrow & \text{otherwise} \end{cases} \\
\llbracket B_1 = B_2 \rrbracket(s) &\triangleq \begin{cases} \llbracket B_1 \rrbracket(s) = \llbracket B_2 \rrbracket(s) & \text{if } \llbracket B_1 \rrbracket(s), \llbracket B_2 \rrbracket(s) \neq \not\downarrow \\ \not\downarrow & \text{otherwise} \end{cases} \\
\llbracket e_1 < e_2 \rrbracket(s) &\triangleq \begin{cases} \llbracket e_1 \rrbracket(s) < \llbracket e_2 \rrbracket(s) & \text{if } \llbracket e_1 \rrbracket(s), \llbracket e_2 \rrbracket(s) \in \mathbb{Z} \\ \not\downarrow & \text{otherwise} \end{cases}
\end{aligned}$$

The basic programming language of definition 4 is also parameterised by *atomic commands*. These commands mutate the heaps, and are *atomic* as they do so in an instantaneous and uninterruptible fashion. Command choices range from simple variable assignment, through heap cell allocation and deallocation (in flat heaps), to commands found in complex libraries such as DOM and POSIX.

**Parameter 5** (Atomic commands). Assume a set of **atomic commands**  $\text{ATOMICCMDS}$ , ranged over by  $C, C_1, \dots, C_n$ .

Each atomic command is associated with an *action* that defines its behaviour. An action is a total function, transforming input heaps into *sets* of output heaps. Commands can thus be non-deterministic.

**Parameter 6** (Atomic command actions). Given a set of outcome states (definition 2), for each atomic command  $C \in \text{ATOMICCMDS}$  (parameter 5), assume an atomic command action  $\llbracket C \rrbracket(\cdot) : \text{STRUCTHEAPS} \rightarrow \mathcal{P}(\text{OUTCOMES})$ .

It is occasionally desirable that some commands do not fault, yet never terminate when executed in a state (for example, concurrency primitives such as `lock`). This is modeled by taking the atomic action in such states to be the empty set.

**Example 6** (Commands and actions for primitive heaps). We give a standard set of commands for interacting with flat heaps and variables: assignments of expressions



to variables; allocation and deallocation of flat heap cells; assignment to flat heap cells; and the dereferencing of flat heap cells. Formally, given program variables PVARs (definition 3) and expressions EXPRS (example 5), the commands are: for all  $\mathbf{x} \in \text{PVARs}$  and  $e, e_1, e_2 \in \text{EXPRS}$

$\mathbf{x} := e$	Variable assignment
$\mathbf{x} := \text{alloc}()$	Cell allocation
$\text{free}(e)$	Cell deallocation
$[e_1] := e_2$	Cell update
$\mathbf{x} := [e]$	Cell dereference

Given that  $f[d \mapsto c]$  denotes a function that behaves as  $f$ , except that  $d$  maps to  $c$ , the atomic command actions for the commands are: for all  $\mathbf{x} \in \text{PVARs}$  and  $e, e_1, e_2 \in \text{EXPRS}$

$$\begin{aligned}
\llbracket \mathbf{x} := e \rrbracket(s) &\triangleq \begin{cases} \{s[\mathbf{x} \mapsto \llbracket e \rrbracket(s)]\} & \text{if } \llbracket e \rrbracket(s) \neq \perp \\ \{\perp\} & \text{otherwise} \end{cases} \\
\llbracket \mathbf{x} := \text{alloc}() \rrbracket(s) &\triangleq \{s[\mathbf{x} \mapsto i, i \mapsto v] \mid i \in (\mathbb{N}^+ \setminus \text{dom}(s)), v \in \mathbb{N}\} \\
\llbracket \text{free}(e) \rrbracket(s) &\triangleq \begin{cases} \{s \upharpoonright_{\text{dom}(s) \setminus \{\llbracket e \rrbracket(s)\}}\} & \text{if } \llbracket e \rrbracket(s) \in \text{dom}(s) \\ \{\perp\} & \text{otherwise} \end{cases} \\
\llbracket [e_1] := e_2 \rrbracket(s) &\triangleq \begin{cases} \{s[\llbracket e_1 \rrbracket(s) \mapsto \llbracket e_2 \rrbracket(s)]\} & \text{if } \llbracket e_1 \rrbracket(s) \in \text{dom}(s), \\ & \llbracket e_2 \rrbracket(s) \neq \perp \\ \{\perp\} & \text{otherwise} \end{cases} \\
\llbracket \mathbf{x} := [e] \rrbracket(s) &\triangleq \begin{cases} \{s[\mathbf{x} \mapsto s(\llbracket e \rrbracket(s))]\} & \text{if } \llbracket e \rrbracket(s) \in \text{dom}(s) \\ \{\perp\} & \text{otherwise} \end{cases}
\end{aligned}$$

The assignment action is terminating and deterministic (as expression evaluation is deterministic). Faults in evaluating the expression become faults of the command action.

The action for allocation adds a new cell to the heap at a fresh address. This address is assigned to the variable, and is chosen non-deterministically from those not already used by the heap. As there are infinitely many addresses to pick (recall the heap domain is  $\mathbb{N}^+$ ), this command never faults. The contents of the new heap cell is also

non-deterministically selected from all possible values. This models the behaviour of heap cell allocation in a ‘C’-like language that has no memory constraints.

The deallocation action removes a cell from the heap. This command can fault if the evaluation of the expression does, or if the contents are not the address of a heap cell. It is deterministic. Similarly, the cell update command alters the contents of a heap cell referenced by an expression. If either expression faults, or the first expression does not reference a flat heap cell, the command faults. The cell dereference action is similar to normal variable assignment, but looks up the result of evaluating the expression in the heap. We could provide an expression for heap cell dereference, but instead use a command. This will simplify reasoning about commands that use expression evaluation.

### 2.1.3. Operational semantics

We give the meaning of programs via an operational semantics. Our semantics defines a small-step transition system, relating pairs of programs and structured heaps to triples of action labels, continuation programs and outcomes. The action label indicates what interactions a step may have with the structured heap. These actions will form a trace which, along with the heap at each step, can be seen as the meaning of a program. As in [12], these trace semantics are necessary to provide program reasoning for the parallel composition rule. Moreover, by associating actions with each step taken during evaluation, the soundness of the reasoning will be easier to prove.

**Definition 5** (Action labels). Given a set of boolean expression  $\text{BOOLEXPRS}$  (parameter 3) and atomic commands  $\text{ATOMICCMDS}$  (parameter 5), the set of **action labels**  $\text{ACTIONLBS}$ , ranged over by  $\alpha, \alpha_1, \dots, \alpha_n$ , consists of labels for: the identity action; the action of evaluation for each boolean expression to either true, false or fault; and the action of each atomic command:

$\text{ACTIONLBS} \triangleq$	$\begin{aligned} & \{\text{ID}\} \\ \cup & \{\mathcal{E}_{\top}(B) \mid B \in \text{BOOLEXPRS}\} \\ \cup & \{\mathcal{E}_{\perp}(B) \mid B \in \text{BOOLEXPRS}\} \\ \cup & \{\mathcal{E}_f(B) \mid B \in \text{BOOLEXPRS}\} \\ \cup & \{\mathcal{A}(C) \mid C \in \text{ATOMICCMDS}\} \end{aligned}$	<p><b>Label denotes...</b></p> <ul style="list-style-type: none"> <li>no interaction with heap</li> <li>evaluation of B on heap, resulting in true</li> <li>evaluation of B on heap, resulting in false</li> <li>evaluation of B on heap, resulting in fault</li> <li>execution of atomic command C on heap</li> </ul>
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The use of different labels for the outcome of evaluating boolean expressions will ease the proof when showing soundness of the `if` and `while` rules in our reasoning system.

With these labels, we can define the single-step transition relation for our operational semantics. It represents one “computation step” of an imperative machine.

**Definition 6** (Single-step labelled transition relation). Given programs PROGRAMS (definition 4), structured heaps STRUCTHEAPS (parameter 2), atomic command actions  $\llbracket C \rrbracket(\cdot)$  (parameter 6), action labels ACTIONLBLs (definition 5), and outcome states OUTCOMES (definition 2), the **labelled single-step transition relation**  $\longrightarrow_{\mathcal{C}}$  (PROGRAMS  $\times$  STRUCTHEAPS)  $\times$  (ACTIONLBLs  $\times$  PROGRAMS  $\times$  OUTCOMES), with elements  $((C_1, s_1), (\alpha, C_2, s_2)) \in \longrightarrow_{\mathcal{C}}$  written  $C_1, s_1 \xrightarrow{\alpha} C_2, s_2$ , is defined by the following rules:

<p style="text-align: center; margin: 0;">ATOMIC COMMAND:</p> $\frac{s_o \in \llbracket C \rrbracket(s)}{C, s \xrightarrow{\mathcal{A}(C)} \mathbf{skip}, s_o}$	<p style="text-align: center; margin: 0;">SEQ SKIP:</p> $\frac{}{(\mathbf{skip}; C), s \xrightarrow{\text{ID}} C, s}$
<p style="text-align: center; margin: 0;">SEQ REDUCE:</p> $\frac{C_1, s \xrightarrow{\alpha} C'_1, s_o}{(C_1; C_2), s \xrightarrow{\alpha} (C'_1; C_2), s_o}$	<p style="text-align: center; margin: 0;">IF TRUE:</p> $\frac{\llbracket B \rrbracket(s) = \mathbf{true}}{\mathbf{if} (B) C_1 \mathbf{else} C_2, s \xrightarrow{\mathcal{E}_{\top}(B)} C_1, s}$
<p style="text-align: center; margin: 0;">IF FALSE:</p> $\frac{\llbracket B \rrbracket(s) = \mathbf{false}}{\mathbf{if} (B) C_1 \mathbf{else} C_2, s \xrightarrow{\mathcal{E}_{\perp}(B)} C_2, s}$	<p style="text-align: center; margin: 0;">IF FAULT:</p> $\frac{\llbracket B \rrbracket(s) = \not\downarrow}{\mathbf{if} (B) C_1 \mathbf{else} C_2, s \xrightarrow{\mathcal{E}_{\not\downarrow}(B)} \mathbf{skip}, \not\downarrow}$
<p style="text-align: center; margin: 0;">WHILE TRUE:</p> $\frac{\llbracket B \rrbracket(s) = \mathbf{true}}{\mathbf{while} (B) C_1, s \xrightarrow{\mathcal{E}_{\top}(B)} (C_1; \mathbf{while} (B) C_1), s}$	
<p style="text-align: center; margin: 0;">WHILE FALSE:</p> $\frac{\llbracket B \rrbracket(s) = \mathbf{false}}{\mathbf{while} (B) C_1, s \xrightarrow{\mathcal{E}_{\perp}(B)} \mathbf{skip}, s}$	<p style="text-align: center; margin: 0;">WHILE FAULT:</p> $\frac{\llbracket B \rrbracket(s) = \not\downarrow}{\mathbf{while} (B) C_1, s \xrightarrow{\mathcal{E}_{\not\downarrow}(B)} \mathbf{skip}, \not\downarrow}$

$$\begin{array}{c}
\text{PAR LEFT REDUCE:} \\
\frac{\mathbb{C}_1, s \xrightarrow{\alpha} \mathbb{C}'_1, s_o}{(\mathbb{C}_1 \parallel \mathbb{C}_2) \xrightarrow{\alpha} (\mathbb{C}'_1 \parallel \mathbb{C}_2), s_o} \\
\text{PAR LEFT SKIP:} \\
\frac{}{(\mathbf{skip} \parallel \mathbb{C}), s \xrightarrow{\text{ID}} \mathbb{C}, s}
\end{array}
\qquad
\begin{array}{c}
\text{PAR RIGHT REDUCE:} \\
\frac{\mathbb{C}_2, s \xrightarrow{\alpha} \mathbb{C}'_2, s_o}{(\mathbb{C}_1 \parallel \mathbb{C}_2) \xrightarrow{\alpha} (\mathbb{C}_1 \parallel \mathbb{C}'_2), s_o} \\
\text{PAR RIGHT SKIP:} \\
\frac{}{(\mathbb{C} \parallel \mathbf{skip}), s \xrightarrow{\text{ID}} \mathbb{C}, s}
\end{array}$$

There is no transition for the program **skip**. This program is considered to be completely executed, and to have **terminated**.

Via the small-step transition relation, we define a multi-step program evaluation relation which takes programs through many small-steps to a final outcome. It is effectively the reflexive, transitive closure of the single-step evaluation relation, but checks that the outcome of each step is not fault before taking another. If any step does produce fault, the multi-step evaluation relation immediately relates the program to fault.

**Definition 7** (Multi-step program evaluation relation). Given programs PROGRAMS (definition 4), structured heaps STRUCTHEAPS (parameter 2) outcome states OUTCOMES (definition 2), and the labelled single-step transition relation (definition 6), the **multi-step program evaluation relation**  $\rightsquigarrow_{\mathbb{C}}$  (PROGRAMS  $\times$  STRUCTHEAPS)  $\times$  OUTCOMES, with elements  $((\mathbb{C}, s), s_o) \in \rightsquigarrow$  written  $\mathbb{C}, s \rightsquigarrow s_o$ , is defined by the following rules:

$$\frac{\mathbb{C}, s \xrightarrow{\alpha} \mathbb{C}', s_o \quad s_o \in \text{STRUCTHEAPS} \quad \mathbb{C}', s_o \rightsquigarrow s_o'}{\mathbb{C}, s \rightsquigarrow s_o'}$$

$$\frac{}{\mathbf{skip}, s \rightsquigarrow s} \qquad \frac{\mathbb{C}, s \xrightarrow{\alpha} \mathbb{C}', \perp}{\mathbb{C}, s \rightsquigarrow \perp}$$

Notice not all programs will have an outcome in the multi-step evaluation; specifically, programs with infinite loops never reduce to an outcome. However, programs that fault always have an outcome, as they immediately reduce to the fault state. Programs therefore do one of three things: *diverge*, never terminating nor faulting; *terminate*, ending with some structured heap; or *fault*, failing somehow. We call the process of a machine applying the transition rules *program execution* (or *running* a program).

By examining the transition labels on each step used in the multi-step evaluation, we can see a program execution as a trace of interactions with the heap. Every label indicates either no interaction (the ID label), an interaction that does not update the heap (evaluating a boolean expression, the  $\mathcal{E}$  labels), or arbitrary update (performing an atomic command,  $\mathcal{A}(c)$ ). As programs may not terminate, traces need not be finite. Each program can be associated with many traces due to command non-determinism.

**Example 7** (Traces). The simple program  $x := 1; y := 2$  has the trace  $\mathcal{A}(x := 1), \mathcal{A}(y := 2)$ , indicating the two actions on the heap are performed in order. The program `while (true) skip` has the infinite trace  $\mathcal{E}_{\top}(\text{true}), \text{ID}, \mathcal{E}_{\top}(\text{true}), \text{ID}, \mathcal{E}_{\top}(\text{true}), \text{ID}, \dots$ . By examining the literal expression `true`, it is evident no heap access is performed. The program  $x := 1 \parallel y := 2$  has the two possible traces  $\mathcal{A}(x := 1), \mathcal{A}(y := 2)$  and  $\mathcal{A}(y := 2), \mathcal{A}(x := 1)$ , because parallel composition interleaves evaluation in a non-deterministic order.

## 2.2. Abstracting program states

We now turn to reasoning about imperative machines, providing *local* reasoning in the style of separation logic. To give pre- and post-conditions for programs, we first build an abstract representation of structured heaps. These are sets of *instrumented structured heaps*. The types of instrumentation depend on the goals of the reasoning being performed. Many separation logics (e.g. [63]) require no instrumentation. However, concurrent separation logic benefits from *fractional permissions* - rational numbers associated with values indicating ownership. In chapter 3, we will use rich instrumentation to enable us to split abstract structured data.

### 2.2.1. Instrumented structured heaps

Structured heaps provide the state of imperative machines. We now define *instrumented structured heaps*. Sets of these can informally be seen as the “states” of the *program reasoning*, and will be used to abstract the possible states of an imperative machine. These sets will provide the pre- and post-condition triples of our Hoare logic. A single instrumented structured heap consists of instrumented addresses mapping to instrumented values.

**Parameter 7** (Instrumented addresses and values). Assume a set of **instrumented addresses**  $\text{INSTADDRS}$ , ranged over by  $\mathbf{a}, \mathbf{a}_1, \dots, \mathbf{a}_n$ . Assume also a set of **instrumented values**  $\text{INSTVALS}$ , ranged over by  $\mathbf{v}, \mathbf{v}_1, \dots, \mathbf{v}_n$ .

**Parameter 8** (Instrumented structured heaps). Given sets of instrumented addresses  $\text{INSTADDRS}$  and values  $\text{INSTVALS}$  (parameter 7), assume a set of **instrumented structured heaps**  $\text{INSTHEAPS}$ , ranged over by  $\mathbf{s}, \mathbf{s}_1, \dots, \mathbf{s}_n$ , with type:

$$\text{INSTHEAPS} \subseteq \{ \mathbf{s} \mid \mathbf{s} : \text{INSTADDRS} \xrightarrow{\text{fin}} \text{INSTVALS} \}$$

Define also the **empty instrumented structured heap**  $\mathbf{0}_{\mathbf{s}}$  as the function with empty domain and co-domain,  $\mathbf{0}_{\mathbf{s}} \triangleq \{ \} \mapsto \{ \}$ .

**Example 8** (Instrumented primitive heaps). Primitive heaps (example 4) can be used without any instrumentation, as in the original separation logic work [52]. The structured heaps used for the reasoning are therefore identical to the structured heaps used by the machine. Let  $\text{INSTADDRS} = \text{PVARs} \cup \mathbb{N}^+$ , and  $\text{INSTVALS} = \text{PVALS}$ , and define the instrumented structured heaps as:

$$\text{INSTHEAPS} \triangleq \left\{ s \mid \begin{array}{l} s : \text{PVARs} \cup \mathbb{N}^+ \xrightarrow{\text{fin}} \text{PVALS}, \\ \forall a \in \text{dom}(s). a \in \mathbb{N}^+ \implies s(a) \in \mathbb{N} \end{array} \right\}$$

**Example 9** (Primitive heaps with fractional permissions). Recall the fractional permissions introduced in section 1.2.1. We can create instrumented structured heaps that use these permissions by pairing values with some  $\pi \in (0, 1]$ . Let  $\text{INSTADDRS} = \text{PVARs} \cup \mathbb{N}^+$  as in example 8, but now define  $\text{INSTVALS} = \text{PVALS} \times \mathbb{Q}$ . The instrumented structured heaps become:

$$\text{INSTHEAPS} \triangleq \left\{ s \mid \begin{array}{l} s : \text{INSTADDRS} \xrightarrow{\text{fin}} \text{INSTVALS}, \\ \forall a \in \text{dom}(s). a \in \mathbb{N}^+ \implies (s(a) \downarrow_1 \in \mathbb{N} \wedge s(a) \downarrow_2 \in (0, 1]) \end{array} \right\}$$

This definition mirrors that of the heaps for normal primitive heaps but checks that each fraction associated with the flat heap cells is in the range  $(0, 1]$ .

An instrumented heap can be *reified* into a set of structured heaps. Reification gives an interpretation of the instrumented heap, typically based upon the instrumentation it is using. There are no constraints a reification function. The choice endows a *meaning* or *interpretation* to instrumented heaps, which will later define the kinds of properties we can prove. We first define *primitive reification*, which works on a single instrumented heap. Full reification will be defined on sets of heaps as the pointwise lift of this function.

**Parameter 9** (Primitive reification function). Given instrumented heaps `INSTHEAPS` (parameter 8) and structured heaps `STRUCTHEAPS` (parameter 2), assume a **primitive reification function**  $\llbracket \cdot \rrbracket : \text{INSTHEAPS} \rightarrow \mathcal{P}(\text{STRUCTHEAPS})$ .

Notice that primitive reification does not generate outcome states, only structured heaps. Thus, the reification of an instrumented heap never includes the fault state. This *fault avoiding* reification ensures that the machine states described by instrumented heaps are always useful.

The behaviour of primitive reification is dependent on both the instrumentation associated with heaps, and what the user of this framework wishes the abstraction to mean. Sometimes, as in the primitive heap case (example 4), we need no instrumentation. In this case, reification will be simple.

**Example 10** (Reification for primitive heaps). The instrumented primitive heaps of example 8 have no instrumentation. Pick the structured heaps as those of 4, and then select the primitive reification function as  $\llbracket \mathbf{s} \rrbracket = \{\mathbf{s}\}$ .

As the reification function is a free choice, we can create quite different interpretations of the same underlying instrumented heaps.

**Example 11** (Alternative reification for primitive heaps). An alternative choice of primitive reification for primitive heaps interprets the instrumented heap as every primitive structured heap which is *consistent* with the data in the instrumented heap:

$$\llbracket \mathbf{s} \rrbracket = \{s \in \text{STRUCTHEAPS} \mid \forall a \in \text{dom}(\mathbf{s}). s(a) = \mathbf{s}(a)\}$$

Here, the instrumented heap  $\{1 \mapsto 5\}$  primitively reifies to *every* heap in which 1 addresses value 5. We will see how the choice of primitive reification in example 10 generates program reasoning similar to *classical* separation logic. This alternative choice generates reasoning similar to *intuitionistic* separation logic. For the rest of this chapter, we will work with the first choice unless otherwise stated.

Often, instrumentation must be removed to generate machine heaps, as in the case of the fractional permissions (example 9).

**Example 12** (Reification for primitive heaps with fractional permissions). Fractional permissions are used purely to facilitate reasoning correctness. They have no bearing on the underlying structured heaps, so we pick a primitive reification function that just erases the permission:

$$\llbracket \mathbf{s} \rrbracket = \lambda a. \mathbf{s}(a) \downarrow_1$$

Instrumented heaps can describe *partial* machine states. For example, the instrumented heap  $\mathbf{x} \xrightarrow{0.5} 1$  (example 9) describes only half permission on a heap cell, implying that the other half is missing. It is thus natural to compose instrumented heaps. To this end, we introduce *primitive instrumented heap composition*, allowing two instrumented heaps to be joined. Full composition will be the pointwise lift of this to sets of instrumented heaps.

**Parameter 10** (Primitive instrumented heap composition). Given instrumented heaps  $\text{INSTHEAPS}$  with their unit  $\mathbf{0}_s$  (parameter 8), assume a **primitive instrumented heap composition** operator:

$$\otimes : \text{INSTHEAPS} \rightarrow \text{INSTHEAPS} \rightarrow \text{INSTHEAPS}$$

where the operator is associative and commutative with unit  $\mathbf{0}_s$ . That is, for all  $\mathbf{s}, \mathbf{s}_1, \mathbf{s}_2, \mathbf{s}_3 \in \text{INSTHEAPS}$   $(\mathbf{s}_1 \otimes \mathbf{s}_2) \otimes \mathbf{s}_3 = \mathbf{s}_1 \otimes (\mathbf{s}_2 \otimes \mathbf{s}_3)$ ,  $\mathbf{s}_1 \otimes \mathbf{s}_2 = \mathbf{s}_2 \otimes \mathbf{s}_1$  and  $\mathbf{s} \otimes \mathbf{0}_s = \mathbf{s}$ .

The choice of primitive composition function depends on the instrumentation being used. Indeed, a key use of instrumentation is to restrict the set of valid compositions. In the separation logic example (example 17), a good choice is disjoint function union, allowing two heaps to be composed only if they share no addresses in common. However,



when using fractional permissions, two heaps can compose even if they have addresses in common, as long as the sum of the permissions associated with these is at most 1. Good choices of composition are those that enable flexible *decomposition*, as composition implicitly defines *separation* ( $\mathbf{s} = \mathbf{s}_1 \otimes \mathbf{s}_2$  can be read both as “ $\mathbf{s}_1 \otimes \mathbf{s}_2$  compose to give  $\mathbf{s}$ ”, but also as “ $\mathbf{s}$  decomposes to give  $\mathbf{s}_1 \otimes \mathbf{s}_2$ , where  $\mathbf{s}_1$  and  $\mathbf{s}_2$  are separate”).

**Example 13** (Primitive composition for primitive heaps). For instrumented primitive heaps (example 8), primitive composition joins heaps with disjoint domains (that is, that share no variables or heap cells):

$$\mathbf{s}_1 \otimes \mathbf{s}_2 \triangleq \begin{cases} \mathbf{s}_1 \cup \mathbf{s}_2 & \text{if } \text{dom}(\mathbf{s}_1) \cap \text{dom}(\mathbf{s}_2) = \emptyset \\ \text{undefined} & \text{otherwise} \end{cases}$$

**Example 14** (Primitive composition with fractional permissions). For primitive heaps using fractional permissions (example 9), primitive composition joins instrumented states by taking the function union, but in the case of an address being in both, *adding* the permission. If any permission would exceed 1, composition is undefined.

$$\mathbf{s}_1 \otimes \mathbf{s}_2 \triangleq \begin{cases} \text{undefined} & \text{if } \exists \mathbf{a} \in \text{dom}(\mathbf{s}_1) \cap \text{dom}(\mathbf{s}_2) \text{ s.t. } \mathbf{s}_1(\mathbf{a}) \downarrow_2 + \mathbf{s}_2(\mathbf{a}) \downarrow_2 > 1 \\ \lambda \mathbf{a}. \begin{cases} \mathbf{s}_1(\mathbf{a}) & \text{if } \mathbf{a} \in \text{dom}(\mathbf{s}_1) \setminus \text{dom}(\mathbf{s}_2) \\ \mathbf{s}_2(\mathbf{a}) & \text{if } \mathbf{a} \in \text{dom}(\mathbf{s}_2) \setminus \text{dom}(\mathbf{s}_1) \\ (\mathbf{s}_1(\mathbf{a}) \downarrow_1, \mathbf{s}_1(\mathbf{a}) \downarrow_2 + \mathbf{s}_2(\mathbf{a}) \downarrow_2) & \text{if } \mathbf{s}_1(\mathbf{a}) \downarrow_1 = \mathbf{s}_2(\mathbf{a}) \downarrow_1 \\ \text{undefined} & \text{otherwise} \end{cases} \end{cases}$$

Notice that the addition of fractional permissions has allowed *more separation* than is possible with composition for primitive heaps (example 13).

### 2.2.2. Views

We are now in a position to define *views* over instrumented structured heaps. The views system, introduced in [22] with a summary given in section 1.2.2, is a program reasoning

framework based around state abstractions also known as views. Our views are formed from choices of instrumented heaps, with the associated primitive reification and composition functions. We call this collection of objects an *addressed value view*.

**Definition 8** (Addressed value view). Given a set of structured heaps  $\text{STRUCTHEAPS}$  and instrumented structured heaps for them  $\text{INSTHEAPS}$  (parameter 2 and parameter 8 respectively), a primitive reification function  $\llbracket \cdot \rrbracket : \text{INSTHEAPS} \rightarrow \mathcal{P}(\text{STRUCTHEAPS})$  (parameter 9) and a primitive composition function with unit  $\mathbf{0}_s$ ,  $\otimes : \text{INSTHEAPS} \rightarrow \text{INSTHEAPS} \rightarrow \text{INSTHEAPS}$  (parameter 10), an **addressed value view** consists of a set of **views**  $\text{VIEWS}$ , a **reification function**  $\llbracket \cdot \rrbracket$ , a binary **composition operator**  $*$ , and a **unit element**  $0$ :

$$\text{Addressed value view} = \left( \begin{array}{l} \text{VIEWS}, \llbracket \cdot \rrbracket : \text{VIEWS} \rightarrow \mathcal{P}(\text{STRUCTHEAPS}), \\ * : \text{VIEWS} \rightarrow \text{VIEWS} \rightarrow \text{VIEWS}, 0 \end{array} \right)$$

with the properties:

1.  $\text{VIEWS} \triangleq \mathcal{P}(\text{INSTHEAPS})$  is a set of **views**, ranged over by  $p, p_1, \dots, p_n, p, q, r$ .
2. The **reification function**,  $\llbracket \cdot \rrbracket : \text{VIEWS} \rightarrow \mathcal{P}(\text{STRUCTHEAPS})$  is defined as the pointwise lift of primitive reification:

$$\llbracket p \rrbracket \triangleq \bigcup \{ \llbracket \mathbf{s} \rrbracket \mid \mathbf{s} \in p \}$$

3. The **composition function**  $* : \text{VIEWS} \rightarrow \text{VIEWS} \rightarrow \text{VIEWS}$  is the pointwise lift of primitive composition:

$$p * q \triangleq \{ \mathbf{s}_1 \otimes \mathbf{s}_2 \mid \mathbf{s}_1 \in p, \mathbf{s}_2 \in q \}$$

4.  $0$  is a unit of  $*$ , defined as  $0 \triangleq \{ \mathbf{0}_s \}$ .

Addressed value views are views in the sense of [22], and are similar to those the authors constructed from separation algebras. However, we allow a choice of primitive reification function in our construction rather than always using the identity, and use our instrumented heaps rather than arbitrary cancellative monoids for constructing the views.

Each view is a “perspective” on the possible underlying machine states. A single view does not tell us which machine state we will be in, and typically provides a conservative

approximation. The compositional nature of views means that these perspectives can be joined, generally resulting in richer knowledge of, and capabilities over, the underlying states. If two views are *incompatible*, then we have conflicting facts about the machine states, and thus no useful information at all.

**Comment 2.** I find it helpful to anthropomorphise views. Consider a view as an entity that holds a set of opinions about the world. The opinions are the instrumented states, and the world is the set of machine states that reify from them. Composition attempts to form an agreement between two views about the actual states of the world, with the resultant entity having somehow merged the knowledge of both. In this sense,  $*$  is forming a *consensus* between parties about what the machine is actually doing. When the parties either hold opinions about different parts of the world (i.e. separation logic), or have the same opinions about the same part (i.e. separation logic with fractional permissions), this is easy. When the parties have differing opinions about the same parts of the world, forming a consensus can be harder, corresponding to more complex logics such as Rely-Guarantee [47] or CAP [23].

We define a set of useful functions and relations over addressed value views.

**Definition 9** (Operations on views). Given an addressed value view  $(\text{VIEWS}, \llbracket \cdot \rrbracket, \otimes, 0)$  (definition 8), and recalling that  $\text{VIEWS}$  is formed of sets of instrumented structured heaps  $\text{INSTHEAPS}$  (parameter 8), we define notions of entailment, disjunction, conjunction and negation on the set  $\text{VIEWS}$  as:

1. **Entailment:** The **entailment relation** on views,  $\models \subset \text{VIEWS} \times \text{VIEWS}$ , is the subset relation:  $\models \triangleq \subseteq$ .
2. **Disjunction:** The **disjunction** function on views,  $\bigvee : \mathcal{P}(\text{VIEWS}) \rightarrow \text{VIEWS}$ , is standard set union:  $\bigvee \triangleq \bigcup$ .
3. **Conjunction:** The **conjunction** function on views,  $\bigwedge : \mathcal{P}(\text{VIEWS}) \rightarrow \text{VIEWS}$ , is standard set intersection:  $\bigwedge \triangleq \bigcap$ .
4. **Negation:** The **negation** function on views,  $\neg : \text{VIEWS} \rightarrow \text{VIEWS}$ , is the standard set complement:  $\neg p \triangleq \text{INSTHEAPS} \setminus p$ .

One can informally consider views as “logical formulæ” with instrumented states as their “models”. For example, the first operation (entailment) ensures that every instrumented state in the left hand side is contained within the right hand side. This reflects the

operation of logical entailment, in that it holds if all “models” of the left view are “models” of the right. Disjunction gives a view which contains *every* “model” of the parameters. The other two have similar readings.

**Comment 3.** Whilst this informal notion of views as logical formulæ with instrumented states as models can aid understanding, it is important to note it is *not* a logic in any standard sense. Critically, I provide no syntactic proof rules that deduce truths about views (only rules that deduce truths about programs). Their manipulation remains purely “semantic”, in that one must consider the contents and reifications of their component instrumented heaps.

Two useful sets of views are the *boolean expression truth views*. These views reify to the structured heaps in which a given boolean expression will evaluate to true (or false). These views will be used to define the pre- and post-conditions of the **if** and **while** rules of the program logic (theorem 1), which will require analysis of boolean guard expressions.

**Definition 10** (Boolean truth views). Let  $B \in \text{BOOLEXPRS}$  be any boolean expression (parameter 3) with associated boolean expression evaluation function  $\llbracket \cdot \rrbracket(\cdot)$  (parameter 4). The **truth views** of  $B$  are defined as:

$$\begin{aligned} \text{exprTrue}(B) &\triangleq \{s \in \text{INSTHEAPS} \mid \forall s \in \llbracket s \rrbracket. \llbracket B \rrbracket(s) = \mathbf{true}\} \\ \text{exprFalse}(B) &\triangleq \{s \in \text{INSTHEAPS} \mid \forall s \in \llbracket s \rrbracket. \llbracket B \rrbracket(s) = \mathbf{false}\} \end{aligned}$$

Boolean expression evaluation (parameter 4) is deterministic, and results in one of true, false or fault. Therefore, if an expression evaluates to either true or false, it *cannot* evaluate to fault. We define the *boolean safety view* as the disjunction of the two truth views, and so describe the structured heaps in which a boolean expression can never evaluate to fault.

**Definition 11** (Boolean safety view). Let  $B \in \text{BOOLEXPRS}$  be any boolean expression (parameter 3), and  $\vee$  be disjunction on views (definition 9). The associated **boolean expression safety view** is defined as:

$$\text{safe}(B) \triangleq \text{exprTrue}(B) \vee \text{exprFalse}(B)$$

**Example 15** (Primitive heaps expression safety). In addition to boolean expressions, our primitive heaps example also has basic expressions (example 5). We can define an expression safety view for these. Let  $e \in \text{EXPRS}$  be any basic expression. The associated **basic expression safety view** is defined as:

$$\text{safe}(e) \triangleq \{s \in \text{INSTHEAPS} \mid \forall s \in \llbracket s \rrbracket. (e)(s) \neq \perp\}$$

## 2.3. Program reasoning

We now introduce our program reasoning framework, utilising views as the pre- and post-conditions for programs.

### 2.3.1. Semantic Hoare triples

We call a triple consisting of a program with associated pre- and post-condition views a *semantic Hoare triple*.

**Definition 12** (Semantic Hoare triples). Given an addressed value view  $(\text{VIEWS}, [\cdot], *, 0)$  (definition 8), and programs  $\text{PROGRAMS}$  (definition 4), the set of **semantic Hoare triples**  $\text{VIEWTRIPLES}$ , ranged over by  $vt, vt_1, \dots, vt_n$ , is defined as:

$$\text{VIEWTRIPLES} \triangleq \text{VIEWS} \times \text{PROGRAMS} \times \text{VIEWS}$$

An element  $(p, \mathbb{C}, q) \in \text{VIEWTRIPLES}$  is written  $\{p\} \mathbb{C} \{q\}$ .

We use the term *semantic* because the pre-and post-conditions are views, rather than syntactic assertions. The intended meaning of a semantic Hoare triple  $\{p\} \mathbb{C} \{q\}$  is that, when  $\mathbb{C}$  is run with a heap contained within the reification of view  $p$ , then it will never fault and, if the program terminates, the outcome will be a heap reified from view  $q$ .

To formalise this interpretation, first recall the action labels of definition 5. Each label represents the interaction of a program step with a structured heap: either no interaction; the evaluation of an expression; or the application of an atomic command. In a similar vein, we can define the interaction of a label with a pair of views  $p, q$  representing the program state before and after the action associated with the label. This *action judgement* considers the effect of the action on the structured heaps generated by reifying  $p$  and  $q$ .

More than this, it considers the effects when those views are composed with an arbitrary frame  $r \in \text{VIEWS}$ .

We do this as we are developing a local Hoare reasoning framework. Commands are specified using views that describe *only* the resource they need. They are free to update the resource in their pre- and post-conditions as needed, but must *not* disturb resource their specification does not mention. By “baking” all possible frames  $r$  into the judgement, unchanged on both sides, we are assured that unmentioned resource is not meaningfully changed, a property called *stability*.

**Definition 13** (Action judgement). Given a set of action labels (definition 5) and an addressed value view  $(\text{VIEWS}, \llbracket \cdot \rrbracket, *, 0)$  (definition 8), the **action judgement**  $\cdot \models \{\cdot\}\{\cdot\} \subseteq \text{ACTIONLBLETS} \times \text{VIEWS} \times \text{VIEWS}$  is the largest relation such that:

$$\alpha \models \{p\}\{q\} \iff \forall r \in \text{VIEWS}. [\alpha](\llbracket p * r \rrbracket) \subseteq \llbracket q * r \rrbracket$$

where, given that  $\llbracket C \rrbracket$  is the action of atomic command  $C$  (parameter 6), the function  $[\cdot](\cdot) : \text{ACTIONLBLETS} \rightarrow \mathcal{P}(\text{STRUCTHEAPS}) \rightarrow \mathcal{P}(\text{STRUCTHEAPS})$  is defined as:

$$\begin{aligned} [\text{ID}](S) &\triangleq S \\ [\mathcal{E}_{\top}(\text{B})](S) &\triangleq \{s \in S \mid \llbracket \text{B} \rrbracket(s) = \mathbf{true}\} \\ [\mathcal{E}_{\perp}(\text{B})](S) &\triangleq \{s \in S \mid \llbracket \text{B} \rrbracket(s) = \mathbf{false}\} \\ [\mathcal{E}_{\neq}(\text{B})](S) &\triangleq \{s \in S \mid \llbracket \text{B} \rrbracket(s) = \neq\} \\ [\mathcal{A}(C)](S) &\triangleq \bigcup \{\llbracket C \rrbracket(s) \mid s \in S\} \end{aligned}$$

Informally, the action judgement  $\alpha \models \{p\}\{q\}$  says that applying action  $\alpha$  to the states described by  $p$  results in states described by  $q$ . Notice that the action judgement associated with boolean expression evaluation requires that the post-condition have restricted the set of heaps to those that result in the outcome indicated on the label. These actions are used to direct the flow control in cases of **if** and **while**, and this filtering of heaps matches the operational effect of the label. For example, operationally, the first branch of **if** retains only those states which evaluate the guard expression to true. By having the action judgement perform the same filtering, soundness of the reasoning is easier to prove.

The “baking in” of the frame ensures the *locality* of the action judgement, and the stability of resource not mentioned. Locality is a key property. Assume that  $\alpha \models \{p\}\{q\}$ . Then, by definition, both of the follow properties hold.

1. **Extensibility:** We can compose  $p$  and  $q$  with any arbitrary view  $r$ , and the judge-

ment will still hold. This property allows knowledge about interactions with “small” heaps to be extended to knowledge about larger heaps, which is the essence of the frame rule of local reasoning.

2. **Locality:** The action labelled  $\alpha$  is *local* to the resource described by  $p$  and  $q$ . These resources must be all it needed, and all it can have meaningfully changed. To see  $p$  is all that is needed, imagine the action needed more resource than can be reified from  $p$ . By picking  $r = 0$ , the command would then fault on heaps from  $\lfloor p \rfloor$ . However, fault is not contained in any reification (parameter 9), so cannot be within  $\lfloor q \rfloor$ , and so the judgement could not have held. To see that nothing else is altered, pick arbitrary  $r$ . For all non-divergent commands, it must be the case that  $q * r$  is also defined. If the action had altered resource outside of  $p$ , we would then be able to find some  $r$  that represented this resource, yet must be unchanged when composed with  $q$ . Thus, the types of changes the action could perform on resource not represented by  $p$  must be trivial, and so  $r$  must be stable.

Via the action judgement, we formalise the meaning of a semantic triple (definition 12).

**Definition 14** (Semantic triple judgement). Given structured heaps  $\text{STRUCTHEAPS}$  (parameter 2) and action labels  $\text{ACTIONLBS}$  (definition 5), for all programs  $\mathbb{C}_1 \in \text{PROGRAMS}$  (definition 4) and views  $p_1, q \in \text{VIEWS}$  (definition 8), the **semantic triple judgement**  $\models \{p_1\} \cdot \mathbb{C}_1 \cdot \{q\} \subseteq \text{VIEWTRIPLES}$  is defined as the largest relation such that  $\models \{p_1\} \mathbb{C}_1 \{q\}$  if and only if:

1. if  $\mathbb{C}_1 = \text{skip}$ , then the action judgement  $\text{ID} \models \{p_1\} \{q\}$  holds.
2. if  $\mathbb{C}_1 \neq \text{skip}$ , then for all action labels  $\alpha \in \text{ACTIONLBS}$  and structured heaps  $s_1 \in \lfloor p_1 \rfloor$ , if there is some  $s_2 \in \text{STRUCTHEAPS}$  and  $\mathbb{C}_2 \in \text{PROGRAMS}$  such that  $\mathbb{C}_1, s_1 \xrightarrow{\alpha} \mathbb{C}_2, s_2$ , then there exists some  $p_2 \in \text{VIEWS}$  such that:
  - a)  $\alpha \models \{p_1\} \{p_2\}$
  - b)  $\models \{p_2\} \mathbb{C}_2 \{q\}$

Consider figure 2.2, which gives an example program  $\mathbb{C}$  with associated views, traces and view reifications. The views column represents a list of views generated by unfolding the semantic triple judgement  $\models \{p_1\} \mathbb{C} \{q\}$ . The judgement forces the views to be a good *abstraction* of the possible structured heaps at each state, no matter what program trace actually occurs. Notice that the reified views always contain the possible trace states at each step.

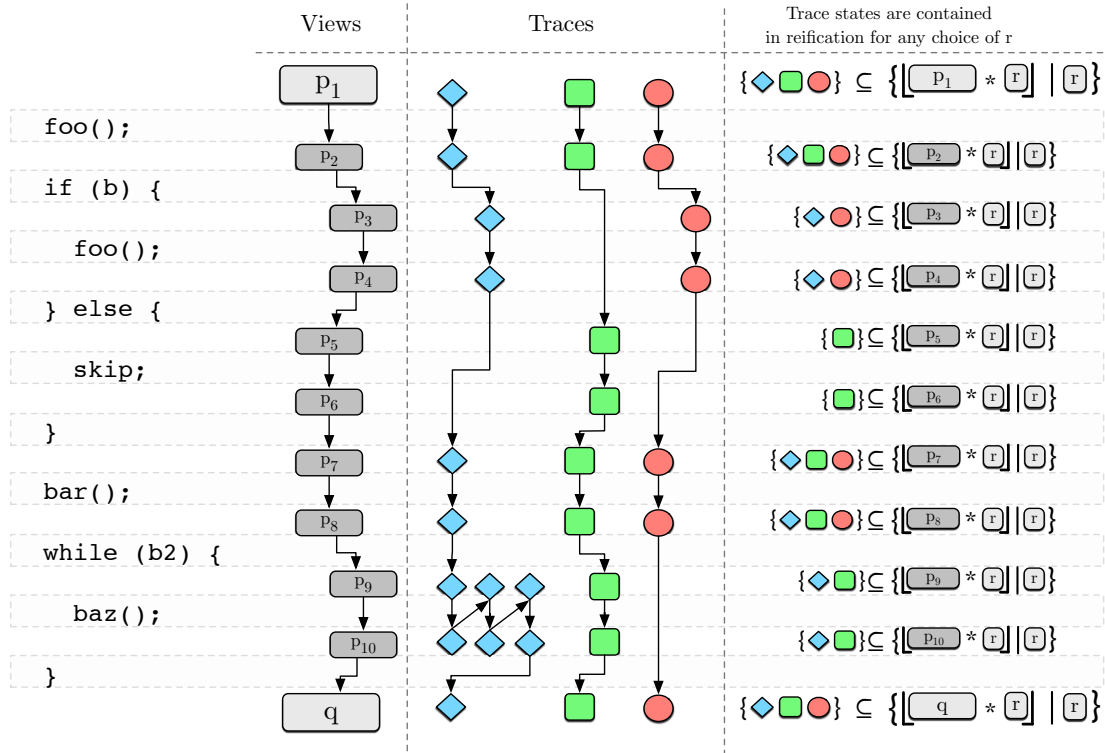


Figure 2.2.: Demonstration of the relationship between a program, views, the possible program action traces, and reified views. This figure represents the unfolding of the semantic triple judgement  $\models \{p_1\} \mathbb{C} \{q\}$  (definition 14) and the relationship between the structured heaps reified from the views and possible traces of the program.



### 2.3.2. Deriving valid semantic Hoare triples

It is prohibitively complex to construct valid semantic Hoare triples by analysing an entire program. Instead, we provide a set of *semantic inferences* that allow the construction of correct triples. We start with the program primitives; i.e. those programs not composed from smaller programs.

**Proposition 1** (Skip judgement). For any view  $p \in \text{VIEWS}$ ,  $\models \{p\} \text{ skip } \{p\}$  is a valid semantic triple judgement.

*Proof.* As  $\mathbb{C} = \text{skip}$ , the judgement falls into the first case of definition 14. Hence, we must show  $\text{ID} \models \{p\}\{p\}$ . This holds trivially, as  $\forall r. [p * r] \subseteq [p * r]$  always.  $\square$

Atomic commands are given semantic triple specifications via axioms. All atomic commands must have at least one axiom.

**Parameter 11** (Atomic command axioms). Given a set of atomic commands  $\text{ATOMICCMDS}$  (parameter 5) and an addressed value view  $(\text{VIEWS}, [\cdot], *, 0)$  (definition 8), assume a set of **atomic command axioms**  $\text{AXIOMS} \subseteq (\text{VIEWS} \times \text{ATOMICCMDS} \times \text{VIEWS})$  such that for each  $\mathbb{C} \in \text{ATOMICCMDS}$ , there exists  $p, q \in \text{VIEWS}$  with  $(p, \mathbb{C}, q) \in \text{AXIOMS}$ .

These axioms must sound, in that they are valid abstractions of the command actions. This *atomic soundness* property must be proven for each atomic command.

**Parameter 12** (Atomic soundness). Given a set of atomic commands  $\text{ATOMICCMDS}$  (parameter 5) and the action judgement (definition 13), the **atomic soundness property** of axioms states that, for every  $(p, \mathbb{C}, q) \in \text{AXIOMS}$ ,  $\mathcal{A}(\mathbb{C}) \models \{p\}\{q\}$  holds.

**Example 16** (Primitive heap axiomatisation). We now give axioms for the commands of primitive heaps (example 6). These commands involve expressions that can access variables in the heap. When the specific expression is unknown, the number of variables involved in the command action is equally unknown. The axioms for these commands must express sufficient additional resource to safely evaluate the expression. To accomplish this, we add additional resource to the specifications of such commands and use the expression safety view of definition 11 to ensure this resource

contains all the needed variables are present.

With an expression and a view containing sufficient variable resource to ensure non-faulting evaluation of the expression, we can describe a view that describes when the expression evaluates to a certain value. Formally, given a set of expressions  $\text{EXPRS}$  evaluated by  $([\cdot])(\cdot)$  (definition 5) into program values  $\text{PVALS}$ , and an addressed value view  $(\text{VIEWS}, [\cdot], *, 0)$ , the **expression evaluation view**  $\Rightarrow : \text{EXPRS} \rightarrow \text{PVALS} \rightarrow \text{VIEWS}$ , written  $e \Rightarrow i$ , is defined as

$$e \Rightarrow v \triangleq \{s \mid \llbracket s \rrbracket = \text{heaps}, \forall h \in \text{heaps}. ([e])(h) = v\}$$

Notice that the view  $e \Rightarrow v$  always entails the safety view,  $(e \Rightarrow v) \models \text{safe}(e)$  by definition. By using conjunction between some view  $p$  and the expression evaluation view, e.g.  $p \wedge e \Rightarrow v$ , we describe a view which has the variables of  $p$  and safely evaluates  $e$  to  $v$ .

Let  $\mathbf{x} \rightarrow v$  be the singleton view with variable  $\mathbf{x}$  mapping to value  $\mathbf{x}$ , and  $i \mapsto j$  be the singleton view with flat heap cell at address  $i$  mapping to value  $j$ . The axioms are then defined as follows: for all  $\mathbf{x} \in \text{PVARs}$ ,  $v \in \text{PVALS}$ ,  $e, e_1, e_2 \in \text{EXPRS}$  and  $o \in \text{VIEWS}$

$$\begin{aligned} \{(\mathbf{x} \rightarrow v * o) \wedge e \Rightarrow i\} \quad \mathbf{x} := e \quad \{\mathbf{x} \rightarrow i * o\} \\ \{\mathbf{x} \mapsto v\} \quad \mathbf{x} := \text{alloc}() \quad \left\{ \bigvee_{i \in \mathbb{N}^+, j \in \mathbb{N}} \mathbf{x} \mapsto i * i \mapsto j \right\} \\ \{i \mapsto j * (o \wedge e \Rightarrow i)\} \quad \text{free}(e) \quad \{o\} \\ \{i \mapsto j * (o \wedge e_1 \Rightarrow i \wedge e_2 \Rightarrow k)\} \quad [e_1] := e_2 \quad \{i \mapsto k * o\} \\ \{i \mapsto j * ((\mathbf{x} \rightarrow v * o) \wedge e \Rightarrow i)\} \quad \mathbf{x} := [e] \quad \{i \mapsto j * \mathbf{x} \rightarrow j * o\} \end{aligned}$$

The first axiom, variable/expression assignment, is an example of the extra resource needed by expressions. Consider the atomic actions of this command (definition 6). The expression  $e$  is evaluated, which by definition can use arbitrary variable resource. The result of the evaluation is placed in heap cell  $\mathbf{x}$ . No other resource is read or updated by the action, and so is not mentioned.

We mirror this in the axiom. The first conjunct of the pre-condition,  $\mathbf{x} \mapsto v * o$ ,

describes a heap that contains the cell addressed by  $\mathbf{x}$  with some value. It also contains *arbitrary* additional heap cells that are disjoint from  $\mathbf{x}$ , given by  $o$ , that describe additional variables needed to evaluate  $e$ . The second conjunct,  $e \Rightarrow i$ , gives *all* views that describe heaps evaluating  $e$  to  $i$ . By conjoining them, the pre-condition describes exactly heaps with variable  $\mathbf{x}$  and arbitrary other variables, that safely evaluate  $e$  to  $i$ . As there is an axiom for *every* choice of  $i$ , it does not matter what specific value the expression evaluates to.

The post-condition view  $\mathbf{x} \rightarrow i * o$  is similar to the pre-condition, except that the  $\mathbf{x}$  variable cell now contains the result of evaluating  $e$ , and has dropped the conjunction. The unchanged nature of the other cells is assured by the presence of  $o$ . The second conjunct is unnecessary, as the first conjunction contains all the information needed. As  $i$  was chosen for the entire axiom, the correct result is bound to the variable.

To show atomic soundness, we must show that the post-condition captures the effect of the atomic command on the pre-condition, and that the action is still well-described whenever the conditions are extended with arbitrary view  $r$ . The post-condition described above is exactly the effect of the atomic action. Preservation of frames is assured as the command does not touch any cells other than  $\mathbf{x}$  or those mentioned in  $o$ . If the frame  $r$  mentions one of these cells, the pre-condition becomes the empty set, and so the action judgement holds vacuously.

Turn now to allocation,  $\mathbf{x} := \text{alloc}()$ . Recall that the atomic action for this command (definition 6) is given as:

$$\llbracket \mathbf{x} := \text{alloc}() \rrbracket(s) \triangleq \{s[\mathbf{x} \mapsto i, i \mapsto v] \mid i \in \mathbb{N}^+ \setminus \text{dom}(s), v \in \mathbb{N}\}$$

The action creates a new flat heap cell at any unused positive integer address with any natural number as a value, *and* updates the variable  $\mathbf{x}$  to the address added. It is thus non-deterministic; we do not know the address that will be created, nor the value it will contain. To prove atomic soundness of the axiom, consider the pre-condition. It describes the singleton instrumented heap with variable  $\mathbf{x}$  mapping to any value. The post-condition describes an infinite set of instrumented heaps, covering every combination of a single flat-heap cell address/value alongside  $\mathbf{x}$  mapping to that address. This infinity of possibilities is the standard method for handling the non-local nature of allocation; we do not know *which* other addresses are used, but we do know there will be free addresses available, and that the command will choose one of them.

Formally, we show:

$$\forall r \in \text{VIEWS}. [\mathbf{x} := \text{alloc}()] [\mathbf{x} \mapsto v * r] \subseteq \left[ \bigvee_{i \in \mathbb{N}^+, j \in \mathbb{N}} (\mathbf{x} \mapsto i * i \mapsto j) * r \right]$$

Fix arbitrary  $r \in \text{VIEWS}$ . Then, by reification being the pointwise lift of primitive reification (example 10), and composition being function union (example 13):

$$\llbracket \{\mathbf{x} \mapsto v\} * r \rrbracket = \{ \{\mathbf{x} \mapsto v\} \sqcup s_r \mid s_r \in \llbracket r \rrbracket, \mathbf{x} \notin \text{dom}(s_r) \}$$

Pick an arbitrary member  $s_p \in \llbracket \{\mathbf{x} \mapsto v\} * r \rrbracket$ . Then

$$\llbracket \llbracket \mathbf{x} := \text{alloc}() \rrbracket (s_p) \rrbracket = \{ s_p[\mathbf{x} \mapsto i, i \mapsto v'] \mid i \in \mathbb{N}^+ \setminus \text{dom}(s_p), v' \in \mathbb{N} \}$$

This result set is non-empty, as each  $s_p$  is a finite function on an infinite domain. Pick an arbitrary  $s_q \in \llbracket \llbracket \mathbf{x} := \text{alloc}() \rrbracket (s_p) \rrbracket$ . Note that

$$\left[ \bigvee_{i \in \mathbb{N}^+, j \in \mathbb{N}} (\mathbf{x} \mapsto i, i \mapsto j) * r \right] = \left\{ \left\{ \mathbf{x} \mapsto i, i \mapsto j \right\} \sqcup s_r \mid \begin{array}{l} i \in \mathbb{N}^+, j \in \mathbb{N}, \\ s_r \in \llbracket r \rrbracket, \\ \mathbf{x} \notin \text{dom}(s_r), \\ i \notin \text{dom}(s_r) \cup \{\mathbf{x}\} \end{array} \right\}$$

Call this set  $S$ . The result follows if  $s_q$  is a member of  $S$ . Observe that  $s_p$  is any heap in which  $\mathbf{x}$  exists alongside arbitrary other cells  $s_r$  added by  $r$ . The heap  $s_q$  is any heap in which  $\mathbf{x}$  points to an arbitrary  $i \in \mathbb{N}^+$  not present in  $s_r$ , and in which  $i$  points to an arbitrary  $v \in \mathbb{N}$ . The set  $S$  is by definition *every* such heap, ergo  $s_q \in S$  as required.

The cases for cell deallocation, assignment and dereference are similar.

The skip rule and axioms provide valid semantic judgements for the most primitive programs. To construct judgements about composite programs, we provide a set of *semantic inference rules*. These inferences take simpler valid judgements and program fragments and build more complex judgements.

**Theorem 1** (Semantic inference rules). *Given a set of boolean expressions  $\text{BOOLEXPRES}$  (parameter 3), programs  $\text{PROGRAMS}$  (definition 4) and addressed value view  $(\text{VIEWS}, \llbracket \cdot \rrbracket, *, 0)$  (definition 8), the rules in the following two groups of **semantic inference rules** are valid, in that if the assumed semantic Hoare triple judgements hold then the conclusion*

semantic Hoare triple judgement holds. The rules are: for all  $B \in \text{BOOLEXPRES}$ , programs  $C, C_1, C_2 \in \text{PROGRAMS}$  and  $p, p_1, p_2, q, r, o \in \text{VIEWS}$

**Program rules:** These rules are driven by the syntax of the program.

1. **Sequencing rule:** If  $\models \{p\} C_1 \{o\}$  and  $\models \{o\} C_2 \{q\}$ , then  $\models \{p\} C_1; C_2 \{q\}$ .
2. **If rule:** If  $p \models \text{safe}(B)$ ,  $\models \{p \wedge \text{exprTrue}(B)\} C_1 \{q\}$  and  $\models \{p \wedge \text{exprFalse}(B)\} C_2 \{q\}$ , then  $\models \{p\} \text{if } (B) C_1 \text{ else } C_2 \{q\}$ .
3. **While rule:** If  $p \models \text{safe}(B)$  and  $\models \{p \wedge \text{exprTrue}(B)\} C \{p\}$ , then  $\models \{p\} \text{while } (b) C \{p \wedge \text{exprFalse}(b)\}$
4. **Parallel Composition rule:** If  $\models \{p_1\} C_1 \{p_2\}$  and  $\models \{p_2\} C_2 \{q_2\}$ , then  $\models \{p_1 * p_2\} C_1 \parallel C_2 \{q_2 * q_2\}$ .

**Abstraction rules:** These rules are driven by the structure of the views abstracting the state.

1. **Frame rule:** For all  $r \in \text{VIEWS}$ , if  $\models \{p\} C \{q\}$  then  $\models \{p * r\} C \{q * r\}$
2. **Disjunction rule:** Let  $I$  be some index set. If, for all  $i \in I$ ,  $\models \{p_i\} C \{q_i\}$ , then  $\models \{\bigvee_{i \in I} p_i\} C \{\bigvee_{i \in I} q_i\}$ .
3. **Consequence rule:** For all  $p', q' \in \text{VIEWS}$ , if  $p \models p'$ ,  $q' \models q$  and  $\models \{p'\} C \{q'\}$  then  $\models \{p\} C \{q\}$ .

*Proof.* The semantic triple judgement  $\models \{p_1\} C_1 \{q\}$  (definition 14) is defined co-inductively. The proof is therefore co-inductive. Unpacking the definition, we must show that:

1. If the program has finished (that is, it is `skip`), the structured heaps reified from the triple pre-condition  $p_1$  are contained within those reified from the post-condition  $q$ , when considered under all frames. Formally,  $C_1 = \text{skip}$  implies  $\text{ID} \models \{p_1\} \{q\}$ .
2. If the program has not finished, and so takes a step to a new state, then there is *some* view that both abstracts the structured heap after the step, and functions as a pre-condition for the continuation program. Formally, for all  $s_1 \in |p_1|$ , if there are some  $\alpha, C_2$  and  $s_2$  such that  $C_1, s_1 \xrightarrow{\alpha} C_2, s_2$ , then is some view  $p_2$  such that  $\alpha \models \{p_1\} \{p_2\}$  and  $\models \{p_2\} C_2 \{q\}$ .

Referring again to figure 2.2, this proof justifies the existence of the sequence of views that abstract the structured heaps. In essence, the uses of co-induction create a simulation relation, demonstrating that for every “step” the assumed judgements take with the action judgement, the conclusion judgement can match the “step”.

1. **Sequencing rule:** We proceed by co-induction. Assume ①  $\models \{p\} \mathbb{C}_1 \{o\}$  and ②  $\models \{o\} \mathbb{C}_2 \{q\}$ . Consider  $\mathbb{C}_1$ . It is either **skip**, or another command.

If it is **skip**, then by the single-step transition relation, the program  $\mathbb{C}_1; \mathbb{C}_2$  takes an ID-labelled step to  $\mathbb{C}_2$ . By ① and the definition of the semantic triple judgement (definition 14),  $\text{ID} \models \{p\}\{o\}$ . By ② and definition 14, the entire judgement  $\models \{p\} \mathbb{C}_1; \mathbb{C}_2 \{q\}$  must hold.

If  $\mathbb{C}_1$  is not **skip**, then by the single-step labelled transition relation (definition 6), there is some  $\mathbb{C}'_1$  such that  $\mathbb{C}_1; \mathbb{C}_2$  transitions via some action labelled  $\alpha$  to  $\mathbb{C}'_1; \mathbb{C}_2$ . Moreover, by ① there is some  $s \in \text{VIEWS}$  such that  $\alpha \models \{p\}\{s\}$  and  $\models \{s\} \mathbb{C}'_1 \{o\}$ . The co-inductive hypothesis can now be applied to  $\models \{s\} \mathbb{C}'_1; \mathbb{C}_2 \{q\}$ , completing the result.

2. **If rule:** Assume ①  $p \models \text{safe}(\text{B})$ , ②  $\models \{p \wedge \text{exprTrue}(\text{B})\} \mathbb{C}_1 \{q\}$ , and ③  $\models \{p \wedge \text{exprFalse}(\text{B})\} \mathbb{C}_2 \{q\}$ .

By examination of the transitions of **if**  $(\cdot) \cdot \text{else } \cdot$ , three outcomes can occur. Expression  $\text{B}$  may evaluate to fault, and thus the program takes an  $\mathcal{E}_i(\text{B})$ -labelled step to fault. It may evaluate to true, in which case an  $\mathcal{E}_\top(\text{B})$ -labelled step is taken to the program  $\mathbb{C}_1$ . It may evaluate to false, in which case an  $\mathcal{E}_\perp(\text{B})$ -labelled step is taken to  $\mathbb{C}_2$ . If the program does not reduce to fault, the state is not altered.

Take the first outcome,  $\mathcal{E}_i(\text{B})$ . By the fault-free property of reification (parameter 9), no reification can contain the fault state. Therefore, the expression evaluation on the pre-condition states must not fault (otherwise, the post-condition could not represent the result). That this does not occur is ensured by ①, and the boolean expression safety view (definition 11); by the definition of views as sets of instrumented heaps, and of entailment as set containment, any instrumented heap in  $p$  cannot reify to fault.

Take the second outcome,  $\mathcal{E}_\top(\text{B})$ . For this to occur, on some heaps reified from  $p$ ,  $\text{B}$  evaluates to true. The semantics state that **if**  $(\text{B}) \mathbb{C}_1 \text{ else } \mathbb{C}_2, s \xrightarrow{\mathcal{E}_\top(\text{B})} \mathbb{C}_1, s$ , where  $s \in [p]$  and  $(\text{B})(s) = \text{true}$  on those states in  $p$ . The semantic triple judgement requires us to show the existence of  $p_2$  such that  $\mathcal{E}_\top(\text{B}) \models \{p\}\{p_2\}$  and  $\models \{p_2\} \mathbb{C}_1 \{q\}$ . By the definition of conjunction and the action judgement,  $\mathcal{E}_\top(\text{B}) \models \{p\}\{p \wedge \text{exprTrue}(\text{B})\}$  holds, and  $\models \{p \wedge \text{exprTrue}(\text{B})\} \mathbb{C}_1 \{q\}$  holds by ②. Therefore, selecting  $p_2 = p \wedge \text{exprTrue}(\text{B})$ , both requirements of the triple judgement are fulfilled.

The third outcome,  $\mathcal{E}_\perp(\text{B})$ , is similar, but using ③.

3. **While rule:** Assume ①  $p \models \text{safe}(\text{B})$  and ②  $\models \{p \wedge \text{exprTrue}(\text{B})\} \mathbb{C} \{p\}$ .

By examining the transitions for `while ( B ) C`, three outcomes can occur: B evaluates to true, B evaluates to false, or the evaluation of B faults. If it faults during evaluation, the program takes an  $\mathcal{E}_\perp(B)$  step to the fault state. If it evaluates to true, then the program takes an  $\mathcal{E}_\top(B)$  step to `C`; `while (B) C`. If it evaluates to false, the program takes an  $\mathcal{E}_\perp(B)$  step to `skip`. If the evaluation does not fault, the state is not altered.

The first outcome,  $\mathcal{E}_\perp(B)$ , is discharged as in the `if` case.

For the second outcome,  $\mathcal{E}_\top(B)$ , we must demonstrate some  $p_2$  such that  $\mathcal{E}_\top(B) \models \{p\}\{p_2\}$  and  $\models \{p_2\} C; \text{while } (B) C \{q\}$  hold. Pick  $p_2$  as  $p \wedge \text{exprTrue}(B)$ . Then,  $\mathcal{E}_\top(B) \models \{p\}\{p \wedge \text{exprTrue}(B)\}$  holds by the action being  $\mathcal{E}_\top(B)$  and the  $\text{exprTrue}(B)$  conjunction. The semantic triple  $\models \{p \wedge \text{exprTrue}(B)\} C; \text{while } (B) C \{q\}$  holds by a combination of  $\textcircled{2}$  and the sequencing rule already proven.

For the third outcome,  $\mathcal{E}_\perp(B)$ , the semantics takes an  $\mathcal{E}_\perp(B)$  step to `skip`, and we know the structured heap evaluates B to false. We must show that  $\mathcal{E}_\perp(B) \models \{p\}\{p_2\}$  and  $\models \{p_2\} \text{skip } \{q\}$ . Pick  $p_2 = p \wedge \text{exprFalse}(B)$ . We must thus show  $\mathcal{E}_\perp(B) \models \{p\}\{p \wedge \text{exprFalse}(B)\}$ , which is true by the definition of the action judgement in the  $\mathcal{E}_\perp(B)$  case. We must also show  $\models \{p \wedge \text{exprFalse}(B)\} \text{skip } \{p \wedge \text{exprFalse}(B)\}$ , which is true by the skip rule (proposition 1).

4. **Parallel Composition rule:** We proceed by co-induction. Assume that  $\textcircled{1} \models \{p_1\} C_1 \{q_1\}$  and  $\textcircled{2} \models \{p_2\} C_2 \{q_2\}$ .

It must be the case that  $C_1 \parallel C_2$  takes an  $\alpha$ -labelled transition for some  $\alpha$ . By examination of the transition system, there are three cases: either  $C_1$  is `skip`,  $C_2$  is `skip`, or neither are `skip`. If either program is `skip`, the small-step semantics allow the program to take an ID. If either one is non-`skip`, the semantics allow that program to take a  $\alpha$  step.

If  $C_i = \text{skip}$ ,  $C_j = C'$  for  $\{i, j\} \in \{1, 2\}$ , and the semantics reduces the skip case, then  $\text{ID} \models \{p_i\}\{q_i\}$  holds by the hypothesis, and thus  $\text{ID} \models \{p_i * p_j\}\{q_i * p_j\}$  by the definition of the action judgement.  $\models \{q_i * p_j\} C_j \{q_i * q_j\}$  holds by the frame rule inference (which we prove sound shortly).

If, say,  $C_1$  is not `skip`, it takes some  $\alpha$  action to  $C'_1$  (the case for  $C_2$  is similar). By  $\textcircled{1}$ ,  $\alpha \models \{p_1\}\{p'_1\}$  for some  $p'_1$ , and  $\models \{p'_1\} C'_1 \{q_1\}$ . The judgement  $\models \{p_2\} C_2 \{q_2\}$  still holds, ergo  $\models \{p'_1 * p_2\} C'_1 \parallel C_2 \{q_1 * q_2\}$  holds by the co-inductive hypothesis.

To prove the abstraction rules:

1. **Frame rule:** We proceed by co-induction.

Assume that  $\textcircled{1} \models \{p\} \mathbb{C} \{q\}$ . If  $\mathbb{C} = \mathbf{skip}$ , then  $\text{ID} \models \{p\}\{q\}$  by  $\textcircled{1}$ , and by the definition of the action judgement,  $\text{ID} \models \{p * r\}\{q * r\}$  for all  $r$ . Ergo,  $\models \{p * r\} \mathbb{C} \{q * r\}$ , as required.

If  $\mathbb{C} \neq \mathbf{skip}$ , then by  $\textcircled{1}$  then for all action labels  $\alpha$  and  $s \in \llbracket p \rrbracket$  such that  $\mathbb{C}, s \xrightarrow{\alpha} \mathbb{C}', s'$  for some state  $s'$ . Moreover, there is some view  $p_2$  such that  $\alpha \models \{p\}\{p_2\}$  and  $s' \in \llbracket p_2 \rrbracket$ . By definition of the action judgement,  $\alpha \models \{p * r\}\{p_2 * r\}$  must hold, and  $\models \{p_2\} \mathbb{C}' \{q\}$ . Then,  $\models \{p_2 * r\} \mathbb{C}' \{q * r\}$  holds by the co-inductive hypothesis.

2. **Disjunction rule:** To prove the rule of disjunction, we require two preliminary lemmas. First, that  $*$  distributes over disjunction.

$$\begin{aligned} p * \bigvee \{q_i\}_{i \in I} &= p * \bigcup \{q_i\}_{i \in I} && \text{(Definition of disjunction)} \\ &= \bigcup \{p * q_i\}_{i \in I} && \text{(Pointwise definition of *)} \\ &= \bigvee \{p * q_i\}_{i \in I} && \text{(Definition of disjunction)} \end{aligned}$$

Second, that disjunction is a morphism with respect to reification.

$$\begin{aligned} \llbracket \bigvee \{p_i\}_{i \in I} \rrbracket &= \llbracket \bigcup \{p_i\}_{i \in I} \rrbracket && \text{(Definition of disjunction)} \\ &= \bigcup \llbracket \{p_i\} \rrbracket_{i \in I} && \text{(Pointwise definition of reification)} \end{aligned}$$

Now assume the hypothesis for the rule: that  $\textcircled{1}$  for all  $i \in I$ ,  $\models \{p_i\} \mathbb{C} \{q_i\}$ . Proceed by co-induction. If  $\mathbb{C} = \mathbf{skip}$  then by  $\textcircled{1}$ , for each  $i$ ,  $\text{ID} \models \{p_i\}\{q_i\}$ . Unpacking this judgement,  $\forall r \in \text{VIEWS}. \llbracket p_i * r \rrbracket \subseteq \llbracket q_i * r \rrbracket$  holds. Call this result  $\textcircled{2}$ .

We must show  $\text{ID} \models \{\bigvee_{i \in I} p_i\}\{\bigvee_{i \in I} q_i\}$ . This is equivalent to  $\forall r \in \text{VIEWS}. \llbracket r * \bigvee_{i \in I} p_i \rrbracket \subseteq \llbracket r * \bigvee_{i \in I} q_i \rrbracket$ . By disjunction distribution, this is equal to  $\forall r \in \text{VIEWS}. \llbracket \bigvee_{i \in I} r * p_i \rrbracket \subseteq \llbracket \bigvee_{i \in I} r * q_i \rrbracket$ . By disjunction morphism, this is equal to  $\forall r \in \text{VIEWS}. \bigcup_{i \in I} \llbracket r * p_i \rrbracket \subseteq \bigcup_{i \in I} \llbracket r * q_i \rrbracket$ . For each element of the left hand side, its presence in the right hand side is ensured by  $\textcircled{2}$  and the commutativity of  $*$ .

If  $\mathbb{C} \neq \mathbf{skip}$ , then the program takes some  $\alpha$ -labelled step to  $\mathbb{C}'$  and for each  $p_i$  there is some  $r_i$  such that  $\alpha \models \{p_i\}\{r_i\}$  and  $\models \{r_i\} \mathbb{C}' \{q_i\}$ . By similar reasoning to the skip case, we have  $\alpha \models \{\bigvee_{i \in I} p_i\}\{\bigvee_{i \in I} r_i\}$ , and the co-inductive hypothesis provides  $\models \{\bigvee_{i \in I} r_i\} \mathbb{C}' \{\bigvee_{i \in I} q_i\}$ .

3. **Consequence rule:** To prove the rule of consequence, we require the **action preservation** lemma: that the entailment  $p \models q$  is no more than is allowed by the identity action,  $\text{ID} \models \{p\}\{q\}$ . For all  $r \in \text{VIEWS}$ :



$$\begin{array}{lll}
p \models q & \triangleq p \subseteq q & \text{by definition} \\
\text{therefore } p \models q \implies & p * r \subseteq q * r & * \text{ pointwise} \\
\text{therefore } p \models q \implies & [p * r] \subseteq [q * r] & [\cdot] \text{ pointwise} \\
\text{therefore } p \models q \implies & \text{ID} \models \{p\}\{q\} & \text{definition of action judgement}
\end{array}$$

Proceed by co-induction. Assume that, for all  $p', q' \in \text{VIEWS}$ , ①  $p \models p'$ , ②  $q' \models q$  and ③  $\models \{p'\} \mathbb{C} \{q'\}$ .

Consider  $\mathbb{C}$ . If  $\mathbb{C} = \text{skip}$ , then by ③  $\text{ID} \models \{p'\}\{q'\}$ . We must show  $\text{ID} \models \{p\}\{q\}$ . To see this, consider ① and the action preservation lemma, which implies that  $\text{ID} \models \{p\}\{p'\}$  holds. Note that  $\text{ID} \models \{p\}\{p'\}$  together with  $\text{ID} \models \{p'\}\{q'\}$  implies  $\text{ID} \models \{p\}\{q'\}$  by the definition of the action judgement. Using this, with ② and action preservation, produces  $\text{ID} \models \{p\}\{q\}$ , as required.

If  $\mathbb{C} \neq \text{skip}$  then by ③, there is some action  $\alpha$ , view  $p_2$  and program  $\mathbb{C}'$  such that  $\alpha \models \{p'\}\{p_2'\}$  and  $\models \{p_2'\} \mathbb{C}' \{q'\}$ . We must show  $\alpha \models \{p\}\{p_2'\}$ , and  $\models \{p_2'\} \mathbb{C}' \{q\}$ . The first holds by ①,  $\text{ID} \models \{p\}\{p'\}$  together with  $\text{ID} \models \{p'\}\{q'\}$ , and action preservation. The second holds by the co-inductive hypothesis.

□

In addition to these rules, the *rule of conjunction* is often included in Hoare reasoning.

**Definition 15** (Semantic rule of conjunction). Let  $I$  be some index set. If, for all  $i \in I$ ,  $\models \{p_i\} \mathbb{C} \{q_i\}$ , then  $\models \{\bigwedge_{i \in I} p_i\} \mathbb{C} \{\bigwedge_{i \in I} q_i\}$ .

We do not include this rule amongst those of theorem 1 as it is not always sound. Our views represent sets of instrumented machine states, where the choice of instrumentation is not restricted. It is therefore possible that two valid proofs for the same program use *different choices* of instrumentation. In this case, there would be two valid triples  $\models \{p_1\} \mathbb{C} \{q_1\}$  and  $\models \{p_2\} \mathbb{C} \{q_2\}$ . For the rule of conjunction to be sound, the triple  $\models \{p_1 \wedge p_2\} \mathbb{C} \{q_1 \wedge q_2\}$  must be correct. However, recall that conjunction is defined as intersection of views (which are sets of instrumented heaps). It may be the case that  $p_1 \wedge p_2$  describes a non-empty view, yet  $q_1 \wedge q_2$  describes an empty view. The only programs for which such a post-condition is sound are divergent, so the rule can be used to generate unsound results. In chapter 3 we will see an example of instrumentation that is natural and useful, but for which the rule of conjunction does not hold.

We can ensure the soundness for the rule of conjunction with a condition on choices of actions and reification (and hence the action judgement). We call the property *primitive conjunctivity*.

**Definition 16** (Primitive conjunctivity). A set of actions labels  $\text{ACTIONLBLS}$  (definition 5) have the **primitive conjunctivity property** if: for all  $\alpha \in \text{ACTIONLBLS}$  and sets of views  $p$  and  $q$  (definition 8) indexed by  $I$ ,  $\forall i \in I. \alpha \models \{p_i\}\{q_i\} \implies \alpha \models \{\bigwedge_{i \in I} p_i\}\{\bigwedge_{i \in I} q_i\}$ .

This property ensures that, if an action was permitted in each of the potential conjuncts, the same action must be permitted in the resultant conjunction. If this property holds, then the semantic rule of conjunction is sound.

**Lemma 1** (Soundness of the semantic rule of conjunction). *Assume that primitive conjunctivity holds. Then, the rule of conjunction is sound.*

*Proof.* We proceed by co-induction. Assume that ① for all  $i \in I, \models \{p_i\} \subset \{q_i\}$ . Furthermore, assume ② primitive conjunctivity (definition 16). If  $\mathbb{C} = \text{skip}$  then by ①, for each  $p_i$ , the program takes an ID step,  $\text{ID} \models \{p_i\}\{q_i\}$ . By ②,  $\text{ID} \models \{\bigwedge_{i \in I} p_i\}\{\bigwedge_{i \in I} q_i\}$  holds.

If  $\mathbb{C} \neq \text{skip}$  then by ①, for each  $p_i$  there exists some  $\alpha, r_i$  and  $\mathbb{C}'$  such that both  $\alpha \models \{p_i\}\{r_i\}$  and  $\models \{r_i\} \mathbb{C}' \{q_i\}$ . By ②,  $\alpha \models \{\bigwedge_{i \in I} p_i\}\{\bigwedge_{i \in I} r_i'\}$  holds. By the co-inductive hypothesis,  $\models \{\bigwedge_{i \in I} r_i'\} \mathbb{C}' \{\bigwedge_{i \in I} q_i\}$ .  $\square$

### 2.3.3. Semantic consequence

The consequence rule in theorem 1 uses the entailment relation of definition 9. Informally, entailment  $p_1 \models p_2$  is justified by the instrumented states of  $p_1$  being a subset of those in  $p_2$ . We can show that everything that can be accomplished with  $p_1$  can still be accomplished with  $p_2$ . We can define a similar relationship using the underlying machine states. The views framework calls this the *semantic consequence* relation. Semantic consequence is similar to entailment, but where entailment  $p \models q$  works with relationships between the instrumented states in  $p$  and  $q$ , semantic consequence works with relationships between the uninstrumented machine state sets described by the views,  $[p]$  and  $[q]$ .

**Definition 17** (Semantic Consequence). The **semantic consequence relation**  $\preceq \subset \text{VIEWS} \times \text{VIEWS}$  is defined such that:

$$(p, q) \in \preceq \iff \text{ID} \models \{p\}\{q\}$$

An element  $(p, q) \in \preceq$  is written write  $p \preceq q$ .

By unpacking the use of the action judgment in this definition, we see  $p \preceq q \iff \forall r \in \text{VIEWS}. [p * r] \subseteq [q * r]$ . The relation allows an *view shift* from  $p$  to  $q$ , as long as the update does not change the machine level states under any frame. The embedding of  $r$  into the check is required to ensure that any change to the instrumentation does not invalidate instrumentation elsewhere. For example, consider fractional permissions. Without the “baked in” frame, the semantic consequence  $\mathbf{x} \xrightarrow{0.5} v \preceq \mathbf{x} \xrightarrow{1} v$  would be allowed, as both views have the same reification. However, that update has invalidated many previously allowable frames, such as  $\mathbf{x} \xrightarrow{0.25} v$ .

We can replace the entailment relation of definition 9 with semantic consequence. Moreover, any relationship allowed by entailment is equally allowed by semantic consequence.

**Lemma 2** (Semantic consequence is an entailment relation). *The semantic consequence relation  $\preceq$  is a sound replacement for the entailment relation  $\models$  of definition 9. Moreover, it subsumes  $\models$ ,  $\models \subseteq \preceq$ .*

*Proof.* Recall the proof of soundness of the consequence rule in theorem 1. The proof obligation required for soundness was that all actions were preserved by the entailment,  $p \models q \implies \text{ID} \models \{p\}\{q\}$ . This is evidently true of the semantic consequence relation, which is defined as  $\text{ID} \models \{p\}\{q\}$ .  $\square$

**Comment 4** (Intuitionistic separation logic). Recall the two reification operations for primitive heaps in examples 10 and 11. Under the first definition of reification, semantic consequence coincides with the entailment relation.

With the second definition, however, semantic consequence can do *more* than entailment. As this reification is “completing”, it endows semantic consequence with a weakening ability, so that  $p_1 * p_2 \preceq p_1$ . This works because this reification will *provide*  $p_2$ :

$$\begin{aligned}
& p_1 * p_2 \preceq p_1 \\
\iff & \forall r \in \text{VIEWS}. [p_1 * p_2 * r] \subseteq [p_1 * r] \\
\iff & \cup_{\mathbf{s}_1 \in p_1 * p_2 * r} \{s_1 \in \text{STRUCTHEAPS} \mid \forall a \in \text{dom}(\mathbf{s}_1). s_1(a) = \mathbf{s}_1(a)\} \\
& \subseteq \cup_{\mathbf{s}_2 \in p_1 * r} \{s_2 \in \text{STRUCTHEAPS} \mid \forall a \in \text{dom}(\mathbf{s}_2). s_2(a) = \mathbf{s}_2(a)\}
\end{aligned}$$

The resultant set inclusion states that all heaps which are *consistent* with  $p_1 * p_2 * r$  are contained within those which are consistent with  $p_1 * r$ , where consistent means that, for any shared addresses, the values stored in those cells are the same. The set inclusion holds as the domain of  $p_1 * p_2 * r$  is strictly larger than that of  $p_1 * r$  by the definition of primitive composition for primitive heaps.

That resource can be “forgotten about”,  $p * q \preceq p$ , is one of the defining characteristics of intuitionistic separation logic. Therefore, under semantic consequence, program reasoning with this choice of reification behaves intuitionistically. A small change to the reification function has large an effect on what can be proven.

### 2.3.4. The magic wand and weakest pre-conditions

The “magic wand” connective in separation logic forms a left adjoint of the separating conjunction with respect to entailment. That is, if  $A * B \models C$ , then  $A \models B \multimap C$ . Informally, if  $B \multimap C$  holds, then the data described by it could be extended by  $B$  to result in data described by  $C$ . One common use of the magic wand is to express *parametric weakest pre-conditions*. The parametric weakest pre-condition of a program is some pre-condition  $p$  parametric in an assertion  $q$ , such that when the program is run in states satisfying the pre-condition, the resulting states satisfy  $q$ . Moreover, this pre-condition must be the *weakest*. The existence of parametric weakest pre-conditions for the axioms imply that a Hoare logic is complete for straight line code.

In our framework<sup>1</sup>, the magic wand does not always carry the intuition from Separation Logic. The standard definition would be:

**Definition 18** (Magic wand). The **magic wand**  $\multimap : \text{VIEWS} \times \text{VIEWS}$  connective is defined as follows:

<sup>1</sup>And indeed, in views in general, where magic wand is sometimes not definable.

$$p \multimap q \triangleq \bigvee \{o \in \text{VIEWS} \mid o * p \Vdash q\}$$

However, this does not account for view update performed by semantic consequence. We can recover the desired property with a connective identical to magic wand, but using semantic consequence rather than entailment. Due to our notation, we call this the “magic flower”:

**Definition 19** (Magic Flower).

$$p \multimap^* q \triangleq \bigvee \{o \in \text{VIEWS} \mid o * p \preceq q\}$$

**Lemma 3** (Magic flower adjointness). *The magic flower is a left adjoint to semantic consequence,  $(p \multimap^* q) * p \preceq q$ .*

*Proof.* By calculation.

$$\begin{aligned} (p \multimap^* q) * p &= \bigvee \{o \in \text{VIEWS} \mid o * p \preceq q\} * p && \text{Expand flower definition} \\ &= \bigvee \{o \in \text{VIEWS} * p \mid o * p \preceq q\} && * \text{ distributes over disjunction} \\ &= \bigcup \{o \in \text{VIEWS} * p \mid o * p \preceq q\} && \text{Definition of disjunction} \\ &\preceq q && \text{Definition of set comprehension} \end{aligned}$$

□

Aside from a short demonstration of weakest pre-condition construction, we will not use the magic wand nor flower in this thesis. Partially, this is because we have not found it useful outside of weakest pre-condition construction. Also, our long term research aim is *automation*, where proofs that use adjoints have been problematic.

## 2.4. Syntactic proof theory

The reasoning system presented in the last section is *semantic*, in the sense that inferences are made by analysing the underlying meaning of views. To aid reasoning, we now provide a *syntactic* proof theory. As far as possible, we use standard approaches to write *assertions* which act as the pre- and post-conditions of syntactic Hoare triples. These are manipulated

using a local Hoare logic with the standard syntactic deduction rules. We prove this system sound by interpreting it into the views of the previous section.

### 2.4.1. Assertion language

When constructing proofs, it is standard to link information between the pre- and post-conditions via *logical variables*. Logical variables are unrelated to program variables.

**Parameter 13** (Logical variables and values). Given a set of program variables PVARs (definition 3), assume a non-empty set of **logical variables** LVARs, ranged over by  $x, x_1, \dots, x_n$ , that is disjoint from program variables,  $\text{LVARs} \cap \text{PVARs} = \emptyset$ . Assume also a non-empty set of **logical values** LVALs, ranged over by  $V, V_1, \dots, V_n$ .

Logical variables are associated with logical values via *logical environments*.

**Definition 20** (Logical environments). Given a set of logical variables LVARs and values LVALs, the set of **logical environments** LENVS, ranged over by  $\Gamma, \Gamma_1, \dots, \Gamma_n$ , is the set of partial functions from logical variables to logical values:

$$\text{LENVS} = \{\Gamma \mid \Gamma : \text{LVARs} \rightarrow \text{LVALs}\}$$

To allow simple computation with logical variables and values, we include *logical expressions*. These expressions always include literals for logical values, logical variable lookup, and equality. They are extended with specific operations that aid reasoning in specific situations. Common extensions include set and string manipulation. The meaning of logical expressions is given by an *evaluation function*. This function can perform any interpretation, but must have standard behaviour with respect to literals and logical variables.

**Parameter 14** (Logical expressions and interpretation). Given a set of logical variables LVARs and values LVALs (parameter 13), and a set of logical environments LENVS (definition 20), assume a set of **logical expressions** LEXPRs, ranged over by  $E, E_1, \dots, E_n$ , where  $\text{LVARs} \cup \text{LVALs} \cup \{E_1 = E_2 \mid E_1, E_2 \in \text{LEXPRES}\} \subseteq \text{LEXPRES}$ . Assume also a **logical expression evaluation** function  $\llbracket \cdot \rrbracket (\cdot) : \text{LEXPRES} \rightarrow \text{LENVS} \rightarrow \text{LVALs}$  that, for all  $\Gamma \in \text{LENVS}, x \in \text{LVARs}$ , satisfies the equations:

$$\begin{aligned}
\langle V \rangle(\Gamma) &= V \\
\langle x \rangle(\Gamma) &= \Gamma(x) \\
\langle E_1 = E_2 \rangle(\Gamma) &= \langle E_1 \rangle(\Gamma) = \langle E_2 \rangle(\Gamma)
\end{aligned}$$

We now give a syntactic assertion language. By itself, an assertion in this language has no meaning, and is considered just a block of text with a certain format.

**Definition 21** (Syntactic assertion language). The **syntactic assertion language** ASSTS, ranged over by  $P, Q, R, P_1, \dots, P_n$ , is defined by induction as follows:

$P ::=$	$P_1 * P_2$	Separating conjunction
	<b>emp</b>	Empty assertion
	$P_1 \wedge P_2$	Conjunction
	$P_1 \vee P_2$	Disjunction
	$\neg P$	Negation
	<b>false</b>	Falsity
	$P_1 \Rightarrow P_2$	Implication
	$\exists x. P$	Existential logical variable
	<b>btrue(B)</b>	Boolean truth filter
	<b>bfalse(B)</b>	Boolean false filter
	<b>bsafe(B)</b>	Boolean expression safety
	<b>E</b>	Logical expression
	$-$	Domain specific assertions

A syntactic assertion is given meaning via an *interpretation* into a view. The interpretation function is a parameter of our framework, allowing for the user-defined behaviour of domain specific assertions. However, any choice for it must provide a standard set of behaviours for the assertions of definition 21. The interpretation also takes a logical environment, allowing the evaluation of logical expressions. Interpretation of logical expressions will result in the empty view (that is, false) if the logical expression itself cannot be evaluated.

**Parameter 15** (Assertion interpretation). Assume a **assertion interpretation** function  $\langle \cdot \rangle' : \text{LENVS} \rightarrow \text{ASSTS} \rightarrow \text{VIEWS}$  that satisfies the following equations

$$\begin{array}{ll}
\langle P * Q \rangle^\Gamma &= \langle P \rangle^\Gamma * \langle Q \rangle^\Gamma & \langle P \Rightarrow Q \rangle^\Gamma &= (\text{INSTHEAPS} \setminus \langle P \rangle^\Gamma) \cup \langle Q \rangle^\Gamma \\
\langle \text{emp} \rangle^\Gamma &= 0 & \langle \exists x. P \rangle^\Gamma &= \bigvee_{v \in \text{LVALS}} \langle P \rangle^{\Gamma[x \mapsto v]} \\
\langle P \wedge Q \rangle^\Gamma &= \langle P \rangle^\Gamma \wedge \langle Q \rangle^\Gamma & \langle \text{btrue}(B) \rangle^\Gamma &= \text{exprTrue}(B) \\
\langle P \vee Q \rangle^\Gamma &= \langle P \rangle^\Gamma \vee \langle Q \rangle^\Gamma & \langle \text{bfalse}(B) \rangle^\Gamma &= \text{exprFalse}(B) \\
\langle \neg P \rangle^\Gamma &= \neg \langle P \rangle^\Gamma & \langle \text{bsafe}(B) \rangle^\Gamma &= \text{safe}(B) \\
\langle \text{false} \rangle^\Gamma &= \emptyset & \langle E \rangle^\Gamma &= \begin{cases} \text{INSTHEAPS} & \text{if } \langle E \rangle(\Gamma) = \mathbf{true} \\ \emptyset & \text{otherwise} \end{cases}
\end{array}$$

The majority of definition 15 is standard. Of note is the existential logical variable quantification,  $\exists x. P$ . This interprets to the infinite disjunction of the interpretations of  $P$  under *all possible bindings to  $x$* . This is the semantic interpretation of existential quantification used in [22]: if there is *some* value one can assign to the logical variable such that  $P$  is satisfiable, it will be contained within this disjunct. The logical expression assertion evaluates logical expressions, and treats them as boolean outcomes. It is useful for the analysis of logical variables, so that (for example)  $P \wedge v = 5$  is a valid assertion, equivalent to  $P$  if  $v$  is 5, and equivalent to *false* otherwise.

It is useful to determine if one syntactic assertion entails another. We thus lift the entailment relation of definition 9 to syntactic assertions.

**Definition 22** (Entailment assertion). Given the entailment relation  $\models$  (definition 9), assertions ASSTS (definition 21) and logical environments LENVS (definition 20), the **entailment assertion**  $\models_\Gamma \subseteq \text{ASSTS} \times \text{ASSTS}$  is defined as:

$$(P, Q) \in \models_\Gamma \iff \forall \Gamma \in \text{LENVS}. \langle P \rangle^\Gamma \models \langle Q \rangle^\Gamma$$

An element  $(P, Q) \in \models_\Gamma$  is written  $P \models_\Gamma Q$ .

Similarly, we will want to determine when two assertions semantically entail each other.

**Definition 23** (Semantic consequence assertion). Given the semantic consequence relation  $\preceq$  (definition 17), assertions ASSTS (definition 21) and logical environments LENVS (definition 20), the **semantic consequence assertion**  $\preceq_\Gamma \subseteq \text{ASSTS} \times \text{ASSTS}$  is defined as:



$$(P, Q) \in \preceq_{\Gamma} \iff \forall \Gamma \in \text{LENVS}. (P)^{\Gamma} \preceq (Q)^{\Gamma}$$

An element  $(P, Q) \in \preceq_{\Gamma}$  is written  $P \preceq_{\Gamma} Q$ .

### 2.4.2. Local Hoare reasoning

Just as definition 12 built semantic Hoare triples with views, we build *syntactic Hoare triples* with assertions.

**Definition 24** (Syntactic Hoare triples). Given a set of assertions ASSTS (definition 21) and programs PROGRAMS (definition 4), the set of **syntactic Hoare triples** SYNTRIP is defined as:

$$\text{SYNTRIP} \triangleq \text{ASSTS} \times \text{PROGRAMS} \times \text{ASSTS}$$

We can then give syntactic axioms, mirroring the semantic axioms of definition 11.

**Parameter 16** (Syntactic axioms). Given a set of atomic commands ATOMICCMDS (parameter 5), assume a set of **syntactic axioms** SYNAXIOMS  $\subseteq \text{ASSTS} \times \text{ATOMICCMDS} \times \text{ASSTS}$ , such that, for each  $c \in \text{ATOMICCMDS}$ , there exists some  $P, Q \in \text{ASSTS}$  with  $(P, c, Q) \in \text{SYNAXIOMS}$ .

**Example 17** (Primitive heap logic). To give a syntactic presentation of our primitive heap example (example 16), we must first provide logical variables, values and expressions (parameters 13 and 14 respectively). For primitive heaps, we require the ability to link pre- and post-conditions via logical variables. For this, we need only use set of program values for logical values, LVALS = PVALS. The set of logical expressions is the minimal set given by the definition. The interpretation is the smallest function satisfying the definitional equations.

We can then define the domain specific syntactic assertions for primitive heaps with variables as resource. There are two standard cells: a variable-as-resource cell, and the flat heap cell. There is also an *expression evaluation assertion*. This links the result of evaluating an expression to the result of evaluating a logical expression. It

will be used to determine the outcome of evaluating expressions.

The assertions are defined as: for all  $x \in \text{PVARs}$ ,  $E, E_1, E_2 \in \text{LEXPRS}$ ,  $e \in \text{EXPRS}$

$$\begin{aligned} x \rightarrow E & \quad \text{Variable cell} \\ E_1 \mapsto E_2 & \quad \text{Flat heap cell} \\ e \Rightarrow E & \quad \text{Expression evaluation cell} \end{aligned}$$

Recalling the view syntax of example 16, their interpretations are:

$$\begin{aligned} \langle x \rightarrow E \rangle^\Gamma & = x \mapsto \langle E \rangle(\Gamma) \\ \langle E_1 \mapsto E_2 \rangle^\Gamma & = \langle E_1 \rangle(\Gamma) \mapsto \langle E_2 \rangle(\Gamma) \\ \langle e \Rightarrow E \rangle^\Gamma & = e \Rightarrow \langle E \rangle(\Gamma) \end{aligned}$$

It will often be useful to describe a variable where we do not know the value. We can derive such an assertion as:

$$x \rightarrow - \triangleq \exists x. x \rightarrow x$$

The syntactic axioms are: for all  $x \in \text{PVARs}$ ,  $e \in \text{EXPRS}$ ,  $x, y, z \in \text{LVARs}$ ,  $P \in \text{ASSTs}$

$$\begin{array}{lll} \{(x \rightarrow x * P) \wedge e \Rightarrow Y\} & x := e & \{x \rightarrow Y * P\} \\ \{x \rightarrow x\} & x := \text{alloc}() & \{\exists x, y. (x \rightarrow x * x \mapsto y)\} \\ \{x \mapsto y * (P \wedge e \Rightarrow x)\} & \text{free}(e) & \{P\} \\ \{x \mapsto y * (P \wedge e_1 \Rightarrow x \wedge e_2 \Rightarrow z)\} & [e_1] := e_2 & \{x \mapsto z * P\} \\ \{y \mapsto z * ((x \rightarrow x * P) \wedge e \Rightarrow y)\} & x := [e] & \{y \mapsto z * x \rightarrow z * P\} \end{array}$$

Every syntactic axiom must correspond to some semantic axiom (definition 11) in that for *every* interpretation of the syntactic axiom, we can find a semantic axiom that justifies it.

**Parameter 17** (Syntactic small axiom soundness). Assume that for each syntactic small axiom  $(P, C, Q) \in \text{SYNAXIOMS}$  and for any logical environment  $\Gamma \in \text{LENVS}$ , there exists some semantic axiom  $(\langle P \rangle^\Gamma, C, \langle Q \rangle^\Gamma) \in \text{AXIOMS}$ .

The axioms for primitive heaps 17 can be seen as sound simply by interpreting them into their underlying views.

Program proofs are constructed using *syntactic proof rules*. These rules are standard from local Hoare logics, but extend the consequence rule into a *semantic consequence rule* by using the semantic consequence relation of definition 17. This presentation is used by [22], and allows for proofs that change the instrumentation associated with assertions.

**Definition 25** (Syntactic proof rules). Given assertions ASSTS (definition 21) and the syntactic entailment and semantic consequence relations  $\models_{\Gamma}$  and  $\preceq_{\Gamma}$  (definition 22 and definition 23 respectively), there are two classes of proof rules: those driven by analysis of program syntax, and those driven by analysis of assertion syntax. They are: for all assertions  $P, Q, R, O \in \text{ASSTS}$

### Program syntax rules

$$\begin{array}{c}
\text{SKIP RULE:} \\
\frac{}{\vdash \{P\} \text{ skip } \{P\}} \\
\\
\text{AXIOM RULE:} \\
\frac{(P, C, Q) \in \text{SYNAXIOMS}}{\vdash \{P\} C \{Q\}} \\
\\
\text{SEQUENCING RULE:} \\
\frac{\vdash \{P\} C_1 \{O\} \quad \vdash \{O\} C_2 \{Q\}}{\vdash \{P\} C_1; C_2 \{Q\}} \\
\\
\text{IF RULE:} \\
\frac{P \models_{\Gamma} \text{bsafe}(B) \quad \vdash \{P \wedge \text{btrue}(B)\} C_1 \{Q\} \quad \vdash \{P \wedge \text{bfalse}(B)\} C_2 \{Q\}}{\vdash \{P\} \text{if } (B) C_1 \text{ else } C_2 \{Q\}} \\
\\
\text{WHILE RULE:} \\
\frac{P \models_{\Gamma} \text{bsafe}(B) \quad \vdash \{P \wedge \text{btrue}(B)\} C \{P\}}{\vdash \{P\} \text{while } (B) C \{P \wedge \text{bfalse}(B)\}} \\
\\
\text{PARALLEL RULE:} \\
\frac{\vdash \{P_1\} C_1 \{Q_1\} \quad \vdash \{P_2\} C_2 \{Q_2\}}{\vdash \{P_1 * P_2\} C_1 \parallel C_2 \{Q_1 * Q_2\}}
\end{array}$$

### Assertion syntax rules

$$\begin{array}{c}
\text{FRAME RULE:} \\
\frac{\vdash \{P\} \mathbb{C} \{Q\}}{\vdash \{P * R\} \mathbb{C} \{Q * R\}} \\
\\
\text{CONSEQUENCE RULE:} \\
\frac{P \preceq_{\Gamma} P' \quad \vdash \{P'\} \mathbb{C} \{Q'\} \quad Q' \preceq_{\Gamma} Q}{\vdash \{P\} \mathbb{C} \{Q\}} \\
\\
\text{DISJUNCTION RULE:} \\
\frac{\vdash \{P_1\} \mathbb{C} \{Q_1\} \quad \vdash \{P_2\} \mathbb{C} \{Q_2\}}{\vdash \{P_1 \vee P_2\} \mathbb{C} \{Q_1 \vee Q_2\}} \\
\\
\text{CONJUNCTION RULE*}: \\
\frac{\vdash \{P_1\} \mathbb{C} \{Q_1\} \quad \vdash \{P_2\} \mathbb{C} \{Q_2\}}{\vdash \{P_1 \wedge P_2\} \mathbb{C} \{Q_1 \wedge Q_2\}} \\
\\
\text{EXISTENTIAL ELIMINATION RULE:} \\
\frac{\vdash \{P\} \mathbb{C} \{Q\}}{\vdash \{\exists x. P\} \mathbb{C} \{\exists x. Q\}}
\end{array}$$

\* The conjunction rule is only valid if the underlying view system has the primitive conjunctivity property (definition 16).

We now show that any syntactic triple derived with our proof system is a valid triple.

**Theorem 2** (Soundness of the syntactic rules). *Given logical environments LENVS (definition 20), assertions ASSTS (definition 21) and programs PROGRAMS (definition 4), for all logical environments  $\Gamma \in \text{LENVS}$ , assertions  $P, Q \in \text{ASSTS}$  and programs  $\mathbb{C} \in \text{PROGRAMS}$ ,  $\vdash \{P\} \mathbb{C} \{Q\}$  implies  $\models \{(P)\}^{\Gamma} \mathbb{C} \{(Q)\}^{\Gamma}$ .*

*Proof.* The proof is by induction on the structure of the derivation. It is straightforward, as most of the syntactic rules have an equivalent semantic rule in theorem 1. We demonstrate only one example of this case. We also give the existential case, which has no associated semantic rule.

**Sequential case:** For any logical environment  $\Gamma \in \text{LENVS}$ , the assertions  $P, Q$  and  $O$  interpret to some views  $p, q$  and  $o$  respectively. By the premises,  $\vdash \{P\} \mathbb{C}_1 \{O\}$  and  $\vdash \{O\} \mathbb{C}_2 \{Q\}$  hold. By the inductive hypothesis  $\models \{p\} \mathbb{C}_1 \{o\}$  and  $\models \{o\} \mathbb{C}_2 \{q\}$  hold. By the semantic inference rules,  $\models \{p\} \mathbb{C}_1; \mathbb{C}_2 \{q\}$  holds. This is equal to  $\models \{(P)\}^{\Gamma} \mathbb{C}_1; \mathbb{C}_2 \{(Q)\}^{\Gamma}$ , hence  $\vdash \{P\} \mathbb{C}_1; \mathbb{C}_2 \{Q\}$  is valid.

**Existential case:** Assume that, for all  $\Gamma \in \text{LENVS}$ ,  $\vdash \{P\} \mathbb{C} \{Q\}$ . By the inductive hypothesis,  $\models \{(P)\}^{\Gamma} \mathbb{C} \{(Q)\}^{\Gamma}$  holds. By the assumption and inductive hypothesis, it must be that, for all  $\Gamma$  and  $V$ , both  $\{(P)\}^{\Gamma[x \mapsto V]}$  and  $\{(Q)\}^{\Gamma[x \mapsto V]}$  hold. Therefore, by the semantic conjunction rule,  $\models \{\bigvee_{V \in \text{LVALS}} \{(P)\}^{\Gamma[x \mapsto V]}\} \mathbb{C} \{\bigvee_{V \in \text{LVALS}} \{(Q)\}^{\Gamma[x \mapsto V]}\}$  holds. The

conclusion pre-condition  $\exists x. P$  interprets as  $\bigvee_{v \in \text{LVALS}} (P)^{\Gamma[x \mapsto V]}$ , and similarly for  $Q$ . The result follows.  $\square$

### 2.4.3. Additional language rules

The programming language we have defined is simple, supporting only a minimal set of standard commands. This is sufficient for demonstrating our reasoning and for giving examples, as the techniques of this thesis are focused on axiomatic specifications for libraries. The specific choices of language features (such as local variables and functions) are orthogonal to our contributions. To ease the presentation of examples, we will occasionally assume some of these richer features, but will never require them for our library specifications.

We expect our techniques to apply to richer languages without difficulty. Partially, this is due to our use of views [22]. The views system provides a general pattern for developing reasoning systems. For example, results from the views paper show how any separation algebra can be directly lifted into a view system. Existing separation logic work, such as that on Java [56] or JavaScript [34] should be easily recast into a view. Our library reasoning can then be directly applied. In the long term, we will use this approach to provide richer automated reasoning, by linking with separation logic reasoning tools such as Verifast [45].

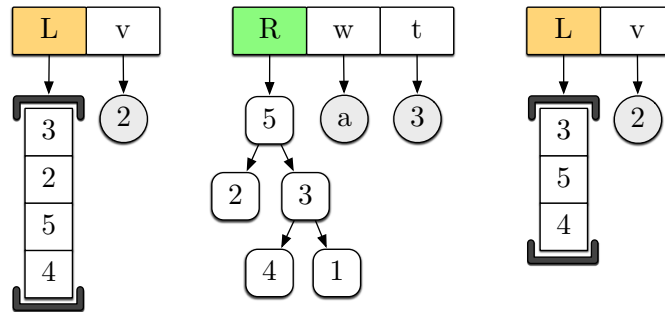
## 2.5. Summary

This chapter has introduced *addressed value views*, a specialisation of the views framework [22]. We will build on this foundation in the rest of our thesis, using it to provide the core program logic for structural separation logic. There are few novel contributions in this chapter, as it merely specialises existing work to our specific needs. However, two minor contributions are included:

1. **Views for a WHILE language:** Section 2.3 gives reasoning rules for the standard WHILE language features, `if` and `while`. As opposed to [22], which was given in terms of more abstract language with non-deterministic choice and iteration, this allows views-style reasoning about standard example programs.
2. **Syntactic proof theory:** Section 2.4 offers a syntactic proof theory for views, adding a standard syntactic assertion language and associated syntactic Hoare rules. This enables standard rules such as existential elimination, which are not given in the views publications.

### 3. Structural separation logic

We now turn to programming with and reasoning about libraries that manipulate *abstract* data. Rather than creating data structures such as lists and trees, by imposing shapes on heap cells via inductive or abstract predicates, we define structured heaps that store rich data in single cells. For example, by picking structured heap values that are “entire lists”, heaps can store a list or tree at a single address. The first and last diagrams below store a whole list at address L, whilst the centre stores a whole tree at address R (the other cells are simple variables):



Along with these rich values, we define collections of atomic commands that manipulate these values directly. For example, the command `remove(2)`, which removes a value from a list, transforms the above left-hand list heap into the right-hand list. Our choices of structured heaps and commands are designed to give imperative machines that match the mental model programmers have of libraries. We will give two library examples in section 3.1, working with *lists* and *unranked trees* of integers.

Reasoning about these libraries requires more than standard separation. The interesting structure is not in the *domain* of the heap, but rather in the *values*. Standard techniques from separation logic (section 1.2.1) are not sufficient, as we want to split the structured values into smaller sub-data. The data splitting approaches of context or segment logic (section 1.2.3) are also insufficient, as we want to retain the simple shape of, and reasoning associated with, heaps. This chapter introduces *structural separation logic* to enable fine-grained reasoning about these structures whilst retaining the natural heap reasoning style

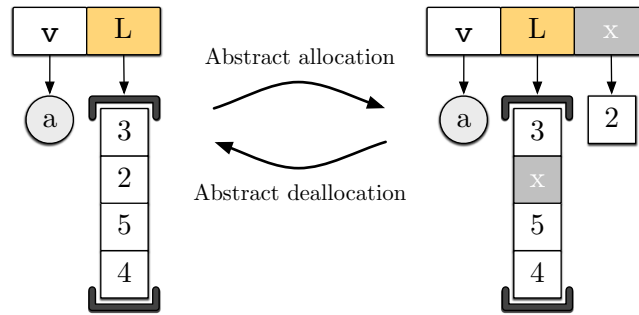


Figure 3.1.: Abstract allocation and deallocation for the sub-data 2 of the list stored at L.

of separation logic.

Structural separation logic consists of two key contributions. The first is *abstract heaps*. Abstract heaps are an instrumentation for structured heaps that enables fine-grained reasoning about structured heaps. They are similar in shape to structured heaps, but allow the reasoning to create *abstract heap cells* at *structural addresses*. When fine grained access to *sub-data* of some rich value is desired, that sub-data can be “cut out” from its current location, and promoted to a fresh abstract heap cell. This process, which exists only to facilitate program reasoning, *abstract allocation and deallocation*, is our second contribution.

Abstract allocation and deallocation is illustrated in figure 3.1. On the left-hand side is a structured heap without any abstract heap cells. If we wanted to isolate the list element 2 for analysis, we can apply abstract allocation. This process finds a new *abstract address*  $\mathbf{x}$ , cuts the element 2 from the list, and places it a new abstract heap cell at address  $\mathbf{x}$ . The cutting is achieved using an approach similar to multi-holed contexts (section 1.2.3), and so leaves behind a *body address* in the place of the element 2. Once the analysis is complete, this body address enables *abstract deallocation*, in which the abstract heap cell is destroyed, and its contents replace the body address  $\mathbf{x}$ , a process we call *compression*. Abstract allocation is like machine allocation, in that we do not know exactly *which* structural address will be picked, but we do know there will be one available to choose regardless of the addresses used in any frame. Like machine allocation, this ensures that no possible frames can be invalidated by the creation of abstract heap cells.

Abstract allocation enables the isolation of fine grained data in abstract heap cells. These abstract cells can then be used to give small footprints to commands that operate on rich data. Consider a command `remove(x)` that removes  $\mathbf{x}$  from a list. Via abstract heap cells we can give an axiom to the command that describes the precise effect of

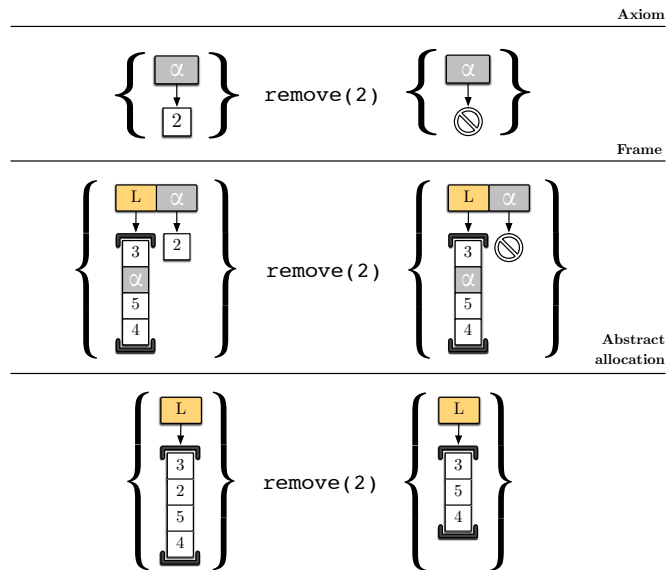


Figure 3.2.: Derivation using abstract allocation.

removing a single list element. Proofs using abstract heaps can describe the effect of commands only on the data used, such as in figure 3.2, in which the logical variable  $\alpha$  is used to contain the address used for abstract allocation. The proof first uses abstract allocation to isolate the list element 2 in an abstract heap cell. The resultant abstract heap behaves exactly like a normal heap, so we can use the standard frame rule to set aside the rest of the list at  $L$ . The axiom for `remove` can then be applied, the frame restored, and abstract deallocation used to merge the (now empty) data at  $\alpha$  back into the list.

The abstract heap cells and body addresses, despite being only instrumentation (and hence invisible to program commands), do create different instrumented structured heaps. The various abstract heaps in figure 3.3 are all distinct, as they have differing uses of structural addresses to cut up the data. However, once all the abstract heap cells are compressed, they do represent the same structured heap. As our framework uses the view system, each of these heaps are related by *semantic consequence* (section 2.3.3). The structural addressing, being instrumentation, can be updated by semantic consequence. This allows abstract allocation and deallocation to be achieved in the proof theory by the semantic consequence rule of the program logic.

Structural separation logic can be created for many types of structured data, and can be used to give small axioms for many types of libraries. The data and libraries we



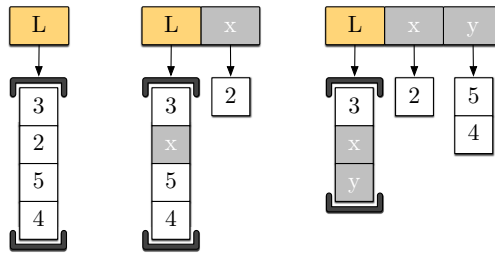


Figure 3.3.: Different abstract heaps for the same list. Each list heap (and tree heap) is identical in terms of content, but has different *structural addressing*. The heaps are all *semantic consequences* of each other.

demonstrate in this chapter are simple, allowing us to focus on the technical details of structural separation logic. Here, we introduce two example libraries, those of *lists* and *trees*. The tree case is similar to the list case in terms of constructing heaps, but has a quite different set of commands. Later, we use the techniques from this chapter to reason about larger libraries such as DOM (chapter 4) and the POSIX file system (chapter 6).

### 3.1. Structured data libraries

We begin by constructing imperative machines for *lists* and *trees*. We give an abstract representation of the data, and library commands that work directly on this abstract representation.

#### 3.1.1. List library

We consider finite lists of unique positive integers, defined in two parts: *partial lists* are sequences of numbers; and *lists* are partial lists wrapped in delimiters that indicate they are complete. We split the definition this way to emphasise the *structured* nature of lists as being composed of many partial lists. It will also prove convenient for defining the atomic commands. We use unique elements to ensure each command can operate on a guaranteed distinct element, which reduces the verbosity of our library commands<sup>1</sup>.

**Definition 26** (Lists). The set of lists `LISTS`, ranged over by  $l, l_1, \dots, l_n$  is defined by induction as: for all  $i \in \mathbb{N}^+$

<sup>1</sup>This choice is purely to simplify the presentation, and is not a requirement of structural separation logic

$$\begin{aligned}
pl &::= i \mid pl_1 \otimes pl_2 \mid \emptyset && \text{Partial lists} \\
l &::= [pl] && \text{Lists}
\end{aligned}$$

where  $\otimes$  is associative with left and right identity  $\emptyset$ , and each list contains only distinct numbers. Lists are equal up to the properties of  $\otimes$ .

Example lists are  $[1 \otimes 5 \otimes \emptyset]$ ,  $[6 \otimes 2]$  and  $[\emptyset]$ . The  $\otimes$  operator being associative but not commutative ensures the lists are ordered. The identity property means that  $[1 \otimes 5 \otimes \emptyset]$  is equal to  $[1 \otimes 5]$ . Note that  $[5 \otimes 5]$  is *not* one of these lists, as the elements are not unique.

List modules typically allow programmers to work with many lists, creating, updating and destroying them as necessary. For simplicity, we initially consider a module that works on just a single list and will consider the multiple list case in section 3.5.2. We therefore build *list heaps* by extending the variables as resource heaps (introduced in example 2) with a single *list address* mapping to a complete list structure.

**Definition 27** (List heaps). Given sets of program variables PVARs and program values PVALS (definition 3), and lists LISTS, and assuming a single heap list address L, where  $L \notin \text{PVARs}$ , the set of **list machine heaps** LISTHEAPS, ranged over by  $lh, lh_1, \dots, lh_n$ , is defined as:

$$\text{LISTHEAPS} \triangleq \{lh \mid lh : (\text{PVARs} \xrightarrow{\text{fin}} \text{PVALS}) \sqcup (\{L\} \rightarrow \text{LISTS})\}$$

Notice that the list portion of the heap is a total function from a singleton domain; there is always exactly one list in the heap. An example list heap instance is given in figure 3.4.

Consider the commands that might manipulate a list. Common operations are appending an element to the back of a list, removing an element, searching the list for an element, and finding elements adjacent to other elements. Rather than aim for the most realistic collection of commands here, we pick a small set designed to demonstrate a range of structural manipulations. We choose the following list commands, and in chapters 4 and 6 we will examine more realistic libraries.

1. **append(i)**: Appends the value of variable **i** to the list, making it the last element. Faults if the value of **i** is already in the list, or if it is not a positive integer.
2. **remove(i)**: Removes the value of variable **i** from the list. Faults if the value of **i** is not in the list.

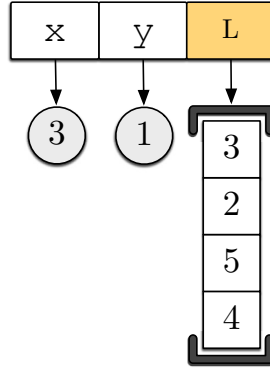


Figure 3.4.: An instance of a list heap (definition 27), storing the variables  $x$  and  $y$  and the list  $L$  with value  $[3 \otimes 2 \otimes 5 \otimes 4]$ .

3.  $i := \text{getFirst}()$ : Assigns to  $i$  the first element of the list, or 0 if the list has no elements.
4.  $j := \text{getRight}(i)$ : Assigns to  $j$  the first element in the list that is directly to the right of the value contained in  $i$ . If the value of  $i$  is the last element of the list, assigns 0. Faults if the value of  $i$  is not in the list.

Recall that in primitive heaps (example 4), faults occur only when the heap contained insufficient addresses (that is, a missing variable or unallocated heap cell). These list commands can also fault based upon the *values* contained within heap addresses.

**Definition 28** (List update commands). Given a set of program variable names  $PVARS$  (definition 3) and program expressions  $EXPRS$  (example 5), the **atomic commands of the list update language** are those of the variable system (example 2), and the following additional commands: for all  $i, j \in PVARS, e \in EXPRS$

$$\begin{array}{l}
 C := \text{append}(e) \\
 \quad | \text{remove}(e) \\
 \quad | i := \text{getFirst}() \\
 \quad | j := \text{getRight}(e)
 \end{array}$$

Given partial lists  $PARTIALLISTS$  (definition 26), the actions of the list commands are: for all  $i, j \in \mathbb{N}^+, pl, pl_1, pl_2 \in PARTIALLISTS$

$$\begin{aligned}
\llbracket \text{append}(e) \rrbracket(s) &\triangleq \begin{cases} \{s[L \mapsto [pl \otimes ([e](s))]]\} & \text{if } s(L) = [pl], ([e](s)) \in \mathbb{N} \\ \{s[L \mapsto [pl_1 \otimes ([e](s) \otimes pl_2)]]\} & \exists pl_1, pl_2. pl = pl_1 \otimes ([e](s) \otimes pl_2) \\ \{\ell\} & \text{otherwise} \end{cases} \\
\llbracket \text{remove}(e) \rrbracket(s) &\triangleq \begin{cases} \{s[L \mapsto [pl_1 \otimes pl_2]]\} & \text{if } s(L) = [pl_1 \otimes ([e](s) \otimes pl_2)] \\ \{\ell\} & \text{otherwise} \end{cases} \\
\llbracket i := \text{getFirst}() \rrbracket(s) &\triangleq \begin{cases} \{s[i \mapsto i]\} & \text{if } s(L) = [i \otimes pl] \\ \{s[i \mapsto 0]\} & \text{if } s(L) = [\emptyset] \end{cases} \\
\llbracket j := \text{getRight}(e) \rrbracket(s) &\triangleq \begin{cases} \{s[j \mapsto j]\} & \text{if } s(L) = [pl_1 \otimes ([e](s) \otimes j \otimes pl_2)] \\ \{s[j \mapsto 0]\} & \text{if } s(L) = [pl \otimes s(i)] \\ \{\ell\} & \text{otherwise} \end{cases}
\end{aligned}$$

Notice how each command operates on the entire list heap cell, yet does not interact with the majority of the list. For example, the successful `remove` case analyses the list as  $[pl_1 \otimes ([e](s) \otimes pl_2)]$ , and results in the list  $[pl_1 \otimes pl_2]$ . The partial lists  $pl_1$  and  $pl_2$  are unchanged, as elements are unique so there can be no further instances of  $e$ , thus their structure is unanalysed. A normal programmer's intuition for these commands is that they act *only* on the element(s) being operated on; the result of evaluating  $e$  in the remove case. This is not obviously reflected in the operational actions, but will be clearly visible in the axiomatic semantics.

This small set of commands can be used to write implementations for many library commands we do not axiomatise. The specifications we will derive for these implementations could equally well have been included as axioms for the commands. These implementations are both a sanity check and shortcut. Given a command implementation, if our axioms allow us to derive the specification we would expect, then we have more confidence that our subset is useful (as it proves useful programs) and complete (in that we have sufficient primitive commands to achieve all needed manipulations of the data structure). Moreover, if our reasoning is sound, there is no need to prove the soundness of additional axioms for these commands. We take this approach in both our DOM and POSIX file system examples (chapters 4 and 6 respectively).

Command implementations are also a convenient source of demonstrations for our reasoning. A command `i := getLast()` can be implemented by using `getFirst`, and re-

```

i := getLast()  $\triangleq$  local j {
  i := 0;
  j := getFirst();
  while (j  $\neq$  0)
    i := j;
    j := getRight(i)
}

b := contains(k)  $\triangleq$  local i {
  i := getFirst();
  b := false;
  while (i  $\neq$  0)
    if (i = k)
      b := true;
    else
      skip;
    i := getRight(i);
}

```

Figure 3.5.: Example programs for list commands.

peatedly applying `getRight` until 0 is returned. Similarly, a command `contains(k)` that determines if an element stored in variable `i` exists in the list can be given. Both of these examples are presented in figure 3.5.

In these examples, we assume a simple local variable construction, `local p1, ..., pn { C }`, which restricts the scope of variables `p1` through `pn` to the program `C`. We justify using such a language extension in section 2.4.3, and it is used only to aid readability of the examples. It does not change the expressivity of the system in a meaningful fashion.

### 3.1.2. Tree library

The techniques we used to build the imperative machine for lists can also be used to build a similar machine for trees. We consider trees as finite, unranked trees consisting of *nodes* uniquely identified by positive integers.

**Definition 29** (Trees). The set of **trees** TREES, ranged over by  $t, t_1, \dots, t_n$ , is defined inductively as follows: for all  $i \in \mathbb{N}^+$

$$t ::= i[t] \mid t_1 \otimes t_2 \mid \emptyset$$

where  $\otimes$  is associative with left and right identity  $\emptyset$ , and each tree contains unique node identifiers. Trees are equal up to the properties of  $\otimes$ .

Some example trees are given in figure 3.6. Notice that a tree may have many root nodes, and that their children are ordered. For convenience, we often write  $i[\emptyset]$  as just  $i$ .

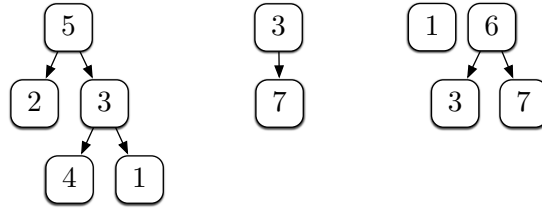


Figure 3.6.: The example trees  $5[2[\emptyset] \otimes 3[4[\emptyset] \otimes 1[\emptyset]]]$ ,  $3[7[\emptyset]]$  and  $1[\emptyset] \otimes 6[3[\emptyset] \otimes 7[\emptyset]]$ .

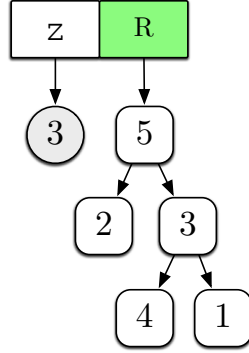


Figure 3.7.: An instance of a tree heap (definition 30), storing the variable  $z$ , and the tree  $5[2 \otimes 3[4 \otimes 1]]$ .

As in lists, we work with a tree module that manipulates just a single tree. This is a surprisingly realistic scenario. For example, web browsers typically work with a single DOM tree representing the web-page. There is also only a single file system on many operating systems.

**Definition 30** (Tree heaps). Given program variables PVARs (definition 3) and trees TREES (definition 29), and assuming a single heap tree address  $R \notin \text{PVARs}$ , the set of **tree heaps** TREEHEAPS, ranged over by  $th, th_1, \dots, th_n$ , is defined as:

$$\text{TREEHEAPS} \triangleq \{th \mid th : (\text{PVARs} \xrightarrow{\text{fin}} \text{PVALS}) \sqcup (\{R\} \rightarrow \text{TREES})\}$$

Notice that the tree at address  $R$  may contain many root nodes; there is a distinction between roots of the tree, and addresses in the heap. One can think of this as being two different namespaces: entire trees are stored in the heap at an address, and, once in the

tree values, there may be many roots. A tree heap instance is given in figure 3.7.

As in lists, we pick commands designed to demonstrate different types of structural manipulations, rather than the most realistic library. The command `createNode` demonstrates how the tree can grow in size by the creation of nodes. The commands `appendNode` and `removeSubtree` demonstrate structural manipulations that can *fault* based on specific properties of the tree being analysed. The `getChild` command demonstrates analysis of the tree children. Their behaviors are:

1. `nID := createNode()`: Creates a node using a fresh identifier, without a parent, and assigns the new node identifier to the variable `nID`.
2. `appendNode(pID, cID)`: Moves the subtree identified by the value of `cID` to be the last child of the node identified by `pID`. Faults if either `pID` or `cID` do not identify nodes within the tree, or if the node identified by `cID` is an ancestor of the node identified by `pID`.
3. `nID := getChild(pID, i)`: Assigns the identifier of the  $i + 1^{\text{th}}$  child of the node identified by `pID` to `nID`. If  $i$  is negative, or the node has fewer than  $i + 1$  children, assigns 0 to `nID`. Faults if `nID` does not identify a node.
4. `removeSubtree(nID)`: Deletes the subtree identified by `nID` from the tree. Faults if `nID` does not identify a node within the tree.

The notion of partial lists was useful in giving actions to the list commands in definition 28. For example, the `remove(i)` command analysed the list as  $[pl_1 \otimes s(i) \otimes pl_2]$ , and gave the result  $[pl_1 \otimes pl_2]$ . Notice that  $pl$  and  $pl_2$  were just “carried” between the input and result. Their specific properties are ignored, so the command will work with any choice for them. It will be similarly convenient to give actions to tree commands using *single holed tree contexts*. As trees are structurally more complex, contexts will allow us to extract sub-trees from a tree whilst ignoring the surrounding data. Tree contexts are intuitively “trees with a hole”, and have an associated composition function that fills the hole to form a complete tree. Note that, by construction, there will be exactly one context hole in a single-holed tree context.

**Definition 31** (Single-holed tree contexts). Given a set of trees TREES (definition 29), the set of **single-holed tree contexts** TREECONTEXTS, ranged over by  $\mathbf{ct}, \mathbf{ct}_1, \dots, \mathbf{ct}_n$ , is defined inductively as follows: for all  $i \in \mathbb{N}^+, t \in \text{TREES}$

$$\mathbf{ct} ::= i[\mathbf{ct}] \mid \mathbf{ct} \otimes t \mid t \otimes \mathbf{ct} \mid -$$

where  $-$  is the context hole,  $\otimes$  is associative with left and right identity  $\emptyset$ , and each tree context contains unique node identifiers. Tree contexts are equal up to the properties of  $\otimes$ .

**Definition 32** (Tree context composition). Given sets of single-holed tree contexts  $\text{TREECONTEXTS}$  (definition 31) and trees  $\text{TREES}$  (definition 29), the **tree context composition** function  $comp : \text{TREECONTEXTS} \rightarrow \text{TREES} \rightarrow \text{TREES}$  is defined by induction on the structure of tree contexts as:

$$\begin{aligned} comp(i[\mathbf{ct}], t) &\triangleq i[comp(\mathbf{ct}, t)] \\ comp(\mathbf{ct} \otimes t_1, t_2) &\triangleq comp(\mathbf{ct}, t_1) \otimes t_2 \\ comp(t_1 \otimes \mathbf{ct}, t_2) &\triangleq t_1 \otimes comp(\mathbf{ct}, t_2) \\ comp(-, t) &\triangleq t \end{aligned}$$

If the result would have duplicate node identifiers,  $comp$  is undefined. The instance  $comp(\mathbf{ct}, t)$  is written  $\mathbf{ct} \circ t$ .

The action for `getChild` in definition 34 will require the ability to calculate the length of a list of siblings.

**Definition 33** (Tree length). Given a set of trees  $\text{TREES}$  (definition 29), the **tree length** function  $len : \text{TREES} \rightarrow \mathbb{N}$ , returning the number of nodes in a tree, is defined by induction on trees in the standard way.

The tree module consists of the following atomic commands and associated actions, defined in terms of the above functions.

**Definition 34** (Tree update commands). Given a set of program variable names  $\text{PVARs}$  (definition 3) and program expressions  $\text{EXPRs}$  (example 5), the atomic commands of the tree update library are those of the variable system (example 2), the following additional commands: for all  $\mathbf{nID}, \mathbf{cID}, \mathbf{pID}, \mathbf{t}, \mathbf{i} \in \text{PVARs}$ ,  $e \in \text{EXPRs}$



```

C ::=  nID := createNode()
      | appendNode(pID, cID)
      | nID := getChild(pID, e)
      | removeSubtree(nID)

```

Given sets of trees TREES (definition 29) and single-holed tree contexts TREECONTEXTS (definition 31), the actions of the tree commands are as follows: for all  $c, i, p, \in \mathbb{N}^+, t, t_1, t_2, t_3 \in \text{TREES}, \mathbf{ct}, \mathbf{ct}_1, \mathbf{ct}_2 \in \text{TREECONTEXTS}$

$$\begin{aligned}
\llbracket \mathbf{nID} := \text{createNode}() \rrbracket (s) &\triangleq \left\{ s[\mathbf{R} \mapsto t_1, \mathbf{nID} \mapsto i] \mid \begin{array}{l} i \in \mathbb{N}^+, t_1 = s(\mathbf{R}) \otimes i[\emptyset], \\ \nexists \mathbf{ct}, t_2. s(\mathbf{R}) = \mathbf{ct} \circ i[t_2] \end{array} \right\} \\
\llbracket \text{appendNode}(\mathbf{pID}, \mathbf{cID}) \rrbracket (s) &\triangleq \begin{cases} \begin{array}{l} s(\mathbf{pID}) = p, \\ s(\mathbf{cID}) = c, \\ s(\mathbf{R}) = \mathbf{ct}_1 \circ c[t_2] \\ \mathbf{ct}_1 \circ \emptyset = \mathbf{ct}_2 \circ p[t_2] \end{array} & \{s[\mathbf{R} \mapsto \mathbf{ct}_2 \circ p[t_2 \otimes c[t_1]]]\} \text{ if} \\ \{\not\downarrow\} & \text{otherwise} \end{cases} \\
\llbracket \mathbf{nID} := \text{getChild}(\mathbf{pID}, e) \rrbracket (s) &\triangleq \begin{cases} \begin{array}{l} s(\mathbf{pID}) = p, ([e])(s) = i \\ \{s[\mathbf{nID} \mapsto c]\} \text{ if } s(\mathbf{R}) = \mathbf{ct} \circ p[t_1 \otimes c[t_2] \otimes t_3], \\ i = \text{len}(t_1) \end{array} \\ \begin{array}{l} s(\mathbf{pID}) = p, ([e])(s) = i, \\ \{s[\mathbf{nID} \mapsto 0]\} \text{ if } s(\mathbf{R}) = \mathbf{ct} \circ p[t], \\ i < 0 \vee i \geq \text{len}(t) \end{array} \\ \{\not\downarrow\} & \text{otherwise} \end{cases} \\
\llbracket \text{removeSubtree}(\mathbf{nID}) \rrbracket (s) &\triangleq \begin{cases} \{s[\mathbf{R} \mapsto \mathbf{ct} \circ \emptyset]\} & \text{if } s(\mathbf{nID}) = i, s(\mathbf{R}) = \mathbf{ct} \circ i[t] \\ \{\not\downarrow\} & \text{otherwise} \end{cases}
\end{aligned}$$

The actions in definition 34 are largely straightforward. The only complexity is within `appendNode`, which must check that the node `cID` is not an ancestor of the node `pID`. If it were, performing the append would create a cyclic “tree”. This property is ensured by

```

c := count(n)  $\triangleq$  local d {
  c := 0;
  d := getChild(n, c);

  while (d  $\neq$  0)
    c := c + 1;
    d := getChild(n, c)
}

```

Figure 3.8.: Example tree program.

first splitting into the new child, and a surrounding context via  $\mathbf{ct}_1 \circ c[t_2]$ . The context  $\mathbf{ct}_1$  is re-analysed by extracting the parent (having first filled the child’s position with empty),  $\mathbf{ct}_1 \circ \emptyset = \mathbf{ct}_2 \circ p[t_2]$ . This ensures that we cannot find the parent as a descendant of the child, which is equivalent to the property we need.

As in the list module, we can define additional commands in terms of the given set. A command `count(n)` that returns the number of children of node `n` is given in figure 3.8.

## 3.2. Abstract heaps

The list and tree libraries (definition 28 and definition 34 respectively) allow programs that manipulate the abstract structures without concern for the implementation. Our use of partial lists and single-holed contexts show that whilst the command actions are on *entire* heap cells, they do not access most of the data in a cell. The majority of the commands can be tracked to only the *local* sub-data they use. We now define *abstract heaps*, which provide a principled approach to reasoning about updates on local sub-data. Abstract heaps extend the structured heaps of an imperative machine (parameter 2) with *structural addresses*. These addresses enable *addressable values* and *abstract heap cells*.

Addressable values are instrumentation for machine values that allow them to be cut-up and re-joined on structural addresses via *compression* and *decompression*. In figure 3.9, decompression uses address `x` cuts a list into *super-data*, containing the body address `x`, and *sub-data* that was removed via the cut. With decompression, we can localise sub-data on which we want to act. The cut data can be re-joined by compression on `x`.

We then use structural addresses to add *abstract heap cells* to structured heaps. This extends the heap domain beyond just `MACHINEADDRS`, allowing it to store sub-data in these “virtual” cells, using structural addresses as *abstract address*. Abstract heap cells are purely instrumentation, invisible to the program, and exist to allow fine-grained access

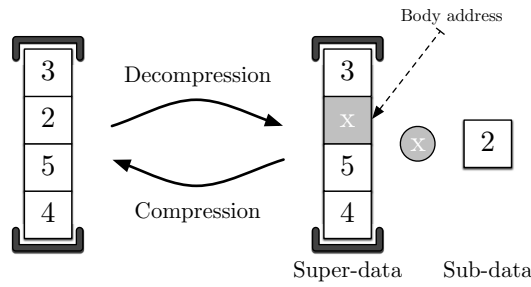


Figure 3.9.: Compression and decompression

to sub-data during program proofs. We call the process of creating and removing abstract heap cells *abstract allocation*, and it is illustrated in figure 3.1. Abstract allocation “picks” some fresh structural address not in use in the heap as either a body nor heap address, uses decompression to cut the data on the chosen address, and promotes the resulting sub-data to a heap cell. Abstract deallocation reverses this process.

Abstract heaps are built upon a choice of imperative machine. We first demonstrate their construction via our list and tree examples. We give body addresses and compression to the data structures, creating *abstract lists* and *abstract trees*. We then use this instrumentation to give an informal notion of how abstract heaps can be built in these cases. Having introduced the concepts with lists and trees, we will then generalise body addresses and compression to data in general. Using this general presentation, we will formalise abstract heaps.

We require a choice of structural addresses to act as both body and abstract heap addresses in our examples. We will use the same set of structural addresses for all data.

**Definition 35** (Structural addresses). Assume a countably infinite set of **structural addresses**  $\text{STRUCTADDRS}$ , ranged over by  $\mathbf{x}, \mathbf{y}, \mathbf{z}, \dots$ .

### 3.2.1. Abstract lists and trees

#### Abstract lists

To define abstract heaps for lists, we must give an approach for compression and decompression of list data using structural addresses. We call addressable list data *abstract lists*, and define them using multi-holed contexts. We use structural addresses to label the context holes, and the holes themselves act as body addresses.

**Definition 36** (Abstract lists). Given a set of structural addresses  $\text{STRUCTADDRS}$  (definition 35), the set of **abstract partial lists**  $\text{ABSPARTIALLISTS}$ , ranged over by  $\mathbf{pl}, \mathbf{pl}_1, \dots, \mathbf{pl}_n$ , and the set of **abstract lists**  $\text{ABSLISTS}$ , ranged over by  $\mathbf{l}, \mathbf{l}_1, \dots, \mathbf{l}_n$  is defined by induction as: for all  $i \in \mathbb{N}, \mathbf{x} \in \text{STRUCTADDRS}$

$$\begin{aligned} \mathbf{pl} ::= & i \mid \mathbf{pl}_1 \otimes \mathbf{pl}_2 \mid \emptyset \mid \mathbf{x} && \text{Abstract partial lists} \\ \mathbf{l} ::= & [\mathbf{pl}] && \text{Abstract lists} \end{aligned}$$

where  $\otimes$  is associative with left and right identity  $\emptyset$ , and each list contains only distinct numbers and body addresses. Abstract (partial) lists are equal up to the properties of  $\otimes$ .

Notice we define a set of  $\text{ABSLISTS}$  and  $\text{ABSPARTIALLISTS}$ . This is because, in abstract heaps, whilst the root address  $L$  will store abstract lists, the abstract heap cells will store only partial lists.

It will be useful to extract the set of body addresses present in an abstract list.

**Definition 37** (Addresses of abstract lists). Given a set of abstract lists  $\text{ABSLISTS}$  (definition 36) using structural addresses  $\text{STRUCTADDRS}$  (definition 35), the **addresses function**  $\text{addr} : (\text{ABSLISTS} \cup \text{ABSPARTIALLISTS}) \rightarrow \mathcal{P}(\text{STRUCTADDRS})$  is defined by induction as:

$$\begin{aligned} \text{addr}(i) & \triangleq \emptyset \\ \text{addr}(\mathbf{pl}_1 \otimes \mathbf{pl}_2) & \triangleq \text{addr}(\mathbf{pl}_1) \cup \text{addr}(\mathbf{pl}_2) \\ \text{addr}(\emptyset) & \triangleq \emptyset \\ \text{addr}(\mathbf{x}) & \triangleq \{\mathbf{x}\} \\ \text{addr}([\mathbf{pl}]) & \triangleq \text{addr}(\mathbf{pl}) \end{aligned}$$

We define compression for abstract lists using standard context substitution on body addresses. As addresses are unique, at most one will be removed by compression.

**Definition 38** (Compression for abstract lists). Given a set of abstract lists  $\text{ABSLISTS}$  (definition 36) using structural addresses  $\text{STRUCTADDRS}$  (definition 35), the **compression function**  $\text{comp} : \text{STRUCTADDRS} \rightarrow (\text{ABSLISTS} \cup \text{ABSPARTIALLISTS}) \rightarrow (\text{ABSLISTS} \cup \text{ABSPARTIALLISTS}) \rightarrow (\text{ABSLISTS} \cup \text{ABSPARTIALLISTS})$  is defined by induction on the structure of abstract lists as:

$$\begin{aligned}
\text{comp}(\mathbf{x}, i, \mathbf{pl}) &\triangleq i \\
\text{comp}(\mathbf{x}, \mathbf{pl}_1 \otimes \mathbf{pl}_2, \mathbf{pl}_3) &\triangleq \text{comp}(\mathbf{x}, \mathbf{pl}_1, \mathbf{pl}_3) \otimes \text{comp}(\mathbf{x}, \mathbf{pl}_2, \mathbf{pl}_3) \\
\text{comp}(\mathbf{x}, \emptyset, \mathbf{pl}) &\triangleq \emptyset \\
\text{comp}(\mathbf{x}, \mathbf{y}, \mathbf{pl}) &\triangleq \begin{cases} \mathbf{pl} & \text{if } \mathbf{x} = \mathbf{y} \\ \mathbf{y} & \text{otherwise} \end{cases} \\
\text{comp}(\mathbf{x}, [\mathbf{pl}_1], \mathbf{pl}_2) &\triangleq [\text{comp}(\mathbf{x}, \mathbf{pl}_1, \mathbf{pl}_2)]
\end{aligned}$$

If the result would not be a member of ABSLISTS, it is undefined. The application  $\text{comp}(\mathbf{x}, \mathbf{l}_1, \mathbf{l}_2)$  is written  $\mathbf{l}_1 \otimes \mathbf{l}_2$ . We overload  $\text{comp}$  to also act on partial lists.

The compression operation implicitly defines decompression. Consider three abstract lists,  $\mathbf{l}_1, \mathbf{l}_2$  and  $\mathbf{l}_3$ , where  $\mathbf{l}_1 \otimes \mathbf{l}_2 = \mathbf{l}_3$ . We can read the equality in both directions, stating that  $\mathbf{l}_1$  and  $\mathbf{l}_2$  *compress* using  $\mathbf{x}$  to form  $\mathbf{l}_3$ , and also that  $\mathbf{l}_3$  *decompresses* via  $\mathbf{x}$  into  $\mathbf{l}_1$  and  $\mathbf{l}_2$ . This gives a notion of *super-data* and *sub-data*. As  $\mathbf{l}_3$  decompresses to  $\mathbf{l}_1$  and  $\mathbf{l}_2$ , we consider  $\mathbf{l}_1$  to be *super-data* of  $\mathbf{l}_2$  and, symmetrically,  $\mathbf{l}_2$  to be *sub-data* of  $\mathbf{l}_1$ .

### Abstract heaps for lists

Decompression allows us to cut-up our data and build *abstract heaps*. Abstract heaps are an instrumented version of structured heaps, and form the models of our local reasoning for rich structured data. We will formally define them in section 3.4. However, they are quite intuitive objects. For this list case, an abstract heap is an extension of the list heap (definition 27), adding *abstract heap cells*. We define abstract heaps in two parts. First, we give *pre-abstract heaps* for lists, which extend the structured heaps for lists by using abstract lists as values, and allowing *abstract heap cells* containing abstract partial lists.

$$\mathbf{h}_l : (\text{PVARs} \xrightarrow{\text{fin}} \text{PVALS}) \sqcup (\{\text{L}\} \rightarrow \text{ABSLISTS}) \sqcup (\text{STRUCTADDRS} \xrightarrow{\text{fin}} \text{ABSPARTIALLISTS})$$

Notice that, whilst  $\text{L}$  maps to ABSLISTS, the structural addresses map to partial lists. Via abstract deallocation, we shall create abstract heap cells using abstract partial lists only. We can never remove the entire list from  $\text{L}$ , so it can store only abstract lists.

Pre-abstract lists are not yet usable for reasoning, as they may not represent *consistent* data. Figure 3.10 gives the three typical problems. Pre-abstract list heap (a) contains two  $\mathbf{x}$  body addresses. This is *ambiguous*, as it connects the same sub-data to two different super-data. This is nonsense, as data cannot belong to two super-data simultaneously- this

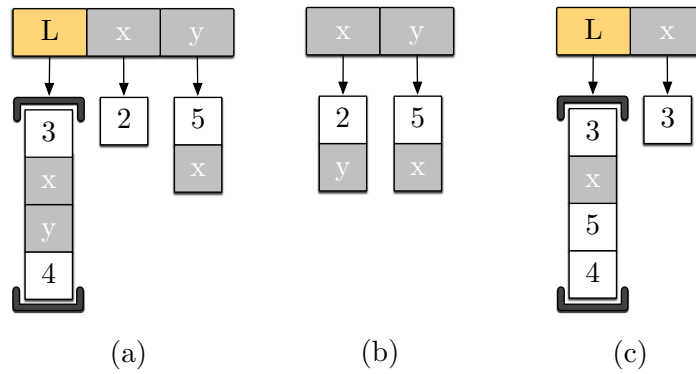


Figure 3.10.: Invalid abstract list heaps.

would create a cyclical data structure. Pre-abstract list heap (b) has structural address  $\mathbf{x}$  present as a body address in the value at address  $\mathbf{y}$ . Similarly, the address  $\mathbf{y}$  is present as a body address in the value at address  $\mathbf{x}$ . This is a *cycle*, where the addressing connects sub-data to itself. Data cannot be super-data of itself, so this is nonsense. Finally, pre-abstract list heap (c) correctly uses structurally addresses, but after abstract cell  $\mathbf{x}$  is compressed, will represent an invalid list containing two 3 elements. Structural addresses must always compress to valid instances of the data structure.

The *abstract list heaps* are those pre-abstract list heaps without these problems. They represent structured list heaps that have used structural addresses to sensibly cut-up lists, so that by repeatedly apply abstract deallocation, they return to a normal structured list heap. However, these abstract heaps may be *partial*, where some of the sensibly cut-up data is absent. In these cases, an abstract heap is useful if we can find *some* additional resource that, when added, results in a abstract heap that can be deallocated to a normal heap.

Figure 3.11 represents three valid abstract lists heaps. Abstract list heap (a) is a normal structured list heap, using no structured addressing. Such heaps are always good abstract heaps. Abstract list heap (b) represents one abstract allocation, where address  $\mathbf{x}$  has been used. By deallocating  $\mathbf{x}$ , we return to a normal heap. Abstract list heap (c) represents a single abstract heap cell. Even though there is nothing to deallocate, it is still a valid abstract heap, as we can find resource (e.g. the cell  $L$  from (b)) that, when added, would allow it to collapse to a structured heap. These three heaps are, respectively, the models of the pre-conditions at each step of figure 3.2.

As abstract heaps are the models of our reasoning, we must provide an interpretation

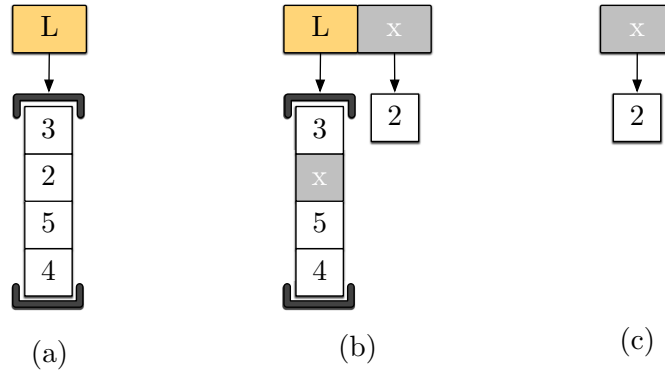


Figure 3.11.: Valid abstract list heaps.

of them as machine heaps via the *reification* process used in the framework (chapter 2). We use a reification of *completion*. The completion process takes a heap, augments it with additional data, then uses abstract deallocation repeatedly until no further addresses can be deallocated. If the result is a structured heap, it is kept as a result. The completion of pre-abstract heap  $\mathbf{x} \mapsto 2$  is given in figure 3.12.

### Abstract trees

Just as with lists, body addresses can be added to trees to enable the creation of abstract tree heaps. We will follow this pattern for all future examples: given a data structure, we add structural addresses to the structure, define an *addr*s function, and give a notion of compression *comp*.

**Definition 39** (Abstract trees). Given a set of structural addresses `STRUCTADDRS` (definition 35), the set of **abstract trees** `ABSTREES`, ranged over by  $\mathbf{t}, \mathbf{t}_1, \dots, \mathbf{t}_n$ , is defined inductively as follows: for all  $i \in \mathbb{N}^+$ ,  $\mathbf{x} \in \text{STRUCTADDRS}$

$$\mathbf{t} ::= i[\mathbf{t}] \mid \mathbf{t}_1 \otimes \mathbf{t}_2 \mid \emptyset \mid \mathbf{x}$$

where  $\otimes$  is associative with left and right identity  $\emptyset$ , and each tree contains only distinct tree identifiers and body addresses. Abstract trees are equal up to the properties of  $\otimes$ .

The addresses and compression functions for abstract trees are defined similarly to the list case.

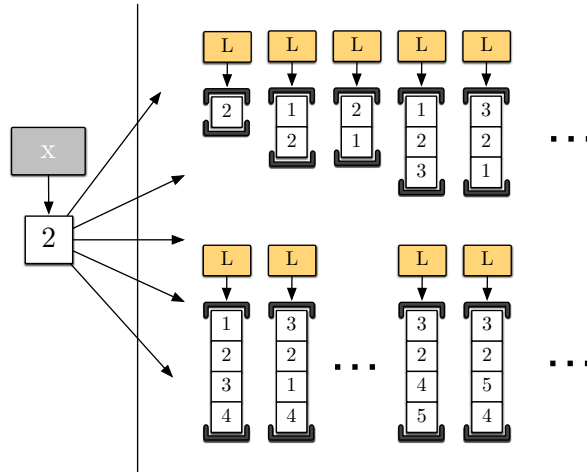


Figure 3.12.: Part of the completion for an abstract list heap  $\mathbf{x} \mapsto 2$ . The set is infinite, consisting of all possible lists containing element 2. We do not consider program variables.

**Definition 40** (Addresses of abstract trees). Given a set of abstract trees  $\text{ABSTREES}$  (definition 39) on structural addresses  $\text{STRUCTADDRS}$  (definition 35), the **addresses function for abstract trees**  $\text{addr} : \text{ABSTREES} \rightarrow \mathcal{P}(\text{STRUCTADDRS})$  is defined similarly to definition 37.

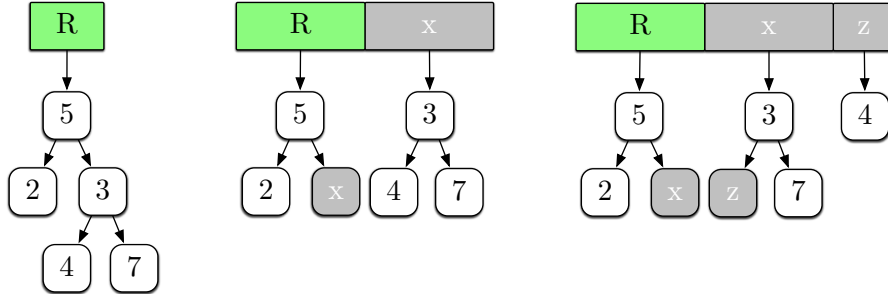
**Definition 41** (Compression for abstract trees). Given a set of abstract trees  $\text{ABSTREES}$  (definition 39) on structural addresses  $\text{STRUCTADDRS}$  (definition 35), the **compression function for abstract trees**  $\text{comp} : \text{STRUCTADDRS} \rightarrow \text{ABSTREES} \rightarrow \text{ABSTREES} \rightarrow \text{ABSTREES}$  is defined similarly to definition 38. As there, the instance  $\text{comp}(\mathbf{x}, \mathbf{t}_1, \mathbf{t}_2)$  is written  $\mathbf{t}_1 \otimes \mathbf{t}_2$ .

Abstract tree heaps are very similar to the abstract list heaps introduced earlier. The type of the pre-abstract tree heaps is:

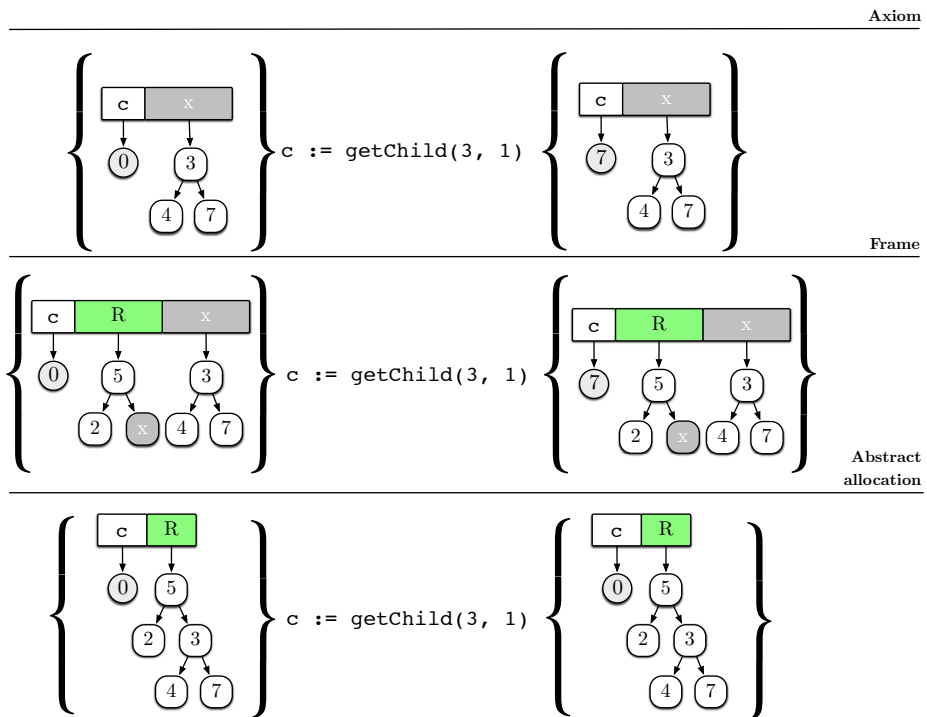
$$\mathbf{h}_t : (\text{PVARs} \xrightarrow{\text{fin}} \text{PVALS}) \sqcup (\{\mathbf{R}\} \rightarrow \text{ABSTREES}) \sqcup (\text{STRUCTADDRS} \xrightarrow{\text{fin}} \text{ABSTREES})$$



The abstract tree heaps are, as in the list case, the pre-abstract tree heaps with well-formedness conditions. They must not have ambiguity nor cycles in their structural addressing, no can the addressing hide invalid data. Three example abstract tree heaps, all representing the same structured heap, are:



With these heaps, we can use abstract allocation to create local-reasoning style proofs, such as the following for  $c := \text{getChild}(3, 1)$ .



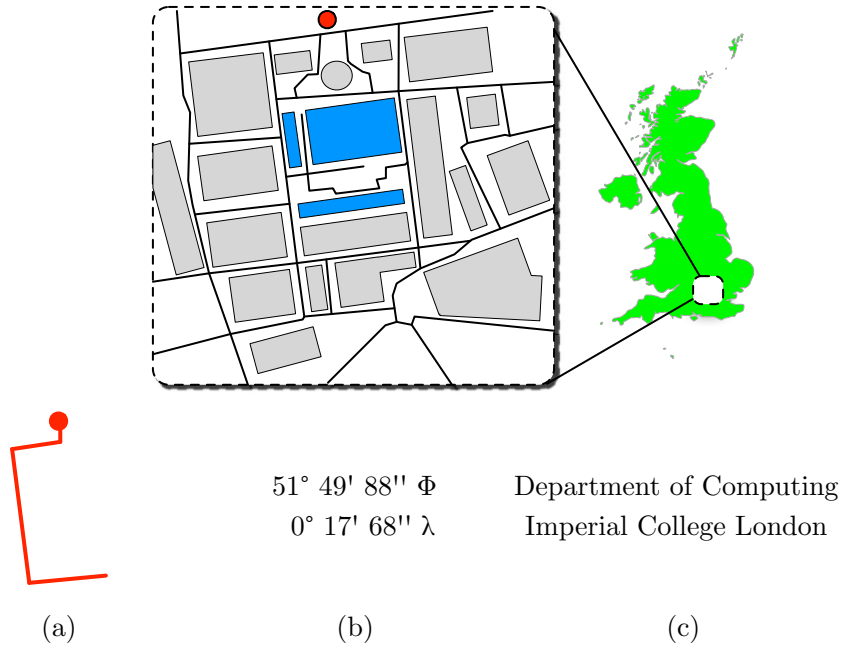


Figure 3.13.: A map of the Imperial College South Kensington campus area, and its position within the United Kingdom. Below the map are three methods of finding the department of Computing: (a) some street directions from a known location, (b) latitude and longitude, and (c) the department's unique name.

### 3.2.2. Formalising abstract heaps

We have introduced abstract lists and abstract trees, using *compression* on structural *body addresses* to enable the cutting up of rich data. This allowed us to also introduce abstract heaps, where the structural *abstract addresses* act as instrumentation atop structured heaps. We now generalise these concepts, giving a notion of compression for arbitrary rich data, and using it to formalise the construction of abstract heaps.

To add compression to lists and heaps, we used multi-holed contexts. The context holes acted as body addresses within data, and context application was compression. We now show that, with minor changes, the *multi-holed context algebras* of [21] can be used to give a generally useful notion of addressing in data. We will not focus on the context-like nature of compression and holes, instead treating them as structural addresses in data. We explain why via an analogy of *maps*.

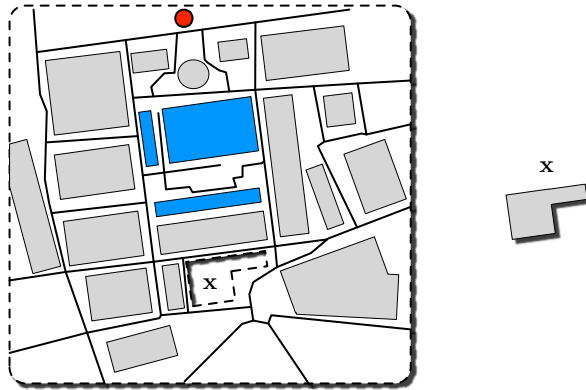
Figure 3.13 contains a map of the area around the Department of Computer Science

at Imperial College London. The dot at the top represents a nearby monument, the Albert Memorial statue. The central via an buildings are the college. For the purposes of this analogy, let the area mapped be our data. Below the map are three possible descriptions for the location of the department. On the left are a set of walking directions: Start at the statue, follow the highlighted roads, and you will arrive at the department. Secondly, there are longitude and latitude co-ordinates. Finally, we have simply the name of the department. All three identify the department in the mapped area: the first by a relationship with something else, the second by some property of the current building that houses it, and the third by a unique characteristic. Because all tell us how to identify a smaller area within the map, we consider the department *sub-data* of the mapped area.

However, we do not consider all of these descriptions to be *addresses*. A good address is one that identifies the target, no matter the frame of reference. Notice that the college is sub-data of the mapped area, but the area itself is sub-data of the entire United Kingdom. The walking directions are therefore *not* good directions in general; their starting point is just “Albert Memorial Statue”, and there are many such statues in the UK. They will take you to a different place when when starting at the wrong statue. The co-ordinates are also not a good address. Were the department to move, they would no longer allow one to find it, as they identify the building rather than the department itself. However, the name of the department will always identify it. It may not tell you how to find it, but it will certainly allow you to determine you have reached the right place.

We will use addresses to refer to sub-data within structures, and therefore need addresses that identify not just single data, but entire sub-structures. Moreover, the data will be mutated by program commands. We therefore need addresses that will be *robust* under mutation. They must survive the creation, destruction, and update of the data they are addressing. Much like the “department name” address above, we cannot use addresses that are tied to some transient property of the data.

Structural addresses will give us these properties. Compression and decompression using unique *body addresses*, when added as instrumentation, ensures that no machine command will have knowledge of them. We can think of these labels as “temporary signs”, in that when we want to identify some sub-data, we erect a structural address, which gives us both a handle for the sub-data, and its relationship with the super-data. When we no longer need the sub-data, the signs are torn down, and may be reused elsewhere. Consider the following version of the figure 3.13 map. It represents the same area as before, but using structural addressing and decompression, a region within it has been highlighted.



We have no knowledge of the building that stands there, not even its name. However, the decompression using a structural address means it is clearly delineated within the data. As long as the address  $\mathbf{x}$  is not removed or duplicated, we will be able to find the region just by looking for  $\mathbf{x}$ . Addresses need not tell us specifically *how* to find sub-data, They are not necessarily *instructions*, but rather unique “tags” or “handles”. As instrumentation layed over values, we will be able to identify them as distinct elements inside many types of data.

To capture our informal description, we define *structural addressing algebras*. These extend some set of values with structural addressing. Structural addressing algebras form the basis of abstract heap construction. If  $\text{VALUES}$  are the machine values of some structured heap, and  $(\text{VALUES}, \text{STRUCTADDRS}, \text{DATA}, \text{adrs}, \text{comp})$  is a structural addressing algebra on  $\text{VALUES}$ , then  $\text{DATA}$  are the values stored in the associated abstract heaps. The addresses  $\text{STRUCTADDRS}$  form the body addresses of the data. The definition of compression and decompression are given by  $\text{comp}$ . The addresses function  $\text{adrs}$  helps limit the system to sane choices of  $\text{comp}$ .

**Definition 42** (Structural addressing algebra). Given an arbitrary countable set of values  $\text{VALUES}$ , a **structural addressing algebra** on  $\text{VALUES}$  consists of a countably infinite set of **structural addresses**  $\text{STRUCTADDRS}$  (definition 35), a countable set of **addressable data**  $\text{DATA}$ , an **addresses** function  $\text{adrs}$ , and a **compression** function  $\text{comp}$ :

$$\text{Structural addressing algebra} = \left( \begin{array}{c} \text{STRUCTADDRS, DATA,} \\ \text{addrs : DATA} \rightarrow \mathcal{P}(\text{STRUCTADDRS}), \\ \text{comp : STRUCTADDRS} \xrightarrow{\text{fin}} \text{DATA} \rightarrow \text{DATA} \rightarrow \text{DATA} \end{array} \right)$$

where STRUCTADDRS is ranged over by  $\mathbf{x}, \mathbf{y}, \mathbf{z}, \dots$ , DATA is ranged over by  $d, d_1, \dots, d_n$ , and the instance  $\text{comp}(\mathbf{x}, d_1, d_2)$  is written  $d_1 \otimes_{\mathbf{x}} d_2$ . The following properties hold:

1. *Value containment*: Values are addressable data,  $\text{VALUES} \subseteq \text{DATA}$ .
2. *Unaddressed values*: For all  $v \in \text{VALUES}$ ,  $\text{addrs}(v) = \emptyset$
3. *Address properties*: For all  $d_1, d_2 \in \text{DATA}$  and  $\mathbf{x} \in \text{STRUCTADDRS}$ , if  $d_1 \otimes_{\mathbf{x}} d_2$  is defined then:
  - a) *Containment*:  $\mathbf{x} \in \text{addrs}(d_1)$ .
  - b) *Non-overlap*:  $\text{addrs}(d_1) \cap \text{addrs}(d_2) \subseteq \{\mathbf{x}\}$
  - c) *Preservation*:  $(\text{addrs}(d_1) \setminus \{\mathbf{x}\}) \cup \text{addrs}(d_2) = \text{addrs}(d_1 \otimes_{\mathbf{x}} d_2)$
4. *Identity*: For all  $d \in \text{DATA}$  and  $\mathbf{x} \in \text{STRUCTADDRS}$ , there exists some  $d_{\mathbf{x}} \in \text{DATA}$  such that  $d_{\mathbf{x}} \otimes_{\mathbf{x}} d = d$ .
5. *Arbitrary addresses*: For all  $d_1 \in \text{DATA}$  and  $\mathbf{x}, \mathbf{y} \in \text{STRUCTADDRS}$  where  $\mathbf{x} \in \text{addrs}(d_1)$  and  $\mathbf{y} \notin \text{addrs}(d_1)$  or  $\mathbf{y} = \mathbf{x}$ , there exists  $d_{\mathbf{y}} \in \text{DATA}$  such that  $d_1 \otimes_{\mathbf{x}} d_{\mathbf{y}}$  is defined, and that for all  $d_2 \in \text{DATA}$ , if  $d_1 \otimes_{\mathbf{x}} d_2$  is defined, then  $d_1 \otimes_{\mathbf{x}} d_{\mathbf{y}} \otimes_{\mathbf{y}} d_2 = d_1 \otimes_{\mathbf{x}} d_2$ .
6. *Compression left-cancellativity*: For all  $d_1, d_2, d_3 \in \text{DATA}$  and  $\mathbf{x} \in \text{STRUCTADDRS}$ , if  $d_1 \otimes_{\mathbf{x}} d_2 = d_1 \otimes_{\mathbf{x}} d_3$ , then  $d_2 = d_3$ .
7. *Compression quasi-associativity*: For all  $d_1, d_2, d_3 \in \text{DATA}$  and  $\mathbf{x}, \mathbf{y} \in \text{STRUCTADDRS}$  where  $\mathbf{y} \in \text{addrs}(d_2)$  and either  $\mathbf{y} \notin \text{addrs}(d_1)$  or  $\mathbf{x} = \mathbf{y}$ ,  $(d_1 \otimes_{\mathbf{x}} d_2) \otimes_{\mathbf{y}} d_3 = d_1 \otimes_{\mathbf{x}} (d_2 \otimes_{\mathbf{y}} d_3)$ .
8. *Compression quasi-commutativity*: For all  $d_1, d_2, d_3 \in \text{DATA}$  and  $\mathbf{x}, \mathbf{y} \in \text{STRUCTADDRS}$  where  $\mathbf{x} \notin \text{addrs}(d_3)$  and  $\mathbf{y} \notin \text{addrs}(d_2)$ ,  $(d_1 \otimes_{\mathbf{x}} d_2) \otimes_{\mathbf{y}} d_3 = (d_1 \otimes_{\mathbf{y}} d_3) \otimes_{\mathbf{x}} d_2$ .

where undefined terms are considered equal.

These algebras are a minor variant on the multi-holed context algebras of [21]. Specifi-

cally, we have no longer require a number of the identity properties previously used. We also require that the algebra be built upon some set of values, representing unaddressed data.

The following properties of structural addressing algebras are useful:

- **Body addresses are optional:** The value containment property allows us work with abstract heaps that contain complete values (that is, values not containing any body addresses). This will ensure that normal structured heaps are a subset of abstract heaps. The unaddressed values property ensures that the choice of addressing does not conflict with data already present in values.
- **We can always find an address on which to decompress:** The arbitrary addresses property states that, if data can be decompressed, it can be decompressed using infinitely many addresses. Consider the decompression  $d_1 \otimes d_2$ . We can find an infinite subset of  $s \subset \text{STRUCTADDRS}$  such that  $\text{addrs}(d_1) \cap s = \emptyset$ , as each data is finite, and there are infinitely many addresses. For any  $\mathbf{y} \in s$ , there must be some  $d_{\mathbf{y}}$  where  $d_1 \otimes d_{\mathbf{y}}$  and  $d_1 \otimes \mathbf{y} \circledast d_2$  is defined and equal to  $d_1 \otimes d_2$  by the arbitrary addressing property. This last equality ensures that the new address has not meaningfully altered the structure of the data, as no matter what data is eventually placed into the address  $\mathbf{y}$ , it will give the same outcome as it did for address  $\mathbf{x}$ .

This will allow abstract allocation where, if it happens that we chose an address that was already in use, we can find another. This will be useful in constructing abstract heaps, as abstract allocation will always require a fresh address to allocate a new abstract heap cell.

- **Data can be extended:** The address preservation, quazi-associativity and quazi-commutativity properties ensure that incomplete data are *separate* and can be *extended*.

Take some  $d \in \text{DATA}$  that we wish to work with. To extend it “downward” with further sub-data, we can find some  $d_d$  and  $\mathbf{x}$  where  $d \otimes d_d$  is defined. To extend it upward with further super-data, we can find some  $d_u$  and  $\mathbf{y}$  where  $d_u \circledast d$  is defined. Quazi-associativity means that we need not consider which extension we knew about first:  $d_u \circledast (d \otimes d_d)$  and  $(d_u \circledast d) \otimes d_d$  are always equal. Notice that it may not be defined even if both parts are individually defined: address preservation ensures that the contained addresses will be the same regardless of which compression occurs first, but it may be that  $d_u$  and  $d_d$  are incompatible (e.g. both contain the same node identifier in the tree case).

Quazi-commutativity is similar, but covers the case where we have more than one sub-data; e.g.  $d(\otimes)d_{d_1}$  and  $d(\odot)d_{d_2}$  both being defined for some data, and  $\mathbf{x} \neq \mathbf{y}$ . In this case,  $d(\otimes)d_{d_1}(\odot)d_{d_2} = d(\odot)d_{d_2}(\otimes)d_{d_1}$ . Notice that whichever compression occurs first, address preservation ensures that the other address will not be destroyed.

These two cases ensure that we are free to *extend* data by collapsing new data into body addresses. The order of these extensions is irrelevant, as long as we do not introduce ambiguous addressing. Moreover, extending data via one address does not remove an existing ability to extend it via another. We can manipulate these data *separately*.

Both the abstract lists and abstract trees we defined are structural addressing algebras. Both follow directly from the definitions (e.g. structural addresses in definition 35 and abstract lists 36), and from compression being defined as linear substitution (definition 38).

**Lemma 4** (Structural addressing algebra for lists). *Given lists `LISTS` and abstract lists `ABSLISTS` (definition 26) and definition 36 respectively) and structural addresses `STRUCTADDRS` (definition 35), the tuple  $(\text{LISTS}, \text{ABSLISTS}, \text{STRUCTADDRS}, \text{addr}, \text{comp})$  is a structural addressing algebra.*

**Lemma 5** (Structural addressing algebra for trees). *Given trees `TREES` and abstract trees `ABSTREES` (definition 29 and definition 39 respectively) and structural addresses `STRUCTADDRS` (definition 35), the tuple  $(\text{TREES}, \text{ABSTREES}, \text{STRUCTADDRS}, \text{addr}, \text{comp})$  is a structural addressing algebra.*

We now formalise abstract heaps using structural addressing algebras. These heaps build upon a choice of structured heaps, using a structural address algebra to give notions of compression to the values, and structural addresses to add abstract heap cells.

Abstract heaps are the instrumented heaps associated with a choice of structured heap. Throughout this section, we will assume some choice of the following:

**Assumptions 1** (Objects for abstract heap construction). Assume the following parameters:

- A choice of underlying imperative machine on which to build an abstract heap. This includes machine addresses `MACHINEADDRS`, values `MACHINEVALS` and structured heaps `STRUCTHEAPS` (parameters 1 and 2).
- The structural addresses of definition 35.

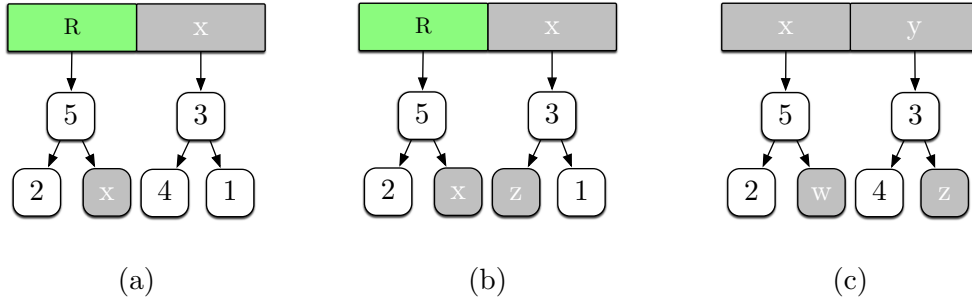


Figure 3.14.: One complete, and two incomplete abstract heaps. Heap (a) represents an abstract heap where all addresses used are present. Heap (b) has body address  $\mathbf{x}$  matching with abstract heap address  $\mathbf{x}$ . However, the body address  $\mathbf{z}$  has no corresponding heap address. Heap (c) has body addresses  $\mathbf{w}$  and  $\mathbf{z}$  with no matching heap addresses. Moreover, the heap address  $\mathbf{y}$  has no matching body address.

- A choice of structural addressing algebra (definition 42) on `MACHINEVALS` using `STRUCTADDRS: (MACHINEVALS, STRUCTADDRS, DATA, addr, comp)`.

We build the definition of an abstract heap for the objects in assumptions 1. As abstract heaps are a superset of standard structured heaps, the set of instrumented addresses for them are the machine addresses plus a set of structural addresses.

**Definition 43** (Abstract heap addresses). Given the objects of assumptions 1, the set of **abstract heap addresses** `ADDRS`, ranged over by  $\mathbf{a}, \mathbf{a}_1, \dots, \mathbf{a}_n$ , is defined as:

$$\text{ADDRS} \triangleq \text{MACHINEADDRS} \cup \text{STRUCTADDRS}$$

The values stored in abstract heaps are drawn from the data of the structural algebra. Consider figure 3.14. Abstract heaps may be *complete*. Either they use just machine addresses and values or, as in case (a) of the figure, they are use structural addressing but data compress together to form machine values. Abstract heaps may also be *partial*, as in cases (b) and (c). These heaps consist of abstract heap cells with no matching body address or values containing body addresses that are not present in the heap domain.

We build the definition of abstract heaps in two stages. First, we define *pre-abstract heaps*, which are the naïve definition formed using the data of a structural addressing algebra as instrumented data. These heaps will have the undesirable properties we examined in the list case (figure 3.10). We test for these properties by defining the *collapse* process,



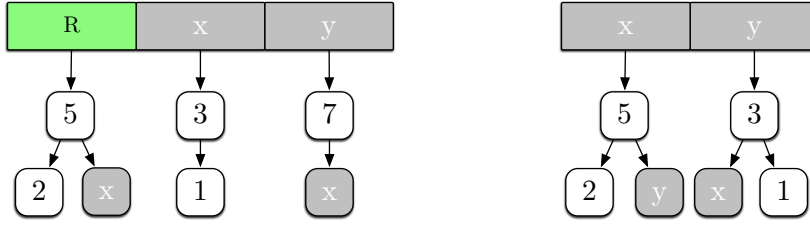


Figure 3.15.: On the left, an *ambiguous* pre-abstract heap, where the same address is used by two bodies. On the right, a *cyclic* pre-abstract heap, where structural addressing creates data that is connected to itself.

which compresses the structural addresses within abstract heaps (in essence, undoing abstract allocation). We then define the *abstract heaps* as these pre-abstract heaps which collapse to a concrete structured heaps.

**Definition 44** (Pre-abstract heaps). Given the objects of assumptions 1 and addresses ADDR<sub>S</sub> (definition 43), the set of **pre-abstract heaps** PREABSHEAPS, ranged over by  $\mathbf{ph}, \mathbf{ph}_1, \dots, \mathbf{ph}_n$ , have the type:

$$\text{PREABSHEAPS} = \left\{ \mathbf{ph} \mid \mathbf{ph} : \text{ADDRS} \xrightarrow{\text{fin}} \text{DATA} \right\}$$

Pre-abstract heaps can be seen the union of three types of heap.

1. By construction, they contain all normal structured heaps (this follows from the value containment property of structural addressing algebras definition 42, and the addresses of definition 43).
2. They contain heaps with sensible uses of structural addressing. This means the values are either complete values, or structured data representing decompressed values that could be compressed together again.
3. They contain nonsensical uses of addressing, or use addressing to disguise badly formed data. These heaps describe no structured data at all. Their use of structural addresses may be *ambiguous*, where multiple values contain the same body address. They may also be *cyclic*, such that addressing connects data to itself. These cases are illustrated in figure 3.15.

The set of abstract heaps will be defined as those pre-abstract heaps with sensible addressing (points 1 and 2 above). We will filter out the nonsensical heaps via the *collapse*

operation. The *single-step collapse* relation describes collapsing one sub-data associated with some structural address in the heap domain into the matching body address of one super-data in the co-domain. It does this only if there is exactly one place in which the compression can occur (so that ambiguous addresses remain uncompressed).

**Definition 45** (Single-step collapse relation). Given the objects of assumptions 1, the **single-step collapse relation**  $\downarrow \subset \text{PREABSHEAPS} \times \text{PREABSHEAPS}$  is defined such that, for all  $\mathbf{ph}_1, \mathbf{ph}_2 \in \text{PREABSHEAPS}$ ,  $(\mathbf{ph}_1, \mathbf{ph}_2) \in \downarrow$  if and only if both:

1. there exists some  $\mathbf{x} \in \text{STRUCTADDRS}$  and some unique  $\mathbf{a} \in \text{ADDRS}$  such that  $\mathbf{x} \in \text{dom}(\mathbf{ph}_1)$ ,  $\mathbf{a} \in \text{dom}(\mathbf{ph}_1)$ ,  $\mathbf{a} \neq \mathbf{x}$ ,  $\mathbf{x} \in \text{addrs}(\mathbf{ph}_1(\mathbf{a}))$  and  $\mathbf{ph}_1(\mathbf{a}) \otimes \mathbf{ph}_1(\mathbf{x})$  is defined;
2. the abstract cell at address  $\mathbf{x}$  has been compressed into the matching body address, and removed from the heap; that is,  $\mathbf{ph}_2 = \mathbf{ph}_1[\mathbf{a} \mapsto \mathbf{ph}_1(\mathbf{a}) \otimes \mathbf{ph}_1(\mathbf{x})] \downarrow_{\text{dom}(\mathbf{ph}_1) \setminus \{\mathbf{x}\}}$ .

An element  $(\mathbf{ph}_1, \mathbf{ph}_2) \in \downarrow$  is written  $\mathbf{ph}_1 \downarrow \mathbf{ph}_2$ . The fact  $\nexists \mathbf{ph}_2. \mathbf{ph}_1 \downarrow \mathbf{ph}_2$  is written  $\mathbf{ph}_1 \not\downarrow$ .

This single-step collapse removes exactly one structural address from a pre-abstract heap. The repeated use of the single-step collapse is expressed in the *collapse relation*.

**Definition 46** (Collapse relation). Given the objects of assumptions 1 and the single-step collapse relation of definition 45, the **collapse relation**,  $\downarrow^* \subset \text{PREABSHEAPS} \times \text{PREABSHEAPS}$ , is the reflexive, transitive closure of the single-step collapse relation  $\downarrow$ . If  $(\mathbf{ph}_1, \mathbf{ph}_2) \in \downarrow^*$ , we write  $\mathbf{ph}_1 \downarrow^* \mathbf{ph}_2$ .

The collapse relation can reveal invalid pre-abstract heaps, by reducing them to a form where no further collapses are possible. As this process essentially performs repeated abstract deallocation, if it “gets stuck” at any point, then the starting point cannot have been created via sensible uses of abstract allocation. For example, the heap  $\mathbf{R} \mapsto 5[2 \otimes \mathbf{x}] * \mathbf{x} \mapsto 3[1] * \mathbf{y} \mapsto 7[\mathbf{x}]$  is stuck; it can collapse no further. The only body address ( $\mathbf{x}$ ) is used ambiguously, and the single-step collapse relation will collapse only if there is a unique heap cell into which it can compress. Analogously, the heap  $\mathbf{x} \mapsto 5[2 \otimes \mathbf{y}] * \mathbf{y} \mapsto 3[\mathbf{x} \otimes 1]$  is cyclic, and has two collapses that go no further:  $\mathbf{x} \mapsto 5[2 \otimes 3[\mathbf{x} \otimes 1]]$  or  $\mathbf{y} \mapsto 3[5[2 \otimes \mathbf{y}] \otimes 1]$ .

Take any pre-abstract heap  $\mathbf{ph}$  and machine heap  $h$ . If  $\mathbf{ph} \downarrow^* h$ , then it must be that the structural addressing used in  $\mathbf{ph}$  was consistent, as every address could be compressed

away until only complete data remained. If this is possible, then  $h$  is the *only* machine heap that the collapse process can generate from  $\mathbf{ph}$ . We give the details of the proof in the appendix, section A.2. However, the result is easy to state.

**Theorem 3** (Unique collapse). *For all  $\mathbf{ph}_1 \in \text{PREABSHEAPS}$ , if there exists  $h \in \text{STRUCTHEAPS}$  such that  $\mathbf{ph}_1 \downarrow^* h$ , then for all  $\mathbf{ph}_2 \in \text{PREABSHEAPS}$ ,  $\mathbf{ph}_1 \downarrow^* \mathbf{ph}_2$  and  $\mathbf{ph}_2 \not\downarrow$  implies  $\mathbf{ph}_2 = h$ .*

This theorem is about pre-abstract heaps that are *complete*, in that they collapse to a structured heap. However, as stated, we will also work with *partial* pre-abstract heaps (figure 3.14). These heaps are critical, as they will represent abstract heaps where some of the data has been “framed off”, and so will form the models of our local reasoning. We must therefore determine which pre-abstract heaps represent partial abstract heaps, a goal we achieve with *completion*. Completion adds in arbitrary extra resource to the domain of a pre-abstract heap, then determines if the result can collapse to a structured heap. If this was possible, then the partial heap must represent a portion of at least one useful abstract heap. If there is *no* additional resource we can add to which allows a pre-abstract heap to collapse to a structured heap, then it must be nonsense.

The completion process must consider adding both abstract heap cells and machine addressed heap cells. Recall figure 3.14. The heap labelled (b) will require an abstract heap cell addressed by  $\mathbf{z}$  to complete the tree structure. The heap labelled (c) will require three heap cells to complete the tree structure: abstract cells  $\mathbf{w}$  and  $\mathbf{z}$ , plus a machine cell at R.

**Definition 47** (Pre-abstract heap completions). Given the objects of assumption 1 and pre-abstract heaps  $\text{PREABSHEAPS}$  (definition 44), the **completions** of a pre-abstract heap,  $(\cdot) : \text{PREABSHEAPS} \rightarrow \mathcal{P}(\text{STRUCTHEAPS})$ , are defined as:

$$(\mathbf{ph}_1) \triangleq \left\{ s \in \text{STRUCTHEAPS} \mid \mathbf{ph}_2 \in \text{PREABSHEAPS}, (\mathbf{ph}_1 \sqcup \mathbf{ph}_2) \downarrow^* h \right\}$$

A pre-abstract heap  $\mathbf{ph}$  is called **completable** if  $(\mathbf{ph}) \neq \emptyset$ .

We define the abstract heaps as all those pre-abstract heaps which have completions.

**Definition 48** (Abstract heaps). Given the objects of assumption 1, the **abstract heaps**  $\text{ABSHEAPS}$ , ranged over by  $\mathbf{h}, \mathbf{h}_1, \dots, \mathbf{h}_n$ , are those pre-abstract heaps which are completable.

$$\text{ABSHEAPS} \triangleq \{\mathbf{ph} \in \text{PREABSHEAPS} \mid (\mathbf{ph}) \neq \emptyset\}$$

Both ambiguous and cyclic pre-abstract heaps have no completions, and are thus not abstract heaps. Recall figure 3.15. The ambiguous left-hand heap can never collapse to a machine heap, as the collapse process will never be able to completely remove the abstract cell  $\mathbf{x}$  because it exists twice as a body address. The right-hand heap will never complete, as the collapse process will eventually reduce it to either  $\mathbf{x} \mapsto 5[2 \otimes 3[\mathbf{x} \otimes 1]]$  or  $\mathbf{y} \mapsto 3[5[2 \otimes \mathbf{y}] \otimes 1]$ . It will collapse no further, as to get rid of, e.g., body address  $\mathbf{x}$ , it would have to have an abstract heap cell addressed  $\mathbf{x}$ , which is not possible as  $\mathbf{x}$  already exists.

**Lemma 6** (Abstract heaps are unambiguous). *All  $\mathbf{h} \in \text{ABSHEAPS}$  are unambiguous, in that there are no  $\mathbf{x} \in \text{STRUCTADDRS}$  such that there exists  $\mathbf{a}_1, \mathbf{a}_2 \in \text{ADDRS}$  with  $\mathbf{x} \in \mathbf{h}(\mathbf{a}_1)$  and  $\mathbf{x} \in \mathbf{h}(\mathbf{a}_2)$ .*

**Lemma 7** (Abstract heaps are acyclic). *All  $\mathbf{h} \in \text{ABSHEAPS}$  are acyclic, in that there are no structural addresses  $\mathbf{x} \in \text{STRUCTADDRS}$  and (possibly empty) chain of single-step collapse steps such that  $\mathbf{h} \downarrow^* \mathbf{h}_n$  with  $\mathbf{x} \in \text{addrs}(\mathbf{h}_n(\mathbf{x}))$ .*

The proofs of these lemmas are in the appendix, lemmas 28 and 29. Therefore, abstract heaps are either: complete machine heaps using no structural addressing; abstractly addressed heaps that collapse to complete machine heaps, so the addressing is merely a re-expression of the concrete data; or heaps in which structural addresses are present, but some super- or sub-data is not present. We can see any of these situations as describing a heap which is *consistent*. It may be that the heap is incomplete, but there must exist at least one extension to it that would result in a concrete structured heap.

### 3.3. Reasoning about abstract heaps

Reasoning with abstract heaps follows the pattern introduced in chapter 2. We first show that abstract heaps are instrumented structured heaps (parameter 8), and have primitive reification and composition compatible with the notion of views (definition 8). This ensures that our framework can be used with abstract heaps, and so provides us with a sound Hoare logic for program verification.

We select  $\text{INSTHEAPS} = \text{ABSHEAPS}$  in our framework. As the completion function on abstract heaps removes all structural addressing, generating the set of heaps *consistent* with the addresses, we pick the it to act as primitive reification.

**Definition 49** (Primitive reification for abstract heaps). Given the completion function  $(\cdot)$  (definition 47), the **primitive reification function for abstract heaps**,  $\llbracket \cdot \rrbracket : \text{ABSHEAPS} \rightarrow \mathcal{P}(\text{STRUCTHEAPS})$ , is defined as:

$$\llbracket \mathbf{h} \rrbracket = (\mathbf{h})$$

Primitive composition for abstract heaps is disjoint function union, if the usage of structural addresses remains sensible.

**Definition 50** (Primitive composition for abstract heaps). The **primitive composition function for abstract heaps**,  $\bullet : \text{ABSHEAPS} \rightarrow \text{ABSHEAPS} \rightarrow \text{ABSHEAPS}$ , is defined as:

$$\mathbf{h}_1 \bullet \mathbf{h}_2 \triangleq \begin{cases} \mathbf{h}_1 \sqcup \mathbf{h}_2 & \text{if } \mathbf{h}_1 \sqcup \mathbf{h}_2 \in \text{ABSHEAPS} \\ \text{undefined} & \text{otherwise} \end{cases}$$

We must prove associativity, commutativity and unit properties for primitive heap composition. The following lemma is helpful in this.

**Lemma 8** (Sub-heaps of an abstract heap are abstract heaps). *Let  $\mathbf{h} \in \text{ABSHEAPS}$ . Then, for all  $\mathbf{ph}_1, \mathbf{ph}_2 \in \text{PREABSHEAPS}$  if  $\mathbf{h} = \mathbf{ph}_1 \bullet \mathbf{ph}_2$  then  $\mathbf{ph}_1, \mathbf{ph}_2 \in \text{ABSHEAPS}$ .*

*Proof.* By definition, a pre-abstract heap  $\mathbf{ph} \in \text{PREABSHEAPS}$  is an abstract heap if we can find some  $\mathbf{ph}'$  and  $h \in \text{STRUCTHEAPS}$  such that  $\mathbf{ph} \sqcup \mathbf{ph}' \downarrow^* h$ .

Assume that  $\mathbf{h} = \mathbf{ph}_1 \bullet \mathbf{ph}_2$ ,  $\mathbf{ph}_1, \mathbf{ph}_2 \in \text{ABSHEAPS}$ . By definition, this means  $\mathbf{h} = \mathbf{ph}_1 \sqcup \mathbf{ph}_2$ . Moreover, as  $\mathbf{h} \in \text{ABSHEAPS}$ , there exists  $\mathbf{ph}_3 \in \text{PREABSHEAPS}$  and  $h \in \text{STRUCTHEAPS}$  such that  $\mathbf{h} \sqcup \mathbf{ph}_3 \downarrow^* h$ . Take  $\mathbf{ph}_1$ , and let  $\mathbf{ph}'_1 = \mathbf{ph}_2 \sqcup \mathbf{ph}_3$ . By associativity and commutativity of  $\sqcup$ , it follows that  $\mathbf{ph}_1 \sqcup \mathbf{ph}'_1 \downarrow^* h$ , ergo  $\mathbf{ph}_1 \in \text{ABSHEAPS}$ . Similar reasoning generates  $\mathbf{ph}_2 \in \text{ABSHEAPS}$ .  $\square$

**Lemma 9** (Primitive composition associates and commutes). *The primitive composition operator for abstract heaps  $\bullet$  (definition 50), associates and commutes with unit  $\{\} \rightarrow \{\}$ .*

*Proof.* Let  $\mathbf{h}_1, \mathbf{h}_2, \mathbf{h}_3 \in \text{ABSHEAPS}$ . Then:

- **Associativity:** We must show  $(\mathbf{h}_1 \bullet \mathbf{h}_2) \bullet \mathbf{h}_3 = \mathbf{h}_1 \bullet (\mathbf{h}_2 \bullet \mathbf{h}_3)$ . Assume  $(\mathbf{h}_1 \bullet \mathbf{h}_2) \bullet \mathbf{h}_3$  is defined. Then, by lemma 8, the definition of  $\bullet$  and the associativity of  $\sqcup$ ,  $(\mathbf{h}_2 \sqcup \mathbf{h}_3)$  is

a defined abstract heap. Theorem 3 and associativity of  $\sqcup$  ensures that  $\mathbf{h}_1 \sqcup (\mathbf{h}_2 \sqcup \mathbf{h}_3)$  is defined, and equal to  $(\mathbf{h}_1 \bullet \mathbf{h}_2) \bullet \mathbf{h}_3$ . The result follows.

- **Commutativity:** We must show  $\mathbf{h}_1 \bullet \mathbf{h}_2 = \mathbf{h}_2 \bullet \mathbf{h}_1$ . This follows directly from the definition of  $\bullet$  as disjoint function union, and the commutativity thereof.
- **Unit:** We must show  $\mathbf{h}_1 \bullet \{\} \rightarrow \{\} = \mathbf{h}_1$ . This follows directly, as  $\mathbf{h}_1 \bullet \{\} \rightarrow \{\} = \mathbf{h}_1 \sqcup \{\} \rightarrow \{\} = \mathbf{h}_1$ .

□

Therefore, abstract heaps with the above notions of reification and composition is an addressed value view (definition 8). This, along with the results of our framework (chapter 2) ensures a sound local Hoare reasoning system.

**Definition 51** (Abstract heaps build addressed value views). Taking the set of instrumented heaps as  $\text{ABSHEAPS}$ , primitive reification as  $(\cdot)$  (definition 49), primitive composition as  $\bullet$  (definition 50 and the identity as the identity function  $\{\} \rightarrow \{\}$ , the following addressed value view is generated:

$$(\mathcal{P}(\text{ABSHEAPS}), [\cdot], *, 0)$$

## Reification

We have selected the completion function (definition 47) as our reification operation. This is a natural choice, as it means partial heaps are interpreted as *all* the machine heaps which they could plausibly represent. However, this choice does result in intuitionistic reasoning, similar to example 11. This is because the completions provide *arbitrary* additional resource, rather than just the resource needed to account for partial structured data. We can define an *extensional* notion of completions as an alternative, which adds only heap cells needed to account for missing structural addresses.

**Definition 52** (Extensional Pre-abstract heap completions). Given the objects of assumption 1, the **extensional completion** of a pre-abstract heap  $(\cdot) : \text{PREABSHEAPS} \rightarrow \mathcal{P}(\text{STRUCTHEAPS})$  is defined as:

$$(\mathbf{ph}) \triangleq \left\{ s \in \text{STRUCTHEAPS} \left| \begin{array}{l} \mathbf{ph}' \in \text{PREABSHEAPS}, (\mathbf{ph} \sqcup \mathbf{ph}') \downarrow^* s, \\ \forall \mathbf{a} \in \text{dom}(\mathbf{ph}'). \mathbf{a} \in \text{STRUCTADDRS} \\ \implies \exists \mathbf{b} \in \text{dom}(\mathbf{ph}). \mathbf{a} \in \text{addr}(\mathbf{ph}(\mathbf{b})), \\ \nexists \mathbf{a} \in \text{dom}(\mathbf{ph}'). \mathbf{ph}(\mathbf{a}) = s(\mathbf{a}) \end{array} \right. \right\}$$

There are two new conditions on this when compared to definition 47. The first states that new abstract heap cells are added *only* to account for body addresses that had no abstract heap cell with a matching structural address. The second ensures that no heap cells are added that remain unchanged during the collapse process (and so were not needed for collapsing). Therefore, the only data added is that which enables a previously impossible collapse step.

We call this different completion *extensional* as it extends the structured data to account for partial data. We call the choice of definition 49 *intuitionistic* to match the previous separation logic terminology. In this thesis, we use intuitionistic completions unless otherwise stated.

**Comment 5.** The intuitionistic approach extends data to complete it, and extends the heap with arbitrary cells. The extension approach is designed to *only* extend data, but can still generate new heap cells to ensure that all abstract heap cells have a machine heap cell into which they can collapse. I choose to use the intuitionistic approach, as the extensional definition seems rather a “half way house”. It can be used to provide *arbitrary* additional structured data with, for example, heaps such as  $\mathbf{x} \mapsto \emptyset$  (an abstract list heap consisting of one abstract cell with no contents), but provides at most one machine heap cell. I do not find it useful to allow one and not the other, and given that we *must* provide some level of completions to give meaning to partial abstract heaps, working with the intuitionistic case seems the most natural choice.

### 3.3.1. Data assertions

That abstract heaps form an addressed value algebra (definition 51) ensures our framework provides a sound Hoare logic using abstract heaps as models. We need only create an assertion language to describe them. We use two types of syntactic assertion. The first type, **abstract data assertions**, describes abstractly addressed data, and will largely be

defined on a model-by-model basis. These assertions describe the *contents* of structured and abstract heap cells, and there will be different assertion languages for lists, for trees, and for the DOM and POSIX examples of later chapters. However, all these languages must support a set of common operations, including *compression*.

The second type, **abstract heap assertions**, will be common to all libraries, and describes abstract heaps themselves. These assertions are parameterised on the data assertions. The abstract heap assertions describe entire abstract heaps, including the cells and separation between them.

### Abstract data assertions

The set of assertions describing structurally addressed data consists of assertions useful for all types of data, and those chosen for specific data models.

**Definition 53** (Assertions about arbitrary structurally addressed data). Given a set of logical variables LVARs (parameter 13) and logical expressions LEXPRS (parameter 14), the set of **data assertions** DATAASSTS, ranged over by  $\phi, \phi_1, \dots, \phi_n$ , is defined by induction as: for all  $\alpha, \mathbf{x} \in \text{LVARs}$ ,  $\mathbf{E} \in \text{LEXPRS}$

$\phi ::=$	$\phi_1 \wedge \phi_2$	Conjunction
	$\phi_1 \vee \phi_2$	Disjunction
	$\phi_1 \Rightarrow \phi_2$	Implication
	<b>false</b>	Falsity
	$\exists \mathbf{x}. \phi$	Existential logical variable
	$\mathbf{E}$	Logical expression
	$\phi_1 @ \phi_2$	Address compression
	$\diamond \alpha$	Address availability
	$\psi$	Data-model specific assertions

where the data-model specific assertions are defined on a data model by data model basis.

The majority of these assertions are standard. Of note are *address compression* and *address availability*. Address compression  $\phi_1 @ \phi_2$  describes data created by compressing sub-data  $\phi_2$  into super-data  $\phi_1$  on the body address contained in logical variable  $\alpha$ . This is a binding operation, so that  $\alpha$  is *bound* within  $\phi_1$ . Recalling that decompression is defined in terms of compression, address compression also allows us to describe data which can be *decompressed*. The assertion will be used to describe situations where  $\phi_2$  can be abstractly



allocated. The address availability assertion  $\diamond\alpha$  describes data containing body address  $\alpha$ . Ergo, it describes *incomplete* data; data in which *some* sub-data can be compressed. This will be used to describe abstract deallocation.

The data assertions are interpreted with logical environments into sets of data.

**Definition 54** (Data assertion interpretation). Given logical expressions with evaluation function  $\llbracket \cdot \rrbracket(\Gamma)$  (parameter 14) on logical environments  $\text{LENVS}$  (definition 20), data assertions  $\text{DATAASSTS}$  (definition 57), and data  $\text{DATA}$  of some structural addressing algebra (definition 42), the assertion interpretation function  $\llbracket \cdot \rrbracket : \text{LENVS} \rightarrow \text{DATAASSTS} \rightarrow \mathcal{P}(\text{DATA})$  must satisfy: for all  $\phi_1, \phi_2 \in \text{DATAASSTS}, \Gamma \in \text{LENVS}$

$$\begin{aligned}
\llbracket \phi_1 \wedge \phi_2 \rrbracket^\Gamma &\triangleq \llbracket \phi_1 \rrbracket^\Gamma \cap \llbracket \phi_2 \rrbracket^\Gamma \\
\llbracket \phi_1 \vee \phi_2 \rrbracket^\Gamma &\triangleq \llbracket \phi_1 \rrbracket^\Gamma \cup \llbracket \phi_2 \rrbracket^\Gamma \\
\llbracket \phi_1 \Rightarrow \phi_2 \rrbracket^\Gamma &\triangleq (\text{DATA} \setminus \llbracket \phi_1 \rrbracket^\Gamma) \cup \llbracket \phi_2 \rrbracket^\Gamma \\
\llbracket \text{false} \rrbracket^\Gamma &\triangleq \emptyset \\
\llbracket \exists x. \phi \rrbracket^\Gamma &\triangleq \bigcup_{x \in \text{LVALS}} \llbracket \phi \rrbracket^{\Gamma[x \mapsto x]} \\
\llbracket \mathbf{E} \rrbracket^\Gamma &\triangleq \llbracket \mathbf{E} \rrbracket(\Gamma) \\
\llbracket \phi_1 \otimes \phi_2 \rrbracket^\Gamma &\triangleq \{d_1 \otimes d_2 \mid d_1 \in \llbracket \phi_1 \rrbracket^\Gamma, d_2 \in \llbracket \phi_2 \rrbracket^\Gamma, \mathbf{x} = \Gamma(\alpha)\} \\
\llbracket \diamond\alpha \rrbracket^\Gamma &\triangleq \{d \in \text{DATA} \mid \Gamma(\alpha) \in \text{addrs}(d)\} \\
\llbracket \psi \rrbracket^\Gamma &\triangleq \text{Defined on model-by-model basis}
\end{aligned}$$

The majority of the interpretations are standard. As stated, address compression describes data that results can be de-compressed into two two sub-data on an address. Address availability describes data that will accept some sub-data into the specified structural address. The data model specific assertions will depend on the data structure being considered, and will typically be syntactic versions of the data structure algebra.

### 3.3.2. Abstract heap assertions

Recall the heap cell assertions used in our primitive heap example (example 17). We define a similar *abstract heap cell* assertion here, where the values of the cell are described using the data assertions of definition 57. We also include standard variable cells, and the assertion that describes expression evaluation.

**Definition 55** (Abstract heap assertions). Given program variables  $\text{PVARs}$  (definition 3), logical expressions  $\text{LEXPRS}$  (parameter 14), addresses  $\text{ADDRS}$  (definition

43), data assertions DATAASSTS (definition 57), the *abstract heap cell* assertions are defined as: for all  $\mathbf{x} \in \text{PVARs}$ ,  $E \in \text{LEXPRS}$ ,  $\mathbf{a} \in \text{ADDRS}$ ,  $\phi \in \text{DATAASSTS}$ ,  $e \in \text{EXPRS}$

$$\begin{aligned}
 P ::= & \quad \mathbf{x} \rightarrow E && \text{Variable cell} \\
 & | E \mapsto \phi && \text{Heap cell} \\
 & | e \Rightarrow E && \text{Expression evaluation}
 \end{aligned}$$

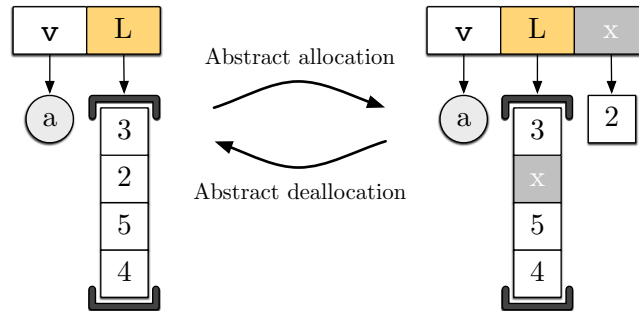
Given a logical expression evaluation function  $\langle \cdot \rangle(\Gamma)$ , (parameter 14), the assertion interpretations are:

$$\begin{aligned}
 \langle \mathbf{x} \rightarrow E \rangle^\Gamma &\triangleq \{ \{ \mathbf{x} \rightarrow V \} \mid V \in \langle E \rangle(\Gamma) \} \\
 \langle E \mapsto \psi \rangle^\Gamma &\triangleq \{ \{ \mathbf{a} \mapsto d \} \mid \mathbf{a} = \langle E \rangle(\Gamma), d \in \langle \psi \rangle^\Gamma \} \\
 \langle e \Rightarrow E \rangle^\Gamma &\triangleq \{ v \in \text{VIEWS} \mid \forall s \in [v]. \langle e \rangle(v) = \langle E \rangle(\Gamma) \} \cap \text{safe}(\langle e \rangle)
 \end{aligned}$$

Notice that, as ADDRs contains both normal and structural addresses, we need only one assertion to describe both standard heap cells and abstractly addressed cells. Abstract heap assertions allow us to describe entire abstract heaps.

### 3.3.3. Abstract allocation

We have now given the constructions of abstract heaps, shown how they form models of our reasoning framework (chapter 2), and created a general assertion language for them. We now demonstrate how abstract heap cells are created and destroyed in the reasoning process. The key observation is that two abstract heaps differing only by uses of abstract allocation and deallocation of some data are *identical* under reification on abstract heaps. Recalling figure 3.1 (the diagram of which is reproduced below for convenience), both sides of the allocation have identical reifications, as  $\downarrow$  collapses  $\mathbf{x}$  and produces the original list (definition 49).



Recall the semantic consequence relation defined in 17. It allows the instrumentation associated with heaps to be altered in proofs, as long as their reifications are not meaningfully changed under any frames. This is precisely the property we require for abstract allocation, if we can be assured that allocating a new abstract heap cell does not clash with any others. We can guarantee this with the standard technique of existentially quantifying over possible addresses (which we have previously used for primitive heap allocation in example 6 and tree node allocation in definition 62). The following *semantic equivalence*, which we call the *abstract allocation and deallocation* property, is therefore justified:

**Theorem 4** (Abstract allocation and deallocation). *Given the semantic consequence relation  $\preceq_{\Gamma}$  (definition 23), data assertions DATAASSTS (definition 57), abstract heap assertions ASSTS (definition 55) and logical variables LVARs (parameter 13), the following semantic equivalence is valid: for all  $\alpha, \beta \in \text{LVARs}$ ,  $\phi_1, \phi_2 \in \text{DATAASSTS}$ , and  $\gamma$  is not free in  $\phi_1$  or  $\phi_2$*

$$(\alpha \mapsto \phi_1 \textcircled{\beta} \phi_2) \preceq_{\Gamma} \exists \gamma. (\alpha \mapsto (\phi_1 \textcircled{\beta} \gamma \wedge \diamond \gamma) * \gamma \mapsto \phi_2)$$

This is the natural property we want for the reasoning. Whenever a proof requires granular data access, we can allocate an abstract heap cell for any addressable sub-data. Whenever the proof no longer needs such fine access, we can collapse the sub-data away.

Reading the equivalence left-to-right, it states “If we can find some heap cell at address  $\alpha$  with data that can, via some address, be decompressed into  $\phi_1$  and  $\phi_2$ , then we can perform the decompression and allocate a cell  $\gamma$  containing  $\phi_2$  whilst knowing for certain that it will compose with the  $\phi_1$  left behind in cell  $\alpha$ ”. The converse reading is also valid, stating that if we have two heap cells that can be compressed, we can deallocate the abstract cell. Notice the use of existential quantification on the address being allocated. This is critical, as this re-expression of the heap must not invalidate any frames. When deallocating, we do not need the existential quantifier, as the address is bound by the compression operation. We use a fresh logical variable  $\gamma$  to capture the fresh address, as it is possible that  $\phi_2$  contains an unbound instance of  $\beta$ . Context application ensures that  $\gamma$  takes the place of  $\beta$  in  $\phi_1$ .

*Proof.* (Theorem 4). First, unroll the equivalence via the consequence assertion to obtain the following pair of semantic consequences, equivalent to the proposition. The left to right case is:

$$\forall \Gamma \in \text{LENVS}. ((\alpha \mapsto (\phi_1 \textcircled{\beta} \phi_2)))^{\Gamma} \preceq ((\exists \gamma. (\alpha \mapsto (\phi_1 \textcircled{\beta} \gamma \wedge \diamond \beta) * \gamma \mapsto \phi_2)))^{\Gamma}$$

The right to left case is:

$$\forall \Gamma \in \text{LENVS}. ((\exists \gamma. (\alpha \mapsto (\phi_1 \circledast \beta) \gamma \wedge \diamond \beta) * \gamma \mapsto \phi_2))^\Gamma \preceq ((\alpha \mapsto (\phi_1 \circledast \phi_2)))^\Gamma)$$

To show the proposition, we must show that both of these semantic consequences hold. Fix some arbitrary  $\Gamma \in \text{LENVS}$ ,  $\mathbf{x} = \Gamma(\alpha)$ ,  $\mathbf{y} = \Gamma(\beta)$ ,  $\mathbf{y} = \Gamma(\gamma)$ . By further unpacking the definition of assertion interpretation and semantic consequence, for the left to right case, we must show: for all  $r \in \text{VIEWS}$

$$\begin{aligned} & \left[ \{ \mathbf{x} \mapsto (d_1 \circledast d_2) \bullet r \mid d_1 \in (\phi_1)^\Gamma[\beta \mapsto \mathbf{y}], d_2 \in (\phi_2)^\Gamma[\beta \mapsto \mathbf{y}] \} \right] \\ & \quad \subseteq \\ & \left[ \bigvee_{\mathbf{y} \in \text{STRUCTADDRS}} \cdot \{ \mathbf{x} \mapsto d_1 \bullet \mathbf{z} \mapsto d_2 \bullet r \mid d_1 \in (\phi_1 \circledast \beta)^\Gamma[\beta \mapsto \mathbf{y}, \gamma \mapsto \mathbf{z}], d_2 \in (\phi_2)^\Gamma[\gamma \mapsto \mathbf{z}] \} \right] \end{aligned}$$

For the right to left case, we must show the converse of the above. Hence, we are actually showing *equality* between the two sets. By unpacking the definition of reification and disjunction, we obtain the following for the left hand side:

$$\begin{aligned} & \left[ \{ \mathbf{x} \mapsto (d_1 \circledast d_2) \bullet r \mid d_1 \in (\phi_1)^\Gamma[\beta \mapsto \mathbf{y}], d_2 \in (\phi_2)^\Gamma[\beta \mapsto \mathbf{y}] \} \right] \\ & = \left[ \llbracket \mathbf{x} \mapsto (d_1 \circledast d_2) \bullet r \rrbracket \mid d_1 \in (\phi_1)^\Gamma[\beta \mapsto \mathbf{y}], d_2 \in (\phi_2)^\Gamma[\beta \mapsto \mathbf{y}] \right] \end{aligned}$$

and the following for the right:

$$\begin{aligned} & \left[ \bigvee_{\mathbf{z} \in \text{STRUCTADDRS}} \cdot \{ \mathbf{x} \mapsto d_1 \bullet \mathbf{z} \mapsto d_2 \bullet r \mid d_1 \in (\phi_1 \circledast \beta)^\Gamma[\beta \mapsto \mathbf{y}, \gamma \mapsto \mathbf{z}], d_2 \in (\phi_2)^\Gamma[\gamma \mapsto \mathbf{z}] \} \right] \\ & = \left[ \bigcup_{\mathbf{z} \in \text{STRUCTADDRS}} \cdot \{ \mathbf{x} \mapsto d_1 \bullet \mathbf{z} \mapsto d_2 \bullet r \mid d_1 \in (\phi_1 \circledast \beta)^\Gamma[\beta \mapsto \mathbf{y}, \gamma \mapsto \mathbf{z}], d_2 \in (\phi_2)^\Gamma[\beta \mapsto \mathbf{z}] \} \right] \\ & = \bigcup_{\mathbf{z} \in \text{STRUCTADDRS}} \cdot \left[ \llbracket \mathbf{x} \mapsto d_1 \bullet \mathbf{z} \mapsto d_2 \bullet r \rrbracket \mid d_1 \in (\phi_1 \circledast \beta)^\Gamma[\beta \mapsto \mathbf{y}, \gamma \mapsto \mathbf{z}], d_2 \in (\phi_2)^\Gamma[\beta \mapsto \mathbf{z}] \right] \end{aligned}$$

The result follows if these two are sets equal for any choice of  $r$ . That is, we must prove:

$$\begin{aligned} & \left[ \llbracket \mathbf{x} \mapsto (d_1 \circledast d_2) \bullet r \rrbracket \mid d_1 \in (\phi_1)^\Gamma[\beta \mapsto \mathbf{y}], d_2 \in (\phi_2)^\Gamma[\beta \mapsto \mathbf{y}] \right] \\ & \quad = \\ & \bigcup_{\mathbf{z} \in \text{STRUCTADDRS}} \cdot \left[ \llbracket \mathbf{x} \mapsto d_1 \bullet \mathbf{z} \mapsto d_2 \bullet r \rrbracket \mid d_1 \in (\phi_1 \circledast \beta)^\Gamma[\beta \mapsto \mathbf{y}, \gamma \mapsto \mathbf{z}], d_2 \in (\phi_2)^\Gamma[\gamma \mapsto \mathbf{z}] \right] \end{aligned}$$

The easier case is right-to-left. Pick any element  $\llbracket \mathbf{x} \mapsto d_1 \bullet \mathbf{z} \mapsto d_2 \bullet r \rrbracket$  from the right-hand side. Then:

$$\llbracket \mathbf{x} \mapsto d_1 \bullet \mathbf{z} \mapsto d_2 \bullet r \rrbracket = \llbracket \mathbf{x} \mapsto (d_1 \circledast d_2) \bullet r \rrbracket$$

$$\begin{aligned}
& \mathbf{R} \mapsto 1[2 \otimes 3] \\
= & \exists \alpha. ((\mathbf{R} \mapsto 1[2 \otimes \alpha]) \textcircled{\alpha} 3) \\
\preceq_{\Gamma} & \exists \alpha. ((\mathbf{R} \mapsto 1[2 \otimes \alpha] \wedge \diamond \alpha) * \alpha \mapsto 3) \\
\preceq_{\Gamma} & \exists \alpha. (\mathbf{R} \mapsto 1[2 \otimes \alpha] * \alpha \mapsto 3)
\end{aligned}$$

Figure 3.16.: Example of abstract allocation splitting a tree heap (definition 39) with three nodes by abstractly allocating a new heap cell  $\alpha$  containing the 3 node.

This follows by the definition of reification as completions (defined in terms of compression), the confluence of completion, the fact that  $\mathbf{z} \in \text{addrs}(d_1)$  by  $d_1 \in (\phi_1 \textcircled{\beta} \gamma \wedge \diamond \gamma)^{\Gamma[\beta \mapsto \mathbf{y}, \gamma \mapsto \mathbf{z}]}$ , and the address irrelevance property that ensures  $d_1 \textcircled{\mathbf{y}} \mathbf{z} \textcircled{\mathbf{z}} d_2 = d_1 \textcircled{\mathbf{y}} d_2$ . Then,  $\llbracket \mathbf{x} \mapsto (d_1 \textcircled{\mathbf{y}} d_2) \bullet r \rrbracket$  is in the left hand side by observation (note that the choice of  $\mathbf{y}$  is irrelevant, as it is bound on the left).

Left-to-right is harder, as the introduction of the new abstract cell  $\mathbf{z}$  could plausibly conflict with some cells in the frame  $r$ . To justify this, we use the same argument that supports allocation of normal heap cells primitive heaps (example 16). As every datum is finite, and  $r$  is a finite map, there are only finitely many “used” structural addresses. By the infinite addresses and address irrelevance properties of structural addressing algebras (definition 42), we can thus replace  $\mathbf{y}$  with some alternative no matter what choice of  $r$  is made. So:

$$\begin{aligned}
& \{ \llbracket \mathbf{x} \mapsto (d_1 \textcircled{\mathbf{y}} d_2) \bullet r \rrbracket \mid d_1 \in (\phi_1)^{\Gamma[\beta \mapsto \mathbf{y}]}, d_2 \in (\phi_2)^{\Gamma[\beta \mapsto \mathbf{y}]} \} \\
= & \{ \llbracket \mathbf{x} \mapsto d_1 \bullet \mathbf{z} \mapsto d_2 \bullet r \rrbracket \mid d_1 \in (\phi_1 \textcircled{\beta} \gamma \wedge \diamond \gamma)^{\Gamma[\beta \mapsto \mathbf{y}, \gamma \mapsto \mathbf{z}]}, d_2 \in (\phi_2)^{\Gamma[\gamma \mapsto \mathbf{z}]} \}
\end{aligned}$$

for *some* choice of  $\mathbf{z}$ , noting that  $\phi_1 \textcircled{\beta} \gamma \wedge \diamond \gamma$  is non-empty when  $\phi_1$  is non-empty by the arbitrary addresses property of structural addressing algebras. As, regardless of the choice of  $r$ , we can find some  $\mathbf{z}$ , the inclusion will hold as we consider *every*  $\mathbf{z}$  in the union.  $\square$

As the abstract allocation relation is contained within semantic consequence, we can use the rule of semantic consequence to perform abstract allocation within a proof. For example, the derivation of figure 3.16 is justified by theorem 4.

**Comment 6.** I see the allocation of abstract heap cells as similar to the use of consequence for (un)rolling inductive predicates in separation logic (discussed in section 1.2.2). Just as an inductive predicate “unrolls” to allow access to the internals of the

structure, we can “allocate” the internals of some rich data. As inductive predicates roll up, we deallocate. Thanks to semantic consequence, the same proof rule justifies both styles of reasoning.

### 3.3.4. Axiomatic specifications for lists and trees

We now give data assertions and axioms for our list library (definition 26). To give these axioms, we will extend the standard logical values to include abstract lists, and logical expressions to include a list membership expression  $E_1 \in E_2$  which tests if the list describe by  $E_1$  is a sublist of the list described by  $E_2$ . This will be used in the `append` command (definition 58) to show that an element does not already exist in the list.

**Definition 56** (Logical values and expressions for lists). Given the set of addresses `ADDRS` (definition 43) and abstract lists `ABSLISTS` (definition 36), the set of logical values for lists `LVALS` is defined as:

$$\text{LVALS} \triangleq \mathbb{Z} \cup \mathbb{B} \cup \text{ADDRS} \cup \text{ABSLISTS}$$

Logical values are thus the standard variable types plus partial lists. The set of logical expressions and their interpretations are those provided by framework (parameter 14), along with *list membership*:

$$E_1 \in E_2$$

The interpretation of this expression, in terms of the interpretation of the standard expressions (parameter 14):

$$\llbracket E_1 \in E_2 \rrbracket(\Gamma) \triangleq \begin{cases} \mathbf{true} & \text{if } \llbracket E_1 \rrbracket(\Gamma) = i, \llbracket E_2 \rrbracket(\Gamma) = \mathbf{l}_3, \mathbf{l}_3 = \exists \mathbf{l}_1, \mathbf{l}_2. \mathbf{l}_1 \otimes i \otimes \mathbf{l}_2 \\ \mathbf{false} & \text{otherwise} \end{cases}$$

The assertions specific to lists are a simple lift of the abstract list data (definition 36).

**Definition 57** (Assertions on lists). Given a set of logical expressions `LEXPRS` and values `LVALS` for lists (definition 56), the set of **list specific data assertions**, providing the model-specific choices for definition , are: for all  $E \in \text{LEXPRS}, \alpha \in \text{LVALS}$

$\psi ::=$	<b>E</b>	Element/Logical expression
	$\psi_1 \otimes \psi_2$	List concatenation
	$\emptyset$	Empty list
	$[\psi]$	Complete list
	$\alpha$	Body address

The data assertion interpretation function is extended by:

$$\begin{aligned}
\langle \mathbf{E} \rangle^\Gamma &\triangleq \langle \mathbf{E} \rangle(\Gamma) \\
\langle \psi_1 \otimes \psi_2 \rangle^\Gamma &\triangleq \{ \mathbf{l}_1 \otimes \mathbf{l}_2 \mid \mathbf{l}_1 \in \langle \psi_1 \rangle^\Gamma, \mathbf{l}_2 \in \langle \psi_2 \rangle^\Gamma \} \\
\langle \emptyset \rangle^\Gamma &\triangleq \{ \emptyset \} \\
\langle [\psi] \rangle^\Gamma &\triangleq \{ [\mathbf{pl}] \mid \mathbf{pl} \in \langle \psi \rangle^\Gamma \} \\
\langle \alpha \rangle^\Gamma &\triangleq \{ \mathbf{x} \mid \mathbf{x} = \Gamma(\alpha) \}
\end{aligned}$$

where, if the results are not sets of abstract lists, the interpretation is undefined.

These assertions allow us to give syntactic axioms to the list commands. Recall the abstract heaps for lists of definition 27, and the abstract heap assertions (definition 55).

**Definition 58** (List update command axioms). The **syntactic axioms for the commands of the list module** (given in definition 28) are as follows: for all  $P \in \text{ASSTS}$ ,  $P$

$\left\{ \begin{array}{l} (\mathbf{L} \mapsto [\mathbf{A} \wedge \neg \exists \alpha. \diamond \alpha] * P) \\ \wedge e \Rightarrow \mathbf{I} \wedge \mathbf{I} \notin \mathbf{A} \\ \{ \alpha \mapsto \mathbf{I} * P \wedge e \Rightarrow \mathbf{I} \} \end{array} \right\}$	<b>append</b> ( $e$ )	$\{ \mathbf{L} \mapsto [\mathbf{A} \otimes \mathbf{I}] * P \}$
$\{ \mathbf{i} \rightarrow - * \mathbf{L} \mapsto [\mathbf{I} \otimes \alpha] \}$	<b>remove</b> ( $e$ )	$\{ \alpha \mapsto \emptyset * P \}$
$\{ \mathbf{i} \rightarrow - * \mathbf{L} \mapsto [\emptyset] \}$	$\mathbf{i} := \text{getFirst}()$	$\{ \mathbf{i} \rightarrow \mathbf{I} * \mathbf{L} \mapsto [\mathbf{I} \otimes \alpha] \}$
$\{ (\mathbf{j} \rightarrow - * \alpha \mapsto \mathbf{I} \otimes \mathbf{J} * P) \wedge e \Rightarrow \mathbf{I} \}$	$\mathbf{i} := \text{getFirst}()$	$\{ \mathbf{i} \rightarrow 0 * \mathbf{L} \mapsto [\emptyset] \}$
$\{ (\mathbf{j} \rightarrow - * \mathbf{L} \mapsto [\alpha \otimes \mathbf{I}] * P) \wedge e \Rightarrow \mathbf{I} \}$	$\mathbf{j} := \text{getRight}(e)$	$\{ \mathbf{j} \rightarrow \mathbf{J} * \alpha \mapsto \mathbf{I} \otimes \mathbf{J} * P \}$
	$\mathbf{j} := \text{getRight}(e)$	$\{ \mathbf{j} \rightarrow 0 * \mathbf{L} \mapsto [\alpha \otimes \mathbf{I}] * P \}$

The semantic axioms are these syntactic axioms interpreted through all logical environments.

The axiom pre-conditions reflect the resource required to ensure fault freedom. Consider the pre-condition for **append**( $i$ ), given by:  $(\mathbf{L} \mapsto [\mathbf{A} \wedge \neg \exists \alpha. \diamond \alpha] * P) \wedge e \Rightarrow \mathbf{I} \wedge \mathbf{I} \notin \mathbf{A}$ . It demands the resource needed to evaluate  $e$ , but also the *entire* list. This is because the command will fault if the result of evaluating  $e$  is already in the list. The only way

to ensure that the command does not fault is to analyse every element of the list. We can capture this using the complete list assertion  $[A]$  with logical variable  $A$ , but must additionally ensure that no data has been abstractly allocated (as the element could then be in another abstract heap cell). This is enforced by the assertion  $\neg\exists\alpha. \diamond\alpha$ , which states that no structural addresses exist in the value of  $A$ . This idiom occurs frequently enough for us to introduce a predicate for it:

**Definition 59** (Complete data predicate). The **is\_complete** predicate is defined as:

$$is\_complete \triangleq \neg\exists\alpha. \diamond\alpha$$

The **remove** command requires only the element to be removed, so the pre-condition assertion  $\alpha \mapsto I$  only needs to refer to the partial list with just element  $I$ . As  $e$  is a program expression, it must evaluate to a natural number in this situation, so the abstract cell must contain a single element. This axiom demonstrates the advantage of the “abstract allocation” approach. The resulting pre-condition is concise, and clearly defines the behavior of the command. Similarly, **getFirst** is well described. If there is a first element, it requires only the evidence of that element (the  $I$ ), and the rest of the list can be removed by allocation using  $\alpha$ . If there is no first element, it requires the empty list as evidence. These axioms exactly describe the actions given to the operation semantics in definition 28, but by virtual of being written in a local manner, we claim are a more intuitive description of the command behaviours.

Soundness of the commands is shown as in the examples of chapter 2. Notice however that some list commands have multiple axioms. This is expected, as richer libraries have commands with state dependent behaviours. In these cases, it is important that at most one of the pre-conditions holds at any one time. If multiple pre-conditions were applicable, the choice of which to use would fall to the person constructing the proof. This results in situations where the program must behave as the *proof* wanted when multiple options are available, rather than the proof behave as the program requires.

**Lemma 10** (Atomic soundness of list commands). *Given structural addressing algebra on abstract lists  $(\text{VIEWS}, [\cdot], *, 0)$  (definitions 42 and 36 respectively), the list commands are atomically sound with respect to their axioms (definition 58), thus satisfying the atomic soundness requirement of the framework (definition 12).*

*Proof.* Atomic soundness requires that, for each semantic axiom  $\{p\} \text{ C } \{q\}$ ,  $\forall r \in \text{VIEWS}. \llbracket \text{C} \rrbracket [p * r] \subseteq [q * r]$ . We show soundness of the first three commands



(`getRight` is similar).

1. `append(i)`: For any frame  $r \in \text{VIEWS}$ , interpretation and reification of the pre-condition results in sets of heaps with the form  $\{\mathbf{L} \mapsto [pl]\} \sqcup h$  where  $h$  contains enough resource to evaluate  $e$ , and where no frame nor completion can affect the contents of  $pl$  as it must contain no structural addresses. The translation of the conjunct  $\mathbf{i} \notin \mathbf{L}$  ensures that  $i \notin pl$ . Therefore, the action of `append` on these heaps is to add a new element  $i$  to the end of the list, resulting in heaps of the form  $\{\mathbf{i} \mapsto i, \mathbf{L} \mapsto [pl \otimes i]\} \sqcup h$ . This is exactly the translation and reification of the post-condition.
2. `remove(i)`: For any frame  $r$ , interpretation and reification of the pre-condition results in sets of heaps with the form  $\{\mathbf{L} \mapsto [pl_1 \otimes i \otimes pl_2]\} \sqcup h$  where  $h$  contains enough resource to evaluate  $e$ . Notice that the value of variable  $\mathbf{i}$  is always present in the list, which has been filled in by the completion process or the frame  $r$  with arbitrary partial lists. The atomic action of `remove` updates the list removing  $i$ , expressly maintaining  $pl_1$  and  $pl_2$ . Interpretation and reification of the post-condition gives heaps of the form  $\{\mathbf{i} \mapsto i, \mathbf{L} \mapsto [pl_1 \otimes pl_2]\} \sqcup h$ , as required.
3. `i := getFirst()`: There are two cases for `getFirst`. For any frame  $r$ , interpretation and reification of  $\mathbf{i} \rightarrow - * \mathbf{L} \mapsto [\mathbf{i} \otimes \alpha]$  results in sets of heaps with the form  $\{\mathbf{i} \mapsto j, \mathbf{L} \mapsto [i \otimes pl]\} \sqcup h$ . The atomic action of the `getFirst` command on these heaps is to assign the value of  $i$  to variable  $\mathbf{i}$ ; no other update is performed. Interpretation and reification of the post-condition using the same frame  $r$  results in heaps of the form  $\{\mathbf{i} \mapsto i, \mathbf{L} \mapsto [i \otimes pl]\} \sqcup h$ , exactly the outcome of the action.

The second pre-condition,  $\mathbf{i} \rightarrow - * \mathbf{L} \mapsto [\emptyset]$ , results in heaps of the form  $\{\mathbf{i} \mapsto j, \mathbf{L} \mapsto [\emptyset]\} \sqcup h$ . To avoid an angelic axiom, soundness requires that these forms be different from that of the first pre-condition, which is evidently true (one contains only lists with at least one element, the other only empty lists). The argument then proceeds as in the first case.

□

### 3.3.5. Assertions and axioms for trees

As we did when defining the underlying data algebra, we follow the pattern used for our list example when defining assertions and axioms for other structures. We first defining logical variables and expressions, then the data assertions and interpretations, finally giving the command axioms. This pattern will be adopted in all future examples.

The logical expression  $|E|$  allows the calculation of the length of a tree sibling list. This is required to axiomatise the `getChild` command.

**Definition 60** (Logical values and expressions for trees). Given a set of addresses `ADDRS` (definition 43) and abstract trees `ABSTREES` (definition 39), the set of **logical values for trees** `LVALS` is:

$$\text{LVALS} = \mathbb{Z} \cup \mathbb{B} \cup \text{ADDRS} \cup \text{TREES}$$

The set of logical expressions and their interpretations are those provided by framework (parameter 14), along with a *sibling list length* expression: for all  $E \in \text{LEXPRS}$

$$|E|$$

The interpretation of this expression is:

$$\llbracket |E| \rrbracket(\Gamma) \triangleq \begin{cases} \text{len}(t) & \text{if } \llbracket E \rrbracket(\Gamma) = t, t \in \text{TREES} \\ \text{undefined} & \text{otherwise} \end{cases}$$

**Definition 61** (Assertions on trees). Given a set of logical expressions `LEXPRS` (definition 14), the set of tree specific data assertions are defined as: for all  $E \in \text{LEXPRS}$

$$\begin{aligned} \psi ::= & \quad E[\psi] && \text{Node} \\ & | \emptyset && \text{Empty tree} \\ & | \psi_1 \otimes \psi_2 && \text{Tree concatenation} \\ & | \alpha && \text{Body address} \end{aligned}$$

The assertion interpretations are:

$$\begin{aligned} \llbracket E[\psi] \rrbracket^\Gamma & \triangleq \{ \llbracket E \rrbracket(\Gamma)[\mathbf{t}] \mid \mathbf{t} \in \llbracket \psi \rrbracket^\Gamma \} \\ \llbracket \emptyset \rrbracket^\Gamma & \triangleq \{ \emptyset \} \\ \llbracket \psi_1 \otimes \psi_2 \rrbracket^\Gamma & \triangleq \{ \mathbf{t}_1 \otimes \mathbf{t}_2 \mid \mathbf{t}_1 \in \llbracket \psi_1 \rrbracket^\Gamma, \mathbf{t}_2 \in \llbracket \psi_2 \rrbracket^\Gamma \} \\ \llbracket \alpha \rrbracket^\Gamma & \triangleq \{ \mathbf{x} \mid \mathbf{x} = \Gamma(\alpha) \} \end{aligned}$$

where, if the result would not be an abstract tree (definition 39), it is undefined.

With these assertions, we can axiomatise the tree commands.

**Definition 62** (Tree update command axioms). The syntactic axioms for the commands of the tree module given in definition 34 are as follows: for all  $P \in \text{AssTs}$ ,  $P$  exact

$$\begin{aligned}
& \{\mathbf{nID} \rightarrow - * \mathbf{R} \mapsto \alpha\} \\
& \mathbf{nID} := \text{createNode}() \\
& \{\exists \mathbf{N}. (\mathbf{nID} \rightarrow \mathbf{N} * \mathbf{R} \mapsto \alpha \otimes \mathbf{N}[\emptyset])\} \\
\\
& \{\mathbf{pID} \rightarrow \mathbf{P} * \mathbf{cID} \rightarrow \mathbf{C} * \alpha \mapsto \mathbf{P}[\beta] * \gamma \mapsto \mathbf{C}[\mathbf{T} \wedge \textit{is\_complete}]\} \\
& \text{appendNode}(\mathbf{pID}, \mathbf{cID}) \\
& \{\mathbf{pID} \rightarrow \mathbf{P} * \mathbf{cID} \rightarrow \mathbf{C} * \alpha \mapsto \mathbf{P}[\beta \otimes \mathbf{C}[\mathbf{T}]] * \gamma \mapsto \emptyset\} \\
\\
& \{(\mathbf{pID} \rightarrow \mathbf{P} * \mathbf{nID} \rightarrow - * \alpha \mapsto \mathbf{P}[\mathbf{T} \otimes \mathbf{N}[\beta] \otimes \gamma] * \mathbf{P}) \wedge e \Rightarrow \mathbf{I} \wedge |\mathbf{T}| = \mathbf{I}\} \\
& \mathbf{nID} := \text{getChild}(\mathbf{pID}, e) \\
& \{\mathbf{pID} \rightarrow \mathbf{P} * \mathbf{nID} \rightarrow \mathbf{N} * \alpha \mapsto \mathbf{P}[\mathbf{T} \otimes \mathbf{N}[\beta] \otimes \gamma] * \mathbf{P}\} \\
\\
& \{(\mathbf{pID} \rightarrow \mathbf{P} * \mathbf{nID} \rightarrow - * \alpha \mapsto \mathbf{P}[\mathbf{T}] * \mathbf{P}) \wedge e \Rightarrow \mathbf{I} \wedge \mathbf{I} < 0 \vee |\mathbf{T}| \leq \mathbf{I}\} \\
& \mathbf{nID} := \text{getChild}(\mathbf{pID}, e) \\
& \{\mathbf{pID} \rightarrow \mathbf{P} * \mathbf{i} \rightarrow \mathbf{I} * \mathbf{nID} \rightarrow 0 * \alpha \mapsto \mathbf{P}[\mathbf{T}] * \mathbf{P}\} \\
\\
& \{\mathbf{nID} \rightarrow \mathbf{N} * \alpha \mapsto \mathbf{N}[\mathbf{T} \wedge \textit{is\_complete}]\} \\
& \text{removeSubtree}(\mathbf{nID}) \\
& \{\mathbf{nID} \rightarrow \mathbf{N} * \alpha \mapsto \emptyset\}
\end{aligned}$$

The semantic axioms are these syntactic axioms interpreted through all logical environments.

The axiom for `createNode` uses an existentially quantified node identifier for the newly created node. This ensures uniqueness in the same way it did for allocation in primitive heaps (example 16). We do not know *what* additional nodes will be present in the tree, as they have been “set aside” leaving only body address  $\alpha$ . However, as all trees are finite, there will infinitely many unused node identifiers, one of which will be bound to  $\mathbf{N}$ . The `appendNode` pre-condition uses the *is\_complete* predicate for the same reason as the pre-condition of list `append` - we must be sure that no sub-data has been abstractly allocated. However, here, the reason is that we must ensure that the parent node is not a descendent of the child. By ensuring that the sub-tree of child  $\mathbf{C}$  is complete, and by knowing that the parent  $\mathbf{P}$  is allocated in abstract cell  $\alpha$ , the separating conjunction assures us that relationship is impossible.

Here, the analysis of data captured in logical variables is used to determine which case

of `getChild` is appropriate. We must be assured that node `N` is the  $i^{\text{th}}$  child. This is accomplished by using the logical expression for sibling length calculation on the prefix siblings-list captured in `T`.

**Lemma 11** (Atomic soundness of tree commands). *Given the views `VIEWS` from a structural addressing algebra on abstract trees (definition 42 and definition 39 respectively), the tree commands are atomically sound with respect to their axioms (definition 62), thus satisfying the atomic soundness requirement of the framework (definition 12).*

We elide the proof, as it is similar in structure to previous atomic soundness proofs (e.g. example 16). The two interesting facets are in `appendNode` and `removeChild`. The `append` command uses pre-condition  $\{i \rightarrow I * \alpha \mapsto P[\beta] * \gamma \mapsto C[T \wedge \text{is\_complete}]\}$ . Notice that the children of node `C` must be complete (that is, have no abstractly allocated sub-tree). This is required to show that the parent node is not a sub-tree of the child (which would create a cycle). We will examine this case in more detail in chapter 7.

In the pre-condition for `removeSubtree`, we also capture the entire sub-tree and ensure that no body addresses are present. This is so that destroying the tree does not destroy a body address. Were it possible to destroy a body address, the axiom could invalidate certain frames. Consider the alternative pre-condition  $nID \rightarrow N * \alpha \mapsto N[\beta]$ . By picking the frame  $\beta \mapsto M[\emptyset]$ , this defective axiom would transform  $nID \rightarrow N * \alpha \mapsto N[\beta] * \beta \mapsto M[\emptyset]$  into  $nID \rightarrow N * \alpha \mapsto \emptyset * \beta \mapsto M[\emptyset]$ . The completions of the pre-condition always have a node labelled `M` as the child of node `N`, so there can never be a node `M` elsewhere in the tree. Completions of the post-condition under this frame would *have to* generate a location for  $\beta$ , which would ensure that every tree contained a node `M`. But, the action of the command is to destroy `N` and all of its children, so the post-condition should *never* contain a node `M`! By destroying the  $\beta$  body address, we would have invalidated any frames containing a  $\beta$  cell, which is counter to atomic soundness.

### 3.3.6. Proofs of examples

We now have all the machinery to prove programs using our abstract list and tree libraries. We show the admissible commands given in sections 3.1.1 and 3.1.2 have their natural specifications. For lists, the commands of figure 3.8 have the following specifications:

$$\begin{aligned}
& \{i \rightarrow - * L \mapsto [A \wedge \text{is\_complete}]\} \\
& \quad i := \text{getLast}() \\
& \{\exists I. (i \rightarrow I * L \mapsto [A \wedge \text{is\_complete}] \wedge ((\exists B. A = B \otimes I) \vee (A = \emptyset \wedge I = 0)))\} \\
& \\
& \{k \rightarrow K * b \rightarrow - * L \mapsto [A]\} \\
& \quad b := \text{contains}(k) \\
& \{k \rightarrow K * ((K \in A \wedge b \rightarrow \text{TRUE}) \vee (K \notin A \wedge b \rightarrow \text{FALSE})) * L \mapsto [A]\}
\end{aligned}$$

The `getLast` specification is perhaps surprising. A suitable small axiom would include the list resource  $L \mapsto [\alpha \otimes I]$ , that is, the last element only. However, we are using an *implementation* here, which requires all the previous elements in order to find the last. This shows that, even when implementable, the specifications for some commands may be smaller when given by axiomatisation.

In giving the programs, we introduced a local variable construction to ease the examples. The corresponding reasoning rule is below. It is applicable only if  $P * p1 \rightarrow - * \dots * pn \rightarrow -$  is satisfiable (that is, if  $P$  does not already mention the newly local variables).

$$\frac{\{P * p1 \rightarrow - * \dots * pn \rightarrow -\} \mathbb{C} \{Q * p1 \rightarrow - * \dots * pn \rightarrow -\}}{\{P\} \text{ local } p1, \dots, pn \{C\} \{Q\}}$$

The proof that `getLast` satisfies this specification is in figure 3.17; the proof for `contains` is in the appendices, section A.1.1. We highlight a small part of the proof in detail, as it outlines an idiom common to many structural separation logic proofs. The first step is *case analysis*, where logical variables capturing structure are broken down into their possibilities. In this case,  $A$  may or may not be an empty list, so we consider both possibilities with the rule of disjunction. If it is empty, we apply the “empty list” instance of the `getFirst` axiom. Otherwise, we must use abstract allocation to localise the surplus data. It can then be framed off, and the axiom applied.

This pattern is very common in all our program proofs. We first manipulate the data representation to *localise* the data of interest to a command. This often involves disjunctions of possibilities. We then apply abstract allocation to set aside unneeded data. Finally, we apply an axiom, then reverse the previous steps with abstract deallocation. In general, we typically elide many of these steps as straightforward, much as separation logic proof sketches do not detail each use of entailment in unrolling inductive predicates.

$$\begin{array}{l}
\{ \quad i \rightarrow - * L \mapsto [A \wedge \textit{is\_complete}] \quad \} \\
i := \textit{getLast}() \triangleq \textit{local } j \{ \\
\quad i := 0; \\
\quad \{ \quad i \rightarrow 0 * j \rightarrow - * L \mapsto [A \wedge \textit{is\_complete}] \quad \} \\
\quad \textit{Use excluded middle to split possibilities for A} \\
\quad \{ \quad i \rightarrow 0 * j \rightarrow - * (L \mapsto [\emptyset] \vee \exists B, C. A = B \otimes C \wedge L \mapsto [A \otimes B] \wedge \textit{is\_complete}) \quad \} \\
\quad \textit{Apply frame rule on i and use rule of disjunction} \\
\quad \quad \textit{Use abstract allocation to isolate first element} \\
\quad \quad \left\{ \begin{array}{l} \exists \alpha. j \rightarrow - * \exists B, C. A = B \otimes C \wedge L \mapsto [(B \otimes \alpha \otimes C)] \\ \exists \alpha. j \rightarrow - * \exists B, C. A = B \otimes C \wedge L \mapsto [B \otimes \alpha] * \alpha \mapsto C \end{array} \right\} \\
\quad \quad \textit{Apply axiom} \\
\quad \quad \left\{ \begin{array}{l} j \rightarrow - * L \mapsto [\emptyset] \\ j := \textit{getFirst}(); \\ j \rightarrow 0 * L \mapsto [\emptyset] \end{array} \right\} \\
\quad \quad \textit{Apply frame rule to isolate list} \\
\quad \quad \left\{ \begin{array}{l} j \rightarrow - * L \mapsto [B \otimes \alpha] \\ j := \textit{getFirst}(); \\ j \rightarrow B * L \mapsto [B \otimes \alpha] \end{array} \right\} \\
\quad \quad \left\{ \quad \exists \alpha. j \rightarrow 0 * \exists B, C. A = B \otimes C \wedge L \mapsto [B \otimes \alpha] * \alpha \mapsto C \quad \right\} \\
\quad \textit{Restore "framed off" data, and take disjunction of result} \\
\quad \left\{ \begin{array}{l} \exists J, B, R, I. i \rightarrow I * j \rightarrow J * L \mapsto [A \wedge \textit{is\_complete}] \wedge \\ ((A = B \otimes J \otimes R \wedge J \neq 0) \vee (A = \emptyset \wedge J = 0 \wedge I = 0) \vee (A = B \otimes I \wedge J = 0)) \end{array} \right\} \\
\quad \textit{while } (j \neq 0) \\
\quad \quad i := j; \\
\quad \quad j := \textit{getRight}(i) \\
\quad \} \\
\left\{ \begin{array}{l} \exists I. (i \rightarrow I * L \mapsto [A \wedge \textit{is\_complete}] \wedge \\ ((\exists B. A = B \otimes I) \vee (A = \emptyset \wedge I = 0))) \end{array} \right\}
\end{array}$$

Figure 3.17.: Proof that `getLast` satisfies its specification

The tree command of figure 3.8 has the following specification:

$$\begin{aligned} & \{ \mathbf{n} \rightarrow \mathbf{N} * \mathbf{c} \rightarrow - * \alpha \mapsto \mathbf{N}[\mathbf{T}] \wedge |\mathbf{T}| = \mathbf{C} \} \\ & \quad \mathbf{c} := \text{count}(\mathbf{n}) \\ & \{ \mathbf{n} \rightarrow \mathbf{N} * \mathbf{c} \rightarrow \mathbf{C} * \alpha \mapsto \mathbf{N}[\mathbf{T}] \wedge |\mathbf{T}| = \mathbf{C} \} \end{aligned}$$

The proof of this specification, which uses the idiom detailed above, is below. The complexity of the loop invariant results from the two possibilities at each loop interaction. Broken down, it contains:

- $\mathbf{n} \rightarrow \mathbf{N} * \mathbf{c} \rightarrow \mathbf{E} * \mathbf{d} \rightarrow \mathbf{D}$  contains the state of the program variables, bound to logical variables.
- $\alpha \mapsto \mathbf{N}[\mathbf{T}] \wedge |\mathbf{T}| = \mathbf{C}$ : Describes the tree resource we currently have: the node  $\mathbf{N}$  whose children are being counted, and a logical variable  $\mathbf{T}$  that captures this resource. The length of this tree is captured in logical variable  $\mathbf{C}$ . Because the length function  $|\mathbf{T}|$  has returned a value, we can infer that the list must contain *no* body addresses (as the function  $|\cdot|$  is undefined in those cases).
- The two possibilities for relating the length  $\mathbf{C}$  of the data captured in  $\mathbf{T}$  to the variables. Either:
  - $\mathbf{D} = 0 \wedge \mathbf{E} = \mathbf{C}$  The identifier returned by the last call to `getChild` is 0, thus the current counter variable must contain the length of the list; or
  - $\mathbf{D} \neq 0 \wedge \exists \mathbf{B}, \mathbf{R}. \mathbf{T} = \mathbf{B} \otimes \mathbf{D} \otimes \mathbf{R} \wedge \mathbf{E} = |\mathbf{B}|$ ) The identifier returned by the last call to `getChild` was not 0. Thus, we can break the list down into a prefix  $\mathbf{B}$ , followed by the element returned  $\mathbf{D}$ , followed by the rest of the list  $\mathbf{R}$ . The length of the prefix must have been assigned to the counter variable.

As in the `getLast` proof, the full text would require case analysis of each possibility. We elide these cases, as they are straightforward to perform, providing little insight into the reasoning.

$$\left\{ \begin{array}{l} \mathbf{n} \rightarrow \mathbf{N} * \mathbf{c} \rightarrow - * \alpha \mapsto \mathbf{N}[\mathbf{T}] \wedge |\mathbf{T}| = \mathbf{C} \\ \mathbf{c} := \text{count}(\mathbf{n}) \triangleq \text{local } \mathbf{d} \{ \\ \quad \mathbf{c} := 0; \\ \quad \mathbf{d} := \text{getChild}(\mathbf{n}, \mathbf{c}) \\ \quad \left\{ \begin{array}{l} \exists \mathbf{D}. \mathbf{n} \rightarrow \mathbf{N} * \mathbf{c} \rightarrow 0 * \mathbf{d} \rightarrow \mathbf{D} * \alpha \mapsto \mathbf{N}[\mathbf{T}] \wedge \\ |\mathbf{T}| = \mathbf{C} \wedge ((\mathbf{C} = 0 \wedge \mathbf{D} = 0) \vee (\mathbf{C} > 0 \wedge \mathbf{D} \neq 0)) \end{array} \right\} \\ \quad \left\{ \begin{array}{l} \exists \mathbf{D}, \mathbf{E}. \mathbf{n} \rightarrow \mathbf{N} * \mathbf{c} \rightarrow \mathbf{E} * \mathbf{d} \rightarrow \mathbf{D} * \alpha \mapsto \mathbf{N}[\mathbf{T}] \wedge \\ |\mathbf{T}| = \mathbf{C} \wedge ((\mathbf{D} = 0 \wedge \mathbf{E} = \mathbf{C}) \vee (\mathbf{D} \neq 0 \wedge \exists \mathbf{B}, \mathbf{R}. \mathbf{T} = \mathbf{B} \otimes \mathbf{D} \otimes \mathbf{R} \wedge \mathbf{E} = |\mathbf{B}|)) \end{array} \right\} \\ \quad \text{while } (\mathbf{d} \neq 0) \\ \quad \quad \mathbf{c} := \mathbf{c} + 1; \\ \quad \quad \mathbf{d} := \text{getChild}(\mathbf{n}, \mathbf{c}) \\ \quad \} \\ \left\{ \mathbf{n} \rightarrow \mathbf{N} * \mathbf{c} \rightarrow \mathbf{C} * \alpha \mapsto \mathbf{N}[\mathbf{T}] \wedge |\mathbf{T}| = \mathbf{C} \right\} \end{array} \right\}$$

### Weakest pre-conditions

As discussed in section 2.3.4, we can give weakest pre-conditions for our frame using the *magic flower*. This is necessary in structural separation logic, as normal entailment is cannot re-express data using abstract addresses. For example, using the natural lifting of  $\Leftarrow^*$  to the syntactic assertion language, the weakest pre-conditions for `createNode()` from our tree example is:

$$\left\{ \begin{array}{l} \exists \alpha, \mathbf{N}. ((\mathbf{R} \mapsto \alpha \otimes \mathbf{N}[\emptyset] * \mathbf{nID} \rightarrow \mathbf{N}) \Leftarrow^* \mathbf{Q}) * \mathbf{R} \mapsto \alpha * \mathbf{nID} \rightarrow \mathbf{J} \\ \mathbf{nID} := \text{createNode}() \\ \mathbf{Q} \end{array} \right\}$$

The derivation, starting with the axiom, is:



$$\begin{array}{c}
\{R \mapsto \alpha * \mathbf{nID} \rightarrow J\} \\
\mathbf{nID} := \text{createNode}() \\
\{\exists N. (R \mapsto \alpha \otimes N[\emptyset] * \mathbf{nID} \rightarrow N)\} \\
\hline
\{((R \mapsto \alpha \otimes N[\emptyset] * \mathbf{nID} \rightarrow N) \Leftarrow * Q) * R \mapsto \alpha * \mathbf{nID} \rightarrow J\} \\
\mathbf{nID} := \text{createNode}() \\
\{((R \mapsto \alpha \otimes N[\emptyset] * \mathbf{nID} \rightarrow N) \Leftarrow * Q) * \exists N. (R \mapsto \alpha \otimes N[\emptyset] * \mathbf{nID} \rightarrow N)\} \\
\hline
\{\exists \alpha, N. ((R \mapsto \alpha \otimes N[\emptyset] * \mathbf{nID} \rightarrow N) \Leftarrow * Q) * R \mapsto \alpha * \mathbf{nID} \rightarrow J\} \\
\mathbf{nID} := \text{createNode}() \\
\{\exists \alpha, N. ((R \mapsto \alpha \otimes N[\emptyset] * \mathbf{nID} \rightarrow N) \Leftarrow * Q) * (R \mapsto \alpha \otimes N[\emptyset] * \mathbf{nID} \rightarrow N)\} \\
\hline
\{\exists \alpha, N. ((R \mapsto \alpha \otimes N[\emptyset] * \mathbf{nID} \rightarrow N) \Leftarrow * Q) * R \mapsto \alpha * \mathbf{nID} \rightarrow J\} \\
\mathbf{nID} := \text{createNode}() \\
\{Q\}
\end{array}$$

Notice that, were  $*$  used rather than  $\Leftarrow *$ , the application of the consequence rule could not allocate and deallocate the  $\alpha$  addressed sub-data. The only post-conditions  $Q$  that would work would be those with  $\alpha$  already extant. It is clear that `createNode` can always be safely used, so that pre-condition would *not* be the weakest in any useful sense.

Similar weakest pre-conditions can be constructed for the list library, and other tree commands.

### 3.3.7. Rule of conjunction

Recall the requirement of conjunction, primitive conjunctivity, given in definition 16, which were dependent on the choice of primitive reification function. The primitive reification function for abstract heaps (definition 47) does not satisfy these requirements. Therefore, when using semantic consequence or semantic strengthening to perform abstract allocation, the rule of conjunction is not sound.

**Lemma 12** (The conjunction rule is unsound). *The rule of conjunction is unsound when reasoning about abstract heaps.*

*Proof.* By counter-example. Consider the tree assertion  $\alpha \mapsto a[b[\emptyset]]$  and the program `skip`. This program run with that assertion as a pre-condition evidently terminates. Assume that the rule of conjunction is sound. Then, the following two derivations are both valid via the rules of semantic consequence and skip:

$$\begin{array}{ccc}
\left\{ \alpha \mapsto a[b[\emptyset]] \right\} & \left\{ \begin{array}{l} \alpha \mapsto a[b[\emptyset]] \\ \exists \beta. (\alpha \mapsto a[\beta] * \beta \mapsto b[\emptyset]) \end{array} \right\} \\
\text{skip} & \text{skip} \\
\left\{ \alpha \mapsto a[b[\emptyset]] \right\} & \left\{ \begin{array}{l} \exists \beta. (\alpha \mapsto a[\beta] * \beta \mapsto b[\emptyset]) \\ \exists \beta. (\alpha \mapsto a[\beta] * \beta \mapsto b[\emptyset]) \end{array} \right\}
\end{array}$$

By the rule of conjunction, the following triple must hold:

$$\left\{ \alpha \mapsto a[b[\emptyset]] \wedge \alpha \mapsto a[b[\emptyset]] \right\} \text{ skip } \left\{ \alpha \mapsto a[b[\emptyset]] \wedge \exists \beta. (\alpha \mapsto a[\beta] * \beta \mapsto b[\emptyset]) \right\}$$

However, whilst the pre-condition of this is defined (and equal to  $\alpha \mapsto a[b[\emptyset]]$ ), the post-condition is unsatisfiable. Even though both halves have the same reification, they are *different* instrumented heaps. Therefore, there are no heaps in common between the first conjunct (describing all heaps with one address pointing to  $a[b[\emptyset]]$ ) and the second (which splits the data across two cells). This means the program must not terminate. But, `skip` always terminates.  $\square$

The unsoundness of the conjunction rule here is not surprising. Abstract allocation is inherently *angelic*; the choice of where and when to split is given to the constructor of the proof. There are many different ways to cut up the same data, and so the uses of abstract allocation and deallocation may differ between two proofs of the same program, and so the post-conditions may describe abstract heaps with different structural addresses.

### 3.4. Comparison to previous structured data reasoning

In section 1.2, we gave an overview of previous work that enables local reasoning about structured data. Here, we more directly compare these methods with structural separation logic, indicating the relative strengths and weaknesses. We will focus on the `appendNode` tree library command given in section 3.1.2. This one command demonstrates many of the challenges of structured data: abstract data representation of trees, localisation of sub-data in terms of the nodes being considered, and consideration of relationships between those nodes (demonstrating that the parent is not an ancestor of the child).

We will compare the proof of the single command `appendNode(p, c)` using structural separation logic, inductive predicates, abstract predicates, and context logic. In all three cases, the proof consists of three steps:

1. **Data localisation:** The representation of the tree is manipulated so that the sub-tree  $p$  and  $c$  are *separate* from other data. The irrelevant tree structure must be “framed off”, ready for the application of the `appendNode` axiom.
2. **Axiom application:** The axiom is applied, and the representation updated.
3. **Data unification:** The “framed off” data is restored, and the representation of the tree is manipulated to reconnect the sub-data with the surrounding data.

As seen in definition , structural separation logic uses an entirely abstract data representation. The localisation phase consists of identifying the sub-data using composition operator,  $d_1 @ d_2$ , then applying abstract allocation and the frame rule. The axiom is a straight forward update of the data representation, using *completeness* in the data to ensure the non-ancestral relationship between the child and parent nodes hold. The derivation of the program, using a simple example tree, is: let  $V = (p \rightarrow B \wedge c \rightarrow D * E)$

$$\frac{\frac{\{V * \beta \mapsto b[\delta] * \delta \mapsto d[complete]\} \text{ appendNode}(p, c) \{V * \beta \mapsto b[\delta \otimes d] * \delta \mapsto \emptyset\}}{\{V * \exists \beta, \gamma, \delta. \alpha \mapsto a[\beta \otimes \delta] * \beta \mapsto b[\delta] * \gamma \mapsto c * \delta \mapsto d[complete]\}} \text{ appendNode}(p, c)}{\frac{\{V * \exists \beta, \gamma, \delta. \alpha \mapsto a[\beta \otimes \delta] * \beta \mapsto b[\delta \otimes d] * \gamma \mapsto c * \delta \mapsto \emptyset\}}{\{V * \alpha \mapsto a[b[c] \otimes d]\} \text{ appendNode}(p, c) \{V * \alpha \mapsto a[b[c \otimes d]]\}}}$$

### 3.4.1. Inductive predicates

We now compare the above to inductive predicates. The standard technique ([63], [69]) for representing a structured data using inductive predicates is to link an *algebraic* tree representation (such as the abstract tree of definition 39) to an underlying heap structure. By picking a heap representation, we can give a *tree* predicate that represents the algebraic structure via a set of heap cells with varying pointer relationships. The difficulty comes in *localising* the data into smaller sub-tree representations. This is accomplished by unfolding the predicate, revealing implementation details. These details, typically an underlying heap structure, are either manipulated directly or are repackaged into predicates representing the sub-data.

One typical example of a tree representation is the *five-pointer* style. Each tree node is represented with a heap cell of five fields: left, right, up, first child and last child. The address of the heap cell is punned as the identifier of the node. The left and right pointers reference the nodes directly left and right siblings (and are null if the node is the first or last of a sibling list). The up field references the nodes parent (and is null if the node is a

root), and the first and last child fields reference the first and last child in a linked list of children. The  $tree(x, T)$  predicate then states that algebraic tree  $T$  is represented in the heap starting at address  $x$  can be given as: let  $first$  and  $last$  extract the identifier of the first and last element of a node list

$$\begin{aligned}
tree(x, T) &\triangleq node(t, 0, 0, 0) \\
node(N[T], l, r, u) &\triangleq first(T) = FC \wedge last(T) = LC \wedge \\
&\quad N \mapsto L, R, U, FC, LC * nodelist(T, N, 0, 0) \\
nodelist(N[T], l, r, u) &\triangleq node(N[T], l, r, u) \\
nodelist(T \otimes U, l, r, u) &\triangleq \exists I. nodelist(T, l, I, u) * nodelist(U, I, r, u) \\
nodelist(\emptyset, l, r, u) &\triangleq emp \wedge l = 0 \wedge r = 0
\end{aligned}$$

Whilst standard, this representation is unwieldy. Notice that a *node* predicate represents the resource for a node and *all* its children. To represent a node in the tree *without* capturing the entire sub-tree, we must include an additional “solo” node predicate. Such a predicate will be required to represent the parent node in  $appendNode(p, c)$ , as the child node  $c$  may be a descendent of  $p$ , and so must not be captured in the representation of  $p$ .

$$solo\_node(n, l, r, u, fc, lc) \triangleq N \mapsto L, R, U, FC, LC$$

We these, we might assume we can axiomatise the  $appendNode(p, c)$  command as by including the solo node for the parent  $c$ , the entire tree for  $c$ , and solo node for the last child of  $p$  (which must be updated to have  $c$  hooked to the end of it, giving a pre-condition like:

$$V * solo\_node(p, L, R, U, FC, LC) * node(c, CL, CR, CU) * solo\_node(LC, LCL, LCR, LCU, LCFC, LCLC)$$

Alas, even this is not sufficient: we must include the parent of the  $c$  node, to update its child pointers, and we must consider the case where  $c$  is already a child of  $p$ . We do not consider these here as these problems, whilst tedious, are surmountable. The critical point is that even once an inductive predicate for the tree is devised, we have only a data representation. We can give an axiom to  $appendNode$ , but to use this axiom, we must still localise the data of interest inside a representation. Thus, we must repeatedly unfold, and refold the predicates, until we have represented  $p$  via the *solo\_tree* predicate,  $c$  using the *node* predicate. One typical step will be extracting a *solo\_node* predicate from a *node* predicate by unfolding the *node* one step, and folding the heap cell exposed into *node*:

$$node(N[T], l, r, u) \iff solo\_node(N, l, r, u, first(T), last(T)) * nodelist(T, N, 0, 0)$$

This rewriting is both tedious and error prone. Therefore, comparing structural separation logic to inductive predicates we see that inductive predicates:

1. Allow verification of implementation: As the representation can be broken open to reveal the underlying heap structure, inductive predicates allow the underlying implementation code to be verified with respect to the representation. Structural separation logic, operating entirely on abstract representations, does not immediately allow this. However, with Raad [59], we are currently working on implementation verification via refinement.
2. Suffer from difficult localisation: The folding and unfolding of the predicates is tedious and error prone when seen against the comparatively simple approach of abstract allocation.
3. Break abstraction: Data localisation requires unfolding the *tree* predicate in order to extract the sub-data. This ties client proofs to the specific implementation of the tree structure. Structural separation logic, working entirely on abstract data, does not suffer from this problem.
4. Use non-uniform presentation: The correct choice of unfolding for the predicates to localise the sub-data is innately tied to the choice of predicates. Thus, the techniques of data localisation differ from library to library. Structural separation logic offers abstract allocation as a uniform approach to localising data.

### 3.4.2. Abstract predicates

Abstract predicates restore abstraction to the presentation of inductive predicates by hiding the predicate bodies from client programs. However, this means predicates cannot be folded and unfolded directly. Data localisation must thus be provided by *axioms*, which then must be justified by any underlying implementation.

We can consider abstract predicates simply by erasing the right-hand sides from the inductive predicates in section 3.4.1. One localisation axiom is the unfolding of the *node* predicate into *solo\_node* and *nodelist*, but one must include many more, as we have to provide such an axiom for every splitting a proof may want. The *signatures* of these predicates, in terms of the parameters they take, must be sufficient to allow these axioms to unambiguously split and rejoin data. Thus, the parameters on, for example,

$node(N[T], l, r, u)$ , leak some of the pointer implementation details into the abstraction. Ever more clever choices of predicates can be devised to minimise this, but due to the underlying need to be justified by a heap structure, this problem always remains. Thus, in comparison to structural separation logic, abstract predicates:

1. Allows verification of several implementations: Abstract predicate do hide some implementation concerns from clients, allowing several implementations to be verified. We hope our upcoming refinement approach will allow a similar result for structural separation logic.
2. Non-uniform presentation: As with inductive predicates, the choice of representations is ad-hoc, and the localisation created on a structure by structure basis.
3. Implementation details leak into predicate signatures: The parameters on “abstract” predicates are needed to ensure that predicates representing sub-data can be reconnected with the predicates representing super-data. These parameters must be sufficient for any underlying implementation to reconnect the data. Thus, the number and choice of parameters is directly linked to expected implementations, whilst offering minimal value to the abstraction.

**Comment 7.** This final point is perhaps the key problem solved by structural separation logic. We allow predicates to be split and reconnected using the mechanism of structural addresses. It is reasonable to see structural separation logic as the first step in a new theory of abstract predicates where *separation* and *connectivity* is abstracted, rather than just *representation*.

### 3.4.3. Context logic

Context logic is much closer to the level of abstraction we can achieve with structural separation logic. It avoids all implementation considerations by operating directly on the abstract representation of the structure. The key difference from structural separation logic is the data localisation phase, where separation is now context application. For example, the derivation for our example program is:

$$\frac{\frac{\{V_c \wedge C \circ_x b[c] \circ_Y d\} \text{ n := appendNode}(p, c) \{V_c \wedge C \circ_x b[c \otimes d] \circ_Y \emptyset\}}{\{V_c \wedge C \circ_x b[c] \circ_Y da[b[c] \otimes d]\}} \quad \text{n := appendNode}(p, c) \quad \{V_c \wedge C \circ_x b[c \otimes d] \circ_Y \emptyset\}}{\{V_c \wedge a[b[c] \otimes d]\} \text{ n := appendNode}(p, c) \{V_c \wedge a[b[c \otimes d]]\}}$$

Separation can only occur in an order, depth last manner. In comparison to structural separation logic, context logic:

1. **Has non-commutative separation:** The separating application connective is not commutative, meaning data must be extracted “depth-last” (with the deepest sub-data coming at the end). This means one cannot reorder the representation of sub-data to fit specific axioms or reasoning rules. Most importantly, this means one cannot give a parallel composition rule. It also results in larger axioms, viz: the covering context needed to handle `appendNode` here (explained in detail in section 1.2.3).
2. **Cannot directly co-exist with heaps:** Separating application ensures that the data presentation is not simply a bag of heap cells. Thus, normal heap style reasoning cannot naturally co-exist with the context structured data. One problematic consequence of this is having more than instance of a data structure is difficult, such as a list and a tree together. Structural separation logic, by reusing the standard heap mechanism, allows heterogeneous data to be combined into a single reasoning system.

#### 3.4.4. Overview of advantages

Structural separation logic unifies the natural abstract reasoning of context logic with the natural “bag” model of separation. Taking the most useful parts of each pre-existing technique, it offers improvements in:

- Abstraction of implementation: The specific choices of an *implementation* are entirely hidden, with only the abstract structure exposed in proofs.
- Abstraction of separation: The connectivity information used by the underlying data structures, such as the pointers in sub-section 3.4.1, are hidden, with data separability abstracted via a uniform equational theory of *compression*.
- Natural folding and unfolding behaviour: The localising of sub-data is achieved via a simple *abstract allocation*.

- Separation-logic style composition: The composition operation of  $*$  both commutes and associates. This enables abstract heaps to co-exist with the standard heaps of other separation logic techniques.

### 3.5. Extensions and alternatives

We now give a short summary of alternative choices and extensions that can be made in the development of structural separation logic.

#### 3.5.1. The generalised frame rule

We now examine how the *generalised frame rule* of the views framework allows an alternate method of performing abstract allocation and deallocation. This allows us to relate our approach to the reasoning of segment logic [38, 70]. We also extend views with a *relational frame rule*, allowing us to compare our approach of semantic consequence with this alternate choice.

Recall the frame rule of theorem 1. The views framework [22] provides a *generalised* version of this rule that can apply functions to the pre- and post-condition. These *generalised frame functions* must satisfy certain properties.

**Definition 63** (Generalised frame functions). Given an addressed value view  $(\text{VIEWS}, \llbracket \cdot \rrbracket, \otimes, 0)$  (parameter 8), transition labels  $\text{ACTIONLBS}$  (definition 5) and atomic command actions  $\llbracket C \rrbracket$  (parameter 6) a function  $f : \text{VIEWS} \rightarrow \text{VIEWS}$  is a **generalised frame function** if it preserves all actions under all frames:

$$\forall p, q \in \text{VIEWS}, \alpha \in \text{ACTIONLBS}. \quad (\forall r \in \text{VIEWS}. \llbracket \alpha \rrbracket [p * r] \subseteq [q * r]) \implies (\forall r \in \text{VIEWS}. \llbracket \alpha \rrbracket [f(p) * r] \subseteq [f(q) * r])$$

Given a generalised frame function, we can generalise the frame rule.

**Proposition 2** (Generalised frame rule). Given an addressed value view  $(\text{VIEWS}, \llbracket \cdot \rrbracket, \otimes, 0)$  (parameter 8) and the semantic triple judgement (definition 14) if, given  $p, q \in \text{VIEWS}$ ,  $\models \{p\} \mathbb{C} \{q\}$  holds then for all generalised frame functions  $f$ , the semantic triple judgement  $\models \{f(p)\} \mathbb{C} \{f(q)\}$  holds.



By defining a family of frame functions  $f_r(p) = p * r$  for all  $r$ , it is evident the generalised frame rule subsumes the standard frame rule. It also admits function with very “non-frame” like behaviour. We can define a generalised frame function for abstract heaps that performs a single-step collapse on a structural address.

**Definition 64** (Hiding frame function). The **hiding frame function** for address  $\mathbf{x}$ ,  $\downarrow_{\mathbf{x}}: \text{VIEWS} \rightarrow \text{VIEWS}$  is defined as:

$$\downarrow_{\mathbf{x}}(p) = \begin{cases} \emptyset & \text{if } \exists \mathbf{h} \in p. \nexists \mathbf{h}'. \mathbf{h} \downarrow \mathbf{h}', \text{dom}(\mathbf{h}') = \text{dom}(\mathbf{h}) \setminus \{\mathbf{x}\} \\ \{\mathbf{h}' \mid \mathbf{h} \in p, \mathbf{h} \downarrow \mathbf{h}', \text{dom}(\mathbf{h}') = \text{dom}(\mathbf{h}) \setminus \{\mathbf{x}\}\} & \text{otherwise} \end{cases}$$

**Lemma 13** (The hiding frame function is a frame function). *The hiding frame function of definition 64 is a generalised frame function in the sense of definition 63.*

*Proof.* Note that the hiding frame function performs exactly one step of the collapse process, or (if there is no abstract address to use), produces the empty view. As reification is defined in terms of collapse, the result then follows by the definition of reification and theorem 3, which ensures that  $\lfloor f(p) * r \rfloor = \lfloor p * r \rfloor$  and  $\lfloor f(q) * r \rfloor = \lfloor q * r \rfloor$ , and the fact that the empty view reifies to the empty set (ensuring the implication holds vacuously if an invalid address is chosen for hiding).  $\square$

The hiding frame function obviates the need to use semantic consequence for abstract allocation and deallocation. Assuming the straightforward lift of  $\downarrow_{\mathbf{x}}$  to a syntactic assertion, it enables a proof structures like:

$\{\alpha \mapsto 2\}$	remove(2)	$\{\alpha \mapsto \emptyset\}$	<i>Axiom</i>
$\{\mathbf{L} \mapsto [3 \otimes \alpha \otimes 5] * \alpha \mapsto 2\}$	remove(2)	$\{\mathbf{L} \mapsto [3 \otimes \alpha \otimes 5] * \alpha \mapsto \emptyset\}$	<i>Gen. frame</i>
$\{\downarrow_{\alpha}(\mathbf{L} \mapsto [3 \otimes \alpha \otimes 5] * \alpha \mapsto 2)\}$	remove(2)	$\{\downarrow_{\alpha}(\mathbf{L} \mapsto [3 \otimes \alpha \otimes 5] * \alpha \mapsto \emptyset)\}$	<i>Consequence</i>
$\{\mathbf{L} \mapsto [3 \otimes 2 \otimes 5]\}$	remove(2)	$\{\mathbf{L} \mapsto [3 \otimes 5]\}$	

This proof recalls those using Wheelhouse’s segment logic [38]. In fact, when lifted to a syntactic assertion of  $f$ , it gives a frame rule exactly matching the *hiding rule* of [38] (see figure 3.18).

However, proofs in this style are more rigid and carry more baggage than those using semantic consequence. They can allocate and deallocate at most one address at a time and they cannot leave data in an allocated state, as the same function must be applied to both the pre- and post-condition. Moreover, the hiding function  $\downarrow_{\mathbf{x}}$  is left in the data, and

$$\frac{\{P\} \mathbb{C} \{Q\}}{\{\text{H}\alpha. P\} \mathbb{C} \{\text{H}\alpha. Q\}} \quad \frac{\{P\} \mathbb{C} \{Q\}}{\{\downarrow_{\alpha} P\} \mathbb{C} \{\downarrow_{\alpha} Q\}}$$

Figure 3.18.: On the left, the *hiding rule* of segment logic. On the right, the collapsing frame function of definition 64 used as a generalised frame function.

must be removed by consequence. Given that, one may as well use semantic consequence to perform the entire process.

### The relationship between semantic consequence and the generalised frame rule

Even though we choose semantic consequence to enable abstract allocation, proofs using the hiding frame function are sufficiently similar to warrant examining the links more closely. Most obviously, semantic consequence is a relation on pre- and post-conditions, whereas the generalised frame rule uses functions over them. This artificially restricts use of the rule. For example, it is difficult to design functions that mutate the pre-condition, but leave the post-condition untouched. To ease these restrictions, we can move to relational transformations.

We define *relational view transformers*,  $\approx$ , as the relational analogue of generalised frame functions. To enable a sound rule using these relations, we require some conditions. The first requirement, left totality, eliminates cases  $\{p'\} \mathbb{C} \{q'\}$  where  $p' \approx p$  but there is no  $q$  such that  $q' \approx q$ . Were such relations allowed, we could pick a relation showing the divergence of any code. The second is the natural extension of the generalised frame rule requirement.

**Definition 65** (Relational view transformers). Given a set of views  $\text{VIEWS}$ , a relation  $\approx \subseteq \text{VIEWS} \times \text{VIEWS}$  is a **relational view transformer** if it is both left total and preserves all actions under frames. That is, for all  $p \in \text{VIEWS}$ , there exists some  $q \in \text{VIEWS}$  such that  $p \approx q$ ; and

$$\forall \begin{array}{l} p, q \in \text{VIEWS}, \\ \alpha \in \text{ACTIONLBS}. \end{array} \quad \begin{array}{l} (\forall r \in \text{VIEWS}. \llbracket \alpha \rrbracket [p * r] \subseteq \llbracket \alpha \rrbracket [q * r]) \\ \implies \\ (\forall r, p', q' \in \text{VIEWS}. p \approx p' \wedge q \approx q' \implies \llbracket \alpha \rrbracket [p' * r] \subseteq \llbracket \alpha \rrbracket [q' * r]) \end{array}$$

With relational view transformers, we can define a relation frame rule.

**Proposition 3** (Relational frame inference). Assume  $\approx$  is a relational view transformer, and  $p, p', q, q' \in \text{VIEWS}$ . If  $p' \approx p$ ,  $q' \approx q$  and  $\{p'\} \subset \{q'\}$ , then  $\{p\} \subset \{q\}$ .

This rule is sound using reasoning similar to the proof of the normal frame rule (theorem 1), and using the properties of relational frame transformers. Note that if  $\approx$  is functional, then this rule collapses to the generalised frame rule. This definition is similar to one proposed by Parkinson during development of the Views framework.

With semantic consequence, if we know  $p \preceq p', q' \preceq q$ , then we can justify the triple  $\{p\} \subset \{q\}$  using the triple  $\{p'\} \subset \{q'\}$ . With relational view transformers, if we know  $p' \approx p$  and  $q' \approx q$ , we can justify the same relationship between triples. The only difference between the relational frame rule and the semantic consequence rule is the order of the pre-condition relationship. Consider then relations  $\mathcal{R}$  which are symmetric and reflexive.

**Lemma 14** (View strengthening and weakening relations). *Let  $\sim \subset \text{VIEWS} \times \text{VIEWS}$  be a symmetric, reflexive relation. Then,  $\sim$  satisfies the requirements of a semantic consequence relation if and only if it satisfies the requirements of a relational frame relation.*

*Proof. Right direction:* We first show that if  $\sim$  is a semantic consequence relation, it is a relational view transformer. We thus have the following properties by assumption: ①  $\sim$  is symmetric; ②  $\sim$  is reflexive; and ③  $\forall p, q \in \text{VIEWS}. p \sim q \implies \forall r \in \text{VIEWS}. [p * r] \subseteq [q * r]$ .

We must show the conditions of definition 65: That  $\sim$  is left total, and that it preserves actions:

$$\begin{array}{l} \forall p, q \in \text{VIEWS}, \\ \alpha \in \text{ACTIONLBSL}. \end{array} \quad \begin{array}{l} (\forall r \in \text{VIEWS}. [\alpha][p * r] \subseteq [q * r]) \\ \implies \\ (\forall r, p', q' \in \text{VIEWS}. p \sim p' \wedge q \sim q' \implies [\alpha][p' * r] \subseteq [q' * r]) \end{array}$$

Left totality is straightforward, as all reflexive relations are left total. For the action preservation property, note that by symmetry, if  $p \sim q$  then  $q \sim p$ . Hence by ③, if  $p \sim q$ , then  $\forall r \in \text{VIEWS}. [p * r] = [q * r]$ . Assume the premise of our goal implication, that ④  $\forall r \in \text{VIEWS}. [\alpha][p * r] \subseteq [q * r]$ .

We must show  $(\forall r, p', q' \in \text{VIEWS}. p \sim p' \wedge q \sim q' \implies [\alpha][p' * r] \subseteq [q' * r])$ . As  $p \sim p'$  and  $q \sim q'$ ,  $[p * r] = [p' * r]$ , and  $[q * r] = [q' * r]$  for all  $r$ . Therefore, that  $[\alpha][p' * r] \subseteq [q' * r]$  is given by ④.

**Left direction:** We now show that if  $\sim$  is a relational view transformer, it is a semantic consequence relation. We thus have the following properties by assumption: ①  $\sim$  is symmetric; ②  $\sim$  is reflexive; ③  $\sim$  is left-total and; ④  $\forall s, t \in \text{VIEWS}, \alpha \in \text{ACTIONLBSL}$ .

$$\begin{aligned}
& (\forall r \in \text{VIEWS}. \llbracket \alpha \rrbracket [s * r] \subseteq [t * r]) \implies \\
& (\forall r, s', t' \in \text{VIEWS}. s \sim s' \wedge t \sim t' \implies \llbracket \alpha \rrbracket [s' * r] \subseteq [t' * r])
\end{aligned}$$

We must show it satisfies the conditions of definition 17. In ④, pick  $\alpha = \text{ID}$ ,  $s = p$  and  $t = p$ . Expanding, we derive:

$$\begin{aligned}
& (\forall r \in \text{VIEWS}. [p * r] \subseteq [p * r]) \implies \\
& (\forall r, s', t' \in \text{VIEWS}. p \sim s' \wedge p \sim t' \implies [s' * r] \subseteq [t' * r])
\end{aligned}$$

The first term of the first implication is a tautology, and so the entire assertion is equivalent to:

$$(\forall r, s', t' \in \text{VIEWS}. p \sim s' \wedge p \sim t' \implies [s' * r] \subseteq [t' * r])$$

Now pick  $s' = p$  and  $t' = q$ . Expanding, we derive:

$$(\forall r \in \text{VIEWS}. p \sim p \wedge p \sim q \implies [p * r] \subseteq [q * r])$$

The first conjunction is discharged by ② (reflexivity). The second contains no mention of  $r$ . Ergo:

$$p \sim q \implies \forall r \in \text{VIEWS}. [p * r] \subseteq [q * r]$$

which was to be shown. □

That the rules are interchangeable with reflexive and symmetric relations is perhaps not surprising. Take the program  $\mathbb{C}$  and views  $p^\top, q^\top, p, q$ . Imagine a valid derivation, a fragment of which has the form below:

$$\begin{array}{c}
\vdots \\
\hline
\{p^\top\} \mathbb{C} \{q^\top\} \\
\hline
\{p\} \mathbb{C} \{q\}
\end{array}$$

We can see the centre triple as partaking in forms of *strengthening* or *weakening* on  $p$ , depending on if we read it bottom to top, or top to bottom (the arguments equally apply to  $q$ ). Reading bottom to top, we see  $p$  as getting weaker;  $p^\top$  must encompass  $p$ . Reading top to bottom, we see  $p$  as getting stronger;  $p^\top$  is more expansive than  $p$ .

Without the frame rule, generalised frame rule, or relational frame rule, the strengthening reading is trivial;  $p^\top = p$  is the only valid choice. However, with the various frame

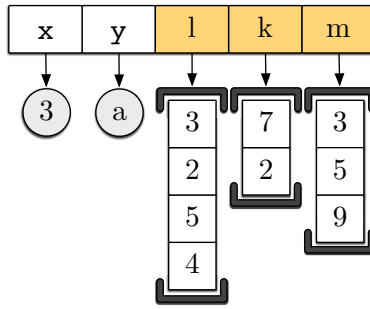


Figure 3.19.: Structured heap containing multiple lists.

rules, our choices for strengthen the pre-condition are much wider. The standard frame rule strengthens  $p$  with more memory cells, justified via the inability of the machine to detect memory locations not accessed. These rules are not well explored; the views framework [22] includes a penalised frame function that expresses the weakening rule used by Rely-Guarantee [47].

For structural separation logic, abstract allocation is used only to change the presentation of the data. We make certain sub-data more accessible, but never change the meaning of heap from the machines perspective. As we are neither strengthening or weakening our view, either rule is equally applicable for abstract allocation. We choose to use semantic consequence, as it can be used both for abstract allocation *and* the folding and unfolding of inductive predicates in separation logic. As we will be using both compression/decompression and inductive predicates in our examples, the uniformity of style is useful.

### 3.5.2. Disjoint structural addresses

We now consider reasoning for libraries that need knowledge of the *machine heap address* that is storing their data. In our structured heap abstraction, such situations arise often. When multiple copies of a data structure are allowed, the heap address can be used to identify the specific *instance* being analysed. Recall the list example of section 3.1.1. This works with exactly one list, so that the programmer to create multiple lists in the heap. When faced with the assertion  $\alpha \mapsto 5$ , there is no ambiguity about which list contained 5. The command `remove(5)` would remove the only element 5 there could possibly be.

This is unrealistic. It is quite reasonable to have many lists in the heap, each of which may have an element 5. These lists are created, destroyed and updated as needed with program commands. Typically, lists are associated with a unique address, and commands

are parameterised with the address of the list being accessed.

We can build an imperative machine for *multiple lists* using the list data structures we have already defined (definition 26). The machine heaps to support this are similar to those of the single list case, but contain zero or more lists.

**Definition 66** (Multiple-list machine heaps). Assume a set of list heap addresses LISTADDRS, ranged over by  $l, k, m, \dots$ , where  $\text{LISTADDRS} \cap \text{PVARs} = \emptyset$ . The set of **multiple-list machine heaps** LISTHEAPS, ranged over by  $lh, lh_1, \dots, lh_n$ , is:

$$\text{LISTHEAPS} \triangleq \{lh \mid lh : (\text{LISTADDRS} \xrightarrow{\text{fin}} \text{LISTS}) \sqcup (\text{PVARs} \xrightarrow{\text{fin}} \text{PVALS})\}$$

We visualise such a heap, with two variables  $x$  and  $y$  and three lists  $a \mapsto [3 \otimes 2 \otimes 5 \otimes 4]$ ,  $d \mapsto [7 \otimes 2]$ , and  $b \mapsto [3 \otimes 5 \otimes 9]$  in figure 3.19. Note that whilst individual lists must have distinct elements, different lists may have the same elements. To manipulate these heaps, we alter the list library given in chapter 3 to support multiple lists:

1.  $l := \text{createList}()$ : Picks a machine fresh heap address, allocating it within the heap, associating it with an empty list, and assigning it to variable  $l$ . Cannot fault.
2.  $\text{destroyList}(l)$ : Removes the entire list identified by the value of  $l$  from the heap. Faults if the value of  $l$  does not identify a list.
3.  $l.\text{append}(e)$ : Appends the result of evaluating  $e$  to the list identified by the value of  $l$ , making it the last element. Faults if  $l$  does not identify a list, if the evaluation of  $e$  is either already in that list, or if the evaluation of  $e$  is not a natural number.
4.  $l.\text{remove}(e)$ : Removes the result of evaluating  $e$  from the list identified by the value of  $l$ . Faults if  $l$  does not identify a list, or if the evaluation of  $e$  is not present in that list.

For brevity, we omit the similar commands for  $l.\text{getFirst}(e)$  and  $l.\text{getRight}(e)$ ; they follow this pattern.

The construction of abstract heaps over these machine heaps is not, by itself, useful. Consider the assertion  $\alpha \mapsto 3$ . It gives no information about the list to which element 3 belongs. Instead, we consider associating each abstract address with the address of the list into which it will ultimately compress:

$$\alpha^L \mapsto v$$

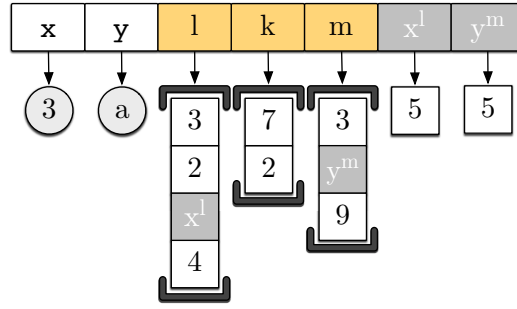


Figure 3.20.: Abstract heap using tagged addresses to manage multiple lists.

This abstract cell assertion states that address  $\alpha$  was allocated from within the list  $L$ . With it, we can axiomatise the commands for working with multiple lists.

### Formalisation

We call this approach **tagged structural addresses**, and formalise it for data in general, later focusing it on the list specific case. Given a set of structural addresses, we create new sets, each containing structural addresses *tagged* with a machine address. Tagged structural addresses are constructed atop the standard structural addresses:

**Definition 67** (Tagged structural addresses). Let  $a \in \text{MACHINEADDRS}$  be a machine address. The set of **structural addresses tagged with  $a$** , ranged over by  $\mathbf{x}^a, \mathbf{y}^a, \mathbf{z}^a$ , is defined as:

$$\text{STRUCTADDRS}^a = \{\mathbf{x}^a \mid \mathbf{x} \in \text{STRUCTADDRS}\}$$

From a structural addressing algebra  $(\text{STRUCTADDRS}, \text{DATA}, \text{addr}, \text{comp})$ , we create new algebras for each machine address, using the tagged structural addresses  $(\text{STRUCTADDRS}^a, \text{DATA}^a, \text{addr}^a, \text{comp}^a)$ . This construction is straight-forward, simply substituting the corresponding tagged address for each use of a structural address. When building the abstract heap, the data stored at a machine address will always use the set of data tagged with appropriate machine address, as seen in figure 3.20. Such abstract heaps are:

**Definition 68** (Abstract heaps over tagged data). The set of **abstract heaps using tagged data and addresses**  $\text{ABSHEAPS}$  is defined as:

$$\text{ABSHEAPS} \subseteq \left\{ \mathbf{h} \mid \mathbf{h} : \left( \begin{array}{c} (\text{PVARs} \xrightarrow{\text{fin}} \text{PVALS}) \\ \sqcup \\ \{a\} \rightarrow \text{DATA}^a \\ \sqcup \\ \text{STRUCTADDRS}^a \xrightarrow{\text{fin}} \text{DATA}^a \end{array} \right) \right\}$$

By construction, these abstract heaps require that body addresses within data must be tagged with the machine address into which the data will collapse. As we are simply changing the set of abstract addresses, we require no new machinery for the collapse process or reification. The normal compression function will ensure that the collapse relation can only place tagged abstract addresses into the appropriate places. The completion process will then ensure that the needed additional cells are provided.

The assertion language is essential identical to the single machine address case. We update the logical environments to ensure that any *tagged logical variable* refers to an underlying structural address that shares the tag. This prevents a logical variable  $\alpha^a$  from actually storing a structural address  $\mathbf{x}^b$ , where  $a \neq b$ .

**Definition 69** (Logical environment). Given a logical environment  $\Gamma \in \text{LENVS}$  (definition 20), the *tagged logical environment*  $\text{LENVS}_T : \text{LVARs} \rightarrow \text{LVALS}$  is defined as: for all  $\alpha, a, \mathbf{x}, b$

$$\Gamma_T(\mathbf{x}) = \begin{cases} \mathbf{x}^a & \text{if } \mathbf{x} = \alpha^a, \Gamma(\alpha^a) = \mathbf{x}^a \\ \text{undefined} & \text{if } \mathbf{x} = \alpha^a, \Gamma(\alpha^a) = \mathbf{x}^b, a \neq b \\ \Gamma(\mathbf{x}) & \text{otherwise} \end{cases}$$

We assume the use the tagged logical environment rather than a normal logical environment through all definitions. Abstract allocation then requires a small change to use these tagged logical variables, but no new underlying theory. This is because, than being a definition, abstract allocation is a result of the semantic consequence relation 17. This was by careful design of the abstract heaps and reification function, which ensured that as long as the *collapse* of two heaps resulted in identical machine heaps, they were semantic



consequences of each other. We have the same property here. By design, these tagged structural addresses can be used to compress and de-compress data as before, as long as the correct addresses are chosen for the data.

We can therefore easily update the relation to use the set of structural addresses appropriate to the data being allocated. This can always be determined by looking at the address of the heap cell the sub-data is being allocated from.

**Proposition 4** (Abstract allocation on tagged addresses). The following semantic equivalence is valid: for all  $a \in \text{MACHINEADDRS}$

$$\begin{aligned} \left( a \mapsto \phi_1 \textcircled{\alpha^a} \phi_2 \right) &\approx_{\Gamma} \exists \alpha^a. (a \mapsto (\phi_1 \wedge \diamond \alpha^a) * \alpha^a \mapsto \phi_2) \\ \left( \alpha^a \mapsto \phi_1 \textcircled{\beta^a} \phi_2 \right) &\approx_{\Gamma} \exists \beta^a. (\alpha^a \mapsto (\phi_1 \wedge \diamond \beta^a) * \beta^a \mapsto \phi_2) \end{aligned}$$

The proof that this proposition holds is similar to that of theorem 4.

### Tagged structural addressing for multiple lists

By creating tagged data and tagged addressing algebras using LISTADDRS, we obtain abstract heaps for multiple lists. With these heaps, we can construct the axioms for the multiple list commands, which are given in figure 3.21. The creation and destruction commands are very similar to those for normal heap cells. The `append` command works on an entire list cell, the address of which is passed as a parameter. The `remove` is the most changed, using an abstract heap cell with a tagged address to indicate that  $\alpha$  is allocated from data stored in the L machine cell. Abstract allocation then behaves in the intuitive manner. In the list instance, the following two semantic consequences hold:

$$\begin{aligned} L \mapsto [1 \otimes 2 \otimes 3] &\approx_{\Gamma} \exists \alpha^L. (L \mapsto [1 \otimes \alpha^L] * \alpha^L \mapsto 2 \otimes 3) \\ \alpha^L \mapsto 2 \otimes 3 &\approx_{\Gamma} \exists \beta^L. (\alpha^L \mapsto 2 \otimes \beta^L * \beta^L \mapsto 3) \end{aligned}$$

Notice that, out of an abstract cell addressed by  $\alpha^L$ , one can only allocated new abstract cells of with addresses of the form  $\beta^L$ . This is because, by construction, such a cell is storing data where the only body addresses are tagged with L.

## 3.6. Summary

This chapter has introduced **structural separation logic**, a program reasoning system for libraries manipulating structured data. Structural separation logic consists of two key contributions:

$$\begin{array}{c}
\{1 \rightarrow -\} \\
1 := \text{createList}() \\
\{\exists L. L \mapsto [\emptyset] * 1 \rightarrow L\} \\
\\
\{L \mapsto [E \wedge \text{is\_complete}] * 1 \rightarrow L\} \\
\text{destroyList}(1) \\
\{1 \rightarrow L\} \\
\\
\{L \mapsto [E \wedge \text{is\_complete}] * 1 \rightarrow L * P * e \Rightarrow R \wedge R \not\subseteq E\} \\
1.\text{append}(e) \\
\{L \mapsto [E \otimes R] * 1 \rightarrow L * P\} \\
\\
\{\alpha^L \mapsto I * 1 \rightarrow L * P \wedge e \Rightarrow I\} \\
1.\text{remove}(e) \\
\{\alpha^L \mapsto \emptyset * 1 \rightarrow L * P\}
\end{array}$$

Figure 3.21.: Example small axioms for the multiple list library

- A model with abstract separation: Section 3.4 defines an algebra for structured data (definition 42) that, when combined with abstract heaps (definition 48), gives a model for splitting entirely abstract data into bags of sub-data that can be considered independently, then reconnected.
- A logic with abstract allocation: Section 3.3 shows how this model can be used as the basis of a separation logic, with splittings managed via *abstract allocation*.

As discussed in section 3.4, these enable several advances of previous techniques for local abstract data reasoning.

1. **Abstraction of separability:** Structural separation logic offers an *abstraction of separability* (or perhaps *connectivity*) for structured data. Most clearly demonstrated in section 3.4, this allows a greater level of abstraction for data than previous separation logic techniques utilising abstract predicates.
2. **General approach for abstract data:** Section 3.4 provides a uniform approach for separation logic reasoning with abstract structured data. As demonstrated in section 3.4, our use of separating conjunction offers advantages over the previous work in context logic, enabling both smaller axioms and concurrent reasoning.
3. **Coexistence with existing separation logic techniques:** Unlike previous work in this area, the abstract heap concept of structural separation logic can reason about programs that manipulate both high-level abstract data and low-level heap

structures. This will prove valuable in the upcoming case studies of chapter 4 and 6.

## 4. The Document Object Model

This chapter uses structural separation logic to present an axiomatic specification for the W3C *Document Object Model* library standard [71]. The Document Object Model (hereafter DOM) library is typically used to analyse and update HTML or XML documents. The standard provides an abstract representation of the trees, and describes a wide range of commands for manipulating these trees. The most common use of DOM is manipulating the content of web pages. Every modern web browser comes with an implementation of DOM that should adhere to the standard, and this implementation is exposed to script programs within a page. This allows code delivered with a web page to alter the structure of the tree representing the browsers internal view of the HTML, and hence the presentation of the web page.

As a library, DOM is exclusively focused on the manipulation of structured data. In figure 4.1, we see the DOM tree generated by parsing a simple HTML document. It consists of a set of *nodes* of varying types. The DOM standard describes the types of nodes and their relationships, and gives a comprehensive set of commands for manipulating them. These commands are similar to those of our tree update library (section 3.1), but are much more extensive.

DOM is very widely adopted. For one, every web browser contains an implementation. That implementations follow the standard is critical, the same script will be delivered to many different web browsers, and script authors expect the behavior to be the same in each case. However, DOM is also used outside of browsers, for creating and processing XML files. XML has become a commonly used standard for data interchange, and most platforms provide a DOM library. Giving an axiomatic specification to the DOM library gives an unambiguous semantics to a widely used specification, a valuable outcome.

DOM is an attractive example for formalisation with structural separation logic. It takes the essence of our small tree examples, and scales it to a real-world problem, allowing us to show our reasoning scales as well. Moreover, it enables reasoning about usages of the DOM library inside other local reasoning logics. Smith, Gardner, and Maffeis are currently extending their separation logic reasoning for JavaScript [34] towards a more complete version of the language, capable of reasoning about real web programs. To

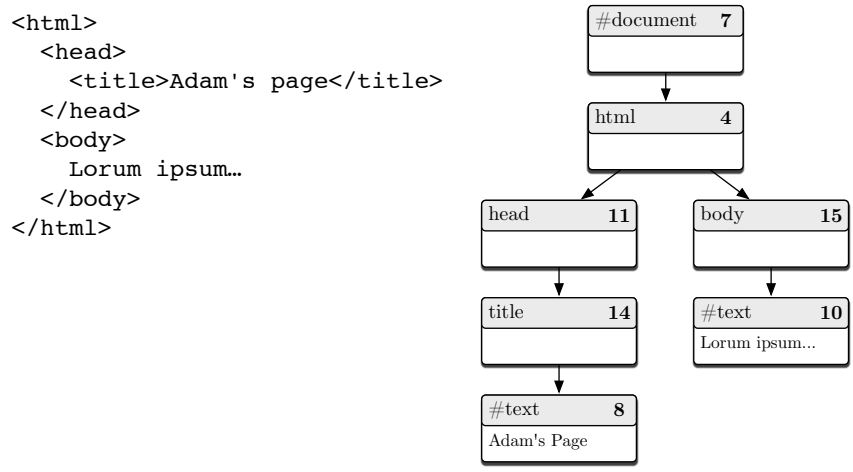


Figure 4.1.: Parsing HTML or XML with a DOM parser results in a *DOM tree*. Each node is uniquely identified, and there are three node types: a *#document* node, which is the root of the tree, *element* nodes which have a name and children, and *#text* nodes, which have associated text.

ensure soundness of these programs, they will require separation logic reasoning for the DOM, something we can now provide.

DOM has previously been studied in the context of local reasoning. As the standard is very large, Gardner, Wheelhouse, Smith and Zarfaty identified a subset they called *featherweight DOM*. We will base our analysis on the same subset, and demonstrate that structural separation logic can provide significantly smaller footprints than were possible with context logic. We will also show the advantages we gain via close integration with heap reasoning, by creating an example that uses both the DOM library and normal heap manipulation.

This chapter follows the pattern set out for the simple tree library given in section 3.1. In section 4.1, we introduce a data structure and structured heap for representing DOM, using the structures introduced by featherweight DOM [35] as the starting point. In section 4.1.1, we introduce the DOM commands, adopted from featherweight DOM, and give operational actions to four of them. Together, these objects provide an imperative machine for programming with the DOM library. We give examples programs for this machine by implementing additional commands not present in the featherweight subset.

In section 4.2.1, we define a structural addressing algebra (definition 42) for DOM, and so give the abstract heaps (definition 48). These enables us to use structural separation logic to reasoning about DOM programs. We axiomatise the featherweight DOM command

set, and prove the examples given in the previous section. In creating our axiomatisation, we uncovered that featherweight DOM slightly deviates from the English specification. We identify why and demonstrate that we have corrected the oversight.

This chapter concludes in section 4.4 with an example of *hybrid* reasoning. We give an program that uses normal heap operations alongside the DOM library, in the form of a *photo library processor*. We consider a photographic library stored in an XML file. The file contains a list of photos, each associated with a name and some image data. As XML is a plain text format, and the image data is an arbitrary list of bytes, an *encoding* is used to represent the image in the XML. Our example program uses the DOM to iterate over the images in the file. It must decode the image data into a standard format, but as the images are all of different sizes, it is standard to use heap memory to store the decoded data. Proving the example memory safe requires a combination of proof techniques, using the DOM reasoning to verify that the XML data is used in a proper manner, and standard heap reasoning to ensure the memory containing the image is used correctly.

## 4.1. DOM library

There are currently three major versions of the DOM standard. Like [35], we focus our reasoning on a fragment of *DOM Core Level 1*, which defines the general shape of the trees and majority of commands for manipulating them. Later versions are mostly concerned with *event handling* and minor updates to the tree shape. We will not work with the entirety of the standard, instead using a small variant on the featherweight interface identified in [35] that extends the subset with *document nodes*. By using this fragment, we focus on the core difficulties of the reasoning without handling the verbosity of the entire document. This fragment was extended to the entire standard by Smith [65]. We expect a similar extension of our work to be equally possible.

DOM Core Level 1 describes the library in object-oriented manner, defining both a data structure and operations for manipulating it via a set of *interfaces*. Acknowledging that not all languages support object-orientation, the library also provides “*a simplified view that allows all manipulation to be done via the Node interface*”. We will base our tree data structure upon the relationships between the objects in the object-oriented interfaces, and base our commands upon the single `Node` interface.

The standard defines a *DOM document* as a collection of *nodes* related by structural invariants. Each of the node types has some common structure:

**Name:** Every node has a name, not necessarily distinct across a document.

**Node identifier:** Every node has an identifier, which is distinct across a document.

**Associated data:** Each node has some associated data, the type of which depends on the node type.

**Node lists:** Every node is associated with a set of *forest identifiers*. When asked, the node must provide an element of this set, which will be used to obtain any children the node may have. The standard mandates that queries for children are always against the *live* state of a node, so it is impossible to simply return a static snapshot of children. At each query for a forest identifier, it is unknown whether an element already in the set will be returned, or whether the set will be extended with fresh identifiers, and one of those returned. This non-determinism is due to under-specification in the standard.

DOM Core level 1 provides twelve node types, of which we consider the three most commonly used. These three are sufficient to demonstrate the reasoning, and write effective examples. The additional node types are similar, and can be added to the presentation with minimal difficulty. The three node types we model are:

**Element nodes:** Element nodes are the most common DOM node, representing a named node with children. They have arbitrary names, with the caveat that the name cannot contain the character #, which is reserved as a prefix of for the names of other node types. The data of these nodes is an ordered list of *child nodes*, which we call the *child forest*. Our basic notation for elements is:

$$\text{Name}_{\text{Identifier}}[\text{Child Forest}]_{\text{Set of forest identifiers}}$$

**Text nodes:** Text nodes represent blocks of text, such as **This is a text node's contents**. These are always named **#text**. The data is a possibly empty string. Although text nodes have no children, they still have a set of forest identifiers. Whenever these identifiers are used to query for a child, the query will report that there are no children. Our basic notation for text nodes is:

$$\text{\#text}_{\text{Identifier}}[\text{String}]_{\text{Set of forest identifiers}}$$

**Document nodes:** Each document has exactly one document node, which is always the root of the DOM tree. They are named **#document**, and contain at exactly one associated child node, called the *document element*. The set of forest identifiers may seem redundant (there is only ever one element), but is required by the DOM standard.

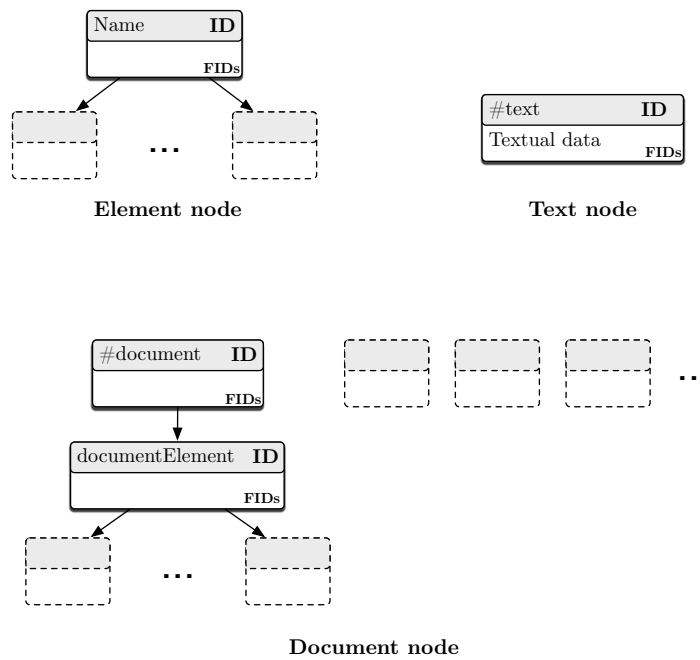


Figure 4.2.: The three node types we model in our DOM structure. The nodes with dashed outlines represent nodes that are optional.

Alongside the document node, we store the set of orphaned elements and text nodes, which we call the *grove*<sup>1</sup>. The grove is where newly created nodes will be placed. Also, DOM has no commands to dispose a node. Whenever a node is no longer needed, it is returned to the grove and “forgotten about”. It is thus natural to consider DOM as garbage collected. Our basic notation for document nodes is:

$$\#document_{\text{Identifier}}[\text{Document element}]_{\text{Set of forest identifiers}} \& \text{ Grove node set}$$

The node types are rendered in figure 4.2. The relationships between the node types defines the structure of a *DOM document*. Each document always has exactly one document node, acting as the root. This document node has exactly one *element* node child. This *document element* node is a normal element, and may have zero or more children. Text nodes can only appear at the leaves of the tree. Orphaned nodes are placed within the grove, but are not considered roots of the DOM tree. An example of this structure is given in figure 4.3.

<sup>1</sup>As we are working with a minor extension to featherweight DOM that includes document nodes, our choice of grove definition is slightly different from previous work [35].



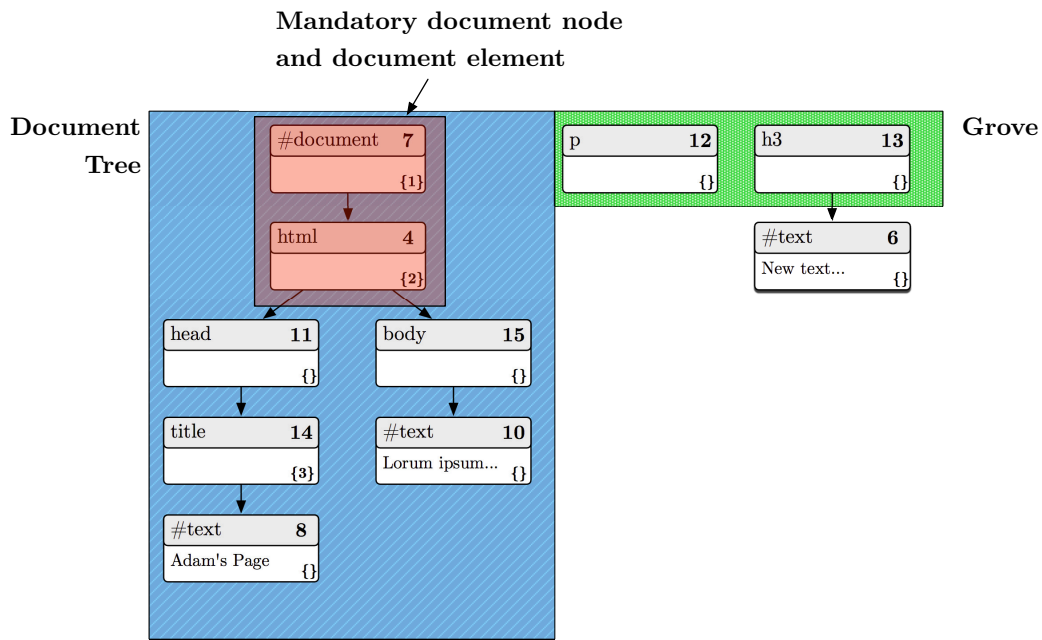


Figure 4.3.: Example structure of a DOM document. The shading indicates the key structured of a DOM tree. Notice that some nodes have empty forest identifier sets. This is a valid choice, assuming those nodes have not yet been queried for a forest identifier.

#### 4.1.1. An imperative machine for DOM

We now build an imperative machine for DOM, formalising the document structure and command subset. We use the following objects to represent node identifiers, forest identifiers, the null (that is, invalid) node identifier, and a set of characters suitable for the strings of DOM.

**Parameter 18** (Primitive DOM sets). Assume the following objects:

- A countably infinite set of **node identifiers** `NODEIDS`, ranged over by  $n, n_1, \dots, n_n, m, o$ .
- A countably infinite set of **forest identifiers** `FORESTIDS`, ranged over by  $fid, fid_1, \dots, fid_n$ , such that  $FORESTIDS \cap NODEIDS = \emptyset$ . Subsets of `FORESTIDS` are ranged over  $fs, fs_1, \dots, fs_n$ .

- A set of **DOM characters** DOMCHARS, ranged over by  $c, c_1, \dots, c_n$ .
- A distinguished value **null** such that  $\mathbf{null} \notin \text{NODEIDS} \cup \text{FORESTIDS}$ .

The data structures representing DOM trees are defined as follows.

**Definition 70** (DOM data structure). Given the primitive DOM sets of parameter 18, the set of **DOM strings** DOMSTRINGS, ranged over by  $s, s_1, \dots, s_n$ , the set of **DOM trees** DOMTREES, ranged over by  $t, t_1, \dots, t_n$ , the set of **DOM forests** DOMFORESTS, ranged over by  $f, f_1, \dots, f_n$ , the set of **DOM groves** DOMGROVES, ranged over by  $g, g_1, \dots, g_n$ , and the set of **DOM documents** DOMDOCS, ranged over by  $doc, doc_1, \dots, doc_n$ , are defined by induction as follows: for all  $c \in \text{DOMCHARS}$ ,  $n \in \text{NODEIDS}$  and  $fs \in \mathcal{P}(\text{FORESTIDS})$

$s ::=$	$c \mid s_1 \cdot s_2 \mid \emptyset_s$	DOM strings
$t ::=$	$s_n[f]_{fs} \mid \#\text{text}_n[s]_{fs}$	DOM trees
$f ::=$	$t \mid f_1 \otimes f_2 \mid \emptyset_f$	DOM forests
$g ::=$	$t \mid g_1 \oplus g_2 \mid \emptyset_g$	DOM groves
$doc ::=$	$\#\text{document}_n[t]_{fs} \ \& \ g$	DOM documents, $t$ an element node

where  $\cdot$  is associative with left and right identity  $\emptyset_s$ ,  $\otimes$  is associative with left and right identity  $\emptyset_f$ , and  $\oplus$  is associative and commutative with identity  $\emptyset_g$ . All data are equal up to the properties of  $\cdot$ ,  $\otimes$  and  $\oplus$ , and contain no duplicate node or forest identifiers.

It is often useful to refer to “DOM data” in general, without knowing whether it is a string, element, text node, grove or document.

**Definition 71** (DOM data). The set of **DOM data** DOMDATA, ranged over by  $d, d_1, \dots, d_n$ , is the union of the DOM data types:

$$\text{DOMDATA} \triangleq \begin{array}{l} \text{DOMSTRINGS} \cup \text{DOMTREES} \cup \text{DOMFORESTS} \cup \text{DOMGROVES} \\ \cup \text{DOMDOCS} \end{array}$$

By definition 70, DOM strings are lists of DOM characters. DOM trees are either an *element* node (with a name, identifier, child forest and forest identifier) or a *text node*

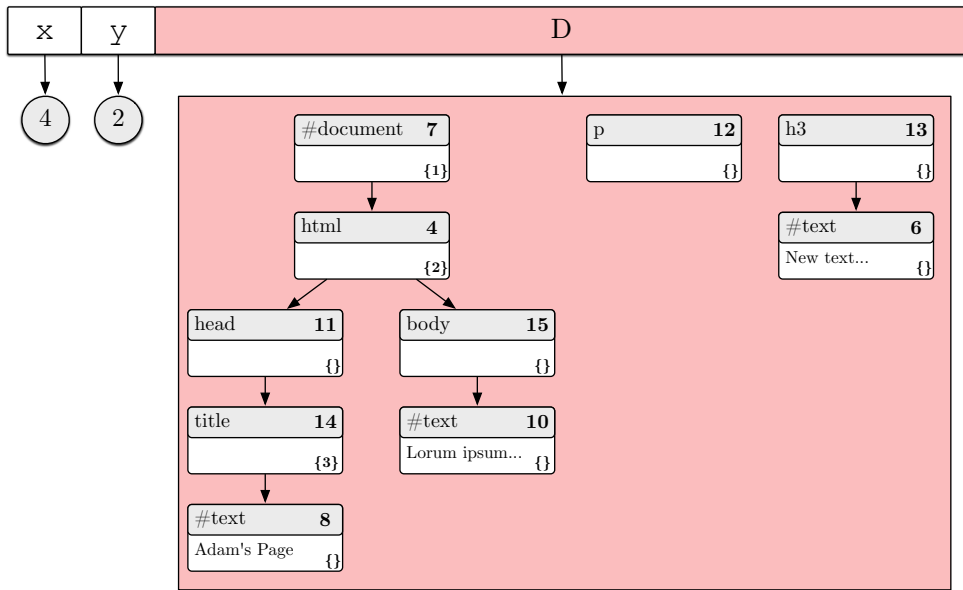


Figure 4.4.: DOM heap structure

(with an identifier and child string). DOM forests are possibly empty lists of DOM trees. DOM groves are possibly empty *sets* of DOM trees. DOM documents are a *document node* with identifier and forest identifier, but have exactly one child element node (which we call the *document element*), and are associated with a grove. This definition is a minor variant on that of featherweight DOM [35].

We create structured heaps for DOM exactly as we did for simple lists and trees in section 3.1.

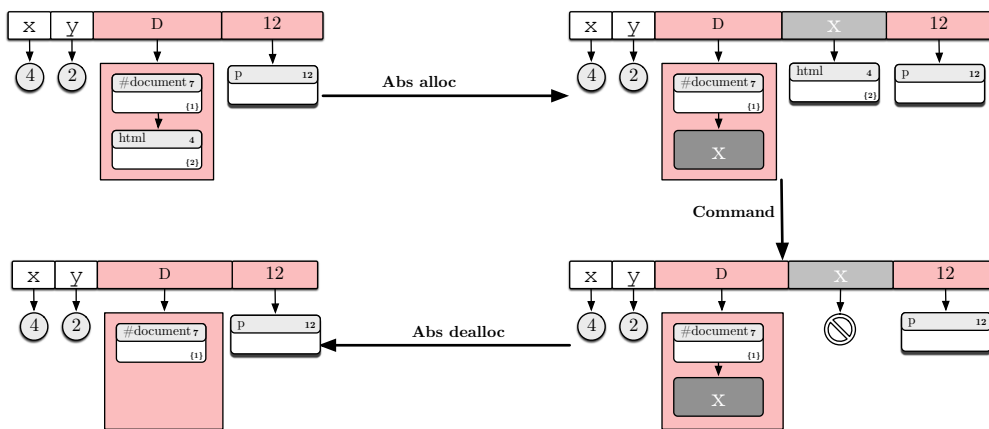
**Definition 72** (DOM structured heaps). Given the DOM sets (parameter 18) and the DOM documents DOMDOCS (definition 70), and letting the program variables  $PVALS = NODEIDS \cup FORESTIDS \cup DOMSTRINGS \cup \{\mathbf{null}\} \cup \mathbb{Z} \cup \mathbb{B}$  and the DOM tree address be  $D \notin PVARs$ , the set of **DOM structured heaps**, ranged over by  $ds, ds_1, \dots, ds_n$ , have the type:

$$\text{STRUCTHEAPS} \subseteq (PVARs \xrightarrow{\text{fin}} PVALS) \sqcup (\{D\} \rightarrow \text{DOMDOCS})$$

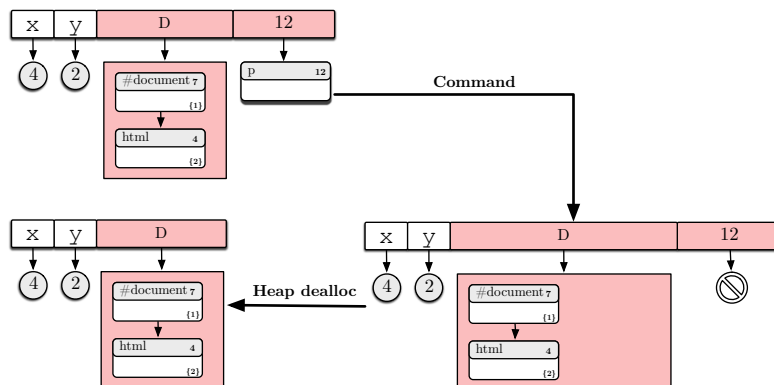
To simplify the presentation, there is exactly one DOM tree in the heap. This models the typical use of DOM in a web browser, where a single tree modelling the currently active page is provided. This can be extended to multiple DOM trees using the tagged

structural addresses of 3.5.2.

In the heap, there is a delineation between the set of heap addresses, and the set of DOM identifiers within the heaps. We do not use DOM identifiers as a heap address at any point. The DOM tree is confined to the value of address D (see figure 4.4). It is, in principle, possible to store DOM data directly in the heap, with `#document` and grove node identifiers being heap addresses. This choice, however, does not treat nodes uniformly, as is the spirit of DOM. It also comes at the price of uniformity in the reasoning. Consider such a scenario, where grove nodes are stored directly in the heap. For example, a command that removed a non-grove node would use abstract allocation to isolate the node, and behave as:



The same command, when applied to a grove node, would behave as:



Ergo, the command would have to account for either a “deep” use of the data (an abstract heap cell), or a “shallow” use of the data (a grove cell). In the first, the cell is removed by abstract deallocation. In the second, an actual heap deallocation step would be required. By isolating all DOM nodes inside the DOM tree cell at address D, and reasoning about them using only abstract heap cells, we ensure uniformity.

## Commands

The commands of our DOM machine are those of the variable system (example 2), plus the DOM commands of featherweight DOM. These commands were chosen as a subset of DOM that demonstrates the range of structural behaviours. Moreover, they were chosen to be *minimal* and *sufficient*, in that they can be used to implement many additional standard DOM commands. We will give some implemented commands in section 4.1.2. Informally, given a DOM structured heap, the commands behave as follows:

**n := createElement(*e*)** : If the expression *e* evaluates to a DOM string *s*, allocates a fresh node identifier *n* and set of forest identifiers *fs*, adds  $s_n[\emptyset_f]_{fs}$  to the end of the grove of the DOM document, and assigns *n* to **n**. Faults if *e* does not evaluate to a DOM string, or if the string contains the character #.

**n := createTextNode(*e*)** : If the expression *e* evaluates to a DOM string *s*, allocates a fresh node identifier *n* and set of forest identifiers *fs*, adds  $\#text_n[s]_{fs}$  to the end of the grove of the document, and assigns *n* to **n**. Faults if *e* does not evaluate to a DOM string.

**s := n.nodeName** : Assigns to **s** the name of the node structure identified by variable **n**. Faults if **n** does not map to a node identifier, or if **n** does not identify a node in the document.

**m := n.parentNode** : Assigns to **m** the node identifier of the parent of the node identified by **n**. If **n** identifies a document node, or if it identifies a node in the document grove, assigns **null**. Faults if **n** does not identify a node.

**c := n.childNodes** : If **n** identifies a node *n*, adds zero or more fresh forest identifiers to the forest identifier associated with *n*, then assigns an element of the resultant set to **c**.

**n := f.item(*e*)** : If the expression *e* evaluates to an integer *i*, and **f** maps to a forest identifier present in the set associated with some node *n*, returns the  $i + 1^{\text{th}}$  node of

the child forest of  $n$ , assigning it to  $\mathbf{n}$ . If  $i$  is less than zero, or greater than or equal to the length of the child forest, assigns **null**. Faults if the evaluation of  $e$  faults, or if  $\mathbf{f}$  does not map to a forest identifier associated with some node.

- $\mathbf{o} := \mathbf{n.removeChild}(\mathbf{m})$  : Removes the node identified by  $\mathbf{m}$  from the child forest of the element node identified by  $\mathbf{n}$ , placing it at the end of the grove list of the document, and assigning the value of  $\mathbf{m}$  to  $\mathbf{o}$ . Faults if either  $\mathbf{n}$  or  $\mathbf{m}$  do not identify nodes, if  $\mathbf{n}$  identifies a document or text node, or if the node identified by  $\mathbf{m}$  is not a child of that identified by  $\mathbf{n}$ .
- $\mathbf{o} := \mathbf{n.appendChild}(\mathbf{m})$  : If  $\mathbf{n}$  identifies an element node  $n$  and  $\mathbf{m}$  identifies a element or text node  $m$  that is not an ancestor of  $n$ , removes node  $m$  from any parent it may have, places it at the end of the child forest of node  $n$ , and assigns  $m$  to the variable  $\mathbf{o}$ . Faults if  $\mathbf{n}$  or  $\mathbf{m}$  do not identify nodes, if  $\mathbf{m}$  if an ancestor of  $\mathbf{n}$ , if  $\mathbf{m}$  identifies a document node, or if  $\mathbf{n}$  identifies a text node or a document node with an existing document element.
- $\mathbf{s} := \mathbf{n.substringData}(e_1, e_2)$  : If  $\mathbf{n}$  identifies a text node with associated data string  $s$ , the expressions  $e_1$  and  $e_2$  evaluate to integer values  $i$  and  $j$  respectively, where  $i \geq 0$  and  $j \geq 0$ , then assigns the sub-string of  $s$  beginning at index  $i$  and continuing for  $j$  characters to  $\mathbf{s}$ . If  $i$  is greater than the length of the string, assigns the empty string. If  $i + j$  exceeds the length of the string, assign the sub-string from index  $i$  to the end of the string. Faults if  $\mathbf{n}$  does not identify a text node, if the evaluation of either  $e_1$  or  $e_2$  faults, or if  $i$  or  $j$  are negative.
- $\mathbf{n.appendChildData}(e)$  : If  $\mathbf{n}$  maps to the identifier of some text node, and  $e$  evaluates to some DOM string  $s$ , appends  $s$  to the child string of the node identified by  $\mathbf{n}$ . Faults if  $\mathbf{n}$  does not identify a text node, if the evaluation of  $e$  faults, or if  $s$  is not a DOM string.
- $\mathbf{n.deleteData}(i, j)$  : Behaves as  $\mathbf{n.substringData}(i, j)$  but rather than returning the data, removes it from the string.

In all the commands, we use expressions for non-identifier parameters, but just variables for identifiers. There are no useful expressions for identifiers other than simple variable lookup, as they are all generated at runtime non-deterministically (so have no useful literals), and there are no expression operations on them. Using just variables here allows us to give simpler axioms.

The commands break down into four groups. The *creation* commands `createElement` and `createTextNode` create new element and text nodes, placing them at the grove level. These commands are straightforward. Notice there is no command to create a document node, as the heaps we work with must contain exactly one. The *query* commands `nodeName`, `parentNode`, `childNodes` and `item` extract properties of a node given its node identifier. Most are straightforward, but the behaviour of `n.childNodes` is unusual. Rather than returning a fixed forest identifier, it behaves non-deterministically. Before returning a forest identifier from the set associated with node `n`, it first adds zero or more fresh identifiers to the set. It therefore returns either an existing identifier or a new identifier. The behaviour is necessary to account for an under-specification in the DOM standard, which we investigate further in section 4.3.

The *structural* commands `appendChild` and `removeChild` are standard. Note that `removeChild` does not destroy the node and the sub-tree, but places it at the grove level. Finally, the *string* commands analyse and update the contents of text nodes. They operate only on the contents of text nodes, rather than on DOM strings in general.

We formalise only a representative subset of of these commands into actions. The number of cases to consider for even the representation set is large, and the rest behave as would be expected from their informal definitions above. For lists and trees, we created the actions using partial lists and single-holed tree contexts respectively (sections 3.1.1 and 3.1.2). Here, we will define the abstract DOM data and compression function that we will use for the abstract heaps, making use of these to define the actions.

**Definition 73** (Abstract DOM data). The set of **abstract DOM strings** `ABSDOMSTRINGS`, ranged over by `s, s1, …, sn`, the set of **abstract DOM trees** `ABSDOMTREES`, ranged over by `t, t1, …, tn`, the set of **abstract DOM forests** `ABSDOMFORESTS`, ranged over by `f, f1, …, fn`, the set of **abstract DOM groves** `ABSDOMGROVES`, ranged over by `g, g1, …, gn`, and the set of **abstract DOM documents** `ABSDOMDOCS`, ranged over by `doc, doc1, …, docn`, are defined by induction as follows: for all  $c \in \text{DOMCHARS}$ ,  $n \in \text{NODEIDS}$  and  $fs \in \mathcal{P}(\text{FORESTIDS})$ ,  $\mathbf{x} \in \text{STRUCTADDRS}$

$\mathbf{s} ::=$	$c \mid \mathbf{s}_1 \cdot \mathbf{s}_2 \mid \emptyset_s \mid \mathbf{x}$	Abstract DOM strings
$\mathbf{t} ::=$	$s_n[\mathbf{f}]_{fs} \mid \#text_n[\mathbf{s}]_{fs}$	Abstract DOM trees
$\mathbf{f} ::=$	$\mathbf{t} \mid \mathbf{f}_1 \otimes \mathbf{f}_2 \mid \emptyset_f \mid \mathbf{x}$	Abstract DOM forests
$\mathbf{g} ::=$	$\mathbf{t} \mid \mathbf{g}_1 \oplus \mathbf{g}_2 \mid \emptyset_g \mid \mathbf{x}$	Abstract DOM groves
$\mathbf{doc} ::=$	$\#document_n[\mathbf{t}]_{fs} \ \& \ \mathbf{g}$ $\mid \#document_n[\mathbf{x}]_{fs} \ \& \ \mathbf{g}$	Abstract DOM documents $\mathbf{t}$ with an abstract element node

where  $\cdot$  is associative with left and right identity  $\emptyset_s$ ,  $\otimes$  is associative with left and right identity  $\emptyset_f$ , and  $\oplus$  is associative and commutative with identity  $\emptyset_g$ . All data are equal up to the properties of  $\cdot$ ,  $\otimes$  and  $\oplus$ , and contain no repeated node or forest identifiers, nor repeated structural addresses.

The set  $\text{ABSDOMDATA}$ , ranged over by  $\mathbf{d}, \mathbf{d}_1, \dots, \mathbf{d}_n$ , is the union of all these sets:

$$\text{ABSDOMDATA} = \text{ABSDOMSTRINGS} \cup \text{ABSDOMTREES} \cup \text{ABSDOMFORESTS} \\ \cup \text{ABSDOMGROVES} \cup \text{ABSDOMDOCS}$$

**Definition 74** (Compression for DOM). Given a set of structural addresses  $\text{STRUCTADDRS}$  (definition 35) and abstract DOM data  $\text{ABSDOMDATA}$  (definition 73), the compression function for the DOM structure  $\text{comp} : \text{STRUCTADDRS} \rightarrow \text{ABSDOMDATA} \rightarrow \text{ABSDOMDATA} \rightarrow \text{ABSDOMDATA}$  is defined as substitution,  $\text{comp}(\mathbf{x}, \mathbf{d}_1, \mathbf{d}_2) = \mathbf{d}_1[\mathbf{d}_2/\mathbf{x}]$ . An application  $\text{comp}(\mathbf{x}, \mathbf{d}_1, \mathbf{d}_2)$  is written  $\mathbf{d}_1 \otimes \mathbf{d}_2$ . If the result would not be contained within  $\text{ABSDOMDATA}$ , the function is undefined.

Giving actions to the `item` and string manipulation commands requires the ability to calculate both the length of strings and length of forests. We give functions for this, which will also prove useful in defining logical expressions for DOM later (definition 81).

**Definition 75** (String length). Given the set of abstract DOM strings  $\text{ABSDOMSTRINGS}$  (definition 73), the **string length** function  $|\cdot|_s : \text{ABSDOMSTRINGS} \rightarrow \mathbb{N}$  is defined as:



$$\begin{aligned}
|\emptyset_s|_s &\triangleq 0 \\
|c|_s &\triangleq 1 \\
|s_1 \cdot s_2|_s &\triangleq |s_1| + |s_2| \\
\text{otherwise} &\quad \text{undefined}
\end{aligned}$$

**Definition 76** (Forest length). Given the set of abstract DOM forests  $\text{ABSDOMFORESTS}$  (definition 73), the **forest length** function  $|\cdot|_f : \text{ABSDOMFORESTS} \rightarrow \mathbb{N}$  is defined analogously to definition 75.

With these, we can define the DOM commands, and give example actions. The majority of the complexity in the actions is in maintaining the structural invariants of the tree. For example, a document node must *only* be the root, and must *always* have exactly one child element node. The actions must preserve these invariants to avoid creating invalid data structure.

**Definition 77** (DOM command actions). Given a set of program variables  $\text{PVARs}$  (definition 3), the atomic commands of DOM are those of the variable system (example 2), and the following additional commands: for all  $s, n, m, i, c \in \text{PVARs}$ ,  $ds \in \text{STRUCTHEAPS}$

$$\begin{aligned}
C \triangleq & \quad n := \text{createElement}(e) \mid n := \text{createTextNode}(e) \mid s := n.\text{nodeName} \\
& \mid m := n.\text{parentNode} \mid c := n.\text{childNodes} \mid n := f.\text{item}(e) \\
& \mid o := n.\text{removeChild}(m) \mid o := n.\text{appendChild}(m) \\
& \mid s := n.\text{substringData}(e, e) \mid n.\text{appendData}(e) \mid n.\text{deleteData}(e, e)
\end{aligned}$$

The actions of the variable commands are given in definition 16. The actions of the DOM commands `createElement`, `appendChild`, `childNodes` and `item` are defined as: for all  $doc \in \text{DOMDOCS}$ ,  $g_1, g_2 \in \text{DOMGROVES}$ ,  $s, s_1, s_2 \in \text{DOMSTRINGS}$ ,  $f, f_1, f_2 \in \text{DOMFORESTS}$ ,  $t_1, t_2 \in \text{ABSDOMTREES}$ ,  $d \in \text{ABSDOMDATA}$ ,  $fs, fs_1, fs_2 \in \mathcal{P}(\text{FORESTIDS})$

$$\begin{aligned}
\llbracket \mathbf{n} := \text{createElement}(e) \rrbracket (ds) &\triangleq \begin{cases} \{ds[\mathbf{n} \mapsto n, \mathbf{D} \mapsto doc \ \& \ g_2] \mid n \in \text{NODEIDS}\} & \text{if } \begin{aligned} &([e])(ds) = s, \# \notin s, \\ &ds(\mathbf{D}) = doc \ \& \ g_1, \\ &g_2 = g_1 \oplus s_n[\emptyset_f]_{\emptyset} \end{aligned} \\ \{\zeta\} & \text{otherwise} \end{cases} \\
\llbracket \mathbf{o} := \mathbf{n}.\text{childNodes} \rrbracket (ds) &\triangleq \begin{cases} \{ds[\mathbf{o} \mapsto o, \mathbf{D} \mapsto \mathbf{d}_1 \otimes t_1]\} & \text{if } \begin{aligned} &ds(\mathbf{D}) = \mathbf{d}_1 \otimes t_1, t_1 = s_{ds(\mathbf{n})}[f]_{fs_1}, \\ &t_2 = s_{ds(\mathbf{n})}[f]_{fs_2}, fs_1 \subseteq fs_2, o \in fs_2 \end{aligned} \\ \{ds[\mathbf{o} \mapsto o, \mathbf{D} \mapsto doc_2]\} & \text{if } \begin{aligned} &ds(\mathbf{D}) = doc_1, doc_1 = \# \text{document}_{ds(\mathbf{n})}[t]_{fs_1} \ \& \ g, \\ &doc_2 = \# \text{document}_{ds(\mathbf{n})}[t]_{fs_2} \ \& \ g, \\ &fs_1 \subseteq fs_2, o \in fs_2 \end{aligned} \\ \{\zeta\} & \text{otherwise} \end{cases} \\
\llbracket \mathbf{o} := \mathbf{f}.\text{item}(e) \rrbracket (ds) &\triangleq \begin{cases} \{ds[\mathbf{o} \mapsto o]\} & \text{if } \begin{aligned} &ds(\mathbf{D}) = \mathbf{d} \otimes s_{1n}[f_1 \otimes s_{2o}[f_3]_{fs_2} \otimes f_2]_{fs_1}, ds(\mathbf{f}) \in fs_1, \\ &([e])(ds) = |f_1|_f \end{aligned} \\ \{ds[\mathbf{o} \mapsto \mathbf{null}]\} & \text{if } \begin{aligned} &ds(\mathbf{D}) = \mathbf{d} \otimes s_n[f]_{fs}, ds(\mathbf{f}) \in fs, \\ &([e])(ds) < 0 \text{ or } ([e])(ds) \geq |f|_f \end{aligned} \\ \{ds[\mathbf{o} \mapsto o]\} & \text{if } ds(\mathbf{D}) = \# \text{document}_n[s_o[f]_{fs_2}]_{fs_1} \ \& \ g, ds(\mathbf{f}) \in fs_1, ([e])(ds) = 1 \\ \{ds[\mathbf{o} \mapsto \mathbf{null}]\} & \text{if } ds(\mathbf{D}) = \# \text{document}_n[t]_{fs} \ \& \ g, ds(\mathbf{f}) \in fs, ([e])(ds) \neq 0 \\ \{ds[\mathbf{o} \mapsto \mathbf{null}]\} & \text{if } ds(\mathbf{D}) = \mathbf{d} \otimes \# \text{text}_{ds(\mathbf{f})}[s]_{fs}, ds(\mathbf{l}) \in fs \\ \{\zeta\} & \text{otherwise} \end{cases} \\
\llbracket \mathbf{o} := \mathbf{n}.\text{appendChild}(\mathbf{m}) \rrbracket (ds) &\triangleq \begin{cases} \{ds[\mathbf{o} \mapsto ds(\mathbf{m}), \mathbf{D} \mapsto \mathbf{t}_1 \otimes \mathbf{t}_4 \oplus \emptyset_f]\} & \text{if } \begin{aligned} &ds(\mathbf{D}) = \mathbf{t}_1 \otimes \mathbf{t}_2 \oplus t, \mathbf{t}_2 = s_{1ds(\mathbf{n})}[\mathbf{f}]_{fs_1}, \\ &t = s_{2ds(\mathbf{m})}[f]_{fs_2}, \mathbf{t}_3 = s_{1ds(\mathbf{n})}[\mathbf{f} \otimes t]_{fs_1} \end{aligned} \\ \{\zeta\} & \text{otherwise} \end{cases}
\end{aligned}$$

These cases span the types of actions needed for the DOM library. As with the tree library of section 3.1.2, the actions use the context structure to analyse the tree. The

`createElement` case simply extends the grove with a fresh node (the `createTextNode` case is similar). The `n.childNodes` case queries the DOM tree, and (due to the non-determinism), updates it. There are three cases. The first is when we can find the sub-tree identified by `n`, capturing it in  $t_1$ , and ignoring the surrounding context  $c_1$ . The action updates the tree with some set of forest identifiers that must include at least the original set  $fs_1$ , and returns an one of these elements. The second case performs the same task for the document node; this is needed to handle its non-uniform shape. The final case is when `n` does not reference a node within the tree. The rest of the query commands are in fact more simple than this case. The `item` command is typical of the index based access to lists of objects (the string manipulation commands are similar). The `n.appendChild` command restructures the tree, and can fault based on the structural relationships of the parameters passed (the `n.removeChild` command is similar).

#### 4.1.2. Examples

Here, we implement several addition DOM commands that are not present in the feather-weight subset. One example is `l := f.length`. This command returns the length of the forest identified by variable `n`.

```

l := f.length  $\triangleq$  local r {
  l := 0;
  r := f.item(l);
  while (r != null)
    l := l + 1;
    r := f.item(l)
}

```

Another command is `v := n.value`. This command returns the *value* associated with a node. The only node type in our subset that with a value (in the DOM sense) is text nodes, whose values are the string stored within them. All other nodes return a **null** value. We implement this in two parts. The first obtains the length of a string, using a similar method to the length of a forest. The second uses the node name to discriminate between text nodes and other node types to ensure it can return the correct value.

```

l := n.stringLength  $\triangleq$  local r {
  l := 0;
  r := n.substringData(1, 1);
  while (r != null)
    l := l + 1;
    r := n.substringData(1, 1)
}
s := n.value  $\triangleq$  local l, name {
  name := n.nodeName;
  if name = "#text"
    l := n.stringLength;
    s := n.substringData(0, l)
  else
    s := null
}

```

Finally, we use this to create a `v := n.childValue` command. This command, not part of the DOM specification, obtains the first child of node `n`, and assigns the value of this child node to `v`. It will prove useful in the photo library example in section 4.4, allowing a more compact program.

```

v := n.childValue  $\triangleq$  local cs, c {
  cs := n.childNodes;
  c := cs.item(0);
  v := c.value
}

```

## 4.2. Reasoning about DOM

We now turn to reasoning about the DOM library. We present the reasoning as we did in the list and tree cases of section 3.1, first defining the structural addressing algebra for DOM, then giving the assertion language and axioms.

### 4.2.1. Abstract DOM data

The structural addressing algebra for DOM is built from the abstract DOM data (definition 73), compression function (definition 74), and the addresses function defined below.

**Definition 78** (Addresses function for DOM). Given the set of abstract DOM data `ABSDOMDATA` (definition 73) and the set of structural addresses `STRUCTADDRS`, the **address function for DOM** `addr : ABSDOMDATA  $\rightarrow$   $\mathcal{P}$ (STRUCTADDRS)` is

defined by induction to collect the set of structural addresses use within some abstract DOM data. It is similar to definition 37.

**Lemma 15** (DOM contexts are structural addressing algebras). *Given DOM data DOMDATA (definition 71), structural addresses STRUCTADDRES (definition 35), abstract DOM data ABSDOMDATA (definition 73), the DOM addresses function *addrs* (definition 78) and DOM compression *comp* (definition 74), the tuple:*

$$(\text{DOMDATA}, \text{STRUCTADDRES}, \text{ABSDOMDATA}, \text{addrs}, \text{comp})$$

*is a structural addressing algebra.*

*Proof.* Straightforward from the definitions. □

Abstract heaps for DOM are defined exactly as those for lists and trees were (sections 3.2.1 and 3.2.1 respectively). Notice that the DOM heap address *D* *still* maps to DOM documents, albeit abstract documents.

**Definition 79** (Abstract heaps for DOM). Given the structural addressing algebra for DOM (lemma 15), and program variables and values (definition 72), the set of **abstract heaps for DOM** are defined as:

$$\begin{aligned} \text{ABSHEAPS} \subseteq & (\text{PVARs} \xrightarrow{\text{fin}} \text{PVALS}) \sqcup (\{\text{D}\} \rightarrow \text{ABSDOMDOCS}) \\ & \sqcup (\text{STRUCTADDRES} \xrightarrow{\text{fin}} \text{ABSDOMDATA}) \end{aligned}$$

#### 4.2.2. Assertion language

We will reuse the abstract heap assertions of definition 55, and the standard framework assertions of definition 21 without change. The data assertion language for DOM is the lift of the data structure algebra of definition 73, plus the connectives required by the data assertions of structural separation logic (definition 57). We will, however, require a richer set of logical values and expressions to check various DOM invariants in the axioms.

**Definition 80** (Logical values). Given node identifiers NODEIDS, forest identifiers FORESTIDS and the null symbol **null** (parameter 18) and the set of abstract DOM data ABSDOMDATA (definition 73), the set of **logical values** LVARs is defined as:

$$\text{LVALS} \triangleq \mathbb{B} \cup \mathbb{Z} \cup \text{NODEIDS} \cup \text{FORESTIDS} \cup \{\mathbf{null}\} \cup \mathcal{P}(\text{FORESTIDS}) \cup \text{ABSDOMDATA}$$

The logical expressions consist of *length* expression, set operations, the *safe name* and *string* expressions, plus a compression expression. The length expression calculates the length of a DOM string or forest, and will be used to axiomatise `item` and the string commands. The standard set operations are used to analyse the sets of forest identifiers. The `safeName(E)` expression determines if `E` evaluates to a string with characters drawn `DOMCHARS` and not containing `#`. It will be used to check the validity of the name passed to `createElement`. The `string(E)` expression determines if the evaluation of `E` is a DOM string, and will be used to validate the parameter to `createTextNode`. The context application expression  $E_1 \textcircled{\alpha} E_2$  allows us to use compression and de-compression to to analyse the contents of logical variables, and will be used in the proofs of our examples.

**Definition 81** (Logical expressions and evaluation). Given a set of logical variables `LVARs` (parameter 13) and logical values `LVALS` (definition 130), the set of **logical expressions** `LEXPRES`, ranged over by `E, E1, …, En`, is defined as the standard logical expressions (definition 14), and the following additional expressions:

$$E ::= |E| \mid E_1 \in E_2 \mid E_1 \cup E_2 \mid \text{safeName}(E) \mid \text{string}(E) \mid E_1 \textcircled{\alpha} E_2$$

The evaluation function  $\langle \cdot \rangle(\Gamma) \cdot : \text{LENVS} \rightarrow \text{LEXPRES} \rightarrow \text{LVALS}$  is defined as that of the variable system (parameter 14), extended with:

$$\begin{aligned}
\langle\langle \mathbf{E} \rangle\rangle(\Gamma) &\triangleq \begin{cases} |\langle\mathbf{E}\rangle(\Gamma)|_s & \text{if } \langle\mathbf{E}\rangle(\Gamma) \in \text{DOMSTRINGS} \\ |\langle\mathbf{E}\rangle(\Gamma)|_f & \text{if } \langle\mathbf{E}\rangle(\Gamma) \in \text{DOMFORESTS} \\ \text{undefined} & \text{otherwise} \end{cases} \\
\langle\mathbf{E}_1 \in \mathbf{E}_2\rangle(\Gamma) &\triangleq \begin{cases} \mathbf{true} & \text{if } \langle\mathbf{E}_2\rangle(\Gamma) \text{ is a set, } \langle\mathbf{E}_1\rangle(\Gamma) \in \langle\mathbf{E}_2\rangle(\Gamma) \\ \mathbf{false} & \text{if } \langle\mathbf{E}_2\rangle(\Gamma) \text{ is a set, } \langle\mathbf{E}_1\rangle(\Gamma) \notin \langle\mathbf{E}_2\rangle(\Gamma) \\ \text{undefined} & \text{otherwise} \end{cases} \\
\langle\mathbf{E}_1 \cup \mathbf{E}_2\rangle(\Gamma) &\triangleq \begin{cases} \langle\mathbf{E}_1\rangle(\Gamma) \cup \langle\mathbf{E}_2\rangle(\Gamma) & \text{if } \langle\mathbf{E}_1\rangle(\Gamma), \langle\mathbf{E}_2\rangle(\Gamma) \text{ are sets} \\ \text{undefined} & \text{otherwise} \end{cases} \\
\langle\text{safeName}(\mathbf{E})\rangle(\Gamma) &\triangleq \begin{cases} \mathbf{true} & \text{if } \langle\mathbf{E}\rangle(\Gamma) = s, s \in \text{DOMSTRINGS,} \\ & \nexists s_1, s_2 \in \text{DOMSTRINGS. } s = s_1 \cdot \# \cdot s_2 \\ \mathbf{false} & \text{otherwise} \end{cases} \\
\langle\text{string}(\mathbf{E})\rangle(\Gamma) &\triangleq \begin{cases} \mathbf{true} & \text{if } \langle\mathbf{E}\rangle(\Gamma) = s, s \in \text{DOMSTRINGS,} \\ \mathbf{false} & \text{otherwise} \end{cases} \\
\langle\mathbf{E}_1 \textcircled{\alpha} \mathbf{E}_2\rangle(\Gamma) &\triangleq \langle\mathbf{E}_1\rangle(\Gamma) \otimes \langle\mathbf{E}_2\rangle(\Gamma), \mathbf{x} = \Gamma(\alpha)
\end{aligned}$$

**Definition 82** (DOM data assertions). Given the set of logical variables LVARs (definition 13) and logical expressions LEXPRs (definition 81), the set of **DOM data assertions** DATAASSTS, ranged over by  $\phi, \phi_1, \phi_n$ , is defined by induction as follows: for all  $N, D, FS, \alpha \in \text{LVARs}, E, s \in \text{LEXPRES}$

$$\begin{aligned}
\phi ::= & c \mid \phi_1 \cdot \phi_2 \mid \emptyset_s \mid s_N[\phi]_{FS} \mid \#\text{text}_N[\phi]_{FS} \mid \phi_1 \otimes \phi_2 \mid \emptyset_f \mid \phi_1 \oplus \phi_2 \mid \emptyset_g \\
& \mid \#\text{document}_D[\phi_1]_{FS} \ \& \ \phi_2 \mid \alpha \mid \phi_1 \textcircled{\alpha} \phi_2 \mid \phi_1 \implies \phi_2 \mid \mathbf{false} \mid E
\end{aligned}$$

We give the semantics of a DOM data assertion via a satisfaction relation, and will give the set interpretation of assertions in terms of this.

**Comment 8.** For large data assertion languages such as DOM, I have found this approach easier to understand than giving the set interpretation directly (as was done for the lists of section 3.2.1 and trees of section 3.2.1).

**Definition 83** (Satisfaction relation for DOM data). Given the set of logical environments  $\text{LENVS}$  (definition 81), the abstract DOM data  $\text{ABSDOMDATA}$  (definition 73) and the DOM data assertions  $\text{DATAASSTS}$  (definition 82), the **DOM data assertion satisfaction relations**  $\models$ :  $(\text{LENVS} \times \text{ABSDOMDATA}) \times \text{DATAASSTS}$ , with elements written  $\Gamma, \mathbf{d} \models \phi$ , is defined by induction as:

$$\begin{array}{ll}
\Gamma, \mathbf{s} \models c & \iff \mathbf{s} = c \\
\Gamma, \mathbf{s} \models \phi_1 \cdot \phi_2 & \iff \exists \mathbf{s}_1, \mathbf{s}_2. \mathbf{s} = \mathbf{s}_1 \cdot \mathbf{s}_2 \wedge \Gamma, \mathbf{s}_1 \models \phi_1 \wedge \Gamma, \mathbf{s}_2 \models \phi_2 \\
\Gamma, \mathbf{s} \models \emptyset_s & \iff \mathbf{s} = \emptyset_s \\
\Gamma, \mathbf{t} \models S_N[\phi_2]_{\text{FS}} & \iff \exists s, n, \mathbf{f}, fs. \begin{array}{l} s = \langle\langle S \rangle\rangle(\Gamma) \wedge n = \Gamma(\text{N}) \wedge fs = \Gamma(\text{FS}) \\ \wedge \mathbf{t} = s_n[\mathbf{f}]_{fs} \wedge \Gamma, \mathbf{f} \models \phi \end{array} \\
\Gamma, \mathbf{t} \models \# \text{text}_N[\phi]_{\text{FS}} & \iff \exists n, \mathbf{s}, fs. \begin{array}{l} n = \Gamma(\text{N}) \wedge fs = \Gamma(\text{FS}) \\ \wedge \mathbf{t} = \# \text{text}_n[\mathbf{s}]_{fs} \wedge \Gamma, \mathbf{s} \models \phi \end{array} \\
\Gamma, \mathbf{f} \models \phi_1 \otimes \phi_2 & \iff \exists \mathbf{f}_1, \mathbf{f}_2. \mathbf{f} = \mathbf{f}_1 \otimes \mathbf{f}_2 \wedge \Gamma, \mathbf{f}_1 \models \phi_1 \wedge \Gamma, \mathbf{f}_2 \models \phi_2 \\
\Gamma, \mathbf{f} \models \emptyset_f & \iff \mathbf{f} = \emptyset_f \\
\Gamma, \mathbf{g} \models \phi_1 \oplus \phi_2 & \iff \exists \mathbf{g}_1, \mathbf{g}_2. \mathbf{g} = \mathbf{g}_1 \oplus \mathbf{g}_2 \wedge \Gamma, \mathbf{g}_1 \models \phi_1 \wedge \Gamma, \mathbf{g}_2 \models \phi_2 \\
\Gamma, \mathbf{g} \models \emptyset_g & \iff \mathbf{g} = \emptyset_g \\
\Gamma, \mathbf{d} \models \# \text{document}_D[\phi_1]_{\text{FS}} \& \phi_2 & \iff \begin{array}{l} n = \Gamma(\text{D}), fs = \Gamma(\text{FS}), \\ \exists n, \mathbf{t}, fs, \mathbf{g}. \wedge \mathbf{d} = \# \text{document}_n[\mathbf{t}]_{fs} \& \mathbf{g} \\ \wedge \Gamma, \mathbf{d} \models \phi_1 \wedge \Gamma, \mathbf{g} \models \phi_2 \end{array} \\
\Gamma, \mathbf{d} \models \alpha & \iff \mathbf{d} = \Gamma(\alpha) \\
\Gamma, \mathbf{d} \models \phi_1 \textcircled{\wedge} \phi_2 & \iff \exists \mathbf{d}_1, \mathbf{x}, \mathbf{d}_2. \begin{array}{l} \mathbf{x} = \Gamma(\alpha), \mathbf{d} = \mathbf{d}_1 \textcircled{\wedge} \mathbf{d}_2 \wedge \\ \Gamma, \mathbf{d}_1 \models \phi_1 \wedge \Gamma, \mathbf{d}_2 \models \phi_2 \end{array} \\
\Gamma, \mathbf{d} \models \phi_1 \Rightarrow \phi_2 & \iff \Gamma, \mathbf{d} \models \phi_1 \Rightarrow \Gamma, \mathbf{d} \models \phi_2 \\
\Gamma, \mathbf{d} \models \text{false} & \iff \text{never} \\
\Gamma, \mathbf{d} \models E & \iff \mathbf{d} = \langle\langle E \rangle\rangle(\Gamma)
\end{array}$$

The standard first-order logic assertions are derived from  $\Rightarrow$  and **false** in the standard way.

We define the following useful assertions.



**Definition 84** (Derived assertions). Given the DOM data assertions DOMASSTS (definition 82) and the abstract heap assertions ASSTS (definition 55), we define the following **derived assertions for DOM**:

$$\begin{aligned}
s_N[\phi] &\triangleq \exists L. s_N[\phi]_L \\
s[\phi] &\triangleq \exists N, L. s_N[\phi]_L \\
\Diamond_f \phi &\triangleq \mathbf{true} \otimes \phi \otimes \mathbf{true} \\
\Box_f \phi &\triangleq \neg \Diamond_f (\neg \phi)
\end{aligned}$$

The first two assertions simply quantify over node or forest identifiers, and are used to reduce verbosity. The assertion  $\Diamond_f \phi$  is read “*somewhere in this forest,  $\phi$  holds*”. It describes a DOM forest in which there is *some* sub-forest satisfying  $\phi$ . The assertion  $\Box_f \phi$  is the dual, read “*everywhere in this forest,  $\phi$  holds*”. It describes a forest in which *every* sub-forest satisfies  $\phi$  (as it is not possible to find a sub-forest for which it does not hold).

So far, we have only given syntactic assertions. We now define the assertion interpretation function.

**Definition 85** (Data assertion interpretation). Given the set of DOM data assertions DATAASSTS (definition 82) and the logical environments LENVS (definition 20), the set interpretation of a DOM assertion  $(\cdot)^{\Gamma} : \text{DATAASSTS} \rightarrow \text{LENVS} \rightarrow \mathcal{P}(\text{ABSDOMDATA})$  is defined as:

$$(\psi)^{\Gamma} \triangleq \{\mathbf{d} \mid \Gamma, \mathbf{d} \models \psi\}$$

**Lemma 16** (Data assertions are valid). *The DOM data assertions, and the interpretation above satisfy the behaviour required of the data assertions of structural separation logic in definition 57.*

*Proof.* Follows by the definition of the satisfaction relation, when compared to the requirements of definition 85.  $\square$

### 4.2.3. Axioms

We give syntactic axioms for the featherweight DOM library in figure 4.5. The associated semantic axioms are found by taking the union of the syntactic assertion interpretations under all logical variables. Atomic soundness of the axioms is similar to tree axioms shown sound in lemma 11.

The axioms are mostly straightforward. We examine a few axioms typical; the others are similar.

1. The `createElement(s)` command adds a new element to the grove. It has pre-condition  $(\mathbf{n} \rightarrow \mathbf{N} * \alpha \mapsto \emptyset_g * E) \wedge e \Rightarrow s \wedge \text{safeName}(s)$ . It uses an abstract cell  $\alpha$  as the location in which to place the new cell. That  $\alpha$  is *not* deep within the DOM tree, but is instead at the grove level, is ensured by the pre-condition  $\alpha \mapsto \emptyset_g$ . Such an  $\alpha$  can only have been allocated from the grove. To satisfy the DOM specification, we must ensure the name chosen for the node is valid; this is assured with `safeName`). The post-condition  $\exists \mathbf{N}, \text{FS}. \mathbf{n} \rightarrow \mathbf{N} * \alpha \mapsto \text{S}_N[\emptyset_f]_{\text{FS}} * P$  asserts that the node is created, using the standard existential quantification for the new node and forest identifiers.
2. There are many cases of `n.parentNode`, the command returning the identifier for the parent of `n`. The first two simply require that we provide an abstract cell containing the node `n` along with its parent; no other data is required, so is not used in the axiom. The more interesting is grove case (the third axiom), using pre-condition  $\mathbf{n} \rightarrow \mathbf{N} * \mathbf{m} \rightarrow - * \alpha \mapsto \# \text{document}_N[\beta]_{\text{FS}} \& \text{S}_N[\delta]_{\text{FS}_2} \oplus \gamma$ . Elements in the grove must return `null` when queries for their parent. However, if we used the pre-condition  $\alpha \mapsto \text{S}_N[\beta]_{\text{FS}}$  to capture node `n`, the axiom could be used to show that every node has no parent! Instead, we capture the fact that grove nodes are siblings of the document node to ensure there is no ambiguity.
3. The `appendChild` axiom is similar to the equivalent command considered for trees in definition 62. However, here, the axiom leaves the assertion  $\emptyset_f \vee \emptyset_g$  in place of `m` once moved. This is because we do not know if the node `m` has come from a forest of grove position. By using the disjunct, the choice is left to the *frame*. Once  $\beta$  is placed larger environment, and abstract deallocation used, the disjunct will collapse to either a forest or grove empty case.

Our axioms are significantly smaller and simpler than those of featherweight DOM [35]. For example, the `appendChild` axiom given there is:

$$\begin{aligned} & \{ \emptyset_f \multimap (\mathbf{c} \circ \text{PNM}_P[\text{PF}]_{\text{PFID}}) \circ \text{CNM}_C[\text{CF}]_{\text{CFID}} \} \\ & \quad \text{appendChild}(p, c) \\ & \{ (\mathbf{c} \circ (\text{PNM}_P[\text{PF}]_{\text{PFID}} \otimes \text{CNM}_C[\text{CF}]_{\text{CFID}})) \} \end{aligned}$$

In this axiom, a *covering context* `c` is used to capture the smallest tree with both `p` and `c` as descends. The need for this is discussed in section 1.2.3. Moreover, this axiom requires the adjoint of context composition to ensure that `c` is not a descendent of the

p. Our axiom does not require the covering context, reducing the footprint considerably. Similarly, the axiom for `n.nodeName` is given as:

$$\begin{aligned} & \{ \mathbf{s} \rightarrow - * \mathbf{n} \rightarrow N * S_N[\mathbf{F}]_{\text{FS}} \} \\ & \quad \mathbf{s} := \mathbf{n}.nodeName \\ & \{ \mathbf{s} \rightarrow S * \mathbf{n} \rightarrow N * S_N[\mathbf{F}]_{\text{FS}} \} \end{aligned}$$

With structural separation logic, we do not need to include the entire sub-tree under the node being analysed. This is a clearer specification of the data accessed by the command.

**Lemma 17** (Soundness). *The DOM small axioms are atomically sound.*

The soundness argument is similar to those previous used for heaps, lists and trees.

#### 4.2.4. Proofs of examples

Consider the `l := n.length` command implementation in 4.1.2. We prove that it satisfies the triple:

$$\begin{aligned} & \{ \mathbf{f} \rightarrow F * \mathbf{l} \rightarrow - * \alpha \mapsto S_N[\mathbf{CS}]_{\text{FS}} \wedge \mathbf{f} \in \text{FS} \} \\ & \quad \mathbf{l} := \mathbf{f}.length \\ & \{ \mathbf{f} \rightarrow F * \mathbf{l} \rightarrow |\mathbf{CS}| * \alpha \mapsto S_N[\mathbf{CS}]_{\text{FS}} \} \end{aligned}$$

The specification is what would be expected of the command, and the proof the code meets it is given in figure 4.5. When given an element with a child forest identified by `f`, the result of the command is to return the length of the child forest. We follow the idioms introduced in chapter 3, mostly using the frame rule and semantic consequence (and hence abstract allocation) implicitly. This improves the readability of the proofs by eliding uninteresting steps, as the command footprints are generally evident simply by observation. However, in the this proof, we explicitly use semantic consequence and frame as a demonstration of the complete reasoning.

We pick a complex case, requiring the re-expression of resource using consequence before the sub-data is obviously available. Notice that the reasoning still follows the three idiomatic steps given in section 3.3.6: express the data in an equivalent de-compressable form; apply abstract allocation; and finally apply the axiom. Here, the chief work is in transforming the data representation so abstract allocation can be applied. This work would be equally necessary, and equally verbose, were normal separation or context logic being used, as the complexity is in managing the disjunction and logical variables.

The specification for the text node string length command states that the command, given a text node identifier, will return the length of the string contained within it. Note

$$\begin{aligned}
& \{(\mathbf{n} \rightarrow N * \alpha \mapsto \emptyset_g * E) \wedge e \Rightarrow s \wedge \text{safeName}(s)\} \\
& \quad \mathbf{n} := \text{createElement}(e) \\
& \quad \{\exists N, FS. \mathbf{n} \rightarrow N * \alpha \mapsto S_N[\emptyset_f]_{FS} * E\} \\
& \{(\mathbf{s} \rightarrow - * \mathbf{n} \rightarrow - * \alpha \mapsto \emptyset_g * E) \wedge e \Rightarrow s \wedge \text{string}(s)\} \\
& \quad \mathbf{n} := \text{createTextNode}(e) \\
& \quad \{\exists N, FS. \mathbf{s} \rightarrow S * \mathbf{n} \rightarrow N * \alpha \mapsto \#text_N[s]_{FS}\} \\
& \quad \{\mathbf{s} \rightarrow - * \mathbf{n} \rightarrow N * \alpha \mapsto S_N[\beta]_{FS}\} \\
& \quad \quad \mathbf{s} := \mathbf{n}.nodeName \\
& \quad \{\mathbf{s} \rightarrow S * \mathbf{n} \rightarrow N * \alpha \mapsto S_N[\beta]_{FS}\} \\
& \quad \{\mathbf{s} \rightarrow - * \mathbf{n} \rightarrow N * D \mapsto \#document_N[\alpha]_{FS} \& \beta\} \\
& \quad \quad \mathbf{s} := \mathbf{n}.nodeName \\
& \{\mathbf{s} \rightarrow \#document * \mathbf{n} \rightarrow N * D \mapsto \#document_N[\alpha]_{FS} \& \beta\} \\
& \quad \{\mathbf{n} \rightarrow N * \mathbf{m} \rightarrow - * \alpha \mapsto S_P[\beta \otimes S'_N[\delta]_{FS_2} \otimes \gamma]_{FS_1}\} \\
& \quad \quad \mathbf{m} := \mathbf{n}.parentNode \\
& \quad \{\mathbf{n} \rightarrow N * \mathbf{m} \rightarrow P * \alpha \mapsto S_P[\beta \otimes S'_N[\delta]_{FS_2} \otimes \gamma]_{FS_1}\} \\
& \quad \{\mathbf{n} \rightarrow N * \mathbf{m} \rightarrow - * D \mapsto \#document_P[S_N[\delta]_{FS_2}]_{FS_1} \& \beta\} \\
& \quad \quad \mathbf{m} := \mathbf{n}.parentNode \\
& \quad \{\mathbf{n} \rightarrow N * \mathbf{m} \rightarrow P * D \mapsto \#document_P[S_N[\delta]_{FS_2}]_{FS_1} \& \beta\} \\
& \quad \{\mathbf{n} \rightarrow N * \mathbf{m} \rightarrow - * \alpha \mapsto \#document_N[\beta]_{FS} \& S_N[\delta]_{FS_2} \oplus \gamma\} \\
& \quad \quad \mathbf{m} := \mathbf{n}.parentNode \\
& \quad \{\mathbf{n} \rightarrow N * \mathbf{m} \rightarrow \mathbf{null} \alpha \mapsto \#document_N[\beta]_{FS} \& S_N[\delta]_{FS_2} \oplus \gamma\} \\
& \quad \{\mathbf{n} \rightarrow N * \mathbf{m} \rightarrow - * \alpha \mapsto \#document_N[\beta]_{FS} \& \gamma\} \\
& \quad \quad \mathbf{m} := \mathbf{n}.parentNode \\
& \quad \{\mathbf{n} \rightarrow N * \mathbf{m} \rightarrow \mathbf{null} * \alpha \mapsto \#document_N[\beta]_{FS} \& \gamma\} \\
& \quad \{\mathbf{n} \rightarrow N * \mathbf{c} \rightarrow - * \alpha \mapsto S_N[\beta]_{FS_1}\} \\
& \quad \quad \mathbf{c} := \mathbf{n}.childNodes \\
& \{\exists FS_2. \mathbf{n} \rightarrow N * \mathbf{c} \rightarrow C * \alpha \mapsto S_N[\beta]_{FS_2} \wedge FS_1 \subseteq FS_2 \wedge C \in FS_2\}
\end{aligned}$$

Figure 4.5.: Axioms for DOM (continued on on the following page)

$$\begin{aligned}
& \left\{ \begin{array}{l} \mathbf{f} \rightarrow \text{FID} * \mathbf{n} \rightarrow - * \alpha \mapsto S_M[\text{FS} \otimes S_N[\gamma]_{\text{FS}_2} \otimes \beta]_{\text{FS}_1} * E \wedge e \Rightarrow I \\ \wedge \text{FID} \in \text{FS}_1 \wedge I = |\text{FS}| \\ \mathbf{n} := \mathbf{f}.\text{item}(i) \end{array} \right\} \\
& \left\{ \mathbf{f} \rightarrow \text{FID} * \mathbf{n} \rightarrow N * \alpha \mapsto S_M[\text{FS} \otimes S_N[\gamma]_{\text{FS}_2} \otimes \beta]_{\text{FS}_1} * E \right\} \\
& \{ \mathbf{f} \rightarrow \text{FID} * \mathbf{n} \rightarrow - * \alpha \mapsto S_M[\text{FS}]_{\text{FS}} * E \wedge e \Rightarrow I \wedge \text{FID} \in \text{FS} \wedge I < 0 \vee I \geq \text{FS} \} \\
& \quad \mathbf{n} := \mathbf{f}.\text{item}(e) \\
& \quad \{ \mathbf{f} \rightarrow \text{FID} * \mathbf{n} \rightarrow \mathbf{null} * \alpha \mapsto S_M[\text{FS}]_{\text{FS}} \} \\
& \left\{ \begin{array}{l} \mathbf{f} \rightarrow \text{FID} * \mathbf{i} \rightarrow I * \mathbf{n} \rightarrow - * \alpha \mapsto \#\text{document}_M[S_N[\gamma]_{\text{FS}_2}]_{\text{FS}_1} \& \beta \\ \wedge \text{FID} \in \text{FS}_1 \wedge I = 0 \\ \mathbf{n} := \mathbf{f}.\text{item}(i) \end{array} \right\} \\
& \left\{ \mathbf{f} \rightarrow \text{FID} * \mathbf{i} \rightarrow I * \mathbf{n} \rightarrow N * \alpha \mapsto \#\text{document}_M[S_N[\gamma]_{\text{FS}_2}]_{\text{FS}_1} \& \beta \right\} \\
& \left\{ \mathbf{n} \rightarrow N * \mathbf{m} \rightarrow M * \mathbf{o} \rightarrow - * \alpha \mapsto S_N[\gamma \otimes S'_M[\zeta]_{\text{FS}_2} \otimes \delta]_{\text{FS}_1} * \beta \mapsto \emptyset_g \right\} \\
& \quad \mathbf{o} := \mathbf{n}.\text{removeChild}(\mathbf{m}) \\
& \left\{ \mathbf{n} \rightarrow N * \mathbf{m} \rightarrow M * \mathbf{o} \rightarrow M * \alpha \mapsto S_N[\gamma \otimes \delta]_{\text{FS}_1} * \beta \mapsto S'_M[\zeta]_{\text{FS}_2} \right\} \\
& \{ \mathbf{n} \rightarrow N * \mathbf{m} \rightarrow M * \mathbf{o} \rightarrow O * \alpha \mapsto S_N[\gamma]_{\text{FS}_1} * \beta \mapsto S'_M[\text{T} \wedge \text{is\_complete}]_{\text{FS}_2} \wedge S \neq \#\text{text} \} \\
& \quad \mathbf{o} := \mathbf{n}.\text{appendChild}(\mathbf{m}) \\
& \left\{ \mathbf{n} \rightarrow N * \mathbf{m} \rightarrow M * \mathbf{o} \rightarrow O * \alpha \mapsto S_N[\gamma \otimes S'_M[\text{T}]_{\text{FS}_2}]_{\text{FS}_1} * \beta \mapsto (\emptyset_f \vee \emptyset_g) \right\} \\
& \left\{ \mathbf{n} \rightarrow N * \mathbf{m} \rightarrow M * \mathbf{o} \rightarrow O * \alpha \mapsto S_N[\gamma]_{\text{FS}_1} * \beta \mapsto \#\text{text}_M[\gamma]_{\text{FS}_2} \wedge S \neq \#\text{text} \right\} \\
& \quad \mathbf{o} := \mathbf{n}.\text{appendChild}(\mathbf{m}) \\
& \left\{ \mathbf{n} \rightarrow N * \mathbf{m} \rightarrow M * \mathbf{o} \rightarrow O * \alpha \mapsto S_N[\gamma \otimes \#\text{text}_M[\gamma]_{\text{FS}_2}]_{\text{FS}_1} * \beta \mapsto (\emptyset_f \vee \emptyset_g) \right\} \\
& \left\{ \mathbf{n} \rightarrow N * \mathbf{s} \rightarrow - * \alpha \mapsto \#\text{text}_N[S \cdot S' \cdot \beta]_{\text{FS}} * E \wedge e_1 \Rightarrow |S| \wedge e_2 \Rightarrow |S'| \right\} \\
& \quad \mathbf{s} := \mathbf{n}.\text{substringData}(e_1, e_2) \\
& \left\{ \mathbf{n} \rightarrow N * \mathbf{s} \rightarrow S' * \alpha \mapsto \#\text{text}_N[S \cdot S' \cdot \beta]_{\text{FS}} \right\} \\
& \left\{ \mathbf{n} \rightarrow N * \mathbf{s} \rightarrow - * \alpha \mapsto \#\text{text}_N[S \cdot S']_{\text{FS}} * E \wedge e_1 \Rightarrow |S| \wedge e_2 \Rightarrow J \wedge J > |S'| \right\} \\
& \quad \mathbf{s} := \mathbf{n}.\text{substringData}(e_1, e_2) \\
& \left\{ \mathbf{n} \rightarrow N * \mathbf{s} \rightarrow S' * \alpha \mapsto \#\text{text}_N[S \cdot S']_{\text{FS}} \right\} \\
& \left\{ \mathbf{n} \rightarrow N * \alpha \mapsto \#\text{text}_N[\beta]_{\text{FS}} * E \wedge e \Rightarrow S \right\} \\
& \quad \mathbf{n}.\text{appendData}(e) \\
& \left\{ \mathbf{n} \rightarrow N * \mathbf{s} \rightarrow S * \alpha \mapsto \#\text{text}_N[\beta \cdot S]_{\text{FS}} \right\} \\
& \left\{ \mathbf{n} \rightarrow N * \alpha \mapsto \#\text{text}_N[S \cdot S' \cdot \beta]_{\text{FS}} * E \wedge e_1 \Rightarrow |S| \wedge e_2 \Rightarrow |S'| \right\} \\
& \quad \mathbf{n}.\text{deleteData}(e_1, e_2) \\
& \left\{ \mathbf{n} \rightarrow N * \alpha \mapsto \#\text{text}_N[S \cdot \beta]_{\text{FS}} \right\} \\
& \left\{ \mathbf{n} \rightarrow N * \alpha \mapsto \#\text{text}_N[S \cdot S']_{\text{FS}} * E \wedge e_1 \Rightarrow |S| \wedge e_2 \Rightarrow J \wedge J > |S'| \right\} \\
& \quad \mathbf{n}.\text{deleteData}(e_1, e_2) \\
& \left\{ \mathbf{n} \rightarrow N * \alpha \mapsto \#\text{text}_N[S]_{\text{FS}} \right\}
\end{aligned}$$

Axioms for DOM (continued from figure 4.5 on the preceding page)

$\{ \mathbf{f} \rightarrow \mathbf{F} * \mathbf{l} \rightarrow - * \alpha \mapsto \text{NM}_N[\text{CS}]_{\text{FS}} \wedge \mathbf{F} \in \text{FS} \}$   
 $\mathbf{l} := \mathbf{f.length} \triangleq \text{local } \mathbf{r} \{$   
 $\mathbf{l} := 0;$   
 We demonstrate a detailed analysis of  $\mathbf{r} := \mathbf{f.item}(\mathbf{l})$   
 $\{ \mathbf{r} \rightarrow - * \mathbf{f} \rightarrow \mathbf{F} * \mathbf{l} \rightarrow 0 * \alpha \mapsto \text{NM}_N[\text{CS}]_{\text{FS}} \wedge \mathbf{F} \in \text{FS} \}$   
 The logical variable CS must have captured a forest  
 We perform case analysis on the possible values of this  
 It is either empty, or a node plus additional forest  
 $\{ \mathbf{r} \rightarrow - * \mathbf{f} \rightarrow \mathbf{F} * \mathbf{l} \rightarrow 0 * \alpha \mapsto \text{NM}_N[\text{CS}]_{\text{FS}} \wedge \mathbf{F} \in \text{FS} \}$   
 $(\text{CS} = \emptyset_f \vee \exists \text{S,M,CS2,FS2,R. CS} = \text{S}_M[\text{CS2}]_{\text{FS2}} \otimes \text{R})$   
 We then apply the rule of disjunction to consider both cases.  
 Assume for this detailed section that  $\text{CS} \neq \emptyset_f$ .  
 $\{ \mathbf{r} \rightarrow - * \mathbf{f} \rightarrow \mathbf{F} * \mathbf{l} \rightarrow 0 * \alpha \mapsto \text{NM}_N[\text{CS} \wedge \exists \text{S,M,CS2,FS2,R. S}_M[\text{CS2}]_{\text{FS2}} \otimes \text{R}]_{\text{FS}} \wedge \mathbf{F} \in \text{FS} \}$   
 We must frame off the R forest.  
 $\{ \mathbf{r} \rightarrow - * \mathbf{f} \rightarrow \mathbf{F} * \mathbf{l} \rightarrow 0 * \alpha \mapsto \text{NM}_N[\text{CS} \wedge \exists \text{S,M,CS2,FS2,R. S}_M[\text{CS2}]_{\text{FS2}} \otimes \beta(\beta) \text{R}]_{\text{FS}} \wedge \mathbf{F} \in \text{FS} \}$   
 Apply existential elimination  
 $\{ \mathbf{r} \rightarrow - * \mathbf{f} \rightarrow \mathbf{F} * \mathbf{l} \rightarrow 0 * \alpha \mapsto \text{NM}_N[\text{CS} \wedge \text{S}_M[\text{CS2}]_{\text{FS2}} \otimes \beta(\beta) \text{R}]_{\text{FS}} \wedge \mathbf{F} \in \text{FS} \}$   
 Apply abstract allocation.  
 We must record the structure of CS to reestablish the data invariant.  
 $\{ \exists \beta. \alpha \mapsto \text{NM}_N[\text{S}_M[\text{CS2}]_{\text{FS2}} \otimes \beta]_{\text{FS}} * \beta \mapsto \text{R} \wedge \mathbf{F} \in \text{FS} \wedge \text{CS} = \text{S}_M[\text{CS2}]_{\text{FS2}} \otimes \beta(\beta) \text{R} \}$   
 We can now apply the frame rule and the axiom  
 $\mathbf{r} := \mathbf{f.item}(\mathbf{l});$   
 We undo the above steps, and state the loop invariant  
 $\left\{ \left( \begin{array}{l} \exists \text{L,R,CS1,CS2. } \mathbf{f} \rightarrow \mathbf{F} * \mathbf{l} \rightarrow \text{L} * \mathbf{r} \rightarrow \text{R} * \\ (\text{R} = 0 \wedge \alpha \mapsto \text{NM}_N[\text{CS} \wedge \text{CS1}]_{\text{FS}}) \\ \vee \\ (\text{R} \neq 0 \wedge \alpha \mapsto \text{NM}_N[\text{CS} \wedge \text{CS1} \otimes \text{R} \otimes \text{CS2}]_{\text{FS}}) \wedge \text{L} = |\text{CS1}| \end{array} \right) \wedge \mathbf{F} \in \text{FS} \right\}$   
 $\text{while } (\mathbf{r} \neq \text{null})$   
 $\mathbf{l} := \mathbf{l} + 1;$   
 $\mathbf{r} := \mathbf{f.item}(\mathbf{l});$   
 $\}$   
 $\{ \mathbf{f} \rightarrow \mathbf{F} * \mathbf{l} \rightarrow |\text{CS}| * \alpha \mapsto \text{NM}_N[\text{CS}]_{\text{FS}} \}$

Figure 4.5.: Proof of  $\mathbf{l} := \mathbf{f.length}$ .

the use of the *is\_complete* predicate to ensure the entire string contents are present for analysis. We show the code meets this specification in section A.1.2.

$$\begin{aligned} & \{ \mathbf{n} \rightarrow N * \mathbf{l} \rightarrow - * \alpha \mapsto \# \text{text}_N[S \wedge \textit{is\_complete}]_{\text{FS}} \} \\ & \quad \mathbf{l} := \mathbf{n}.\text{stringLength} \\ & \{ \exists \mathbf{L}. \mathbf{n} \rightarrow N * \mathbf{l} \rightarrow \mathbf{L} * \alpha \mapsto \# \text{text}_N[S]_{\text{FS}} \wedge \mathbf{L} = |\mathbf{S}| \} \end{aligned}$$

The node value command has the specification below. We a specification for the element and text node case, and show the code meets it (in the text node case) in figure A.1.

$$\begin{aligned} & \{ \mathbf{n} \rightarrow N * \mathbf{s} \rightarrow - * \alpha \mapsto \text{NM}_{\text{ID}}[\text{CS}]_{\text{FS}} \} \\ & \quad \mathbf{s} := \mathbf{n}.\text{value} \\ & \{ \mathbf{n} \rightarrow N * \alpha \mapsto \text{NM}_{\text{ID}}[\text{CS}]_{\text{FS}} * (\text{NM} = \# \text{text} \wedge \mathbf{s} \rightarrow \mathbf{null}) \vee (\text{NM} \neq \# \text{text} \wedge \mathbf{s} \rightarrow \mathbf{null}) \} \end{aligned}$$

The first child value command has the following specification. The command is intended to extract the text from the very common XML idiom `<nodeName>Text value</nodeName>`. Notice that the pre-condition and post-condition clearly mirror this. The only complexity is in the update to the forest identifier set. We show the code meets this specification in the appendix, section A.1.4.

$$\begin{aligned} & \{ \mathbf{n} \rightarrow N * \mathbf{v} \rightarrow - * \alpha \mapsto \text{NM}_{\text{ID}}[\# \text{text}_{\text{TID}}[\text{VAL}]_{\text{FS1}} \otimes \beta]_{\text{FS2}} \} \\ & \quad \mathbf{l} := \mathbf{n}.\text{childValue} \\ & \{ \exists \text{FS3}. \mathbf{n} \rightarrow N * \mathbf{v} \rightarrow \text{VAL} * \alpha \mapsto \text{NM}_{\text{ID}}[\# \text{text}_{\text{TID}}[\text{VAL}]_{\text{FS1}}]_{\text{FS3}} \wedge \text{FS2} \subseteq \text{FS3} \} \end{aligned}$$

### 4.3. Unsoundness in previous work

The existing work on featherweight DOM [35], [36] and thesis of Smith [65], gives the specification of `n.childNodes` as<sup>2</sup>:

$$\begin{aligned} & \{ \mathbf{n} \rightarrow N * \mathbf{c} \rightarrow - * \alpha \mapsto \text{S}_N[\beta]_{\text{F}} \} \\ & \quad \mathbf{c} := \mathbf{n}.\text{childNodes} \\ & \{ \mathbf{n} \rightarrow N * \mathbf{c} \rightarrow \text{F} * \alpha \mapsto \text{S}_N[\beta]_{\text{F}} \} \end{aligned}$$

where  $\text{F}$  is a single forest identifier, rather than a set. As such, every call to `n.childNodes` returns the same forest identifier. This allows the derivation given in figure 4.6, where the

---

<sup>2</sup>Here updated to use our notation, but essentially identical.

specification states that the program always terminates in a state where variables `x` and `y` are identical. However, this behaviour is not guaranteed by the DOM specification.

That the specification returns the same identifier every time is too strong a statement. Whilst it is correct, in that any DOM implementation with this behaviour is correct, it is too restrictive, in that there are DOM implementations obeying the specification that do not behave in this way. We have seen this behaviour in the widely used WebKit browser engine<sup>3</sup>. This gives rise to a mismatch between the reasoning and DOM, as erroneous conclusions can be drawn about certain DOM programs<sup>4</sup>. The figure 4.6 shows a derivation which is false on some correct DOM implementations.

To support our interpretation, we examine the standard. In [71], the specification for the node interface contains a `childNodes` *attribute*:

```
interface Node {
  :
  readonly attribute NodeList childNodes;
  :
};
```

This attribute<sup>5</sup> is described as:

*“A `NodeList` that contains all children of this node. If there are no children, this is a `NodeList` containing no nodes.*

Our implementation of `NodeList` is to return a forest identifier that references the node associated with the node list. This is acceptable, as we can see the forest identifier returned by `n.childNodes` as a simple “object” that just exposes the `item` command.

Even though declared as an attribute, the `childNodes` property of a node may return different results on each execution. To see this, we examine the specification for attributes given by Interface Description Language [54] (henceforth IDL). IDL is a language for describing object interfaces without committing an underlying implementation programming language, and is used by the DOM standard to define the node interfaces. On the behaviour of attributes, the IDL specification says:

---

<sup>3</sup>It was changed late in 2011 so that the `NodeList` objects resulting from calls are cached across requests (see Webkit bug 76591 [2]). The change was due to performance concerns, rather than standards compliance concerns.

<sup>4</sup>Although we have not yet found an instance of this problem in any examined code

<sup>5</sup>Note that `readonly` describes only that the attribute cannot be assigned to, not that it cannot change by other means.



*“An interface can have attributes as well as operations; as such, attributes are defined as part of an interface. An attribute definition is logically equivalent to declaring a pair of accessor functions; one to retrieve the value of the attribute and one to set the value of the attribute.”*

No restrictions are placed on the behaviour of the accessor functions. They are free to compute the result in any way that satisfies the documented behaviour of the object. As the `childNodes` property says only that the result is “A *NodeList* that contains all children of this node”, it is possible to comply with the specification by returning any object that gives the behaviour; the same one each time, a fresh one each time, or some combination of the two. Thus, the fragment:

```
if (n.childNodes != n.childNodes)
  print "An odd behaviour"
```

will sometimes legitimately result in “An odd behaviour”. The simple JavaScript program given in figure 4.6 can be used in any modern web browser to detect the issue.

In our model, we have corrected the problem by associating each node with a *set* of node list identifiers. The action of the `n.childNodes` command non-deterministically extends this set with zero or more fresh identifiers, and returns an element of the new set. This behaviour captures all possible outcomes: on each invocation, either a new identifier or an existing identifier is returned, with no way for the specification to prove which case occurs.

**Comment 9.** This is the only oversight I have discovered in the DOM specification given in [35], [65], and indicates only the innate complexity of interpreting specifications written in English, especially those dependent on other specifications. The error arose due to the unnatural behaviour of the `attribute` feature in IDL. Most people would have expected an “attribute” to be a consistent property of an object, such that `n.childNodes = n.childNodes` is always true. Whether this behaviour was intended by the IDL authors and not documented, assumed by the DOM authors and not validated, or is intentional, is unknown.

Two conclusions can be drawn here. The first is that specifications should not give words a different meaning to that which will be commonly understood by their audience. The second is that, ideally, authors of axiomatic specifications should validate as many of their assumptions as possible against implementations of the specification,

```

{ n → - * cn1 → - * cn2 → - * D ↦ #documentDN[true]DFS & ∅g }
n := createElement("Test");
{
  ∃N,FS. n → N * cn1 → - * cn2 → - *
  D ↦ #documentDN[true]DFS & TestN[∅f]FS
}
cn1 := n.childNodes;
cn2 := n.childNodes;
{
  ∃N,LFS. n → DN * cn1 → FS * cn2 → FS *
  D ↦ #documentDN[true]DFS & TestN[∅f]FS
}
if (cn1 == cn2)
  { true }
  alert("The child node lists are the same");
else
  { false }
  alert("The child node lists differ");
{ true }

```

```

<!doctype html>
<html>
  <script>
    var n = document.createElement("Test");
    var cn1 = testEle.childNodes;
    var cn2 = testEle.childNodes;
    if (cn1 === cn2)
      alert("The child node lists are the same");
    else
      alert("The child node lists differ");
  </script>
</html>

```

Figure 4.6.: Demonstration of the specification unsoundness in the `n.childNodes` of previous DOM axiomatisations [35]. On WebKit versions prior to change-set 105372, this displays “The child node lists differ”, in violation of the derivation given. Notation changes are due to the differences between our simple imperative language and JavaScript, and the fact our subset allows only one document.

and not just use the prose. When divergences are found, they can be investigated more deeply and either identified as a specification, implementation, axiomatisation bug.

#### 4.4. Hybrid reasoning example

The DOM library is not used solely by web browsers. The ubiquity of libraries such as DOM has led to XML being adopted as a general *data transport format*. Many programs and protocols, rather than creating custom representations for data, now give output in XML. This allows other programs to consume their results using standard library implementations, rather than having to decode complex custom formats. XML, a text-only format, is even even used by programs storing non-text data such as photographs. Storing arbitrary data in XML requires an *encoding*, allowing it to be safely represented as text. One such encoding is *base-64*.

Base-64 is an encoding for data that would otherwise be stored as raw bytes (that is, base-256). Each of the 64 possible symbols in base-64 is represented with a *printable character*<sup>6</sup>. These are chosen to be a subset of most plain text formats, so base-64 allows arbitrary base-256 data to be stored in a text file. Each character in a text file typically uses 1 byte, so there is a size increase when transcoding from base-256 (8 useful bits per byte) to base-64 (7 useful bits per byte).

In this example, we consider a *photo library* XML file. This file represents the output of some image management tool. It stores a list of photographs, each associated with a name and the base-64 encoding of the photographic data. We will write and reason about a program that consumes this file, reading each photograph from the XML file, decoding the photo data from base-64 into a normal base-256 representation, and processing the decoded data. This program has several interesting facets as an example. It combines both DOM reasoning about the XML data with heap reasoning about the base-64 decoding of binary data. This demonstrates the hybrid reasoning of structural separation logic, something not straightforwardly achievable in the previous DOM work via context logic [35]. It is a typical use of the DOM library, analysing a list of nodes via assumptions about the document structure. The program also demonstrates the ability of structural separation logic to enable *parallel* DOM programs in a safe manner.

An example of the photo library XML manipulated by the program is given in figure

---

<sup>6</sup>Typically ‘A’ through ‘Z’ (upper-case letters), ‘a’ through ‘z’ (lower-case letters), ‘0’ through ‘9’ (numbers), and the characters ‘+’ and ‘/’.

```

local f, i, p {
  i := 0;
  c := album.childNodes;
  p := c.item(i);
  while p ≠ null local cs, nameNode, lenNode,
    data64Node, name, len, data64, data {
    cs := p.childNodes;
    nameNode := cs.item(0);
    lenNode := cs.item(1);
    data64Node := cs.item(2);
    name := nameNode.childValue;
    len := lenNode.childValue;
    data64 := data64Node.childValue;
    // Allocate a heap buffer for the data, and decode into it
    data := alloc(len);
    base64Decode(data64, data);
    // Pass the name and buffer to the handler, and cleanup
    handleImage(name, data);
    free(data, len);
    i := i + 1;
    p := c.item(i)
  }
}

```

Figure 4.7.: Example photo library decoder program.

4.8. The program proceeds by using the DOM library to iterate over the each `picture` child of the `album` element. For each picture, it will extract the name of the photo, the length of the photograph data in base-256, and the data’s base-64 representation. It then uses the standard heap memory to allocate a buffer large enough to store the decoded representation of the photo data (as given by the `length` element). The base-64 data and the buffer pointer are passed to a `base64Decode` function that fills the buffer with the decoded photo data. The program then passes this, along with the name, to a handler function. The program text is given in figure 4.7.

Reasoning about this program requires handling both DOM data and normal flat heap cells. We thus extend the DOM heap in the natural way to account for flat heap cells:

$$\text{STRUCTHEAPS} \subseteq \begin{array}{l} (\text{PVARs} \xrightarrow{\text{fin}} \text{PVALS}) \sqcup (\mathbb{N}^+ \xrightarrow{\text{fin}} \mathbb{N}) \sqcup (\{\text{D}\} \rightarrow \text{ABSDOMDOCS}) \\ \sqcup (\text{STRUCTADDRS} \xrightarrow{\text{fin}} \text{ABSDOMDATA}) \end{array}$$

To represent an buffer, we use an inductive predicate *ncells*. The instance *ncells(start, len, bytes)* represents a contiguous sequence of heap memory cells, starting

at address *start*, continuing for *len* cells, and containing the list of bytes in *bytes*.

$$ncells(start, len, bytes) \triangleq \begin{array}{c} (len = 0 \wedge bytes = \epsilon \wedge emp) \\ \vee \\ \left( \begin{array}{l} len > 0 \wedge \exists b, bytes'. bytes = b : bytes' \wedge \\ start \mapsto b * ncells(start + 1, len - 1, bytes') \end{array} \right) \end{array}$$

We create two additional heap axioms to represent allocating and freeing a new n-cell buffer.

$$\begin{array}{c} \{c \rightarrow - * 1 \rightarrow L\} \\ c := allocn(1) \\ \{\exists C, D. c \rightarrow C * 1 \rightarrow L * ncells(C, L, D)\} \\ \\ \{c \rightarrow C * 1 \rightarrow L * ncells(C, L, D)\} \\ \text{freen}(c, 1) \\ \{c \rightarrow C * 1 \rightarrow L\} \end{array}$$

For the purpose of this example, we assume a logical expression  $decode_{64}(E)$  that describes the byte sequence obtained by base-64 decoding the evaluation of the logical expression  $E$ . We also assume an overload for the expression  $|E|$  that allows it calculate the length of a byte sequence. These are used to specify a command `base64Decode(data, buffer)` which takes a variable `data` containing base-64 encoded data, and a variable `buffer` pointing to a run of contiguous heap memory long enough to contain the base-256 representation of the `data` contents. The command decodes the data into the buffer, faulting if the buffer is not large enough.

$$\begin{array}{c} \{\text{data} \rightarrow D * \text{buffer} \rightarrow B * ncells(B, LEN, -) \wedge LEN \geq |decode_{64}(D)|\} \\ \text{base64Decode}(\text{data}, \text{buffer}) \\ \{\text{data} \rightarrow D * \text{buffer} \rightarrow B * ncells(B, LEN, decode_{64}(D))\} \end{array}$$

We now have sufficient predicates and commands to reason about the flat heap part of the example. We must still describe the contents of the album DOM tree, for which we use *schema* predicates. Schema predicates describe the general *shape* of a document, and are somewhat analogous to XML schema [40] or the XDuce assertions of [43]. We define the following predicates to describe the schema of an `album` document:

```

<album>
  <picture>
    <name>Landscape</name>
    <length>12333</length>
    <data>TG9ydW0gaXBzdW0uLg...</data>
  </picture>
  <picture>
    <name>Beach</name>
    <length>44782</length>
    <data>ZG9sb3Igc2l0IGFtZX...</data>
  </picture> ...
</album>

```

Figure 4.8.: Example photo album XML file containing base-64 encoded data. The `album` element has a list of `picture` elements for children. These pictures have three children: a name, the length of the photograph data in base-256, and the base-64 encoded photograph data.

$$\begin{aligned}
sVal(id, name, val) &\triangleq \exists N, FS. name_{id}[\#text_N[val]_{FS}] \\
pic \left( \begin{array}{l} id, name, \\ length, data \end{array} \right) &\triangleq \exists ID_1, ID_2, ID_3. picture_{id} \left[ \begin{array}{l} sVal(ID_1, name, name) \otimes \\ sVal(ID_2, length, length) \otimes \\ sVal(ID_3, data, data) \end{array} \right] \\
&\quad \wedge length = |\text{decode}_{64}(data)| \\
existPic &\triangleq \emptyset \vee \exists \begin{array}{l} ID, NAME, \\ LEN, DATA. \end{array} pic(ID, NAME, LEN, DATA) \\
album(n, f) &\triangleq album_n[\square_f existPic]_f
\end{aligned}$$

The predicate  $sVal(id, name, val)$  describes an element with identifier  $id$  and name  $name$ , and a single child text node containing the string  $val$ . It is used to describe the elements `name`, `length` and `data` in the album document. The predicate  $pic$  describes each `picture` element in the tree, and ensures the claimed data length is actually correct. Similarly, the  $existPic$  describes either a picture with existentially quantified parameters, or the empty element. This is used in the  $album(n, f)$  predicate, which describes an entire album element. It uses the “everywhere” assertion of definition 134 along with  $existPic$  to assert that every child of the album element is either a picture element, or empty. This  $album$  predicate can describe any instance of an album XML file with the shape given in figure 4.8.

After the decoding of the image data, the name and the resultant byte sequence are passed to a dummy command `handleImage` that represents some kind of processing. We

do not model what this command does, and assume it has the following specification, which indicates it does not alter the buffer:

$$\begin{aligned} & \{\mathbf{name} \rightarrow S * \mathbf{buffer} \rightarrow B * \mathit{ncells}(B, \mathit{LEN}, D)\} \\ & \quad \mathit{handleImage}(\mathbf{name}, \mathbf{buffer}) \\ & \{\mathbf{name} \rightarrow S * \mathbf{buffer} \rightarrow B * \mathit{ncells}(B, \mathit{LEN}, D)\} \end{aligned}$$

The decoder program is given in figure 4.9. The pre-condition is  $\mathbf{album} \rightarrow A * \alpha \mapsto \mathit{album}(A, F)$ , and the post-condition is  $\exists F. \mathbf{album} \rightarrow A * \alpha \mapsto \mathit{album}(A, F)$ . This is an example of a specification saying very little, but the Hoare triple saying a lot. The pre- and post-condition that form the specification are equally a specification for `skip`. However, the value of the triple is in examining the code. This, along with the proof that it meets the given specification, (also in figure 4.9), states that the program does work (albeit work unobservable from the specification) and, whatever this is, it does it *safely*. Our memory safe interpretation of triples ensures that every DOM and heap command is used in both a memory safe and structurally correct manner.

There are two notable facets to the proof. The first facet is the use of abstract allocation and frame to isolate resource. We have mostly applied the rule implicitly, but leave in one explicit use labelled ①. This use allows the `while` loop, which intuitively focuses on exactly one `picture` element to actually focus the assertions on that element. In the proof, the use of semantic consequence to perform abstract allocation has allowed a concise restructuring of the heap; we need only one step to isolate and express the sub-data needed, and can then apply the frame rule as normal. The second facet is the mixture of heap and structured resource reasoning. The *ncell* inductive predicate exists alongside the abstract heap cells, allowing a homogeneous reasoning style for structured and standard heaps.

This type of proof is the strength of structural separation logic. We have mixed standard separation-logic-style reasoning using normal inductive predicates with reasoning about highly structured data presented entirely abstractly. Despite this, the style is very similar across the program. There are no jarring switches in reasoning style, the specifications remain readable, and the proof is as easy to manage as those of standard separation logic.

## Towards concurrency

The DOM library is rarely used in concurrent settings. The standard provides no safety guarantees for concurrent usage, and web browsers do not currently support shared memory concurrency at all. However, the proof given above indicates that structural separation logic could help enable concurrent DOM. The photo library is an example where concur-

```

{ album → A * α ↦ album(A, F) }
local f, i, p {
  i := 0;
  { album → A * f → - * i → 0 * p → - * α ↦ album(A, F) }
  f := album.childNodes;
  { ∃FID,FS. album → A * f → FID * i → 0 * p → 0 * α ↦ album(A, FS) ∧ FID ∈ FS }
  p := f.item(i);
  Unpack definition of album predicate
  {
    ∃FID,FS,P,I. (
      album → A * f → FID * i → I * p → P *
      α ↦ (
        album(A, FS) ∧
        album_A[(P = null ∧ ∅) ∨
        (∃PRE,PST,NM,LN,DT. PRE ⊗ pic(P, NM, LN, DT) ⊗ PST ∧ |PRE| = 1)]_FS
      )
    )
  }
  while p ≠ null local cs, nameNode, lenNode, data64Node, name, len, data64, data {
    Use semantic consequence to allocate the pic node (⊙)
    {
      ∃FID,FS,P,I,NM,LN,DT,β. (
        album → A * f → FID * i → I * p → P * cs → - * nameNode → -
        lenNode → - * data64Node → - * name → - * len → - * data64 → - * data → -
        α ↦ (
          album_A[∃PRE,PST.
            (PRE ∧ □existsPic)
            ⊗ β ⊗
            (PST ∧ □existsPic) ∧ |PRE| = 1
          ]_FS
        )
        * β ↦ pic(P, NM, LN, DT)
      )
    }
    Apply frame rule, existential elimination, and expand pic
    {
      cs → - * nameNode → - * lenNode → - * data64Node → - * name → - * len → - * data64 → - * data → -
      * β ↦ ∃NMN,LNN,DTN,CFS. picture_p [
        sVal(NMN, name, NM) ⊗
        sVal(LNN, length, LN) ⊗
        sVal(DTN, data, DT)
      ]_CFS ∧ LN = |decode64(DT)|
    }
    cs := p.childNodes;
    nameNode := cs.item(0);
    lenNode := cs.item(1);
    data64Node := cs.item(2);
    {
      ∃CFID,NMN,LNN,DTN. cs → CFID * nameNode → NMN * lenNode → LNN * data64Node → DTN * data → -
      * name → - * len → - * data64 → - * β ↦ ∃CFS. picture_p [
        sVal(NMN, name, NM) ⊗
        sVal(LNN, length, LN) ⊗
        sVal(DTN, data, DT)
      ]_CFS
      ∧ CFID ∈ CFS ∧ LN = |decode64(DT)|
    }
    name := nameNode.childValue;
    len := lenNode.childValue;
    data64 := data64Node.childValue;
    {
      ∃CFID,NMN,LNN,DTN. cs → CFID * nameNode → NMN * lenNode → LNN * data64Node → DTN * data → -
      * name → NM * len → LN * data64 → DT
      * β ↦ ∃CFS. picture_p [
        sVal(NMN, name, NM) ⊗
        sVal(LNN, length, LN) ⊗
        sVal(DTN, data, DT)
      ]_CFS
      ∧ LN = |decode64(DT)|
    }
    Apply frame rule and existential elimination again
    {
      name → NM * len → LN * data64 → DT * data → - ∧ LN = |decode64(DT)| }
    // Allocate a heap buffer for the data, and decode into it
    data := alloc(len);
    {
      ∃D. name → NM * len → LN * data64 → DT * data → D * ncells(D, LN, -) ∧ LN = |decode64(DT)| }
    base64Decode(data64, data);
  }
}

```

Figure 4.9.: Proof of photo library decoder program (continued on the following page).



```

{  $\exists D. \text{name} \rightarrow \text{NM} * \text{len} \rightarrow \text{LN} * \text{data64} \rightarrow \text{DT} * \text{data} \rightarrow D * \text{ncells}(D, \text{LN}, \text{decode}_{64}(\text{DT}))$  }
// Pass the name and buffer to the handler, and cleanup
handleImage(name, data);
{  $\exists D. \text{name} \rightarrow \text{NM} * \text{len} \rightarrow \text{LN} * \text{data64} \rightarrow \text{DT} * \text{data} \rightarrow D * \text{ncells}(D, \text{LN}, \text{decode}_{64}(\text{DT}))$  }
freen(data, len);
{  $\exists D. \text{name} \rightarrow \text{NM} * \text{len} \rightarrow \text{LN} * \text{data64} \rightarrow \text{DT} * \text{data} \rightarrow D * f \rightarrow \text{FID} * i \rightarrow I$  }
i := i + 1;
p := f.item(i)
Unframe, and apply deallocation

$$\left\{ \begin{array}{l} \exists_{\text{FID}, \text{FS}, \text{P}, \text{I}} \left( \begin{array}{l} \text{album} \rightarrow A * f \rightarrow \text{FID} * i \rightarrow I * p \rightarrow P * \text{cs} \rightarrow - * \text{nameNode} \rightarrow - * \\ \text{lenNode} \rightarrow - * \text{data64Node} \rightarrow - * \text{name} \rightarrow - * \text{len} \rightarrow - * \text{data64} \rightarrow - * \text{data} \rightarrow \\ \alpha \mapsto \left( \begin{array}{l} \text{album}(A, \text{FS}) \wedge \\ \text{album}_A[(P = \text{null} \wedge \emptyset) \vee \\ (\exists \text{PRE}, \text{PST}, \text{NM}, \text{LN}, \text{DT}. \text{PRE} \otimes \text{pic}(P, \text{NM}, \text{LN}, \text{DT}) \otimes \text{PST} \\ \wedge |\text{PRE}| = I)]_{\text{FS}} \end{array} \right) \end{array} \right) \end{array} \right\}$$

}
}
{  $\exists F. \text{album} \rightarrow A * \alpha \mapsto \text{album}(A, F)$  }

```

Continued decoder example from figure 4.9 on the preceding page

rency could be helpful. Rather than processing each `picture` element in sequence, all picture elements could be processed in parallel. This could be achieved with only a mild change the program structure and proof; rather than using the `while` loop directly, replicate the loop body in parallel composition, one for each element. The safety of this is guaranteed by structural separation logic, as we can abstractly allocate each `picture` subtree into a separate cell, as is done in the proof. With context logic, such separation would not be possible. Along with Raad [59], we are currently examining the possibilities for concurrent DOM in detail.

## 4.5. Summary

This chapter has presented a detailed case study regarding using structural separation logic, replicating previous work on abstract structured data reasoning about DOM. We have demonstrated:

1. In this entire chapter, that structural separation logic can be used to provide an accurate specification of a real standard, and is thus at least as suited for this task as previous context logic work.
2. In section 4.2.3, that compared to previous work on context logic, the command axioms can be significantly smaller.
3. With the photo library (example 4.4), that *hybrid reasoning* mixing low- and high-level programming styles can be reasoned about. Both abstract allocation and nor-

mal inductive predicates can co-exist, along with different heap data types. This was not possible with previous techniques

4. With the photo library example and smaller axioms, that structural separation logic lays the foundations for concurrent reasoning.

## 5. Naturally stable promises

We have now demonstrated local reasoning for abstract data via structural separation logic. This is a useful step towards our goal of specifying libraries but, unfortunately, many libraries do not manipulate their data in an entirely local manner. Consider libraries with commands that use wide-ranging analysis to find data for update. These are distinct from the libraries we have so far seen, which access data via unique identifiers. For example, to remove an item from a tree, we identify the item via a unique integer. In DOM, each node is accessed via a unique node identifier, and each forest via a unique forest identifier.

In all these cases, the position in the structure at which an update occurs is entirely determined by searching the structure for the distinct “handle” to the data. In this chapter, we consider libraries which do not use unique identifiers. Commands will specify the data they update by analysing large parts of the entire structure. Even though this analysis seems global, such libraries can still benefit from local reasoning techniques, as the *updates* are still focused on sub-data. This chapter introduces techniques for *weakening* the locality provided by structural separation logic, allowing the reasoning to use some global information about data whilst still manipulating structures in a local fashion.

One common example is tree libraries which use *paths* to identify resources. Such situations are common, one ubiquitous example being the POSIX file systems library

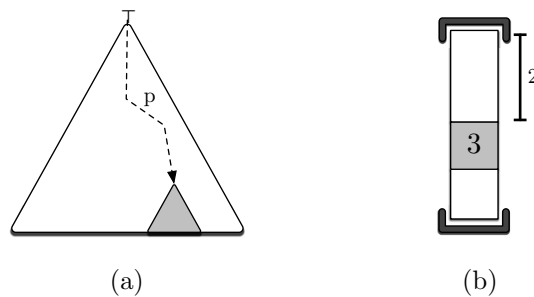


Figure 5.1.: On the left, a sub-tree found by path analysis in the larger tree. On the right, a sub-list found by index within the larger list.

(which we consider in chapter 6). Typically, paths analyse the tree structure from the root, descending until the end of the path, and so identify a sub-tree. Work is then performed on that sub-tree. These updates are, in some sense, *non-local*. The underlying resource needed for their safe execution is quite global, as the path analysis requires all the nodes along the path. However, this global resource is used *only* to identify a position in the structure where update will be performed. Another example is list elements that are accessed by index, similar to the `f.item(i)` command of DOM (chapter 4). Our axiom pre-condition for that command requires the entire list *up to* the element at index `i`. This prefix list is *global* information, not returned by the command, but needed to identify the local resource of interest. The commands use *global properties* to identify *local updates*. Figure 5.1 illustrates the intuitive footprints for two instances of these examples.

Structural separation logic’s abstract allocation operation hides information about the context surrounding sub-data, making axiomatising these commands difficult. Given a sub-data, the only information we can deduce from it are the weak facts guaranteed by the structure of the model. For example, take the assertion  $\alpha \mapsto 2$ , which describes an element 2 within a list of distinct numbers. Because this data is a sub-list, the list model guarantees that it will be *completed* into a machine list. Ergo, we know that the frame must describe lists of the form  $L \mapsto [pl_1 \otimes \alpha \otimes pl_2]$  (albeit possibly split over many abstract heap cells). Moreover, as lists cannot contain duplicate elements, we know  $pl_1$  and  $pl_2$  cannot contain the element 2. This *implicit knowledge* is all we can deduce from the  $\alpha \mapsto 2$  cell alone. It is not, for example, sufficient to determine that the element 2 is at some index  $i$  of the whole list.

This chapter considers extending the implicit knowledge about data with more *explicit knowledge* about the context into which it will fit. Consider figure 5.2 (a). The abstract heap cell  $\mathbf{x}$  contains the partial tree  $1[4 \otimes \mathbf{y}]$ . Moreover, the data stored in the cell it is associated with two *promises*. The *head promise* above it indicates that whatever tree the data ultimately compresses into must place the data at path  $p$ . A *body promise* from the body address  $\mathbf{y}$  states that, whatever sub-data ultimately compresses into  $\mathbf{y}$ , the path  $q$  exists within it. These promises allow the cell  $\mathbf{x}$ , when considered in isolation, to have strong guarantees about the context that will surround it. It cannot *mutate* the information in the promises, as they are read-only information provided by the frame. However, it can use the information in them, knowing that the set of possible frames has been restricted to those which match the shapes of the promises.

Now consider the  $\mathbf{y}$  cell. We can see from the data within cell  $\mathbf{x}$  that, whatever data is stored in cell  $\mathbf{y}$ , it must contain the path  $q$ . Mutations to the data at  $\mathbf{y}$  must not change this fact, or the promise will have been broken. Therefore, when considered in isolation,

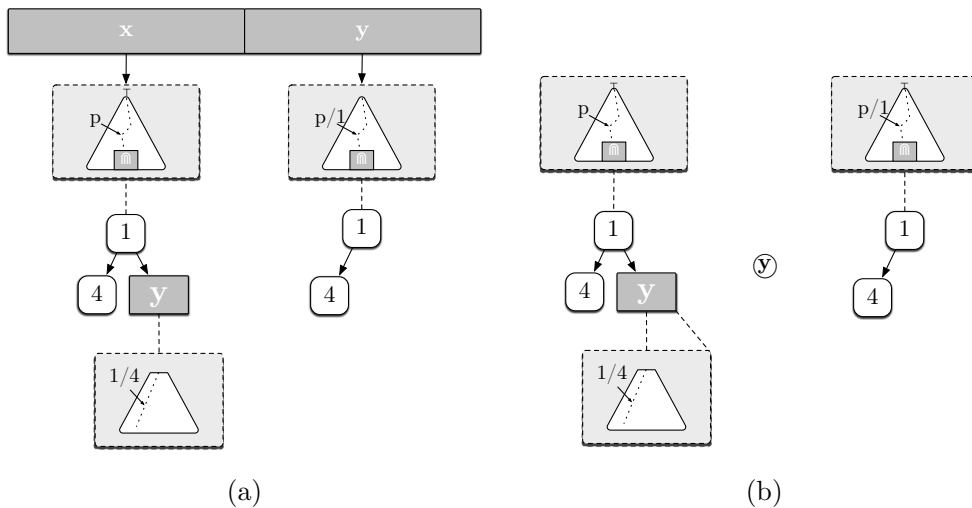


Figure 5.2.: In figure (a), we see an abstract heap holding two abstract cells with promises. The cell at  $x$  has the *head promise* that it will compress into data at path  $p$ , and a *body promise* from  $y$  that the sub-data compressing there will contain path  $1/4$ . The data at  $y$  is this sub-data, which has an *head promise* that it exists at path  $p/1$ . In figure (b), we see a single step of the collapse of this heap; this collapse checks that the sub-data satisfies the promise  $1/4$ , and that the super-data satisfies the promise  $p/1$ . As both hold, the compression is allowed.

the  $y$  cell must still enforce this restriction, or the system will be *unstable*. This chapter only considers *naturally stable* libraries, which have commands that can be axiomatised in a way that never breaks stability. In chapter 7, we will consider specifying libraries that are not naturally stable.

We give two examples of using promises. First, we demonstrate a tree library that identifies data with *linear paths* to show how promises can be used. Then, we adapt the list library of section 3.1.1 with an `item` command that allows list elements to be accessed by index.

### Related work

Two strands of previous work are closely related to promises. The first is *Rely-Guarantee* reasoning [47], [69]. Rely guarantee allows the modelling of interference in concurrent reasoning through *relies* and *guarantees*. Hoare judgements take the form  $R, G \vdash \{P\} C \{Q\}$ . Such a judgement states that  $\{P\} C \{Q\}$  holds if any concurrently executing *environment* program follows the relies  $R$ , and the program  $C$  follows the guarantees  $G$ . The relies and

guarantees are relations on the program state;  $R$  states the possible state updates the environment will do,  $G$  the program.

This work was adapted to *splittable* interference by *Deny-Guarantee* reasoning [26]. Whilst guarantees still state what a thread can do, *denies* state what the environment *cannot* do. Our promises act similarly to guarantees, as they state what the environment cannot do (that is, violate the promises). Careful choice of axioms ensures that deny permissions are unneeded, as no guarantees can be violated. However, in chapter 7, we will see *obligations*, which are conceptually similar to deny permissions.

This chapter is thus perhaps more directly paralleled by *passivity systems*. Passive data, introduced to separation logic in [10], enables reasoning with *read-only* (or *passive*) data. The most commonly known formalism is fractional permissions. Ownership of a fractional heap cell  $x \xrightarrow{0.5} 5$  states that, whatever frame is added on, it cannot have more than half a fractional permission to the variable  $x$ . To ensure stability of the reasoning, no updates that could mutate  $x$  can be performed. A non-unity fraction acts both as a promise (“this heap cell will retain its value”).

Promises extend this passivity to richer structures. They render structured data *passive with respect to sub-data shapes*. That is, certain mutations are allowed, as long as they preserve the shape information that has been shared via promises. Careful choice of axiomatisation ensures that when a promise has been issued, it cannot be violated. In this instance, they carry more information than just a binary “change/don’t change” flag, due to the rich structure of the data.

## 5.1. Libraries with non-local data access

We begin by introducing two example libraries which will benefit from promises. The first, sibling unique trees, is a tree library where sub-trees are identified by paths from the root of the tree. The second, list indexing, extends our simple list library of section 3.1.1 with an *element access via index* command.

### 5.1.1. Sibling unique trees & paths

Our first library using global information to identify local data is *trees with paths*. In this library, sub-trees are identified not by unique handles, but by a *path* of nodes leading from the root of the tree to the sub-tree which will be analysed. We define sibling unique trees, the paths by which nodes in these trees can be identified, tree heaps, and some simple commands over trees.

**Definition 86** (Trees with sibling unique identifiers). The set  $\text{SIBTREES}$ , ranged over by  $st, st_1, \dots, st_n$ , is defined inductively as follows: for all  $i \in \mathbb{N}^+$

$$st ::= i[st] \mid st_1 \otimes st_2 \mid \emptyset$$

where  $\otimes$  is associative and commutative with identity  $\emptyset$ , and each tree contains sibling-unique tree identifiers. Trees are equal up to the properties of  $\otimes$ . We write nodes of the form  $i[\emptyset]$  as  $i$ .

The set of **rooted sibling unique trees**  $\text{SIBROOTEDTREES}$ , ranged over by  $sr, sr_1, \dots, sr_n$ , is defined as the set of trees with exactly one root node:

$$\text{SIBROOTEDTREES} = \{\top[st] \mid st \in \text{SIBTREES}\}$$

Commands manipulating instances of this structure uniquely identify elements by a *path*. A path is a sequence of node identifiers.

**Definition 87** (Tree path). The set of **tree paths**  $\text{TREEPATHS}$ , ranged over by  $pth, pth_1, \dots, pth_n$ , is defined as: for all  $i \in \mathbb{N}^+$

$$pth ::= i \mid \top \mid i/pth \mid \top/pth$$

This definition allows the construction of paths like  $1/\top/2/\top$ . Whilst useless, these paths are harmless, as paths only identify resource by *resolution*. Resolution will never succeed when given such a path.

**Definition 88** (Path resolution). The **path resolution** function  $\text{resolve} : \text{TREEPATHS} \rightarrow \text{SIBTREES} \rightarrow \text{SIBTREES}$  is defined as:

$$\begin{aligned} \text{resolve}(i, t_1 \otimes i[t_2]) &= i[t_2] \\ \text{resolve}(\top, \top[t]) &= \top[t] \\ \text{resolve}(i/pth, t_1 \otimes i[t_2]) &= \text{resolve}(pth, t_2) \\ \text{resolve}(\top/pth, \top[t]) &= \text{resolve}(pth, t) \\ &\text{undefined} \quad \text{in all other cases} \end{aligned}$$

When  $\text{resolve}(pth, t_1) = t_2$  is defined, we say that *pth resolves to  $t_2$  in  $t_1$* .

As an example, take the rooted tree  $\top[2 \otimes 3[1[4 \otimes 5]]]$ . In this tree, the path  $\top/3/1$  resolves to the sub-tree  $1[4 \otimes 5]$ . The path  $\top$  resolves to the entire tree. The path  $\top/6$  does *not* resolve, as root node has no child 6.

The machines associated with these trees are identical to the tree machines of definition 30, except that they store rooted sibling unique trees.

**Definition 89** (Rooted sibling-unique tree heaps). Let  $R \notin \text{PVARs}$  be the **tree heap address**. Then, the set of **rooted sibling-unique tree heaps**  $\text{ROOTEDSIBTREEHEAPS}$  is defined as:

$$\text{ROOTEDSIBTREEHEAPS} \triangleq (\text{PVARs} \xrightarrow{\text{fin}} \text{PVALS}) \sqcup (\{R\} \rightarrow \text{SIBROOTEDTREES})$$

The commands over tree heaps use paths to identify the resource they operate on. Two typical commands are below. We will see many more commands using paths in chapter 6.

1. **createNode**( $e_p, e_n$ ): Creates a new node named with the evaluation of  $e_n$ , as a child of the node found by resolving the path  $e_p$ . If the path does not resolve, or resolves to a node already containing a child named  $e_n$ , the command faults.
2. **removeNode**( $e_p$ ): Removes the tree found by resolving the evaluation of  $e_p$  from the tree. Faults if the evaluation of  $e_p$  does not resolve.

Each command identifies a location within the tree with a path from the root of the tree. To give axioms to these commands, we must demonstrate that the data we are working with is found at a given path. We will use axioms of the form:

$$\begin{aligned} \{e_p \Rightarrow P/N \wedge \alpha^P \mapsto N[T \wedge \text{is\_complete}] * E\} \\ \text{removeNode}(e_p) \\ \{\alpha^P \mapsto \emptyset * E\} \end{aligned}$$

The pre-condition here describes that the path to the node to be removed is  $P/N$ , ergo the  $N$  child of the node found at path  $P$  will be removed. It also describes an abstract heap cell  $\alpha$ , containing  $N$ , and associated with a *path promise*  $P$ . This promise ensures that  $\alpha$  has been de-compressed from under the node at path  $P$ . Any frames that can be applied to this assertion *must not* contradict the fact that  $\alpha$  is found at path  $P$  within the overall tree.

Such axioms split data into two parts: the normal *local data*, which will be used for update, and *global data* represented by promises, which is only used to place the local data in context. This global data will be shared with other heap cells, and may be *updated*



by other commands. For the promises associated with some data to make sense, we must ensure that they cannot be broken by updates. This is a *stability* property.

For the tree path promises of this example, this will be straightforward. Tree path promises are *naturally stable* with respect to the tree commands we give. Naturally stable promises are those which can never be invalidated by the commands of a library, thanks to a careful choice of axioms. With the two commands of this library, the only operation that could invalidate a path promise would be the removal of a sub-tree. We will axiomatise `removeNode` in a manner that ensures stability by checking that no abstract addresses are found beneath the node being removed. As promises are associated with structural addresses, if there are no body addresses in the sub-tree, no promises can be broken. Natural stability also applies to far more complex command sets. In chapter 6, we will show that a large subset of the POSIX file system library can be axiomatised in a naturally stable manner.

### 5.1.2. Lists access via indexing

We now extend the list library of section 3.1.1 with element access via *index*. We add a command much like the `j := f.item(e)` command of DOM:

`j := item(e)`: If  $e$  evaluates to an integer  $i$ , this command returns the element at index  $i$  within the list, faulting if either  $e$  does not evaluate to an integer, or the list has no item at index  $i$ .

Adding this command requires no changes to the underlying data or list machines. As with DOM, one natural choice of axiom is:

$$\begin{aligned} & \{ \alpha \mapsto [\mathbf{B} \otimes \mathbf{J} \otimes \beta] * \mathbf{j} \rightarrow - * \mathbf{E} \wedge e \Rightarrow \mathbf{I} \wedge |\mathbf{B}| = \mathbf{I} \} \\ & \quad \mathbf{j} := \text{item}(e) \\ & \{ \alpha \mapsto [\mathbf{B} \otimes \mathbf{J} \otimes \beta] * \mathbf{j} \rightarrow \mathbf{J} * \mathbf{E} \} \end{aligned}$$

Considered in the light of promises, this axiom is unnecessarily large. The only fact needed by the command is that element  $\mathbf{J}$  is at index  $\mathbf{I}$ . The partial list  $\mathbf{B}$  in the axiom is used only to demonstrate that fact. Consider instead a promise-bearing cell  $\alpha^{\mathfrak{m} \leftarrow i} \mapsto \phi$ . This describes an abstract heap cell  $\alpha$  containing data associated with an *index promise*  $\mathfrak{m} \leftarrow i$ . The index promise states that the data has been allocated from some super-data list (indicated by symbol  $\mathfrak{m}$ ) at index  $i$ . With this promise, the command can be axiomatised as:

$$\{ \alpha^{\mathfrak{m} \leftarrow i} \mapsto \mathbf{J} * \mathbf{j} \rightarrow - * \mathbf{E} \wedge e \Rightarrow \mathbf{I} \} \quad \mathbf{j} := \text{item}(e) \quad \{ \alpha^{\mathfrak{m} \leftarrow i} \mapsto \mathbf{J} * \mathbf{j} \rightarrow \mathbf{J} * \mathbf{E} \}$$

This is a smaller axiom. It uses  $\mathfrak{m} \leftarrow \mathfrak{l}$  to describe that the node  $\mathfrak{J}$  has  $\mathfrak{l}$  left neighbours, but makes no statement about the contents of the neighbours. Critically, the axiom does not *own* those resources. It only “owns” the fact that they exist and that their length will not vary. The actual cells to the left remain free to be owned by another heap cell, as long as the number of them does not change. Again, the promise restricts the set of frames to ensure that  $\alpha$  is only compressed into data that satisfies the promises.

Unfortunately, assertions using the promise  $\mathfrak{m} \leftarrow \mathfrak{l}$  are *not* stable when considered alongside the `remove` list command. Consider the assertion:

$$L \mapsto [3 \otimes 1 \otimes 4 \otimes \alpha] * \alpha^{\mathfrak{m} \leftarrow 3} \mapsto 2$$

This describes valid heaps,  $\alpha$  is at index 3 of list  $L$ . However, we can apply the frame rule to set aside  $\alpha^{\mathfrak{m} \leftarrow 3} \mapsto 2$ . This leaves no information that would prohibit us from removing, for example, element 3 from the list. Once the  $\alpha$  cell is framed on again, it notices that its promise is broken. More concerning, it has been broken at *some point*, and when that was is unknown. The  $\alpha$  cell may have used that promise whilst it was not actually true.

**Comment 10.** In general, it is actually worse than this thanks to the “*ABA*” problem commonly encountered in concurrent reasoning. For example, if the library had a command to add elements at arbitrary indexes, it may be that whilst  $\alpha$  is allocated, the element 3 is removed, but then *restored* (it moves from state A to B to A again, hence the name of the problem). During the time it is in the B state,  $\alpha$  still believes it is the third item of  $L$ , when it is not, but a check at compression time would still pass.

There is a strong relationship between the commands of a library, and the ability to create local axioms with naturally stable promises. Some pairings of commands, such as `remove` and `item`, have a tension between their ideal local axioms. In creating a local `item` specification, we have broken the local `remove` specification. We can restore correct behaviour by reducing the locality of either axiom: either `item` must be specified in terms of the entire list prefix that proves the index of an element, or `remove` must be specified in terms of the list prefix, to ensure it is not breaking the promises associated with an abstract heap cell.

In this chapter, we avoid this problem by restricting `remove` to only the *last* element of a list, essentially making it a random access stack. With this change, there are no commands that can break the index promises. To restore full locality to both commands,

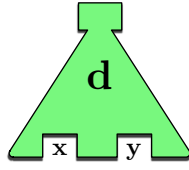


Figure 5.3.: A single datum  $d \in \text{DATA}$ , for some  $\text{DATA}$  of a structural separation algebra.

chapter 7 adds *obligations* to the lists.

## 5.2. Promises

Promises are added to *data* to provide global *context* to each datum. They extend some choice of structural separation algebra, say  $(\text{STRUCTADDRS}, \text{DATA}, \text{addr}, \text{comp})$ . Take some  $d \in \text{DATA}$ , and assume it is *incomplete*, so that (for example) it can be composed into some super-data, and has two body addresses  $\mathbf{x}$  and  $\mathbf{y}$  into which sub-data can be composed. In figure 5.3, we see such a datum.

Such an incomplete datum can be *completed*. In structural separation logic, this happens as part of the completion process (described in section 3.4). To guide this process, we can augment data with the *shapes* of the data which will complete it. Call a super-data into which  $d$  collapses a *head* for  $d$ , and sub-data which collapse into  $\mathbf{x}$  and  $\mathbf{y}$  *bodies* for those addresses. Head and body data must *complete*  $d$ , so that each head data has exactly one hole, and each body data has no holes. We do not know, a-priori, the body address in the super-data into which  $d$  will collapse. Therefore, we will use a *placeholder* head address,  $\mathfrak{m}$ . In figure 5.4, we see such a head datum and two body data.

This single head datum and pair of body data describes only one possibility for the context surrounding  $d$ . We describe the *shapes* of the contexts by gathering head and body data into sets. These sets represent the possible heads and bodies that extend  $d$  to be complete. So that we know which represents the head data, and which represent the body data, we *name* each set with the address to which it pertains. We use the head address  $\mathfrak{m}$  to name the head data, and the associated body address for each set of body data. These named sets are the *promises* associated with  $d$ , as seen in figure 5.5.

We call data paired with promises *promise-carrying data*. It is a new form of data, derived from some structural separation algebra and choice of promises. This new data will form a structural addressing algebra of its own (albeit with some minor differences, which we describe in section A.3). As such, it requires an addresses function and notion of

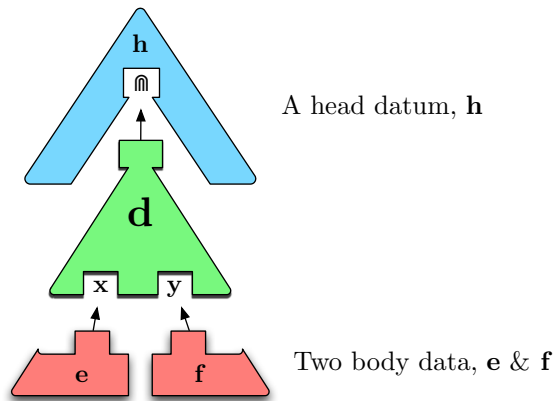


Figure 5.4.: The data  $d$  can be extended by a super-data and two sub-data to be *complete*. We call these types of data *head* and *body* data respectively.

*compression*. The addresses function is straightforward, simply considering the addresses of the data that is paired with the promises.

Compression is more interesting. It requires that we *check* the promises are true. Consider figure 5.6, which shows the compression between the promise-carrying data ( $d, \{\lceil : p_1, \mathbf{x} : p_2, \mathbf{y} : p_3\}$ ) and sub-data ( $f, \{\lceil : p_4\}$ ), using the address  $\mathbf{y}$ , where each  $p_i$  represents the set of data associated with each promise. The super-data  $d$  has a body promise  $\mathbf{y} : p_3$  for the  $\mathbf{y}$  address. The sub-data is carrying a head promise  $\lceil : p_4$ , describing the complete super-data that will fit over it. It has no body addresses, and so no body promises.

Compression must do three things:

1. Check the body promise for  $\mathbf{y}$  associated with  $d$ ,  $\mathbf{y} : p_3$ , is satisfied by the sub-data  $f$ .
2. Check that the head promise associated with  $f$ ,  $\lceil : p_4$ , is satisfied by the super-data  $d$ .
3. Compress the underlying data and remove redundant promises.

Checking that the promises are satisfied involves comparing the data  $d$  and  $f$  with the appropriate promise. In this instance, checking that  $f$  satisfies the promises associated with the  $\mathbf{y}$  hole in  $d$  is straightforward. Because  $f$  is already complete, we need only ensure that  $f$  is in  $p_3$ . Checking that  $d$  satisfies the head promise associated with  $f$  is harder, as  $d$  has more than just the  $\mathbf{y}$  body address, and will still be extended by some

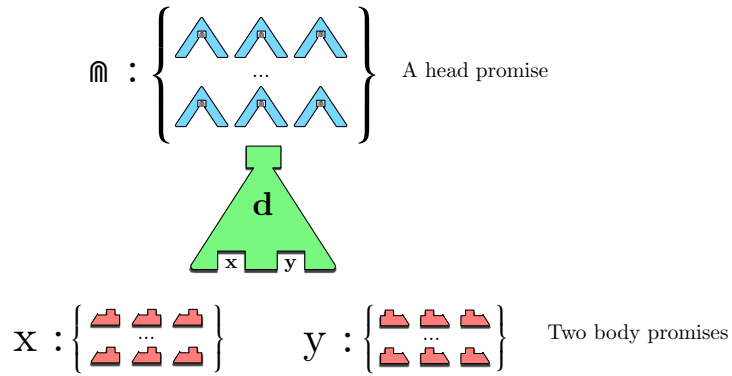


Figure 5.5.: By collecting head data and body data into sets and associating these sets with *names*, we can describe the shape of the context for  $d$ . These named sets are *promises*.

super-data. However, it can be *considered complete* by way of its promises. To check that  $d$  satisfies the head promise  $p_4$ , we use *closure with respect to y*. Closure uses promises to *fill in* missing data. It is with respect to a specific body address, into which the address  $\mathfrak{m}$  will be placed. This ensures that the closures matches the shapes used for head promises. The completion here is illustrated in figure 5.7. The result of this closure can be used to check the promises  $p_4$  hold by standard subset inclusion.

Had we been composing  $d$  into some super-data we would have used *body closure* to complete it, thus allowing the promises associated with the super-data to be checked. As normal closure is used to check head promises, body closure is used to check body

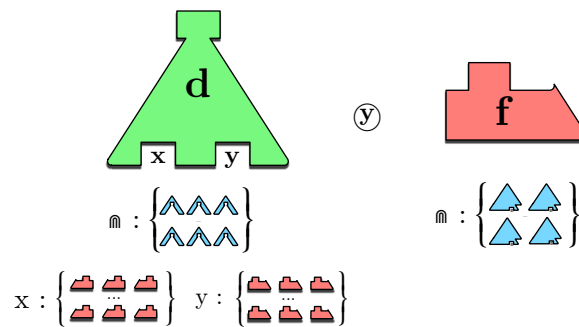


Figure 5.6.: The compression of promise-carrying data  $d$  with sub-data  $f$  using address  $y$ .

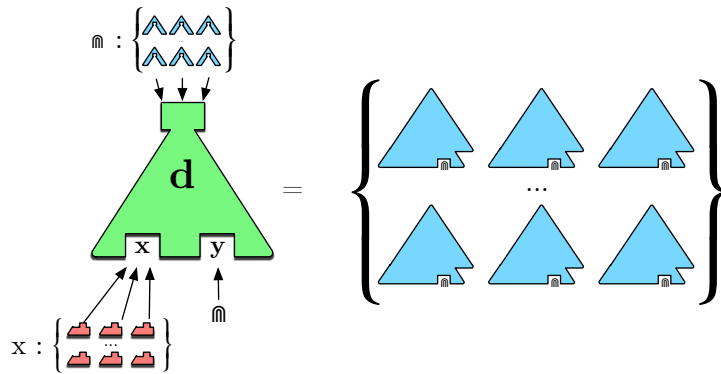


Figure 5.7.: The closure of a promise-carrying data  $d$  with respect to  $y$ . Each head data in the head head promise, and body data int

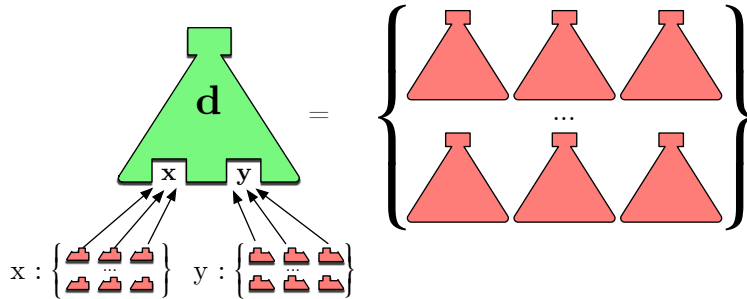


Figure 5.8.: The body closure of a promise-carrying data fills each body address using the promises associated with them.

promises. These have no addresses, so this closure fills all addresses, as shown in figure 5.8.

If the promises pass the checks, so that  $f$  is within  $p_3$ , and  $p_4$  is a subset of the closure of  $d$  with respect to  $y$ , we can perform the compression. The underlying data is compressed using standard data compression. The promises are *merged*, with the head promise for  $f$  discarded (as it now has a head), and the body promise associated with  $y$  discarded (as the address no longer exists). The result is  $(d \circledast y) f, \{\hat{m} : p_1, \mathbf{x} : p_2\}$ .

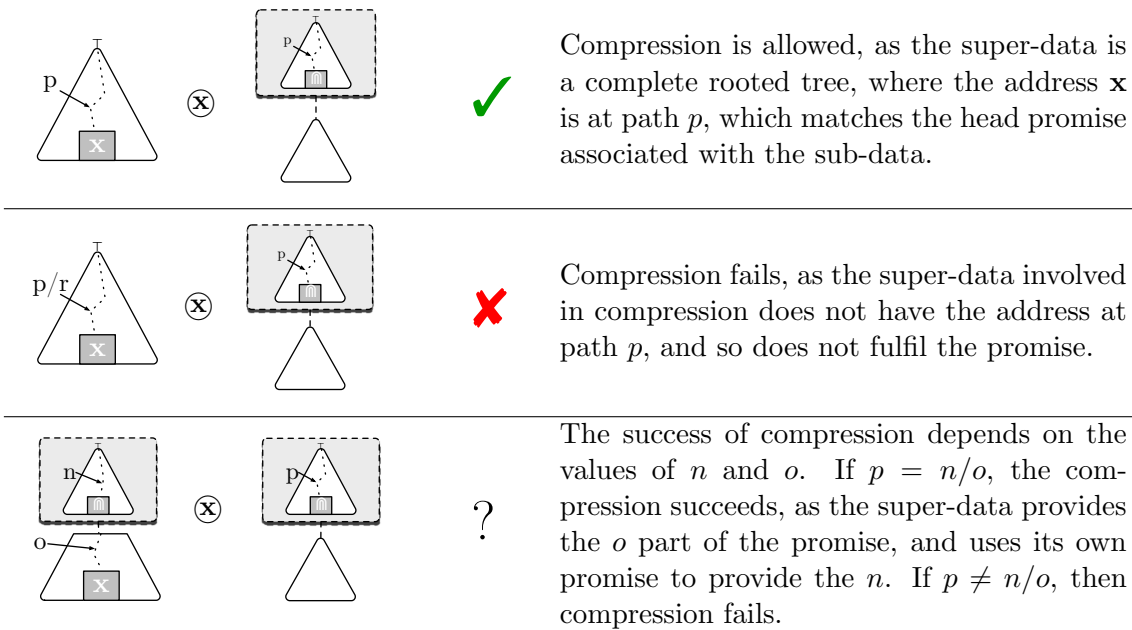



Figure 5.9.: Examples of promises for linear paths. The promise  indicates that the data is at path  $p$  in the tree.

### Path promises for sibling-unique trees

As an example of promise-carrying data, consider figure 5.9. This considers the *path promises* for the trees with paths library. It shows the combinations of promises that allow or disallow compression in a variety of cases.

#### 5.2.1. Formalising promises

We now formalise the intuitions of promises given in the last section. Promises are added to a choice of structural addressing algebra. We will require a method for identifying the single body addressed used in head data. As this address is, in essence, a placeholder for the real address which will be used, we nominate a *placeholder address* from the data. We remove this from the structural addressing algebra to ensure it is not used in describing data. This creates two algebras: The original *full* algebra which will describe promises, and the *underlying* algebra, which will describe data (and cannot use the head address).

**Definition 90** (Underlying data algebra). Let  $(\text{STRUCTADDRS}, \text{DATA}, \text{addr}, \text{comp})$  be a structural separation algebra where  $\mathfrak{m} \in \text{STRUCTADDRS}$ . The derived **underly-**

**ing data algebra** is defined as:

$$(\text{STRUCTADDRS}_{\mathfrak{m}}, \text{DATA}_{\mathfrak{m}}, \text{addrs}_{\mathfrak{m}}, \text{comp}_{\mathfrak{m}})$$

where  $\mathfrak{m}$  is distinguished as the **head address**, and

1.  $\text{STRUCTADDRS}_{\mathfrak{m}} = \text{STRUCTADDRS} \setminus \{\mathfrak{m}\}$
2.  $\text{DATA}_{\mathfrak{m}} = \{d \in \text{DATA} \mid \mathfrak{m} \notin \text{addrs}(d)\}$
3.  $\text{addrs}_{\mathfrak{m}} = \text{addrs}$
4.  $\text{comp}_{\mathfrak{m}} = \text{comp}$

Notice that the underlying data algebra will remain a structural separation algebra, as we have only disabled one address out of a countable infinity. The arbitrary addresses property of the algebra will ensure that any use of it can be replaced by another of the addresses.

We now define a set of *head data*, which are all those sets of data with elements that have only a single body address  $\mathfrak{m}$ . These head data will describe the *shape* of possible head promises. The *vacuous head data* which describes *every possible shape* of super-data that some data can compress into. It will be used to stand in for a missing promise, as it tells data no more than it already knew implicitly.

**Definition 91** (Head data). Given a structural addressing algebra  $(\text{STRUCTADDRS}, \text{DATA}, \text{addrs}, \text{comp})$  and underlying data algebra using head address  $\mathfrak{m}$ , the set of **head data**  $\text{HEADDATA}$ , ranged over by  $\bar{d}, \bar{d}_1, \dots, \bar{d}_n$ , is defined as:

$$\text{HEADDATA} = \{\bar{d} \in \mathcal{P}(\text{DATA}) \mid \forall d \in \bar{d}. \text{addrs}(d) = \{\mathfrak{m}\}\}$$

The **vacuous head data**  $\text{VACHEADDATA} \in \text{HEADDATA}$  is defined as:

$$\text{VACHEADDATA} = \{d \in \text{DATA} \mid \text{addrs}(d) = \{\mathfrak{m}\}\}$$

The set of *head promises* are the pairs of the head address and head data. Here, the head address acts as the *name* for the promise. With respect to some data  $d$ , a head promise  $(\mathfrak{m}, \bar{d})$  is read as “*The data  $d$  is promised that the head eventually fitting over it is contained within  $\bar{d}$* ”. Notice this does *not* say “All data within a head promise fit over the data”. Promises are conservative approximations of the surrounding context of data.



**Parameter 19** (Head promises). Given the set of head data HEADDATA (definition 91), assume a set of **head promises** HEADPROMS, ranged over by  $\pi^H, \dots$ , with the type:

$$\text{HEADPROMS} \subseteq \{\mathfrak{m}\} \times \text{HEADDATA}$$

We now turn to body promises. Body promises are similar to head promises, but describe the shape of the *sub-data* that fit within a datum. These shapes are again described by sets of data, this time called *body data*. To ensure that body promises result in complete data, body data they must contain no addresses. We again describe a vacuous body data, used to stand in for missing body promises.

**Definition 92** (Body data). Given a structural addressing algebra (STRUCTADDRS, DATA, *addrs*, *comp*) and underlying data algebra using head address  $\mathfrak{m}$ , the set of **body data** BODYDATA, ranged over by  $\underline{d}, \underline{d}_1, \dots, \underline{d}_n$ , is defined as:

$$\text{BODYDATA} = \{\underline{d} \in \mathcal{P}(\text{DATA}) \mid \forall d \in \underline{d}. \text{addrs}(d) = \emptyset\}$$

The **vacuous body data** VACBODYDATA  $\in$  BODYDATA is defined as:

$$\text{VACBODYDATA} = \{d \in \text{DATA} \mid \text{addrs}(d) = \emptyset\}$$

Body promises are pairs of structural addresses and body data. With respect to some data  $d$ , a body promise  $(\mathbf{x}, \underline{d})$  is read “The body that fits into address  $\mathbf{x}$  in  $d$  is contained within  $\underline{d}$ ”.

**Parameter 20** (Body promises). Given the set of body data BODYDATA (definition 92), assume a set of body promises BODYPROMS, ranged over by  $\pi^B$ , with the type:

$$\text{BODYPROMS} \subseteq \text{STRUCTADDRS} \times \text{BODYDATA}$$

Given data  $d$  and a set of body promises, we can perform *body promise closure* of the data. This compresses data into every address within  $d$ . If there is a body promise associated with that address, it uses the promised data. If there is no body promise for that address, it uses the vacuous body data. Body promise closure is used during

promise-carrying data compression to check that promises are met.

**Definition 93** (Body promise closure of data). Given a structural addressing algebra  $(\text{STRUCTADDRS}, \text{DATA}, \text{addrs}, \text{comp})$ , associated underlying data algebra using head address  $\mathfrak{m}$ , and body promises  $\text{BODYPROMS}$  (parameter 20), the **body promise closure** function  $\downarrow: \text{DATA}_{\mathfrak{m}} \rightarrow \mathcal{P}(\text{BODYPROMS}) \rightarrow \mathcal{P}(\text{DATA})$  is defined as:

$$\downarrow(d, \Pi^B) = \left\{ d_{(\mathbf{x}_1)} d_1_{(\mathbf{x}_2)} \cdots d_n_{(\mathbf{x}_n)} \left| \begin{array}{l} \text{addrs}(d) = \{\mathbf{x}_1, \dots, \mathbf{x}_n\}, i \in \{1, \dots, n\} \\ \exists \text{unique } \underline{d}_i. (\mathbf{x}_i, \underline{d}_i) \in \Pi^B \implies d_i \in \underline{d}_i, \\ \nexists \underline{d}_i. (\mathbf{x}_i, \underline{d}_i) \in \Pi^B \implies d_i \in \text{VACBODYDATA} \end{array} \right. \right\}$$

This function is well-defined by the quasi-commutativity of compression on the underlying data.

By combining head and body promises, we obtain a general set of *promises*. From this, we draw *promise sets*, which are subsets containing head and body promises, where each promise name is used at most once. When clear, we will call these promise sets call simply “promises”.

**Definition 94** (Promises). Let  $\text{HEADDATA} \cup \text{BODYDATA}$  be ranged over by  $\bar{d}, \bar{d}_1, \dots, \bar{d}_n$ . Then, set of **promise sets**, ranged over by  $\Pi, \Pi_1, \dots, \Pi_n$ , is defined as:

$$\text{PROMISES} = \left\{ \Pi \in \mathcal{P} \left( \begin{array}{c} \text{HEADPROMS} \\ \cup \\ \text{BODYPROMS} \end{array} \right) \left| \begin{array}{l} \nexists \mathbf{x} \in \text{STRUCTADDRS}. \\ \exists \bar{d}_1, \bar{d}_2. \{(\mathbf{x}, \bar{d}_1), (\mathbf{x}, \bar{d}_2)\} \subseteq \Pi \end{array} \right. \right\}$$

Let  $\Pi \in \text{PROMISES}$ . We write the removal of the promise from any address  $\mathbf{x} \in \text{STRUCTADDRS}$  from  $\Pi$  as:

$$\Pi - \mathbf{x} \triangleq \{(\mathbf{y}, \bar{d}) \mid (\mathbf{y}, \bar{d}) \in \Pi, \mathbf{y} \neq \mathbf{x}\}$$

Promise sets are associated with data drawn from the underlying data algebra, and provide context for each datum. We call the pairing of data and a set of promises *promise-carrying data*. Promises are optional, so that the promises paired with a datum need not have a promise associated with the head address, or any given body address. However,

every promise must be useful, so must be either a head promise, or a body promise named for an address within the datum.

**Definition 95** (Promise-carrying data). The set of **promise-carrying data** PROMDATA, ranged over by  $\mathbf{pd}, \mathbf{pd}_1, \dots, \mathbf{pd}_n$  is defined as:

$$\text{PROMDATA} \subseteq \text{DATA}_{\mathfrak{m}} \times \text{PROMISES}$$

where every promise is either a head promise, or a promise from some address in the data: for all  $(d, \Pi) \in \text{PROMDATA}$

$$\forall(\mathbf{x}, \bar{d}) \in \Pi. \mathbf{x} = \mathfrak{m} \text{ or } \mathbf{x} \in \text{addrs}(d)$$

When defining compression, we will need to check if data satisfies promises. When checking body promises, we will use the body closure of definition 93. This body closure uses promises to fill in body addresses. It produces results with the same type as body promises: sets of complete data. When checking head promises, we will use *general closure with respect to an address*. General closure uses promises to complete data, resulting in sets matching the type of head data: complete heads, with a single body address  $\mathfrak{m}$ . We achieve general closure with respect to  $\mathbf{x}$  by first using body closure on the body, whilst ensuring it fills the address  $\mathbf{x}$  with  $\mathfrak{m}$ . We then fit each head promise over the result.

**Definition 96** (General closure of promise-carrying data). Given a promise-carrying datum  $\mathbf{pd} \in \text{PROMDATA}$ , the **closure of  $\mathbf{pd}$  with respect to  $\mathbf{x}$**   $\circlearrowleft_{\mathbf{x}}(\cdot)$  :  $\text{STRUCTADDRS} \rightarrow \text{PROMDATA} \rightarrow \mathcal{P}(\text{DATA})$  is defined as:

$$\circlearrowleft_{\mathbf{x}}((d, \Pi)) = \bigcup \left\{ \bar{d} \circlearrowleft_{\mathfrak{m}} d \mid \begin{array}{l} \bar{d} \in \pi^H, \\ d \in \downarrow(d, (\Pi - \mathfrak{m} - \mathbf{x}) \cup \{(\mathbf{x}, \{\mathfrak{m}\})\}) \end{array} \right\}$$

where  $\pi^H = \pi^{H'}$  if  $\exists \pi^{H'}. (\mathfrak{m}, \pi^{H'}) \in \Pi$ , and  $\pi^H = \text{VACHEADDATA}$  otherwise.

This function is well defined via the quasi-commutativity of the underlying data, and the address irrelevance property of the data.

We can now define compression between promise-carrying data,  $\mathbf{pd}_1 \otimes \mathbf{pd}_2$ . Compression not only compresses the underlying data, but *checks promises*.

**Definition 97** (Compression for promise-carrying data). The **compression function** for promise-carrying data,  $\text{comp}_P : \text{STRUCTADDRS} \rightarrow \text{PROMDATA} \rightarrow \text{PROMDATA} \rightarrow \text{PROMDATA}$  is defined as: for all  $\mathbf{pd}_1 = (d_1, \Pi_1)$  and  $\mathbf{pd}_2 = (d_2, \Pi_2)$

$$\text{comp}_P(\mathbf{x}, \mathbf{pd}_1, \mathbf{pd}_2) = (\text{comp}_{\mathfrak{m}}(\mathbf{x}, d_1, d_2), (\Pi_1 - \mathbf{x}) \cup (\Pi_2 - \mathfrak{m}))$$

where:

1. The underlying data compression is defined:  $\text{comp}_{\mathfrak{m}}(\mathbf{x}, d_1, d_2)$  defined.
2. Any body promise made to  $\mathbf{pd}_1$  by  $\mathbf{pd}_2$  is satisfied. If  $(\mathbf{x}, \underline{d}) \in \Pi_1$ , then  $\downarrow (d_2, \Pi_2) \subseteq \underline{d}$ .
3. Any head promise made to  $\mathbf{pd}_2$  by  $\mathbf{pd}_1$  is satisfied. If  $(\mathfrak{m}, \bar{d}) \in \Pi_2$ , then  $\bar{d} \subseteq \cup_{\mathbf{x}} (\mathbf{pd}_1)$

### 5.2.2. Abstract heaps using promise-carrying data

We now have the components to build *promise-carrying data algebras*. These algebras are the analogy of structural addressing algebras (definition 42), but are built with promise-carrying data.

**Definition 98** (Promise-carrying data algebra). Let  $(\text{STRUCTADDRS}, \text{DATA}, \text{addr}, \text{comp})$  be some structural addressing algebra (definition 42). Let  $(\text{STRUCTADDRS}_{\mathfrak{m}}, \text{DATA}_{\mathfrak{m}}, \text{addr}_{\mathfrak{m}}, \text{comp}_{\mathfrak{m}})$  be the associated underlying data algebra (definition 90). Given a choice of promise-carrying data defined using these previous two algebras  $\text{PROMDATA}$ , the associated **promise-carrying data algebra** is defined as:

$$(\text{STRUCTADDRS}_P, \text{PROMDATA}, \text{addr}_P, \text{comp}_P)$$

where

1. The structural addresses are those of the underlying data:  $\text{STRUCTADDRS}_P = \text{STRUCTADDRS}_{\mathfrak{m}}$ .
2. The addresses function is the addresses function of the underlying data, applied to the underlying data component of a promise-carrying data:  $\text{addr}(\mathbf{pd}) = \text{addr}_{\mathfrak{m}}(\mathbf{pd} \downarrow_1)$ .

3. The compression function  $\text{comp}_P$  is that defined on  $\text{PROMDATA}$ , given in definition 97.

When unambiguous,  $\text{comp}_P(\mathbf{x}, \mathbf{pd}_1, \mathbf{pd}_2)$  is written  $\mathbf{pd}_1 \otimes \mathbf{pd}_2$ , and  $\text{addr}_P$  is written  $\text{addr}$ .

It is important to remember that we are dealing with *data*. These richer data are formed atop of some structural addressing algebra  $(\text{STRUCTADDRS}, \text{DATA}, \text{addr}, \text{comp})$  (definition 42), and so have their own notions of compression and addresses. As we did with promise-carrying data, we now define an algebra for promise-carrying data.

**Definition 99** (Promise-carrying data algebra). Let  $(\text{STRUCTADDRS}, \text{DATA}, \text{addr}, \text{comp})$  be some structural addressing algebra (definition 42). Let  $(\text{STRUCTADDRS}_{\mathfrak{m}}, \text{DATA}_{\mathfrak{m}}, \text{addr}_{\mathfrak{m}}, \text{comp}_{\mathfrak{m}})$  be the associated underlying data algebra (definition 90). Let  $\text{PROMDATA}$  be the set of promise-carrying data defined using these previous two algebras. Then, the associated *promise-carrying data algebra* is defined as:

$$(\text{STRUCTADDRS}_P, \text{PROMDATA}, \text{addr}_P, \text{comp}_P)$$

where

1. The structural addresses are those of the underlying data:  $\text{STRUCTADDRS}_P = \text{STRUCTADDRS}_{\mathfrak{m}}$ .
2. The addresses function is the addresses function of the underlying data, applied to the underlying data component of a promise-carrying data  $\text{addr}_P(\mathbf{pd}) = \text{addr}_{\mathfrak{m}}(\mathbf{pd} \downarrow_1)$ .
3. The compression function  $\text{comp}_P$  is that defined on  $\text{PROMDATA}$ , given in definition 97.

When unambiguous,  $\text{comp}_P(\mathbf{x}, \mathbf{pd}_1, \mathbf{pd}_2)$  is written  $\mathbf{pd}_1 \otimes \mathbf{pd}_2$ , and  $\text{addr}_P$  is written  $\text{addr}$ . The data of the underlying algebra are equivalent to data with no promises  $d \in \text{DATA}_{\mathfrak{m}} = (d, \emptyset, \emptyset)$ .

This algebra inherits many useful properties from the underlying structural addressing algebra. However, we must first ensure that the compression function preserves the well-formedness of the richer data we are using. For the lemmas, assume a structural ad-

dressing algebra  $(\text{STRUCTADDRS}, \text{DATA}, \text{addr}, \text{comp})$  for the promises, and the associated underlying data algebra  $((\text{STRUCTADDRS}_P, \text{PROMDATA}, \text{addr}_P, \text{comp}_P)$ .

**Lemma 18** (Compression is closed on PROMDATA). *Let  $\mathbf{pd}_1, \mathbf{pd}_2 \in \text{PROMDATA}$ ,  $\mathbf{x} \in \text{STRUCTADDRS}$  and  $\mathbf{pd}_1 \otimes \mathbf{pd}_2$  be defined. Then,  $\mathbf{pd}_1 \otimes \mathbf{pd}_2 \in \text{PROMDATA}$ .*

*Proof.* Assume  $\mathbf{pd}_1 = (d_1, \Pi_1)$  and  $\mathbf{pd}_2 = (d_2, \Pi_2)$ . The result of  $\mathbf{pd}_1 \otimes \mathbf{pd}_2$  is:

$$(d_1 \otimes d_2, (\Pi_1 - \mathbf{x}) \cup (\Pi_2 - \mathfrak{m}))$$

As per the definitions of promises and promise-carrying data (definitions 94 and 95 respectively), this will be valid promise-carrying data if: 1) there are no duplicate promise addresses; and 2) every promise is addressed to either  $\mathfrak{m}$  or a body address within the data.

Note first that, by the addressing properties of the underlying structural addressing algebras, the set  $\text{addr}(d_1 \otimes d_2)$  contains the addresses  $\text{addr}((d_1 \setminus \{\mathbf{x}\})) \cup \text{addr}(d_2)$ . Call this the *address containment property*.

1. Proceed by contradiction. Assume an address  $a$  that identifies two promises:

$$\{(a, \bar{d}_1), (a, \bar{d}_2)\} \subseteq (\Pi_1 - \mathbf{x}) \cup (\Pi_2 - \mathfrak{m}).$$

Notice that  $a \neq \mathfrak{m}$ , as by construction  $\mathfrak{m}$  is removed from  $\Pi_2$  in the compression, and  $\Pi_1$  is well-formed. Ergo  $a \in \text{STRUCTADDRS} \setminus \{\mathfrak{m}\}$ . Therefore, by well-formedness of  $\mathbf{pd}_1$  and  $\mathbf{pd}_2$ ,  $a \in \text{addr}_P(\mathbf{pd}_1)$  or  $a \in \text{addr}_P(\mathbf{pd}_2)$ . Assume  $a = \mathbf{x}$ . Then it cannot be duplicated, as  $\mathbf{x}$  is removed from  $\Pi_1$  in the union and  $\Pi_2$  is well-formed. However, it is also the case that  $a \neq \mathbf{x}$ . This follows from the address containment properties, which show that  $\text{addr}(d_1) \cup \text{addr}(d_2) \setminus \{\mathbf{x}\} = \emptyset$ .

2. Proceed by contradiction. Assume there is a promise in  $(a, \bar{d}) \in (\Pi_1 - \mathbf{x}) \cup (\Pi_2 - \mathfrak{m})$  such that  $a \neq \mathfrak{m}$  and  $a \notin \text{addr}(d_1 \otimes d_2)$ . By definition, either  $a \in \Pi_1$  or  $a \in \Pi_2$ . By the well-formedness of  $\mathbf{pd}_1$  and  $\mathbf{pd}_2$ , either  $a \in \text{addr}(\mathbf{pd}_1)$ , or  $a \in \text{addr}(\mathbf{pd}_2)$ . If  $a \neq \mathbf{x}$ , then by the address containment property, it must be that  $a \in \text{addr}(d_1 \otimes d_2)$ , which contradicts its existence.

If  $a = \mathbf{x}$ , then by the construction of  $(\Pi_1 - \mathbf{x}) \cup (\Pi_2 - \mathfrak{m})$ ,  $a$  addresses a promise in  $\Pi_2$ . Ergo,  $a \in \text{addr}(\mathbf{pd}_2)$  by well-formedness. By the address preservation property of structural addressing algebras,  $a \in \text{addr}(d_1 \otimes d_2)$ , which again contradicts its existence.

□

Of the underlying data algebra properties, the following are maintained.

**Lemma 19** (Value containment). *Given DATA that is the underlying structural addressing algebra for the promise-carrying data algebra, and VALUES that is the set of values underlying DATA,  $\text{VALUES} \subseteq \text{DATA}$ .*

*Proof.* Follows from the lift of data without promises being equal to promise-carrying data, and the value containment property of the underlying data.  $\square$

**Lemma 20** (Address properties). *For all  $\mathbf{pd}_1, \mathbf{pd}_2 \in \text{PROMDATA}$  and  $\mathbf{x} \in \text{STRUCTADDRS}$ , if  $\mathbf{pd}_1 \otimes \mathbf{pd}_2$  is defined then:*

1. Containment:  $\mathbf{x} \in \text{addrs}_P(\mathbf{pd}_1)$ .
2. Non-overlap:  $\text{addrs}_P(\mathbf{pd}_1) \cap \text{addrs}_P(\mathbf{pd}_2) \subseteq \{\mathbf{x}\}$
3. Preservation:  $(\text{addrs}_P(\mathbf{pd}_1) \setminus \{\mathbf{x}\}) \cup \text{addrs}_P(\mathbf{pd}_2) = \text{addrs}_P(\mathbf{pd}_1 \otimes \mathbf{pd}_2)$

*Proof.* These follow directly from the addresses function being defined in terms of the underlying addresses function, which is applied to the underlying data.  $\square$

**Lemma 21** (Compression quasi-associates). *Let  $\mathbf{pd}_1, \mathbf{pd}_2, \mathbf{pd}_3 \in \text{PROMDATA}$  and  $\mathbf{x}, \mathbf{y} \in \text{STRUCTADDRS}$ , with  $\mathbf{pd}_1 = (d_1, \Pi_1), \mathbf{pd}_2 = (d_2, \Pi_2), \mathbf{pd}_3 = (d_3, \Pi_3)$ . If  $\mathbf{y} \in \text{addrs}_P(\mathbf{pd}_2)$  and either  $\mathbf{y} \notin \text{addrs}_P(\mathbf{pd}_1)$  or  $\mathbf{y} = \mathbf{x}$ , then*

$$\mathbf{pd}_1 \otimes (\mathbf{pd}_2 \otimes \mathbf{pd}_3) = (\mathbf{pd}_1 \otimes \mathbf{pd}_2) \otimes \mathbf{pd}_3.$$

*Proof.* Assume that  $\mathbf{x} \neq \mathbf{y}$  (the case where  $\mathbf{x} = \mathbf{y}$  is similar). Assume further that the left-hand side,  $\mathbf{pd}_1 \otimes (\mathbf{pd}_2 \otimes \mathbf{pd}_3)$  is defined. Then, it must equal:

$$\left( d_1 \otimes (d_2 \otimes d_3), (\Pi_1 - \mathbf{x}) \cup ((\Pi_2 - \mathbf{y}) \cup (\Pi_3 - \mathfrak{m})) - \mathfrak{m} \right)$$

The removal of a promise from a set is idempotent. Therefore, we have:

$$\left( d_1 \otimes (d_2 \otimes d_3), (\Pi_1 - \mathbf{x}) \cup ((\Pi_2 - \mathbf{y} - \mathfrak{m}) \cup (\Pi_3 - \mathfrak{m})) \right)$$

As  $\mathbf{x} \neq \mathbf{y}$ , there are no promises named  $\mathbf{y}$  in  $\Pi_1$ . Moreover, as standard set union associates, we have:

$$\left( (d_1 \otimes d_2) \otimes d_3, ((\Pi_1 - \mathbf{x}) \cup (\Pi_2 - \mathfrak{m})) - \mathbf{y} \cup (\Pi_3 - \mathfrak{m}) \right)$$

which is equal to  $(\mathbf{pd}_1 \otimes \mathbf{pd}_2) \otimes \mathbf{pd}_3$  (data quasi-associativity holds by the definition of structural addressing algebras). Ergo, if the left is defined, the right is defined and equal to it.  $\square$

The proof that compression is quasi-commutative is similar. The underlying data has a simple left identity property, as  $\mathbf{x} \otimes d = d$  always. The addition of promises breaks this simple identity, as data with a head promise will require additionally properties of super-data. However, there are still identities, which can be seen via construction based upon the choice of  $d$ . We show the existence of left identities below; the right identities are demonstrated in a similar manner.

**Lemma 22** (Existence of left identities). *For all  $\mathbf{pd} \in \text{PROMDATA}$  and  $\mathbf{x} \in \text{STRUCTADDRS}$ , there exists an  $\mathbf{pd}_x \in \text{PROMDATA}$  such that  $\mathbf{pd}_x \otimes \mathbf{pd} = \mathbf{pd}$ .*

*Proof.* Assume  $\mathbf{pd} = (d, \Pi)$ . We will construct  $\mathbf{pd}_x = (\mathbf{x}, \Pi_x)$  by picking  $\Pi_x$  based on the contents of  $\Pi$ . Without further information, the compression is:

$$(\mathbf{x} \otimes d, (\Pi_x - \mathbf{x}) \cup (\Pi - \mathfrak{m}))$$

Assume that  $(\mathfrak{m}, \bar{d}) \in \Pi$ . In this case, we pick  $\Pi_x = \{(\mathbf{x}, \underline{d}), (\mathfrak{m}, \bar{d})\}$ . This is well-formed by construction. By calculation, using the identity property of data:

$$\begin{aligned} & (\mathbf{x} \otimes d, (\Pi_x - \mathbf{x}) \cup (\Pi - \mathfrak{m})) \\ = & (d, (\{(\mathbf{x}, \underline{d}), (\mathfrak{m}, \bar{d})\} - \mathbf{x}) \cup (\Pi - \mathfrak{m})) \\ = & (d, \{(\mathfrak{m}, \bar{d})\} \cup (\Pi - \mathfrak{m})) \\ = & (d, \Pi) \end{aligned}$$

as required. The cases where  $\mathbf{pd}$  has no head promise is similar.  $\square$

The identity properties were used by structural addressing algebras to show that the choice of hole is irrelevant. One could always compress another structural address in, so that  $d \otimes \mathbf{y}$  was defined if  $\mathbf{y} \notin \text{addrs}(d)$ . Without the simple identity properties, we cannot prove this directly. However, we can still show that the choice of address does not matter.

**Lemma 23** (Address irrelevance). *For all  $\mathbf{pd}_1, \mathbf{pd}_2 \in \text{PROMDATA}$  and  $\mathbf{x} \in \text{addrs}_P(\mathbf{pd}_1)$ ,  $\mathbf{y} \notin \text{addrs}_P(\mathbf{pd}_2)$ , if  $\mathbf{pd}_1 \otimes \mathbf{pd}_2$  is defined, there exists  $\mathbf{pd}_y$  such that  $\mathbf{pd}_1 \otimes \mathbf{pd}_y \otimes \mathbf{pd}_2 = \mathbf{pd}_1 \otimes \mathbf{pd}_2$ .*

*Proof.* Knowing that  $\mathbf{pd}_1 \otimes \mathbf{pd}_2$ , assume that  $\mathbf{pd}_1 = (d_1, \Pi_1)$  and  $\mathbf{pd}_2 = (d_2, \Pi_2)$ . We will consider the case where  $(\mathbf{x}, \underline{d}_1) \in \Pi_1$  and  $(\mathfrak{m}, \bar{d}_2) \in \Pi_2$ , (ergo,  $d_2 \subseteq \underline{d}_1$ ,  $\bar{d}_1 \subseteq \bar{d}_2$ ).

We construct  $\mathbf{pd}_y$  in a similar fashion to building the identities for data, by giving it the promises that match the expectations of  $\mathbf{pd}_1$  and  $\mathbf{pd}_2$ . Pick  $\mathbf{pd}_y = (\mathbf{y}, \Pi_y)$  where  $\Pi_y = \{(\mathfrak{m}, \bar{d}_1), (\mathbf{y}, \underline{d}_1)\}$ . The result then follows by calculation, as in lemma 35.  $\square$



These properties are sufficient to demonstrate that promise-carrying data algebras are structural addressing algebras. Ergo, they can be used as the underlying method of splitting data for structural separation logic.

**Corollary 1** (Promise-carrying data algebras are structural addressing algebras). *All promise-carrying data algebras are structural addressing algebras.*

*Proof.* By lemmas 18, 31, 20 33 35 and 36, and the definition of structural separation algebras (definition 42).  $\square$

### 5.3. Reasoning with promises

As stated earlier, the abstract allocation relation is not a specific rule that is added to the system, but rather one deduced from the semantic consequence relation. The exact formulation used in chapter 3 no longer holds, as it is not aware of promises. However, a similar rule can be shown to exist for each use of this enriched framework. It creates and destroys promises when needed, and also repartitions those already in existence, to ensure the continued validity of each abstract heap cell. Providing a general form of this rule is not helpful, as each choice of promise will use heap cells with a notation natural to the promises being issued. We have seen examples in both the path and list indexing cases.

The assertions are similar, except that we need a notion for describing the promises associated with data. As a convention, we attach the promise to the *heap cell address* as a superscript:

$$\mathbf{a}^{P_1, P_2, \dots} \mapsto \phi$$

Such an assertion describes a heap cell addressed by  $\mathbf{a}$ , containing promise-carrying data described by  $\phi$ , where the data is associated the promises set containing promises each described by  $P_i$ . If a heap cell has no promises, we omit the superscript.

The reasoning with these abstract heaps proceeds virtually identically to that seen already in this thesis, with command axioms given using heap cells annotated with promises. However, With views  $p, q$  and  $r$  now describing promise-carrying abstract heaps, *stability* must be carefully checked. That is, if a view  $p$  contains heap cells annotated with promises, and a view  $r$  contains the heap cells with data fulfilling these promises, we must ensure that no update is allowed such that  $r$  becomes  $r'$ , and  $r'$  *breaks* the promises. As with all stability, this is ensured by the standard *atomic soundness* property (parameter 12), which states that: for all axioms  $(p, C, q) \in \text{AXIOMS}$ , with command action  $\llbracket C \rrbracket$

$$\forall r \in \text{VIEWS}. \llbracket C \rrbracket [p * r] \subseteq [q * r]$$

When the choice of promises and command axioms means that this above can be proven for every axiom, then we say the promises are *naturally stable*. This is because they required no more machinery to use than normal structural separation logic.

Stability also governs the creation and removal of promises via abstract allocation and deallocation. Recall that abstract allocation is not something *added* to the reasoning system. Rather, it is an result of the choice of reification and *semantic consequence* relation (described in section 3.3.3). Unlike theorem 4, we cannot give a general form of abstract allocation here. This is because the validity of an allocation depends on the choice of promises for the underlying system, and the specific choices made in the allocation. A typical instance takes the form:

$$(\alpha^{P_1, \dots, P_n} \mapsto \phi_1 \textcircled{\beta} \phi_2) \preceq_{\Gamma} \exists \beta. (\alpha^{Q_1, \dots, Q_n, Q_m} \mapsto (\phi_1 \wedge \diamond \beta) * \beta^{R_1, \dots, R_o} \mapsto \phi_2)$$

The  $Q_i$  and  $R_i$  promises will be a combination of *new* promises for the  $\beta$  hole, and those within  $P_i$  which have been distributed between  $\alpha$  and  $\beta$ . Our inability to give a general form of the rule does not affect soundness, as it remains just a use of the sound semantic consequence rule. Uses must always be justified underlying semantic inclusion  $p \preceq q \iff \forall r \in \text{VIEWS}. [p * r] \subseteq [q * r]$ .

### 5.3.1. Axiomatising the library of trees with paths

We now show how promises allow small axioms for our tree library that uses paths. We add structural addresses to the trees to create abstract trees in the standard way, and so create *abstract rooted trees*. We also add the promise head address to paths, creating *abstract paths*.

**Definition 100** (Abstract rooted trees and paths). The set of **abstract trees**  $\text{ABSTREES}$ , ranged over by  $\mathbf{t}, \mathbf{t}_1, \dots, \mathbf{t}_n$ , is defined inductively as follows: for all  $i \in \mathbb{N}^+$ ,  $\mathbf{x} \in \text{STRUCTADDRS}$

$$\mathbf{t} ::= i[\mathbf{t}] \mid \mathbf{t}_1 \otimes \mathbf{t}_2 \mid \emptyset \mid \mathbf{x}$$

where  $\otimes$  is associative and commutative with identity  $\emptyset$ , and both node identifiers

and body addresses are unique.

The set of **abstract rooted trees**  $\text{ROOTEDTREES}$ , ranged over by  $rt, rt_1, \dots, rt_n$ , is defined as:

$$\text{ABSTRACTROOTEDTREES} = \{\top[\mathbf{t}] \mid \mathbf{t} \in \text{ABSTREES}\} \cup \text{STRUCTADDRS}$$

The set of **abstract tree paths**  $\text{ABSTREEPATHS}$ , ranged over by  $\mathbf{pth}, \mathbf{pth}_1, \dots, \mathbf{pth}_n$ , is defined as: for all  $i \in \mathbb{N}^+$

$$\mathbf{pth} ::= i \mid \top \mid \mathfrak{m} \mid i/\mathbf{pth} \mid \top/\mathbf{pth}$$

where  $/$  is associative. Abstract tree paths are equal up to the associativity of  $/$ .

We consider a structural addresses to be an abstract rooted tree to ensure that the entire tree can be abstractly allocated if needed. We use abstract trees to create an underlying data algebra using head address  $\mathfrak{m}$ , and define  $\text{HEADDATA}$  and  $\text{BODYDATA}$  as per definitions 90, 91 and 92.

Notice that if an abstract path contains an abstract address, it must be  $\mathfrak{m}$  and it must be at the end. We lift path resolution (definition 88) to abstract trees and abstract paths in the natural way. These paths state how to get directly from the top of the tree to a subtree. They are thus *head promises* only. We define promises that describe all the possible head data that resolve a given path to the head promise address. A promise-carrying data with a path promise will be assured that it will eventually compose into a tree, and be resolved to by the promised path.

**Definition 101** (Path promises for linear paths). The function defining head data where the head promise address  $\mathfrak{m}$  is at the end of path  $p$ ,  $\pi' : \text{PATHS} \rightarrow \text{HEADDATA}$  is defined as:

$$\pi^p = \{\bar{d} \in \text{HEADDATA} \mid \forall d \in \bar{d}. \text{resolve}(p, d) = \mathfrak{m}\}$$

The set of **promises for linear paths** are then defined as:

$$\text{HEADPROMS} = \{(\mathfrak{m}, \pi^p) \mid p \in \text{TREEPATHS}\} \cup \{(\mathfrak{m}, \mathfrak{m})\}$$

There are no body promises, so  $\text{BODYPROMS} = \emptyset$ .

$$\begin{aligned}
& \{ \alpha^P \mapsto M[T \wedge \bar{A}\alpha. \Diamond_F(\alpha) \wedge \bar{A}U, V. (U \otimes N \otimes V)] * E \wedge (e_p \Rightarrow P/M) \vee (e_p \Rightarrow M \wedge P = \bar{m} \wedge M = R) \} \\
& \quad \text{createNode}(e_p, e_n) \\
& \quad \{ \alpha^P \mapsto M[T \otimes N[\emptyset]] * E \} \\
& \\
& \{ \alpha^P \mapsto N[T \wedge \textit{is\_complete}] * E \wedge e_p \Rightarrow P/N \} \\
& \quad \text{removeNode}(e_p) \\
& \quad \{ \alpha^P \mapsto \emptyset * E \}
\end{aligned}$$

Figure 5.10.: Axioms for tree commands using paths

The set of head promises encompasses all possible heads that resolve any path  $p$ . The unusual case  $(\bar{m}, \bar{m})$  describes the promise that the data is the entire tree; the only thing that can fit over it is an empty context.

The data-specific assertion language for abstract trees is essentially identical to that for the trees of section 61. We extend logical values with abstract paths. We can now define *promise-carrying heap cell assertions* that use promises to indicate the path at which the body address can ultimately be found.

**Definition 102** (Promise-bearing linear path heap cells). The two promise-bearing linear path heap cells, and their interpretations, are defined as:

$$\begin{aligned}
\langle R \mapsto \phi \rangle^\Gamma &= \{ R \mapsto (d, \emptyset) \} \mid d \in \langle \phi \rangle^\Gamma \\
\langle \alpha^E \mapsto \phi \rangle^\Gamma &= \left\{ \left\{ \begin{array}{l} \mathbf{x} \mapsto (d, \Pi) \\ \emptyset \end{array} \right. \middle| \begin{array}{l} d \in \langle \phi \rangle^\Gamma, p = \langle E \rangle(\Gamma), \\ \Pi = \pi^p \end{array} \right\} \right\} \begin{array}{l} \text{if } \Gamma(\alpha) = \mathbf{x}, \\ \mathbf{x} \in \text{STRUCTADDRES} \\ \text{otherwise} \end{array}
\end{aligned}$$

With these, the axioms for the tree command using paths are given in figure 5.10. They use a simple extension to logical expressions allowing the analysis of paths. Notice that the pre-conditions for the creation commands capture the entire forest being added to. This must happen to ensure the name of node being created does not clash with any existing node name. We do not use *is\_complete* in these cases, but instead the “somewhere at forest level” assertion. The axioms allow body addresses deeper within the sub-tree, as we are *only* checking to ensure the uniqueness of the new sibling’s name.

These axioms are atomically sound with respect to a natural choice for their actions. The proofs follow the same pattern demonstrated before in this thesis. The only interesting

change is the restrictions on possible completions, and additional stability requirements induced by promises. We sketch the case for `removeNode`.

**Lemma 24** (Atomic soundness of `removeNode`). *The command `removeNode(ep)` is atomically sound.*

*Proof.* We must first provide an atomic action for the `removeNode(ep)` command. For convenience, we define this using abstract trees via the  $\mathfrak{m}$  address: for all  $s \in \text{TREEHEAPS}$  and  $e_p \in \text{PATHS}$

$$\llbracket \text{removeNode}(e_p) \rrbracket(s) = \begin{cases} \{s[\mathbf{R} \mapsto \mathbf{t}_1 \mathfrak{m} \emptyset]\} & \text{if } s(\mathbf{R}) = rt, rt = \mathbf{t}_1 \mathfrak{m} \mathbf{t}_2, \text{resolve}(\llbracket e_p \rrbracket(s), \mathbf{t}_1) = \mathfrak{m} \\ \text{undefined} & \text{otherwise} \end{cases}$$

Consider the pre-condition for `removeChild`,  $\alpha^P \mapsto \mathbf{N}[\mathbf{T} \wedge \text{is\_complete}] * E \wedge e_p \Rightarrow \mathbf{P}/\mathbf{N}$ . By the definition of assertion interpretation, this interprets as the view  $\mathbf{x} \mapsto (n[\mathbf{t}], \pi^p) * e$  for some  $\mathbf{x}$ ,  $n$ , and  $\mathbf{t}$  where  $\text{addr}(\mathbf{t}) = \emptyset$ , and path  $p$ , where the evaluation of  $e_p = p/n$ , and  $e$  is arbitrary variable resource. The key use of the promise is to restrict the set of reifications of this resultant view. Because, by definition,  $\mathbf{x}$  will only compress into super-data that place  $\mathbf{x}$  at path  $p$ , and completions added by the reification process will be forced to place  $\mathbf{x}$  at path  $\mathbf{x}$ . Therefore, for all  $r \in \text{VIEWS}$ :

$$\llbracket \mathbf{x} \mapsto (n[\mathbf{t}], \pi^p) * e * r \rrbracket = \{\mathbf{R} \mapsto rt\} \sqcup \text{varHeap}$$

where the sub-tree  $n[\mathbf{t}]$  is found in  $rt$  by resolving path  $e_p$ , and  $\text{varHeap}$  is an arbitrary heap of variables that can evaluate  $e_p$ . This is exactly the safe resource for running the `removeNode(ep)` command. It updates this heap to  $\{\mathbf{R} \mapsto rt'\} \sqcup \text{varHeap}$ , where  $rt'$  is identical to  $rt$  except that the path  $e_p$  now resolves to a directory without an  $\mathbf{N}$  node, the sub-tree it refers to having been removed. By interpreting the post-condition, we see this must always be contained in the reification of  $\alpha^P \mapsto \emptyset * E$ .

The only subtle step in seeing this is that that any choice of  $r$  that composed with pre-condition the will still compose with the post-condition. We must ensure that no  $r$  can have been promised a fact that is no longer true. Fortunately, we have chosen the axiom to ensure that, regardless of the choice of  $r$ , it is stable. The only possible promises invalidated by this command are those regarding paths that encounter  $\mathbf{N}$  during resolution. If such a promise has been issued, the body address it has been issued to must be within the sub-tree at  $e_p$ . However, the axiom ensures that no such body addresses exist (via *is\_complete*), so no promises can have been broken.  $\square$

### 5.3.2. List indexing

We now turn to axiomatising the `item` command for the lists library. We first formalise the promises we will use.

**Definition 103** (Promises for index lists). The **list index head data with respect to  $\mathbf{x}$**  and the **list length body data with respect to  $\mathbf{x}$**  are defined as:

$$\begin{aligned}\Leftarrow i &= \{\bar{d} \mid d \in \bar{d}, d = [pl_1 \otimes \mathbf{x} \otimes pl_2], |pl_1| = i\} \\ \Rightarrow i &= \{\underline{d} \mid d \in \underline{d}, |d| = i\}\end{aligned}$$

The **promises for the list library** are defined as:

$$\text{PROMISES} = \begin{array}{l} \bigcup_{i \in \mathbb{N}} \{(\mathbb{m}, \Leftarrow i)\} \quad \text{List index promises} \\ \cup \bigcup_{i \in \mathbb{N}, \mathbf{x} \in \text{STRUCTADDRS}} \{(\mathbf{x}, \Rightarrow i)\} \quad \text{List length promises} \end{array}$$

Here, we are using more than just head promises. Many promises may accrue as abstract allocation occurs. To ease notation, we define logical expressions for denoting sets of promises.

**Definition 104** (Logical expressions for list promises). The logical expressions for the list library with item command and associated interpretation function are extended with the following expressions:

$$\begin{aligned}\langle \emptyset \rangle(\Gamma) &\triangleq \emptyset \\ \langle A \Leftarrow E \rangle(\Gamma) &\triangleq \begin{cases} \Gamma(A) \Leftarrow \langle E \rangle(\Gamma) & \text{if } \Gamma(A) \in \text{STRUCTADDRS}, \langle E \rangle(\Gamma) \in \mathbb{N} \\ \text{undefined} & \text{otherwise} \end{cases} \\ \langle A \Rightarrow E \rangle(\Gamma) &\triangleq \begin{cases} \Gamma(A) \Rightarrow \langle E \rangle(\Gamma) & \text{if } \Gamma(A) \in \text{STRUCTADDRS}, \langle E \rangle(\Gamma) \in \mathbb{N} \\ \text{undefined} & \text{otherwise} \end{cases} \\ \langle E_1, \dots, E_n \rangle(\Gamma) &\triangleq \{\langle E_1 \rangle(\Gamma), \dots, \langle E_n \rangle(\Gamma)\}\end{aligned}$$

We then define a promise-carrying abstract list heap cell assertion.

**Definition 105** (Promise-carrying heap assertion). The abstract heap assertions of the list library are extended to promise-carrying data with:

$$\langle \mathbb{A}^{E_P} \mapsto \phi \rangle^\Gamma = \left\{ \mathbf{a} \mapsto (d, \Pi) \mid \begin{array}{l} \mathbf{a} = \Gamma(\mathbb{A}), d \in \langle \phi \rangle^\Gamma, \\ \Pi = \langle [E_P] \rangle(\Gamma), \end{array} \right\}$$

The assertion  $\mathbb{A}^\emptyset \mapsto \phi$  is written  $\mathbb{A} \mapsto \phi$ .

The abstract heap construction using these data is similar to the tree case of section 3.3.5. More interesting is the abstract allocation relation. The enriched notion of compression and data means it can now be used to *pick* which promises will be issued at point of allocation. The following are all valid uses, which follows directly from the definitions of compression and abstract heap construction:

$$\begin{aligned} \mathbb{L} \mapsto [1 \otimes 2 \otimes 3] &\rightsquigarrow_\Gamma \quad \exists \alpha. (\mathbb{L} \mapsto [1 \otimes \alpha \otimes 3] * \alpha \mapsto 2) \\ \mathbb{L} \mapsto [1 \otimes 2 \otimes 3] &\rightsquigarrow_\Gamma \quad \exists \alpha. (\mathbb{L}_{\alpha \leftarrow 1} \mapsto [1 \otimes \alpha \otimes 3] * \alpha^{\mathfrak{m} \leftarrow 1} \mapsto 2) \\ &\quad \exists \alpha. (\mathbb{L}_{\alpha \leftarrow 1} \mapsto [1 \otimes \alpha \otimes 3] * \alpha^{\mathfrak{m} \leftarrow 1} \mapsto 2) \\ &\quad \rightsquigarrow_\Gamma \\ &\quad \exists \alpha, \beta. (\mathbb{L}_{\alpha \leftarrow 1}^{\beta \Rightarrow 1} \mapsto [\beta \otimes \alpha \otimes 3] * \alpha^{\mathfrak{m} \leftarrow 1} \mapsto 2 * \beta_{\mathfrak{m} \Rightarrow 1} \mapsto 1) \\ &\quad \exists \alpha, \beta. (\mathbb{L}_{\alpha \leftarrow 1}^{\beta \Rightarrow 1, \alpha \Rightarrow 1} \mapsto [\beta \otimes \alpha \otimes 3] * \alpha_{\mathfrak{m} \Rightarrow 1}^{\mathfrak{m} \leftarrow 1} \mapsto 2 * \beta_{\mathfrak{m} \Rightarrow 1} \mapsto 1) \\ &\quad \rightsquigarrow_\Gamma \\ &\quad \exists \alpha, \beta, \gamma. (\mathbb{L}_{\alpha \leftarrow 1, \gamma \leftarrow 2}^{\beta \Rightarrow 1, \alpha \Rightarrow 1} \mapsto [\beta \otimes \alpha \otimes \gamma] * \alpha_{\mathfrak{m} \Rightarrow 1}^{\mathfrak{m} \leftarrow 1} \mapsto 2 * \beta_{\mathfrak{m} \Rightarrow 1} \mapsto 1 * \gamma^{\mathfrak{m} \leftarrow 2} \mapsto 3) \end{aligned}$$

The axiom for the list indexing command is given below. It uses the index head promise to ensure it is returning the correct element. The length of  $\mathbb{J}$  is checked to ensure we are capturing only a single list element in  $\mathbb{J}$ .

$$\begin{aligned} &\{ \alpha^{\mathfrak{m} \leftarrow 1} \mapsto \mathbb{J} * \mathbb{j} \rightarrow - * E \wedge e \Rightarrow \mathbb{I} \wedge |\mathbb{J}| = 1 \} \\ &\quad \mathbb{j} := \text{item}(e) \\ &\{ \alpha^{\mathfrak{m} \leftarrow 1} \mapsto \mathbb{J} * \mathbb{j} \rightarrow \mathbb{J} * E \} \end{aligned}$$

For ensure these list index promises are naturally stable, we must ensure no command can break them. The only mutative commands, and thus the only commands that could break the promise, are **append** and **remove**. The **append** command evidently cannot break a promise; it only ever extends the *end* of a list, so cannot perturb the index of any body address. However, the **remove** command *could* alter such an index, by removing an element earlier in the list.

To restore natural stability, we simply alter `remove` command to mirror `append`: only the last element of a list can be removed. The new axiom is thus:

$$\begin{array}{c} \{\mathbf{L} \mapsto [\alpha \otimes \mathbf{I}] * E \wedge e \Rightarrow \mathbf{I}\} \\ \text{remove}(e) \\ \{\mathbf{L} \mapsto [\alpha] * E\} \end{array}$$

This is a strong restriction, reducing the list to a stack, and showing the limits of naturally stable promises. In chapter 7, we restore full list behaviour by introducing *obligations*.

## 5.4. Summary

We have demonstrated *promises*, a method for recording additional contextual information about structured data that has been abstractly allocated. Promises allow us to consider more libraries than are possible with pre-existing techniques alone, by contributing:

1. **Smaller axioms for commands:** Promises allow even smaller command axioms than the techniques shown in previous chapters, as seen by the list commands of section 5.3.2. This allows more natural specifications of the behaviours of the commands, and furthers the number of safe concurrent programs that can be proven.
2. **Passive access to structured data:** Promises act as *passive* data; information about the state that cannot be mutated. They thus act similarly to a permission system, but instead of allowing read-only access to the entirety of a datum (thus rendering it immutable), allow *properties* of that datum to be shared instead.
3. **Local reasoning for paths:** Promises allow the assertions demonstrating the *position* of some data in a structure to be handled separately from the data itself. This is most evident with the paths example, section 5.3.1, where the data at a path can be separated from a structure, whilst the nodes along a path need not be. This allows multiple sub-data to be separated at once, and enables true local reasoning for libraries using paths. This is not possible in previous work on context logic.



## 6. POSIX file systems

**Much of the content in this chapter is joint work with Gian Nizk.**

This chapter uses structural separation logic with promises to present a novel axiomatic semantics for the *POSIX file system library*. The primary difficulty with file system reasoning is that most commands access files via *paths*. Rather than accessing resource via a globally unique identifier, these paths are *resolved* with respect to the entire file-system tree. Local reasoning is therefore normally difficult, as each command has a (potential) footprint of the entire tree. We will use promises to show how, despite paths, structural separation logic allows local reasoning about file systems.

File systems are an abstraction between the storage mechanisms of a machine, and the programs that run on that machine. Regardless of the particular hardware used, a file system provides users with the intuitive notion of data stored in a tree-like hierarchy of directories and files. There are several choices of file system abstraction, the two dominant being those of Windows and of POSIX. We focus on the POSIX interfaces as they are both ubiquitous (present in all major operating systems either by default or as an add-on) and comprehensively standardised in [1].

This standardisation of POSIX, whilst detailed, is presented mostly in English. It is complex and contains several subtleties that programmers can miss. Computer users often experience problems caused by programmers incorrectly using a file system API. Mistakes are common in client programs. For example, without care, software installers can make assumptions about file system structures that are not true, causing them to fail and leave the programs they are installing unusable. This chapter attempts focuses on these problems by providing a specification that allows the verification of client code. This is distinct from most formal systems for file systems, which focus on the verification of implementations.

The POSIX specification, even the sub-part dealing with file systems, is too large to consider at once. We therefore provide a compositional specification of a *core subset* of the POSIX file system, distilling from the large specification a set of data structures and commands that captures the essence of the library, whilst being as faithful as possible to the

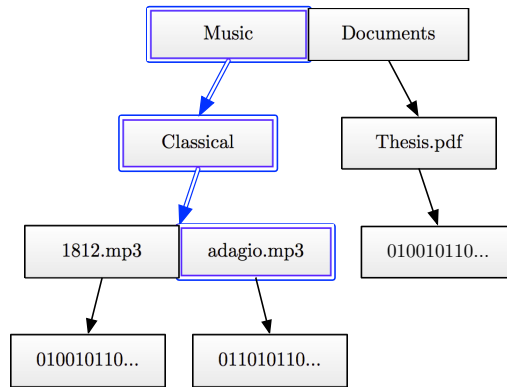


Figure 6.1.: An example file-system tree, with the linear path  $\top/\text{Music}/\text{Classical}/\text{adagio.mp3}$  highlighted.

standard. We term our subset *featherweight POSIXFS*. We provide an imperative machine for featherweight POSIXFS, focusing on the *file system*, which intuitively represents the contents of the hard disk, and the *process heap*, which represents the contents of the heap memory used by programs accessing the file system. We exclude advanced features such as asynchronous IO and sockets, to focus on the fundamental structure of *files* within trees of *directories*. We select a subset of commands relevant to our fragment. This subset is *closed*, in that the commands within it do not create structures outside our model, and *natural*, in that the commands are regularly used in file system programs.

Our fragment initially considers only *linear paths*. These paths take the shortest route to a file during resolution, matching the inductive structure of the directory tree exactly. An example file-system tree is given in figure 6.1, with the path  $/\text{Music}/\text{Classical}/\text{adagio.mp3}$  highlighted. We use promises to axiomatise the commands that identify files via paths, and so demonstrate natural small axioms for our command subset.

We use featherweight POSIXFS to verify a *software installer* example, using structural separation logic to reason locally about updating a widget directory. Software installers provide a common class of client programs for file systems. Newly obtained software is typically provided as a *bundle*, downloaded or copied onto the user’s file system. The goal of a software installer is take this bundle and place the contents at the correct points in the user’s file system such that the software can run. Installers also perform other tasks, such as removing previous versions of the software and purging incompatible files that may have been generated by it. We verify an example software installer, demonstrating that it behaves sensibly: if the installer fails, the file system will be unchanged; if it succeeds,

the program will be successfully installed; and there will be no other outcomes.

Whilst there has been substantial work on the formal specification of file systems, it has mostly focused on *refinement*, giving specifications that are suitable for creating verified implementations. Our work most closely mirrors the Z specification of given by Morgan and Suffrin [50]. This work predates the POSIX standardisation, and has been the basis of most formal work since. For example, based on these specifications, [30] used Z notation to formally specify a set of operations covering primitive file-manipulation functionality, mechanising them in the Z/Eves theorem prover, and refining them to an implementation in Java. This work was part of an effort to build a verified POSIX file system [32], in response to the mini-verification challenge by Joshi and Holzmann [48] to verify a file system associated with a Mars lander.

An alternative approach is given in [41], formalizing an abstract file system by means of partial functions from paths to data. However, this work does not focus on modelling a standardised file system such as POSIX, but rather a more generic file store. As with Z specifications, the main motivation is to verify implementations rather than client programs.

One system verifying the *usage* of file system libraries is *Forest* [28], a Haskell domain-specific language for describing and manipulating file stores. Forest uses classical tree logics to define the semantics of a core calculus for file-system tree shapes, creating a declarative and type-based file system programming paradigm. Again, this is not a specification for a standardised library like POSIX.

## 6.1. Distilling featherweight POSIXFS

The POSIX standard is very large, with issue 7 of the base specifications spanning nearly four thousand pages. It covers many different types of operating system abstraction, of which the file system is just one. The prose is a mix of English descriptions for the formal concepts, C language source code to describe some programming interfaces, and English rationales for certain decisions. Though rigorously defined, the standard is not presented in a way amenable to formal analysis. We therefore first analyse the standard to extract *featherweight POSIXFS*, a tractable subset, focusing on file systems, which we can formalise for analysis. In forming this subset, we refer to many definitions within the standard [1].

### 6.1.1. Pruning the file-system tree

Despite being presented as a tree by most user interfaces, the file-system defined by POSIX is actually a *directed graph*. The nodes of the graph are the seven types of *file*, enumerated in standard reference 3.164. Despite the name, these are not what are generally thought of as files, but instead are just “*objects that can be written to, or read from, or both*”. Each file has a unique *file serial number* (standard reference 3.175), commonly called an *inode*. The graph edges are given by *directory files* (reference 3.129). Each directory file contains an unordered list of *directory entries* (reference 3.130). Each entry is a named *hard link* (reference 3.191) to another file, and the same file may have multiple incoming hard links. The result is a *file system hierarchy* (reference 4.5). One directory file is distinguished as the *root directory* (reference 3.324). The overall result is a graph similar to that of figure 6.2, using inode 1 as the root.

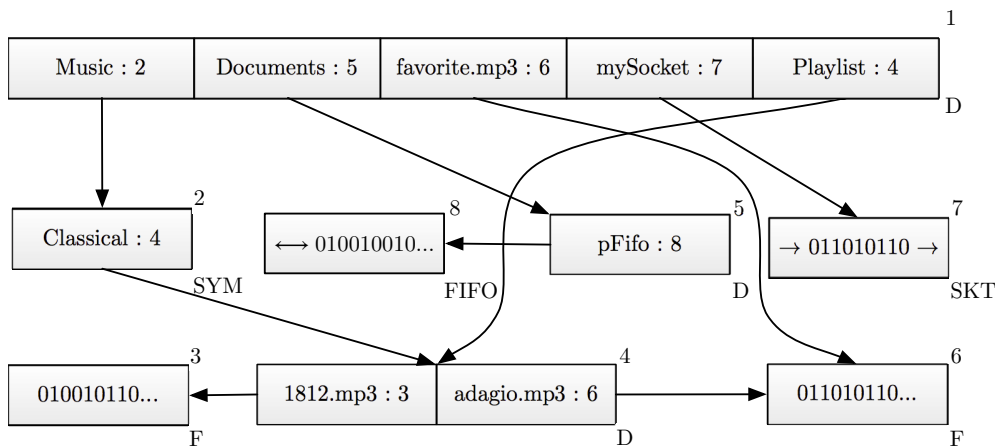


Figure 6.2.: An example file-system, as permitted by the POSIX specification.

Of the seven types of file, we will initially model just *regular files* (labelled by the subscript F in the diagram) and *directories* (labelled D). We will outline a possible formalisation of *symbolic links* in chapter 7. The other types of file (block special, character special, FIFO, and socket) are less commonly used. We believe that they can be added to our subset without difficulty if needed. By removing these file types from consideration, we simplify file systems to those shaped like figure 6.3.

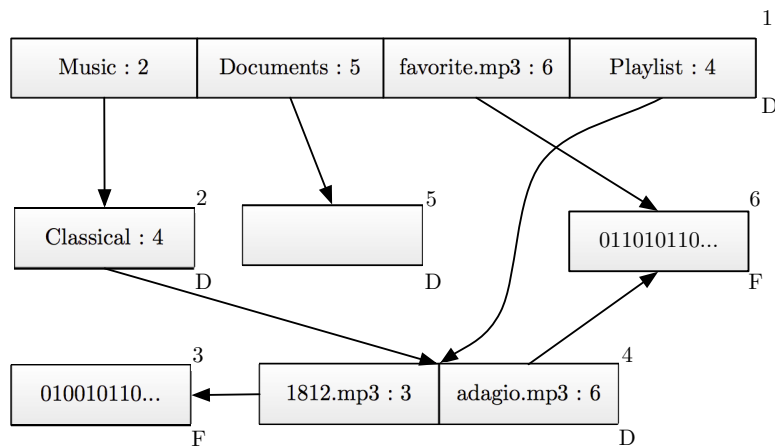


Figure 6.3.: An example file system, considering only regular files and directories.

Figure 6.3 more closely resembles the common intuition of a file-system structure. The directory files consist of file names referring either other directories or regular files. The *regular files* contain byte sequences. However, there may be many hard links to the same file. This is typically desirable, as many different directories may want to reference the same file. Unfortunately, if hard links point to *directories* rather than regular files, *cyclic* file systems can be created. Whilst this is allowed by POSIX, it is disallowed by almost every POSIX implementation due to the complexities of file systems with “loops” in their structure. We restrict our system so that hard links reference regular files only.

With this restriction, a file system can be seen as a tree of directories. Within this tree structures, *files* are referenced (possibly many times) by their inodes, resulting in a heap of files. We will represent the directory tree using an inductive data structure. Our subset will not permit programs to *open* directories as if they were general files (an operation that is rare). We therefore remove their inodes from this structure without affecting the usefulness of our fragment, and obtain a file-system tree like figure 6.4.

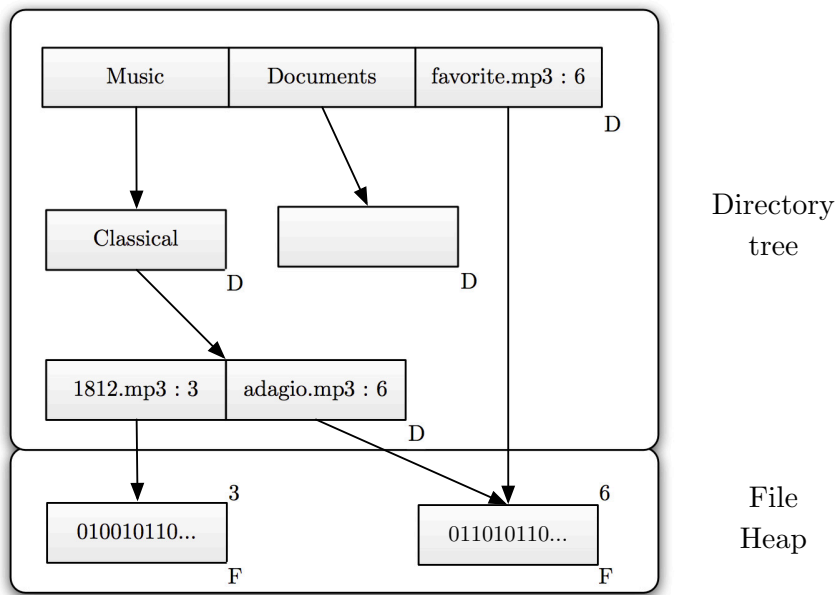


Figure 6.4.: An example file system, considering only trees of directories, with regular files at the fringe.

### Formalisation

We work with file systems under the restrictions above. We first formalise the file-system tree. Two general sets of data are necessary for this.

**Parameter 21** (File systems data for trees). Assume a set of **file names** for directory entries FNAMES (reference 3.170), ranged over by  $a, b, c$ , with a distinguished **root name**  $\top \in \text{FNAMES}$ . Assume also a countably infinite set of **inode identifiers** INODES (reference 3.175), ranged over by  $\iota, \kappa, \dots$ .

To maximise the number of POSIX implementations we can specify, we do not impose a limit on the length of file names. Therefore, the POSIX constant `NAME_MAX`, which defines the system limit on name length, is infinite in our setting. We now define the set of *directories*. Directories can be defined as an inductive data type, representing the trees of directory files (reference 3.129), each containing zero or more directory entries (reference 3.130). We choose our representation by referencing the standard’s specification of the *internal directory structure* (section “format of directory entries” in `dirent.h`). This

gives us free reign to pick a representation of directories that is as general as the standard, as long as we can determine the inode and name for each directory entry. Our subset does not give inodes to directories, so entries in the directory tree are either other *named directories* or *named file links to an inode*.

**Definition 106** (Unrooted directories). The set of **unrooted directories** UDIRS, ranged over by  $ud, ud_1, \dots, ud_n$ , is defined as: for all  $a \in (\text{FNAMES} \setminus \{\top\}), \iota \in \text{INODES}$ :

$ud ::=$	$\emptyset$	Empty directory
	$  a : \iota$	File-link entry named $a$
	$  a[ud]$	Directory entry named $a$
	$  ud_1 + ud_2$	Directory composition

where  $+$  is commutative and associative with identity  $\emptyset$ , and the directory entries have sibling unique names. Unrooted directories are equal up to the properties of  $+$ .

We also define *rooted directories*, which represent the single starting point of the file system hierarchy. Together, rooted and unrooted directories form the set of general directories.

**Definition 107** (Rooted directories). The set of **rooted directories** RDIRS, ranged over by  $rd, rd_1, \dots, rd_n$ , is defined as:

$$\text{RDIRS} \triangleq \{\top[ud] \mid ud \in \text{UDIRS}\}$$

The set of **directories** DIRS, ranged over by  $d, d_1, \dots, d_n$  is defined as:

$$\text{DIRS} \triangleq \text{UDIRS} \cup \text{RDIRS}$$

Every entry within the file-system tree is typed as either a directory or regular file. A file-link entry,  $a : \iota$ , has type F. A directory entry  $a[ud]$  has type D. We also give a type to entries that do not exist, N. This will be used to represent files that cannot be found in a directory tree.

**Definition 108** (Entry types). The set of **entry types** DETYPES is defined as:

$$\text{DETYPES} = \{\text{F}, \text{D}, \text{N}\}$$

We define the *inodes function* to extract the set of used inodes from a directory. This function will be useful in checking well-formedness of our structures.

**Definition 109** (Inodes function). The **inodes function**  $\text{inodes} : \text{DIRS} \rightarrow \mathcal{P}(\text{INODES})$  is defined by induction on directories to collect the set of inodes used. It is similar to definition 37.

Rooted directories form the file-system tree. The heap of regular files consist of inode identifiers pointing to *byte sequences* (reference 3.317). Byte sequences are lists of *bytes* (reference 3.84).

**Definition 110** (Bytes). The set of **bytes** BYTES, ranged over by  $b, b_1, \dots, b_n$ , is defined as the integers in the  $[0, 255]$  interval. The set of **byte sequences** BYTESEQS, ranged over by  $bs, bs_1, \dots, bs_n$ , is defined as: for all  $b \in \text{BYTES}$

$$bs ::= b \mid b \cdot bs \mid \emptyset$$

where  $\cdot$  is associative with identity  $\emptyset$ . Byte sequences are equal up to the properties of  $\cdot$ .

**Definition 111** (Byte sequence length). The **byte sequence length** function  $|\cdot| : \text{BYTESEQS} \rightarrow \mathbb{N}$  is defined as the length of the input byte sequence, and is standard.

We now define a *file-system heap* as the structure which represents a file system. It consists of a rooted directory and a heap of regular files. This heap must contain at least the files referenced by the file-link entries in the file-system tree.

**Definition 112** (File-system heaps). Let  $\mathcal{F}$  be the **directory tree address**, where  $\mathcal{F} \notin \text{INODES}$ . The set of **file systems** FILESYSTEMS, ranged over by  $fs, fs_1, \dots, fs_n$ , is defined as:

$$\text{FILESYSTEMS} \triangleq \left\{ fs \mid \begin{array}{l} fs : (\{\mathcal{F}\} \rightarrow \text{RDIRS}) \sqcup (\text{INODES} \xrightarrow{\text{fin}} \text{BYTESEQS}), \\ \text{inodes}(fs(\mathcal{F})) \subseteq \text{dom}(fs) \end{array} \right\}$$



If a regular file is not referenced by any directory entries, and is not currently in use, the standard says that it will be destroyed. We choose not to model this destruction of regular files by commands, instead assuming that file-system heaps are garbage collected. This allows our presentation to focus on the core behaviours of commands, rather than resource cleanup.

### 6.1.2. Paths

Commands that access the file-system tree identify the data they work on via *pathnames* (POSIX standard [1] reference 3.266, more often simply called *paths*). A path is a list of *pathname components* (reference 3.267) separated by the ‘/’ character. A pathname component is either a directory or regular file name, the reserved name ‘.’ or the reserved name ‘..’ (reference 3.169).

As our subset allows neither hard links to directories nor symbolic links (for the moment), paths consisting of filenames match the inductive structure of the file-system tree. They are *resolved* in the tree, with each component indicating the next sub-tree for resolution, and the final component indicating the target. However, paths containing the ‘.’ and ‘..’ characters are *not* inductively related to the tree. Whereas a normal pathname component states the next directory or file in the path, the ‘.’ name indicates a hard link to the directory that contains it. The ‘..’ name is a hard link to the *parent* of the directory. In POSIX, all directories must contain ‘.’ and ‘..’. As we do not currently allow hard links to directories, we do not consider ‘.’ or ‘..’. Our paths are thus *linear*, taking the shortest route through the file-system tree to the entry they identify.

Linear paths are either *relative* or *absolute*. Relative paths can be used to resolve an entry in any part of the tree. Absolute paths always start at the root. POSIX gives this root directory the path ‘/’. For typesetting clarity, we label the root path ‘/’ as  $\top$ . Ergo, absolute linear paths are of the form  $\top$ /list/of/file/names.

**Definition 113** (Linear Paths). The set of **relative linear paths**  $\text{RELATIVEPATHS}$ , ranged over by  $rp, rp_1, \dots, rp_n$ , is defined as: for all  $a \in \text{FNAMES}$

$$rp ::= a \mid rp/a$$

The set of **absolute linear paths**  $\text{ABSOLUTEPATHS}$ , ranged over by  $ap, ap_1, \dots, ap_n$ , is defined as:

$$\text{ABSOLUTEPATHS} = \{\top\} \cup \{\top/rp \mid rp \in \text{RELATIVEPATHS}\}$$

The set of **linear paths** PATHS, ranged over by  $p, p_1, \dots, p_n$ , is defined as  $\text{PATHS} = \text{RELATIVEPATHS} \cup \text{ABSOLUTEPATHS}$

Paths locate entries within a directory tree via a process of *path resolution* (standard reference 4.12). Path resolution starts at the root of the file system, and follows each path component down one level of the structure to find the entry. The resolution of linear paths matches the inductive structure of the file system hierarchy. Paths do not always resolve in a given directory tree, corresponding to the file not being found.

**Definition 114** (Path resolution). The **path resolution function**  $\text{resolve} : \text{PATHS} \rightarrow \text{DIRS} \rightarrow \text{DIRS}$  is defined as: for all  $a \in \text{FNAMES}, rp \in \text{RELATIVEPATHS}, d, d_1, d_2 \in \text{DIRS}$

$$\begin{aligned} \text{resolve}(a, d + a : \iota) &= a : \iota \\ \text{resolve}(a, d_1 + a[d_2]) &= a[d_2] \\ \text{resolve}(\top, \top[d]) &= \top[d] \\ \text{resolve}(a/rp, d_1 + a[d_2]) &= \text{resolve}(rp, d_2) \\ \text{resolve}(\top/rp, \top[d]) &= \text{resolve}(rp, d) \\ \text{otherwise} &= \text{undefined} \end{aligned}$$

We will need to *concatenate* paths to perform certain operations.

**Definition 115** (Path concatenation). The **path concatenation function**  $\text{pathConcat} : \text{PATHS} \rightarrow \text{PATHS} \rightarrow \text{PATHS}$  is defined as:

$$\begin{aligned} \text{pathConcat}(p, a) &= p/a \\ \text{pathConcat}(p_1, a/p_2) &= \text{pathConcat}(p_1/a, p_2) \end{aligned}$$

where, if the pathname component  $\top$  would appear in the result as anything other than the first component, the result is undefined. The path concatenation  $\text{pathConcat}(p_1, p_2)$  is written  $p_1/p_2$ .

### 6.1.3. Process heap

The heaps of definition 112 represent the state of the storage device holding the file system. We will also need *runtime* structures representing normal heap memory, and specific file-system data held by programs. We call this the *process heap*. The process heap consists of the structures created by opening files and enumerating directories, along with normal heap memory.

An *open file description* (reference 3.253) is a record holding information about the regular files opened by a program. This record contains the inode of the file, and the offset within the file that read and write operations will begin from. Open file descriptors are created by the `open` commands, used by `read`, `write`, and `lseek` and are destroyed by the `close` command (all given in definition 119).

**Definition 116** (Open file description). The set of **open file descriptions** `OPENFILEDESCRIPTORS`, ranged over by  $fd, fd_1, \dots, fd_n$ , is defined as:

$$\text{OPENFILEDESCRIPTORS} \triangleq \text{INODES} \times \mathbb{N}$$

Programs almost never open directories as if they were files. Instead, they enumerate the contents with special directory management commands. All such interaction with directories is via *directory streams* (reference 3.131). These represent the state of an open directory in the form of a *entry set*, recording the entries that were in a directory when it was opened. They are used to support the `opendir`, `readdir` and `closedir` commands (all given in definition 119).

**Definition 117** (Directory streams). The set of **directory streams** `OPENDIRSTREAMS`, ranged over by  $ds, ds_1, \dots, ds_n$  is defined as:

$$\text{OPENDIRSTREAMS} \triangleq \mathcal{P}(\text{FNAMES})$$

The *process heap* is a structured heap holding open file descriptions and directory streams. These structures are referred to via *open file descriptors* (reference 3.166)<sup>1</sup>. The heap also includes a standard heap, as commands that read and write to files use standard heap memory as the destination and source of these commands.

---

<sup>1</sup>Not to be confused with open file descriptions, definition 116.

**Definition 118** (Process heap). Assume a countably infinite set of **open file descriptors** `OPENFILEDESCRIPTORS`, ranged over by  $n, m, \dots$ . The set of **process heaps** `PH`, ranged over by  $\mathbf{ph}, \mathbf{ph}_1, \dots, \mathbf{ph}_n$ , is defined as:

$$\mathbf{PH} \triangleq \left\{ \mathbf{ph} \mid \mathbf{ph} : \begin{array}{l} \text{OPENFILEDESCRIPTORS} \xrightarrow{\text{fin}} \left( \begin{array}{c} \text{OPENDIRSTREAMS} \\ \cup \\ \text{OPENFILEDESCRIPTORS} \end{array} \right) \\ \sqcup \mathbb{N}^+ \xrightarrow{\text{fin}} \mathbb{N} \end{array} \right\}$$

#### 6.1.4. Selection of commands

The POSIX standard defines over 160 commands that manipulate the file system. Not all of these commands are relevant to the set of file types we model. Moreover, many of the commands are *redundant*, in that they can be implemented using the other commands. We pick a *core command* set that is: *closed*, in that it allows all the expected manipulations of the file system structure we define; and *natural*, in that the commands we choose are those typically used by programs.

We classify our core commands into *structural* commands that manipulate the file-system tree, *primitive IO* commands that read and write the contents of files in the heap, *state* commands for querying file attributes, and *heap* commands that allocate and deallocate linear blocks of normal heap memory.

**Definition 119** (Core POSIX fragment). The set of **core POSIX commands** `FSCOMMANDS` consists of the **structural commands** `STRUCTCOMMANDS`, ranged over by  $C_{STR}$ , **IO commands** `IOCOMMANDS`, ranged over by  $C_{IO}$ , **state commands** `STATECOMMANDS`, ranged over by  $C_{STA}$ , and **regular heap commands** `HEAPCOMMANDS`, ranged over by  $C_H$ . These are defined as: for all program expressions `path, existing, new, flags, dir, flags, fd, size, buffer, offset, whence` and program variables `ndir, nfd, nsize, noffset, s` and `ptr`

```

CSTR ::=  mkdir(path) | rmdir(path)
          | link(existing, new) | unlink(path)
          | rename(existing, new)

CIO ::=  ndir := opendir(path)
          | fn := readdir(dir) | closedir(dir)
          | fd := open(path, flags)
          | c := read(fd, buf, sz)
          | c := write(fd, buf, sz)
          | noffset := lseek(fd, offset, whence)
          | close(fd)

CSTA ::= s := stat(path)

CH ::=  ptr := malloc(size)
          free(ptr)

CFS ::=  CSTR | CIO | CSTA | CH

```

### Structural commands

Structural commands are those concerned with manipulating the directory tree. They never access the file heap nor the process heap.

**mkdir(path)** When **path** evaluates to  $p/a$ , this command creates a new empty directory named  $a$  inside the directory resolved to by  $p$ . If  $p$  does not resolve, or  $p/a$  does resolve, the command faults.

**rmdir(path)** Removes the empty directory resolved to by **path**. If **path** does not resolve, resolves to a directory with children, or resolves to the root directory, the command faults.

**link(existing, new)** When the value of **existing** is  $p_1/a$  and that of **new** is  $p_2/a$ , **link** creates a file-link entry  $a$  in the directory named by  $p_2$  linking the same inode as  $p_1/a$ . If  $p_1$  or  $p_2$  do not resolve to existing directories, if  $p_1/a$  resolves to a directory, or if  $p_2/a$  resolves at all, the command faults.

`unlink(path)` Removes the file link resolved to by `path`. If `path` does not resolve to a file link, the command faults.

`rename(existing, new)` The command moves the directory entry resolved to by `existing` so it becomes the directory entry resolved to by `new`. This happens through the following cases:

1. Renaming a file to a new file: If `existing` resolves to a file link to inode  $\iota$ , `new` is of the form  $p/a$  and  $p$  resolves to a directory without an entry named  $a$ , then the directory entry resolved to by `existing` is removed and a new file-link entry named by  $a$  is created, resolved to by `new`, referencing inode  $\iota$ .
2. Renaming a file to the same name as an existing file: If `existing` resolves to a file link to inode  $\iota$  and `new` resolves to any file link, then the directory entries resolved to by `existing` and `new` are removed and a new file-link entry is created, resolved to by `new`, referencing inode  $\iota$ .
3. Renaming a directory to a new directory: If `existing` resolves to a directory that is not an ancestor of `new`, `new` is of the form  $p/a$  and  $p$  resolves to a directory without an entry named  $a$ , then the directory entry resolved to by `existing` is removed and a new directory type entry is created, resolved to by `new`, with the same contents as that originally resolved to by `existing`.
4. Renaming a directory to the same name as an existing directory: If `existing` resolves to a directory that is not an ancestor of `new` and `new` resolves to a directory with no children, then the directory entries resolved to by `existing` and `new` are removed and a new directory type entry is created, resolved to by `new`, with an identical form to that originally resolved to by `existing`.
5. Renaming something to itself: If `new` and `existing` have the same path value, and it resolves to some entry, then `rename` does not affect the file system.

In all other cases, `rename` faults.

### Input/Output commands

These commands are concerned with the contents of files. Whilst some examine the directory tree to set some initial resource state, most access just the file and process heaps.

`ndir := opendir(path)` Creates a fresh directory stream in the process heap containing the names of the entries within the directory resolved to `path` and assigns the address

of the stream to `ndir`. If `path` does not resolve to a directory, the command faults.

`fn := readdir(dir)` Returns an entry from an open directory stream. When the process heap has a directory stream entry for `dir`, then:

1. when the directory stream is non-empty, non-deterministically selects and removes an entry, assigning the value to `fn`;
2. when it is empty, assigns  $\epsilon$  (the empty file name) to `fn`.

If `dir` does not evaluate to a directory stream address or the open directory heap does not have an entry for `dir`, the command faults.

`closedir(dir)` Removes the open directory stream referenced by `dir` from the open directory heap. If `dir` is not a directory stream address or the open directory heap does not have an entry for `dir`, the command faults.

`nfd := open(path, flags)` Either opens or creates a new file.

1. If the directory entry resolved to by `path` is of the form  $a : \iota$ , `open` creates a fresh file descriptor (say,  $n$ ) in the open file heap associated with the open file description  $(\iota, 0)$ , and assigns  $n$  to `nfd`. Additionally, if `flags` is `O_TRUNC`, this command sets the contents of file  $\iota$  in the file heap to be the empty byte sequence.
2. If `path` does not resolve, is of the form  $p/a$ ,  $p$  resolves to a directory, and `flags = O_CREAT`, then: a new entry is added to the file heap with a fresh inode (say,  $\iota$ ) and the empty byte sequence as contents; and, a file-link entry named  $a$  is created in the directory resolved by  $p$  linking the inode  $\iota$ . Then `open` proceeds as in case 1.

If `flags` is not 0, `O_TRUNC` or `O_CREAT`, `path` resolves to a directory type entry, or `path` is of the form  $p/a$  such that  $p$  does not resolve, the command faults.

`c := read(fd, buff, size)` If `fd` refers to a file descriptor with inode  $\iota$  and offset `offset` in the file heap, this command reads from file  $\iota$  at most `size` bytes, starting at `offset`, into the heap buffer pointed to by `buff`. If `offset + size` bytes exceeds the length of the file, reads only until the end. Assigns to `c` the number of bytes read into the buffer. The command updates the file offset by adding the length of the byte sequence actually read. If the open file heap has no entry for `fd`, `size` is not a natural number, or if the buffer pointed to by `buff` is not of at least length `size`, the command faults.

**c := write(fd, buff, size)** If **fd** refers to a file descriptor with inode  $\iota$  and offset *offset*, writes **size** bytes from the heap buffer **buff** to the file associated with  $\iota$ , starting at *offset*, assigning **size** to **nsize**. Any contents present in the file between the offset and the length of **str** are overwritten, and the file length is extended if needed. If the file offset is greater than the length of the file, then the command first writes as many 0 bytes to the file as needed to make the length of the contents equal to the file offset. The file offset is updated by adding **size**. If the process heap has no entry for **fd**, **buff** is not a heap buffer of at least length **size**, the command faults.

**noffset := lseek(fd, offset, whence)** Moves the file offset for an open file descriptor. If the open file heap maps **fd** to an entry  $(\iota, o)$ , and the file heap maps  $\iota$  to an entry with contents **s** then **lseek** changes the open file heap mapping for **fd** to  $(\iota, o')$  and **noffset** is set to  $o'$ , where  $o'$  calculated as:

1. if **whence** is **SEEK\_SET**,  $o'$  is **offset**;
2. if **whence** is **SEEK\_CUR**,  $o'$  is **offset** +  $o$ ;
3. if **whence** is **SEEK\_END**,  $o'$  is **offset** +  $|s|$ .

Note that the command is allowed to set the new file offset to a value greater than the length of the file. If  $o'$  is negative, **whence** is none of **SEEK\_SET**, **SEEK\_CUR** and **SEEK\_END**, or the open file heap has no mapping for **fd**, then **lseek** faults.

**close(fd)** Closes the open file descriptor **fd**, by removing it from the process heap. If the open file heap does not contain an entry **fd**, the command faults.

### Status command

Our single status command just examines the directory tree. We distinguish this from a structural command because it does not modify the file system.

**t := stat(path)** If the directory entry resolved to by **path** is a file-link entry, then assigns the file type constant **F** to **t**. If it is of directory type, assigns **D** to **t**. If the path **path** does not resolve in the tree, assigns the file type **N**.

### Heap commands

The two heap commands allocate and deallocate linear blocks of heap memory. The commands use a *malloc block head* which stores the length of the block that has been



allocated. This block head is a single integer stored in the address just below that of the memory that is returned.

**ptr := malloc(size)** If **size** evaluates to a positive integer *size*, allocates *size* + 1 fresh contiguous heap cells starting at address *ptr*. At address *ptr*, stores *size*. In addresses [*ptr* + 1, *ptr* + *size*], stores non-deterministic bytes. Assigns *ptr* + 1 to **ptr**. Faults if the evaluation of **size** is not a positive integer.

**free(ptr)** : If **ptr** evaluates to a positive integer *ptr*, and the heap contains a positive integer *size* at address *ptr* - 1, deallocates from the heap all cells in the range [*ptr* - 1, *ptr* + *size*]. Faults if **ptr** is not a positive integer, *ptr* - 1 is not a positive integer, or the heap does not contain a cell for every address in the [*ptr* - 1, *ptr* + *size*] range.

Our core POSIX commands are simplified versions of the true POSIX commands, but retain the behaviours pertinent to our subset. We have adapted the specifications to work with a simple imperative language, eliding many artifacts of C. We omit error return codes and treat any errors as faults<sup>2</sup>. Our command descriptions correspond to the behaviour specified by POSIX that is relevant to our file system model choices. The only exceptions are the **readdir** command, and the **malloc** and **free** commands, which we simplify. As specified by POSIX, **readdir** is non-deterministic:

*“If a file is removed from or added to the directory after the most recent call to opendir ..., whether a subsequent call to readdir returns an entry for that file is unspecified.”*

Therefore, mutations made to a directory that is opened via *opendir* can be reflected in calls to **readdir** in one of three ways:

1. All changes are reflected, so that files that have been added to the directory are returned and files that have been removed are not.
2. Some changes are reflected, so that if files are added or removed, whether they will be returned is unknown.
3. No changes are returned. The state of the directory as it was when **opendir** was used is always the state used.

---

<sup>2</sup>We have studied errors, and include them in an upcoming paper based upon this chapter. However, they draw the focus away from the core specification and use of structural separation logic. We thus omit them here.

Ideally, our model would capture the full non-determinism of `readdir`. However, the first two choices raise troubling questions about behaviour. To simplify this initial presentation, we pick the third option: `readdir` will return the elements that were present in the directory at the point it was initially opened with `opendir`.

The POSIX standard for `malloc(size)` and `free` states that `malloc` should return the starting address of a block of memory of length `size`. It also states that, when this address is passed to `free`, the entire block should be deallocated. To enable this, we must store the length of the block, so that `free` can ensure the entire block is removed. Moreover, the standard states that if an address *not* returned by `malloc` is passed to `free`, the behaviour is undefined.

We give a valid specification for `malloc` and `free`, but do not capture the entire non-determinism of the standard. Specifically, we *implement* the behaviour by storing the length of a block in the heap, at the address immediately below the returned heap block. This, in theory, enables programs using our choices to obtain deterministic behaviour not allowed by the standard (such as manually resizing blocks of memory). To capture the full spirit of POSIX here, we would require a more abstract representation of heap memory returned by `malloc`. As this is not the focus of the chapter, we choose the simpler, less general, behaviour.

## 6.2. Imperative machines for featherweight POSIXFS

Having selected the featherweight POSIXFS subset, we now develop an imperative machine for programming with it. This machine is formed of a heap containing variables, a file system heap (definition 112), and a process heap (definition 118). The large range of types used by POSIX means the selection of variable values is much richer than our previous examples. We have seen each of the types before, except the *empty file name* constant  $\epsilon$ . This will be used by commands which would normally return a file name, but cannot due to the state of the file system.

**Definition 120** (Variable values). Let  $\epsilon \notin \text{FNAMES}$  be the **empty filename**. The set of **variable values** `PVALS`, ranged over by  $v, v_1, \dots, v_n$  is defined as:

$$\text{PVALS} \triangleq \mathbb{Z} \sqcup \{\mathbf{true}, \mathbf{false}\} \sqcup \text{FNAMES} \sqcup \{\epsilon\} \sqcup \text{INODES} \sqcup \text{DETTYPES} \\ \sqcup \text{BYTESEQS} \sqcup \text{PATHS} \sqcup \text{OPENFILEDESCRIPTORS}$$

Some commands are parameterised with values that are not addresses or paths. For instance, the `lseek` command takes a parameter `whence` instructing it in what direction to seek. Following POSIX, we define *symbolic constants* to capture these parameters. The constants are `SEEK_SET`, `SEEK_CUR`, `SEEK_END` (for `lseek`) and `O_TRUNC` (for `open`). We will assume that these constants exist in any variable store (corresponding to, for example, them being set up by a runtime or included in some global definitions file).

We enrich the standard set of program expressions with expressions for *path manipulation*. This includes the POSIX commands `basepath` and `dirpath`, which determine the final path component and the prefix leading to the final path component respectively. In POSIX, these are commands. However, as they do not mutate the heap, we give them as expressions.

**Definition 121** (Program expressions). The set of **program expressions** `EXPRS`, ranged over by  $e, e_1, \dots, e_n$ , is extended from that of example 5 by:

$e ::=$	$e_1 \cdot e_2$	Byte sequence concatenation
	$   e $	Byte sequence length
	$  e_1/e_2$	Path concatenation
	$  \text{basepath}(e) \mid \text{dirpath}(e)$	Path analysis

The evaluation of expressions is mostly standard. We give only the cases for path analysis.

**Definition 122** (Evaluation of path expressions). The expression evaluation function  $\llbracket \cdot \rrbracket(\cdot)$  for the expressions of definition 121 is standard, except for: for all  $e$  such that for all  $e_1, e_2$  with  $\llbracket e_1 \rrbracket(s), \llbracket e_2 \rrbracket(s) \in \text{PATHS}$ :

$\llbracket e_1/e_2 \rrbracket(s)$	$= \llbracket e_1 \rrbracket(s) / \llbracket e_2 \rrbracket(s)$	if $\llbracket e_2 \rrbracket(s) \notin \text{ABSOLUTEPATHS}$
$\llbracket \text{basepath}(e) \rrbracket(s)$	$= a$	if $\llbracket e \rrbracket(s) = p/a$
$\llbracket \text{dirpath}(e) \rrbracket(s)$	$= p$	if $\llbracket e \rrbracket(s) = p/a$
$\llbracket \text{basepath}(e) \rrbracket(s)$	$= \top$	if $\llbracket e \rrbracket(s) = \top$
$\llbracket \text{dirpath}(e) \rrbracket(s)$	$= \top$	if $\llbracket e \rrbracket(s) = \top$

The structured heaps for featherweight POSIXFS fuse a variable store, a file system and a process heap.

**Definition 123** (Structured heaps). The set of **structured heaps** for POSIXFS PFSHEAPS, ranged over by  $fs, fs_1, \dots, fs_n$ , are defined as:

$$\text{PFSHEAPS} \triangleq \{fs \mid fs : (\text{PVARs} \stackrel{\text{fin}}{\mapsto} \text{PVALS}) \sqcup \text{FILESYSTEMS} \sqcup \text{PH}\}$$

### 6.2.1. Command actions

In this chapter, we will not prove the soundness of our reasoning with respect to an underlying set of command actions<sup>3</sup>. Omitting the actions does not weaken the rigour of our argument, as the POSIX standard itself does not provide a set of operational semantics. Ergo, any set we create will have to be created by us, and justified against the English prose. We are also responsible for creating the small axioms. Any mismatch between the two would therefore be a surprise, as we are writing the operational semantics purely to justify the axiomatic semantics!

When a library is specified without a pre-existing formal operational semantics, we feel that giving only the axioms more closely fits our library reasoning agenda. This is because we are not trying to capture a specific operational implementation, but the most general *specification* that we can give to the library commands. Soundness of the reasoning should instead be justified both with respect to the prose of the library document, but also against *implementations* of the library both via refinement techniques and empirical testing. We will discuss our ongoing work with refinement in the conclusions of chapter 8.

**Comment 11.** Using actions and axioms, as I did in DOM, is useful when there is a preexisting formalism against which one can check our choices (e.g. [65] for DOM). It is also useful when presenting work to audiences who may be very familiar with operational semantics, but less so with axiomatic. Presenting both allows an easier route to understanding for some people.

## 6.3. Reasoning

We now define the reasoning for featherweight POSIXIFS. We proceed as in previous chapters, first introducing the data types needed to build the abstract heap, then creating an assertion language, and finally presenting the small axioms.

<sup>3</sup>Recall that atomic command actions, parameter 6, give the operational semantics of commands.

### 6.3.1. Abstract file systems

The only rich data type in the file systems presentation is the directory structure. We add structural addressing to this using contexts to create *abstract directories*. Notice that we will not need structural addresses for the process heap (definition 118), as these values have a flat structure.

**Definition 124** (Abstract directories). The set of **abstract unrooted directories** ABSUDIRS, ranged over by  $\mathbf{ud}, \mathbf{ud}_1, \dots, \mathbf{ud}_n$ , is defined as: for all  $a \in (\text{FNAMES} \setminus \{\top\}), \mathbf{x} \in \text{STRUCTADDRS}$

$$\mathbf{ud} ::= \emptyset \mid a : \iota \mid a[\mathbf{ud}] \mid \mathbf{ud}_1 + \mathbf{ud}_2 \mid \mathbf{x}$$

where  $+$  is associative and commutative with identity  $\emptyset$ , directory entries are sibling unique, and body addresses are unique. Abstract unrooted directories are equal up to the properties of  $+$ .

The set of **abstract rooted directories** ABSRDIRS, ranged over by  $\mathbf{rd}, \mathbf{rd}_1, \dots, \mathbf{rd}_n$ , is defined as:

$$\text{ABSRDIRS} \triangleq \{\top[\mathbf{ud}] \mid \mathbf{ud} \in \text{ABSUDIRS}\} \cup \text{STRUCTADDRS}$$

The set of **abstract directories** ABSDIRS, ranged over by  $\mathbf{d}, \mathbf{d}_1, \dots, \mathbf{d}_n$ , is defined as:

$$\text{ABS DIRS} \triangleq \text{ABSUDIRS} \cup \text{ABSRDIRS}$$

Allowing a structural address to be used as an abstract rooted directory ensures that the *entire* directory tree can be abstractly allocated if needed.

The addresses function and composition for this data are defined in the standard manner.

**Definition 125** (Addresses function for directories). The *addresses function*  $\text{addr} : \text{ABS DIRS} \rightarrow \mathcal{P}(\text{STRUCTADDRS})$  is defined by induction to extract the set of structural addresses present in an abstract directory. It is similar to 37.

**Definition 126** (Abstract directories composition). The **abstract directories composition** function  $\text{comp} : \text{STRUCTADDRS} \rightarrow \text{ABS DIRS} \rightarrow \text{ABS DIRS} \rightarrow \text{ABS DIRS}$  is

defined as substitution of a directory entry for the given structural address, if the result is contained within ABSDIRS. It is similar to 38.

This reasoning for featherweight POSIXFS will use path promises in a similar manner to the example of section 5.1.1. As we use linear paths, these promises state the linear path from which the data associated with the promise was allocated. As we are using linear paths, which take the shortest route from the top of the file system tree to the data, these promises will be *head promises*, given in terms of paths that lead to  $\mathfrak{m}$ . Along with the standard paths We use an *empty abstract path*  $\emptyset_p$ , which represents that  $\mathfrak{m}$  is at the very root of the tree. This will describe the situation when the entire file-system tree has been abstractly allocated.

**Definition 127** (Abstract paths). Given the set of absolute paths ABSOLUTEPATHS (definition 113), the set of **abstract paths** ABSTRACTPATHS, ranged over by  $\mathbf{p}, \mathbf{p}_1, \dots, \mathbf{p}_n$ , is defined as:

$$\text{ABSTRACTPATHS} = \{\mathbf{p}/\mathfrak{m} \mid \mathbf{p} \in \text{ABSOLUTEPATHS}\} \cup \{\emptyset_p/\mathfrak{m}\}$$

We lift path resolution to abstract paths on abstract directory trees in the natural way. The only non-evident case is for the path  $\emptyset_p/\mathfrak{m}$ , which only resolves as  $\text{resolve}(\emptyset_p/\mathfrak{m}, \mathfrak{m}) = \mathfrak{m}$ .

**Definition 128** (Promises for file systems). Given a structural addressing algebra formed of abstract directories and the associated composition and addresses function, we construct the underlying data algebra  $(\text{STRUCTADDRS}_{\mathfrak{m}}, \text{DIRS}_{\mathfrak{m}}, \text{addrs}_{\mathfrak{m}}, \text{comp}_{\mathfrak{m}})$  as per definition 90. Then, the **at path** head data  $@ : \text{ABSTRACTPATHS} \rightarrow \mathcal{P}(\text{ABSRDIRS})$  is defined as:

$$@p = \{\mathbf{rd} \in \text{ABSRDIRS} \mid \text{resolve}(p, \mathbf{rd}) \text{ defined, } \text{addrs}(\mathbf{rd}) = \{\mathfrak{m}\}\}$$

The set of HEADPROMS for file systems is a set of rooted directories in which an abstract path resolves.

$$\text{HEADPROMS} \triangleq \{(\mathfrak{m}, @p) \mid p \in \text{ABSTRACTPATHS}\}$$

There are no body promises:  $\text{BODYPROMS} \triangleq \emptyset$ . The set of promise sets PROMISES is formed as per definition 94.

Notice that these head promises always refer to a *directory* into which either another directory or file link can be composed. These promises are naturally stable with respect to the file system commands, for the same reason that the path promises were self stable in section 5.1.1. With these “at path” promises, we define the abstract heap for file systems. Notice that the process heap is unchanged from the imperative machine definition. Process heaps are entirely flat, and require no structural addressing. Notice we use the data and structural addresses from the underlying data algebra.

**Definition 129** (Pre-abstract heap). The set of **pre-abstract heaps** for POSIXFS PFSHEAPS, ranged over by  $\mathbf{fsh}, \mathbf{fsh}_1, \dots, \mathbf{fsh}_n$ , are defined as:

$$\text{ABSPFSHEAPS} \triangleq \left\{ \begin{array}{l} \mathit{fsh} \mid \mathit{fsh} : \begin{array}{l} (\text{PVARs} \xrightarrow{\text{fin}} \text{PVALS}) \\ \sqcup (\{\top\} \rightarrow (\text{ABSRDIRS}_{\bar{m}} \times \text{PROMISES})) \\ \sqcup \text{PH} \\ \sqcup (\text{STRUCTADDRS}_{\bar{m}} \xrightarrow{\text{fin}} (\text{ABSDIRS}_{\bar{m}} \times \text{PROMISES})) \end{array} \end{array} \right\}$$

The set of abstract heaps for featherweight POSIXFS is those pre-abstract heaps with completions which satisfy the path promises.

### 6.3.2. Assertion language

As in previous chapters, we define the assertion language by giving logical values, logical expressions, data specific assertions and, finally, abstract heap assertions. The logical values and expressions for file system reasoning are richer than previous examples. This due to the variety of data types we are manipulating, and the amount of analysis needed in certain axioms (mostly focusing on paths and sets). Paths will be used to denote the path promise on a cell. Ergo, we allow the abstract “empty path”  $\emptyset_p$  to enable the abstract allocation of the entire file system tree.

**Definition 130** (Logical values). The set of **logical values** LVALS are defined as follows:

$$\begin{aligned} \text{LVALUES} \triangleq & \text{PVALS} \cup \mathcal{P}(\text{PVALS}) \cup \text{ABSDIRS} \cup \mathcal{P}(\text{ABSDIRS}) \\ & \cup \text{PATHS} \cup \{\emptyset_p\} \cup \mathcal{P}(\text{ABSOLUTEPATHS}) \cup \{\emptyset_p\} \cup \text{STRUCTADDRS} \end{aligned}$$

The logical expressions allow the standard set of logical expressions, plus normal set operations, byte sequence analysis, and path analysis. The later two are used for axiomatising the `read` and `write` commands, and the structural commands respectively. We allow sets of program values and directories to enable logical variables to store *sets* of entries, which is useful for showing invariants of certain directory structures.

**Definition 131** (Logical expressions and evaluation). The set of **logical expressions**  $\text{LEXPRS}$ , ranged over by  $E, E_1, \dots, E_n$ , is defined inductively as: for all  $V \in \text{PVALS}, L, L_1, L_2 \in \text{LVARs}$

$E ::=$	$V \mid L \mid E_1 \in E_2 \mid E_1 \cup E_2 \mid E_1 \setminus E_2$	Variables literals, and sets
	$\mid E_1 \cdot E_2 \mid  E $	Byte sequence concat. and length
	$\mid E_1/E_2$	Path concatenation
	$\mid \text{basepath}(E) \mid \text{dirpath}(E)$	Path analysis

The **logical expression evaluation function**  $\langle \cdot \rangle(\Gamma) \cdot : \text{LENVS} \rightarrow \text{LEXPRS} \rightarrow \text{LVALS}$  is defined as:

$\langle V \rangle(\Gamma)$	$\triangleq V$
$\langle L \rangle(\Gamma)$	$\triangleq \Gamma(L)$
$\langle E_1 \in E_2 \rangle(\Gamma)$	$\triangleq \begin{cases} \mathbf{true} & \text{if } \langle E_1 \rangle(\Gamma) \in \langle E_2 \rangle(\Gamma), \langle E_2 \rangle(\Gamma) \text{ is a set} \\ \mathbf{false} & \text{if } \langle E_1 \rangle(\Gamma) \notin \langle E_2 \rangle(\Gamma), \langle E_2 \rangle(\Gamma) \text{ is a set} \\ \text{undefined} & \text{otherwise} \end{cases}$
$\langle E_1 \cup E_2 \rangle(\Gamma)$	$\triangleq \begin{cases} \langle E_1 \rangle(\Gamma) \cup \langle E_2 \rangle(\Gamma) & \text{if } \langle E_1 \rangle(\Gamma), \langle E_2 \rangle(\Gamma) \text{ are sets} \\ \text{undefined} & \text{otherwise} \end{cases}$
$\langle E_1 \setminus E_2 \rangle(\Gamma)$	$\triangleq \begin{cases} \langle E_1 \rangle(\Gamma) \setminus \langle E_2 \rangle(\Gamma) & \text{if } \langle E_1 \rangle(\Gamma), \langle E_2 \rangle(\Gamma) \text{ are sets} \\ \text{undefined} & \text{otherwise} \end{cases}$
$\langle E_1 \cdot E_2 \rangle(\Gamma)$	$\triangleq \langle E_1 \rangle(\Gamma) \cdot \langle E_2 \rangle(\Gamma)$
$\langle  E  \rangle(\Gamma)$	$\triangleq  \langle E \rangle(\Gamma) $
$\langle E_1/E_2 \rangle(\Gamma)$	$\triangleq \langle E_1 \rangle(\Gamma) / \langle E_2 \rangle(\Gamma)$
$\langle \text{basepath}(E) \rangle(\Gamma)$	$\triangleq \text{basepath}(\langle E \rangle(\Gamma))$
$\langle \text{dirpath}(E) \rangle(\Gamma)$	$\triangleq \text{dirpath}(\langle E \rangle(\Gamma))$



The set of data assertions are the lift of the file system data type to the assertion language, plus the required assertions of structural separation logic.

**Definition 132** (File system data assertions). The set of **file-system tree assertions**  $\text{FSTREEASSTS}$ , ranged over by  $\phi, \phi_1, \dots, \phi_m$ , are defined as:

$\phi ::=$	$\emptyset$	Empty entry
	$\mathbf{E}[\phi]$	Directory type entry
	$\mathbf{E}_1 : \mathbf{E}_2$	File type entry
	$\phi_1 + \phi_2$	Entry list
	$\top[\phi]$	File system root
	$\mathbf{E}$	Logical expression
	$\phi_1 \textcircled{\wedge} \phi_2$	Address separation
	$\diamond \alpha$	Address availability
	$\phi_1 \Rightarrow \phi_2$	Implication
	<b>false</b>	Falsity
	$\exists x. \phi$	Existential

As we did in satisfaction relation for DOM (definition 83), we define the meaning of file-system tree assertions relationally.

**Definition 133** (File-system tree assertion interpretations). The **file-system tree assertions interpretation relation**,  $\models \subset (\text{LENVS} \times \text{ABSDIRS}_{\bar{m}}) \times \text{FSTREEASSTS}$ , with elements  $((\Gamma, \mathbf{d}), \phi)$  written  $\Gamma, \mathbf{d} \models \phi$  is defined as:

$$\begin{array}{l}
\Gamma, \mathbf{d} \models \quad \emptyset \iff \mathbf{d} = \emptyset \\
\Gamma, \mathbf{d} \models \quad E[\phi] \iff \exists \mathbf{d}_1. \mathbf{d} = \langle E \rangle(\Gamma)[\mathbf{d}_1] \wedge \Gamma, \mathbf{d}_1 \models \phi \\
\Gamma, \mathbf{d} \models \quad E_1 : E_2 \iff \mathbf{d} = \langle E_1 \rangle(\Gamma) : \langle E_2 \rangle(\Gamma) \\
\Gamma, \mathbf{d} \models \quad \phi_1 + \phi_2 \iff \exists \mathbf{d}_1, \mathbf{d}_2. \mathbf{d} = \mathbf{d}_1 + \mathbf{d}_2 \wedge \Gamma, \mathbf{d}_1 \models \phi_1 \wedge \Gamma, \mathbf{d}_2 \models \phi_2 \\
\Gamma, \mathbf{d} \models \quad \top[\phi] \iff \exists \mathbf{d}_1. \mathbf{d} = \top[\mathbf{d}_1] \wedge \Gamma, \mathbf{d}_1 \models \phi \\
\Gamma, \mathbf{d} \models \quad E \iff \mathbf{d} = \langle E \rangle(\Gamma) \\
\Gamma, \mathbf{d} \models \quad \phi_1 \otimes \phi_2 \iff \exists \mathbf{d}_1, \mathbf{x}, \mathbf{d}_2. \begin{array}{l} \Gamma(\alpha) = \mathbf{x} \wedge \mathbf{d} = \mathbf{d}_1 \otimes \mathbf{d}_2 \wedge \\ \Gamma, \mathbf{d}_1 \models \phi_1 \wedge \Gamma, \mathbf{d}_2 \models \phi_2 \end{array} \\
\Gamma, \mathbf{d} \models \quad \diamond \alpha \iff \Gamma(\alpha) = \mathbf{x} \wedge \mathbf{x} \in \text{addrs}(\mathbf{d}) \\
\Gamma, \mathbf{d} \models \quad \phi_1 \Rightarrow \phi_2 \iff (\Gamma, \mathbf{d} \models \phi_1) \implies (\Gamma, \mathbf{d} \models \phi_2) \\
\Gamma, \mathbf{d} \models \quad \mathbf{false} \iff \text{never} \\
\Gamma, \mathbf{d} \models \quad \exists X. \phi \iff \exists x. \Gamma[X \mapsto x], \mathbf{d} \models \phi
\end{array}$$

These assertions satisfy the requirements of data specific assertions by construction. With them, we define a set of useful derived assertions.

**Definition 134** (Derived Assertions). The set of **derived assertions** for file systems reasoning are defined as:

$$\begin{array}{l}
\diamond \phi \triangleq \exists \alpha. (\mathbf{true} \otimes \phi) \\
\text{entry}(A) \triangleq A[\mathbf{true}] \vee \exists I. (A : I) \\
\text{top\_entry}(A) \triangleq \mathbf{true} + \text{entry}(A) \\
\text{top\_address} \triangleq \exists \alpha. \alpha \in \text{STRUCTADDRS} \wedge (\mathbf{true} + \alpha) \\
\text{names}(S) \triangleq \forall A. (A \in S \iff \mathbf{true} + \text{entry}(A)) \\
\odot_i b \triangleq (i = 0 \wedge \emptyset) \vee \left( i > 0 \wedge b \cdot \left( \odot_{i-1} b \right) \right)
\end{array}$$

The assertion  $\diamond \phi$  is the *somewhere in the tree* assertion, stating that  $\phi$  holds at some level of the tree structure. The assertion  $\text{entry}(A)$  describes an entry in the file system; a file or directory named  $A$ . The assertion  $\text{top\_entry}(A)$  states that somewhere in a list of directory entries is the entry named  $A$  (either as a file link, or a directory); it is used by e.g. `mkdir` to ensure the new directory name does not already exist. Asserting  $\text{top\_address}$  is similar, but states there is some structural address present; that is, some children may be

missing due to abstract allocation. The assertion  $names(s)$  describes that every name in the set  $s$  is present as an entry. Finally, the assertion  $\odot_i b$  describes *repeated byte sequence composition*, repeating the byte sequence  $b$  exactly  $i$  times.

We now define the abstract heap assertions, using distinct assertions for each type of data. Notice that only abstract heap cells are annotated with promises, as the root of the file system heap is always at the root path. The annotations on each abstract address indicate the path at which they will compress into their super-data.

**Definition 135** (Abstract heap cells). The set of **abstract heap assertions** are those of definition 55, extending with the following additional assertions:

$$\begin{aligned}
P ::= & \quad \top \mapsto \phi && \text{Root of file-system tree} \\
& | \alpha^E \mapsto \phi && \text{Abstract directory cell} \\
& | x \xrightarrow{F} E && \text{Regular file Cell} \\
& | x \xrightarrow{PH} E && \text{File descriptor heap cell} \\
& | x \mapsto E && \text{Regular heap cell}
\end{aligned}$$

The assertion interpretation function is defined as:

$$\begin{aligned}
\langle \top \mapsto \phi \rangle^\Gamma &= \{ \top \mapsto (\mathbf{rd}, \emptyset) \mid \mathbf{rd} \in \langle \phi \rangle^\Gamma \} \\
\langle \alpha^E \mapsto \phi \rangle^\Gamma &= \begin{cases} \Gamma(\alpha) = \mathbf{x}, \mathbf{x} \in \text{STRUCTADDRS}_{\mathbb{m}}, \\ \{ \mathbf{x} \mapsto (\mathbf{d}, (\mathbb{m}, @\mathbf{p}/\mathbb{m}) \mid \mathbf{d} \in \langle \phi \rangle^\Gamma) \} & \text{if } \langle \mathbf{E} \rangle(\Gamma) = \mathbf{p}, \\ & \mathbf{p} \in \text{ABSPATHS} \cup \{\emptyset_p\} \\ \emptyset & \text{otherwise} \end{cases} \\
\langle x \xrightarrow{F} E \rangle^\Gamma &= \begin{cases} \{ \iota \mapsto \langle \mathbf{E} \rangle(\Gamma) \} & \text{if } \Gamma(x) = \iota, \iota \in \text{INODES} \\ \emptyset & \text{otherwise} \end{cases} \\
\langle x \xrightarrow{PH} E \rangle^\Gamma &= \begin{cases} \{ ofd \mapsto \langle \mathbf{E} \rangle(\Gamma) \} & \text{if } \Gamma(x) = ofd, ofd \in \text{OPENFILEDESCRIPTORS} \\ \emptyset & \text{otherwise} \end{cases} \\
\langle x \mapsto E \rangle^\Gamma &= \begin{cases} \{ x \mapsto \langle \mathbf{E} \rangle(\Gamma) \} & \text{if } \Gamma(x) = x, x \in \mathbb{N}^+ \\ \emptyset & \text{otherwise} \end{cases}
\end{aligned}$$

To axiomatise memory allocation, we require an inductive predicate representing an contiguous block of regular heap memory.

**Definition 136** (n-cells heap predicate). The **n-cells heap predicate**  $ncells$  is defined as:

$$ncells(start, len, data) \triangleq \begin{array}{c} (len = 0 \wedge data = \emptyset * emp) \\ \vee \\ \exists b, bs. \left( \begin{array}{c} len > 0 \wedge data = b \cdot bs * \\ start \mapsto b * ncells(start + 1, len - 1, bs) \end{array} \right) \end{array}$$

### 6.3.3. Axioms

The axioms for featherweight POSIXFS are given in figure 6.5. There are several common themes across the axioms.

**Expression resource:** The assertion  $E$  appears in almost all the assertions, and is used in conjunction with the expression mapping assertion  $\Rightarrow$  to evaluate expressions.

**Mixing of flat and structured data:** All commands working on the file system directory tree use addresses bearing path promises. However, there are many commands operating on the process heap that deal only with *flat* resource. Structural separation logic allows the mixing of both.

**Path analysis:** Many POSIX commands identify resource by a path expression passed to the command. This expression must be broken down into the path components used by individual entries. This is achieved with two predicates:

$$\begin{aligned} path(\mathit{path}, P, B, A) &\triangleq \mathit{path} \Rightarrow P/B/A \vee (\mathit{path} \Rightarrow \top/A \wedge P = \emptyset_p \wedge B = \top) \\ path(\mathit{path}, P, A) &\triangleq \mathit{path} \Rightarrow P/A \end{aligned}$$

The two *path* predicates analyse a program expression, determining the path components that comprise it. The name is overloaded, with the first  $path(\mathit{path}, P, B, A)$  describes a program expression  $\mathit{path}$  that breaks down into a prefix path  $P$ , followed by a directory entry  $B$  and then entry  $A$ . The assertion allows  $P$  to be *empty*, so that  $B = \top$  and the path describes a root entry in the file system. This predicate is typically used to describe an entry  $a$  that does *not* exist, so that we need to know the target directory  $b$  in which it will be created. The second  $path(\mathit{path}, P, A)$  is typically used to identify an existing file  $a$  at path  $P$ .

**Checking for an existing file:** Any axiom that will be adding a new directory entry must ensure the name does not clash with that of an existing file. To ensure this, we must capture all elements of the directory and ensure there are no body addresses. We achieve this with the *can\_create(a)* predicate:

$$\text{can\_create}(a) \triangleq \neg \text{top\_entry}(a) \wedge \neg \text{top\_address}$$

This describes directory entries which do not contain an entry named  $a$ . Note that to ensure this, we must ensure no structural addresses are present in the directory, as this would mean the entry could be in a abstract heap cell. This is used to check that a directory in which a new entry will be created can indeed accept that directory, such as  $a[\top \wedge \text{can\_create}(b)]$ , which describes a directory  $a$ , with contents captured in logical variable  $\top$  that certainly does not contain the entry  $b$ . The directory is then a safe place to create the file named  $A$ . Notice that  $\neg \text{top\_address}$  does not prohibit there being addresses deeper inside the directory. They just cannot be directly *in* the directory.

**Optional body addresses:** Some axioms use *optional* body addresses, where a body address may or may not be present based upon the specific splitting used in a proof. The predicate *maybe( $\alpha$ )* is used to describe that the address  $\alpha$  may or may not exist within a directory tree.

$$\text{maybe}(\alpha) \triangleq (\alpha \vee \emptyset)$$

With these predicates, the axioms are largely straightforward translations of the POSIX specification, and match the English versions described earlier. We detail a few, the rest are similar.

**mkdir(path):** This axiom specifies the directory creation command, and is typical of the file system entry creation command axioms of featherweight POSIXFS. It uses analysis of the *path* expression, breaking it into three components: the *parent* path, which leads up the directory in which the new entry will be added; the *parent directory name*, under which the new entry will be created; and *the new entry name*, which will be added to the parent as an empty directory. This pattern allows the file-system tree resource to be described using an assertion of the form  $\alpha^P \mapsto B[\phi]$ . This describes that the directory  $B$  in which the new entry will be created is found at path  $P$ . The assertion  $\phi$  uses the pattern described above to ensure that no entry

with the target name already exists. Notice that, in the case of  $B = \top$ , the path  $P$  must be the special *empty* path; this allows creation in the root directory.

**rmdir(path)**: Removing a directory from the file-system tree is much simpler than adding one, as removing empty resource cannot affect the stability of the reasoning. Simple path analysis is performed, showing that the target directory is present at path  $P$ . This directory is then removed in the post-condition. Notice that the **rmdir** command can never remove the root directory, as the first path component of  $PATH$  cannot be  $\emptyset_p$  by the construction of paths.

**link(existing, new)**: This command creates a new file-link entry at path *new*, linking to the inode of the file at path *existing*. The pre-condition uses a subtle variation of the common pattern to ensure the new file-link name is not taken. Part of the assertion is  $\beta^{P_2} \mapsto D[C \wedge (\text{maybe}(\alpha) + \text{can\_create}(B))]$ . This describes the directory into which the new entry, named  $B$  will be placed. Rather than disallowing *all* body addresses within this directory, the assertion excludes all addresses *except*  $\alpha$ , which is used to address the existing source file link. If we did not allow this, the axiom would not allow the creation of the new link inside the directory that contains the source: instances like **link**( $\top/a/b$ ,  $\top/a/c$ ). Such instances are allowed by POSIX, and so much be allowed by us.

**rename(old, new)**: Rename is one of the more complex axioms, due to the number of cases it must consider. This is due to the permutations of resource that *old* and *new* may point to, enumerated in the description of section 6.1.4. When one considers each combination of old and new as directories or files as being a distinct command, each case is actually reasonably simple. The most complex cases are the second and four, where the destination is a directory. In these situations, the source is essentially *deleted*, and then recreated. This incurs the complexity of ensuring there will be no name clash at the *new* path.

**fd := open(path, flags)**: As described in section 6.1.4, has three behaviours governed by *flags*. In all cases, extends the process heap with a new entry for the opened file. In the creation case, creates file system hierarchy entries as well.

**c := read(fd, buff, sz)**: Uses the process heap and file heap to read into the heap buffer. There are three cases, based upon the offset recorded in the file heap, and the length of the file data. A heap buffer of sufficient length must be provided.

**dir := opendir(path):** The `opendir` command captures the contents of a directory for later enumeration with `readdir`. To ensure that *every* entry is captured, the assertion `-top_address` is used in the pre-condition. Then, all the names are captured in the logical variable `A`.

**fn := readdir(dir):** The non-determinism we permit for directory enumeration is built into the `readdir` axiom. It uses the snapshot of the directory stored in the process heap to non-deterministically select an element known to be in the directory when `opendir` is called. This element is removed from the set in the process heap, and assigned to the return variable.

**t := stat(path):** There are three `stat` axioms, corresponding to the directory entry, file-link entry and “not found” entry cases. The first two are straight-forward, but the “not found” case requires some subtle analysis. As there is always a root directory in the file system, showing that a path  $p$  does not resolve is equivalent to finding a *prefix* of path  $p$  that does resolve, and a *postfix* that does not. The axiom contains the analysis:

$$\text{PATH} = P_1/D/A/P_2 \vee \text{PATH} = P_1/D/A \vee ((\text{PATH} = T/A/P_2 \vee \text{PATH} = T/A) \wedge P_1 = \emptyset_p \wedge D = T)$$

This breaks the path into the prefix  $P_1/D/A$ , and the postfix  $P_2$ . The abstract heap cells show that the prefix path *does* resolve, but does *not* contain `A`. Thus, the postfix of the path cannot resolve, and so the overall path cannot resolve.

### 6.3.4. Admissible and common commands

Of the POSIX file system commands applicable to our fragment, we have given a core subset of 14 of them. Of the others, many can be implemented using our subset. One example is `remove(path)`. The behaviour of `remove` is that, when given a path: if it resolves to a file, invokes `unlink(path)`; if the path resolves to an empty directory, invokes `rmdir(path)`. If the given path does not exist, the command returns -1 to indicate an error. We can easily implement and verify `remove` using our core fragment, as shown in figure 6.3. There are two success cases with the following specifications. We prove that `remove` meets the first of these specifications in figure 6.3.

$$\left\{ \begin{array}{l} \text{path} \Rightarrow P/A \wedge \text{ret} \rightarrow - * \alpha^P \mapsto A : I * E \\ \text{ret} := \text{remove}(\text{path}) \end{array} \right\} \quad \left\{ \begin{array}{l} \text{path} \Rightarrow P/A \wedge \text{ret} \rightarrow - * \alpha^P \mapsto A[\emptyset] * E \\ \text{ret} := \text{remove}(\text{path}) \end{array} \right\}$$

$$\left\{ \begin{array}{l} \text{ret} \rightarrow 0 \wedge \alpha^P \mapsto \emptyset * E \end{array} \right\} \quad \left\{ \begin{array}{l} \text{ret} \rightarrow 0 * \alpha^P \mapsto \emptyset * E \end{array} \right\}$$

$$\begin{aligned}
& \left\{ \begin{array}{l} \text{path}(\text{path}, P, B, A) \wedge \alpha^P \mapsto B[C \wedge \text{can\_create}(A)] * E \\ \text{mkdir}(\text{path}) \\ \left\{ \alpha^P \mapsto B[C + A[\emptyset]] * E \right\} \\ \left\{ \begin{array}{l} \text{path} \Rightarrow P/A \\ \wedge \alpha^P \mapsto A[\emptyset] * E \end{array} \right\} \\ \text{rmdir}(\text{path}) \\ \left\{ \alpha^P \mapsto \emptyset * E \right\} \end{array} \right\} \\
& \left\{ \begin{array}{l} \text{path}(\text{existing}, P_1, A) \wedge \text{path}(\text{new}, P_2, D, B) \\ \wedge \alpha^{P_1} \mapsto A : I * \beta^{P_2} \mapsto D[C \wedge (\text{maybe}(\alpha) + \text{can\_create}(B))] * E \\ \text{link}(\text{existing}, \text{new}) \\ \left\{ \alpha^{P_1} \mapsto A : I * \beta^{P_2} \mapsto D[C + B : I] * E \right\} \\ \left\{ \begin{array}{l} \text{path}(\text{path}, P, A) \wedge \alpha^P \mapsto A : I * E \\ \text{unlink}(\text{path}) \\ \left\{ \alpha^P \mapsto \emptyset * E \right\} \end{array} \right\} \\ \left\{ \begin{array}{l} \text{path}(\text{existing}, P_1, A) \wedge \text{path}(\text{new}, P_2, B) \\ \wedge \alpha^{P_1} \mapsto A : I_1 * \beta^{P_2} \mapsto B : I_2 * E \\ \text{rename}(\text{existing}, \text{new}) \\ \left\{ \alpha^{P_1} \mapsto \emptyset * \beta^{P_2} \mapsto B : I_1 * E \right\} \end{array} \right\} \\ \left\{ \begin{array}{l} \text{path}(\text{existing}, P_1, A) \wedge \text{path}(\text{new}, P_2, D, B) \\ * \alpha^{P_1} \mapsto A : I * \beta^{P_2} \mapsto D[C \wedge (\text{maybe}(\alpha) + \text{can\_create}(B))] * E \\ \text{rename}(\text{existing}, \text{new}) \\ \left\{ \alpha^{P_1} \mapsto \emptyset * \beta^{P_2} \mapsto D[C + B : I] * E \right\} \end{array} \right\} \\
& \left\{ \begin{array}{l} \text{path}(\text{existing}, P_1, A) \wedge \text{path}(\text{new}, P_2, B) \wedge \alpha^{P_1} \mapsto A[C \wedge \text{is\_complete}] * \beta^{P_2} \mapsto B[\emptyset] * E \\ \text{rename}(\text{existing}, \text{new}) \\ \left\{ \alpha^{P_1} \mapsto \emptyset * \beta^{P_2} \mapsto B[C] * E \right\} \end{array} \right\} \\
& \left\{ \begin{array}{l} \text{path}(\text{existing}, P_1, A) \wedge \text{path}(\text{new}, P_2, D, B) \\ \wedge \alpha^{P_1} \mapsto A[C_1 \wedge \text{is\_complete}] * \beta^{P_2} \mapsto D[C_2 \wedge (\text{maybe}(\alpha) + \text{can\_create}(B))] * E \\ \text{rename}(\text{existing}, \text{new}) \\ \left\{ \alpha^{P_1} \mapsto \emptyset * \beta^{P_2} \mapsto D[C_2 + B[C_1]] * E \right\} \end{array} \right\} \\
& \left\{ \begin{array}{l} \text{path}(\text{existing}, P, A) \wedge \text{path}(\text{new}, P, A) \wedge \alpha^P \mapsto (C \wedge \text{entry}(A)) * E \\ \text{rename}(\text{existing}, \text{new}) \\ \left\{ \alpha^P \mapsto C * E \right\} \end{array} \right\}
\end{aligned}$$

Figure 6.5.: Axioms for featherweight POSIXFS, continued on the next three pages.



$$\begin{aligned}
& \left\{ \text{path} \Rightarrow P/D \wedge \text{dir} \rightarrow - * \alpha^P \mapsto D[C \wedge \neg \text{top\_address} \wedge \text{names}(A)] * E \right\} \\
& \quad \text{dir} := \text{opendir}(\text{path}) \\
& \quad \left\{ \exists H. \left( \text{dir} \rightarrow H * \alpha^P \mapsto D[C \wedge \text{names}(A)] * H \stackrel{\text{PH}}{\mapsto} A \right) * E \right\} \\
& \quad \left\{ A \neq \emptyset \wedge \text{dir} \rightarrow H \wedge \text{fn} \rightarrow - * H \stackrel{\text{PH}}{\mapsto} A * E \right\} \\
& \quad \quad \text{fn} := \text{readdir}(\text{dir}) \\
& \quad \left\{ B \in A \wedge \text{dir} \rightarrow H * \text{fn} \rightarrow B * H \stackrel{\text{PH}}{\mapsto} (A \setminus \{B\}) * E \right\} \\
& \quad \left\{ \text{dir} \rightarrow H * \text{fn} \rightarrow - * H \stackrel{\text{PH}}{\mapsto} \emptyset * E \right\} \\
& \quad \quad \text{fn} := \text{readdir}(\text{dir}) \\
& \quad \left\{ \text{dir} \rightarrow H * \text{fn} \rightarrow \epsilon * H \stackrel{\text{PH}}{\mapsto} \emptyset * E \right\} \\
& \quad \left\{ \text{dir} \rightarrow H * H \stackrel{\text{PH}}{\mapsto} A * E \right\} \\
& \quad \quad \text{closedir}(\text{dir}) \\
& \quad \quad \left\{ \text{dir} \rightarrow H * E \right\} \\
& \quad \left\{ \begin{array}{l} \text{path}(\text{path}, P, B, A) \wedge \text{flags} \Rightarrow \text{O\_CREATE} \\ \wedge \text{fd} \rightarrow - * \alpha^P \mapsto B[C \wedge \text{can\_create}(A)] * E \end{array} \right\} \\
& \quad \quad \text{fd} := \text{open}(\text{path}, \text{flags}) \\
& \quad \left\{ \exists \text{FD}, I. (\text{fd} \rightarrow \text{FD} * \alpha^P \mapsto B[C + A : I] * I \stackrel{\text{F}}{\mapsto} \emptyset * \text{FD} \stackrel{\text{PH}}{\mapsto} (I, 0) * E) \right\} \\
& \quad \left\{ \begin{array}{l} \text{path} \Rightarrow P/A \wedge \text{flags} \Rightarrow 0 \\ \wedge \text{fd} \rightarrow - * \alpha^P \mapsto A : I * I \stackrel{\text{F}}{\mapsto} \text{BS} * E \end{array} \right\} \\
& \quad \quad \text{fd} := \text{open}(\text{path}, \text{flags}) \\
& \quad \left\{ \exists \text{FD}. (\text{fd} \rightarrow \text{FD} * \alpha^P \mapsto A : I * I \stackrel{\text{F}}{\mapsto} \text{BS} * \text{FD} \stackrel{\text{PH}}{\mapsto} (I, 0) * E) \right\} \\
& \quad \left\{ \begin{array}{l} \text{path}(\text{path}, P, A) \wedge \text{flags} \Rightarrow \text{O\_TRUNC} \\ \wedge \text{fd} \rightarrow - * \alpha^P \mapsto A : I * I \stackrel{\text{F}}{\mapsto} \text{BS} * E \end{array} \right\} \\
& \quad \quad \text{fd} := \text{open}(\text{path}, \text{flags}) \\
& \quad \left\{ \exists \text{FD}. (\text{fd} \rightarrow \text{FD} * \alpha^P \mapsto A : I * I \stackrel{\text{F}}{\mapsto} \emptyset * \text{FD} \stackrel{\text{PH}}{\mapsto} (I, 0) * E) \right\} \\
& \quad \left\{ \begin{array}{l} \text{buf} \Rightarrow \text{BUF} \wedge \text{sz} \Rightarrow \text{LEN} \wedge |\text{BS}_2| = 0 \wedge |\text{BS}_3| = |\text{BS}_1| \\ \wedge \text{fd} \rightarrow \text{FD} * c \rightarrow - * \text{ncells}(\text{BUF}, \text{LEN}, \text{BS}_1) * I \stackrel{\text{F}}{\mapsto} \text{BS}_2 \cdot \text{BS}_3 \cdot \text{BS}_4 * \text{FD} \stackrel{\text{PH}}{\mapsto} (I, 0) * E \end{array} \right\} \\
& \quad \quad c := \text{write}(\text{fd}, \text{buf}, \text{sz}) \\
& \quad \left\{ \text{fd} \rightarrow \text{FD} * c \rightarrow \text{LEN} * \text{ncells}(\text{BUF}, \text{LEN}, \text{BS}_1) * I \stackrel{\text{F}}{\mapsto} \text{BS}_2 \cdot \text{BS}_1 \cdot \text{BS}_4 * \text{FD} \stackrel{\text{PH}}{\mapsto} (I, 0 + \text{LEN}) * E \right\}
\end{aligned}$$

Axioms for featherweight POSIXFS (continued from figure 6.5 on the preceding page).

$$\begin{aligned}
& \left\{ \begin{array}{l} \text{buf} \Rightarrow \text{BUF} \wedge \text{sz} \Rightarrow \text{LEN} \wedge |\text{BS}_2| = \text{O} \wedge |\text{BS}_3| < |\text{BS}_1| \\ \wedge \text{fd} \rightarrow \text{FD} * \text{c} \rightarrow - * \text{ncells}(\text{BUF}, \text{LEN}, \text{BS}_1) * \text{I} \xrightarrow{\text{F}} \text{BS}_2 \cdot \text{BS}_3 * \text{FD} \xrightarrow{\text{PH}} (\text{I}, \text{O}) * E \\ \text{c} := \text{write}(\text{fd}, \text{buf}, \text{sz}) \end{array} \right\} \\
& \left\{ \text{fd} \rightarrow \text{FD} * \text{c} \rightarrow \text{LEN} * \text{ncells}(\text{BUF}, \text{LEN}, \text{BS}_1) * \text{I} \xrightarrow{\text{F}} \text{BS}_2 \cdot \text{BS}_1 * \text{FD} \xrightarrow{\text{PH}} (\text{I}, \text{O} + \text{LEN}) * E \right\} \\
& \left\{ \begin{array}{l} \text{buf} \Rightarrow \text{BUF} \wedge \text{sz} \Rightarrow \text{LEN} \wedge \text{O} \geq |\text{BS}_2| \\ \wedge \text{fd} \rightarrow \text{FD} * \text{c} \rightarrow - * \text{ncells}(\text{BUF}, \text{LEN}, \text{BS}_1) * \text{I} \xrightarrow{\text{F}} \text{BS}_2 * \text{FD} \xrightarrow{\text{PH}} (\text{I}, \text{O}) * E \\ \text{c} := \text{write}(\text{fd}, \text{buf}, \text{sz}) \end{array} \right\} \\
& \left\{ \text{fd} \rightarrow \text{FD} * \text{c} \rightarrow \text{LEN} * \text{ncells}(\text{BUF}, \text{LEN}, \text{BS}_1) * \text{I} \xrightarrow{\text{F}} (\text{BS}_2 \cdot \underset{\text{O}-|\text{BS}_2|}{\odot} 0 \cdot \text{BS}_1) * \text{FD} \xrightarrow{\text{PH}} (\text{I}, \text{O} + \text{LEN}) * E \right\} \\
& \left\{ \begin{array}{l} \text{buf} \Rightarrow \text{BUF} \wedge \text{sz} \Rightarrow \text{LEN} \wedge |\text{BS}_1| = \text{O} \wedge |\text{BS}_2| = \text{LEN} \\ \wedge \text{fd} \rightarrow \text{FD} * \text{c} \rightarrow - * \text{ncells}(\text{BUF}, \text{LEN}, \text{V}) * \text{I} \xrightarrow{\text{F}} \text{BS}_1 \cdot \text{BS}_2 \cdot \text{BS}_3 * \text{FD} \xrightarrow{\text{PH}} (\text{I}, \text{O}) * E \\ \text{c} := \text{read}(\text{fd}, \text{buf}, \text{sz}) \end{array} \right\} \\
& \left\{ \text{fd} \rightarrow \text{FD} * \text{c} \rightarrow \text{LEN} * \text{ncells}(\text{BUF}, \text{LEN}, \text{BS}_2) * \text{I} \xrightarrow{\text{F}} \text{BS}_1 \cdot \text{BS}_2 \cdot \text{BS}_3 * \text{FD} \xrightarrow{\text{PH}} (\text{I}, \text{O} + \text{LEN}) * E \right\} \\
& \left\{ \begin{array}{l} \text{buf} \Rightarrow \text{BUF} \wedge \text{sz} \Rightarrow \text{LEN} \wedge |\text{BS}_1| = \text{O} \wedge |\text{BS}_2| \leq \text{LEN} \\ \wedge \text{fd} \rightarrow \text{FD} * \text{c} \rightarrow - * \text{ncell}(\text{BUF}, \text{LEN}, \text{V}) * \text{I} \xrightarrow{\text{F}} \text{BS}_1 \cdot \text{BS}_2 * \text{FD} \xrightarrow{\text{PH}} (\text{I}, \text{O}) * E \\ \text{c} := \text{read}(\text{fd}, \text{buf}, \text{sz}) \end{array} \right\} \\
& \left\{ \text{fd} \rightarrow \text{FD} * \text{c} \rightarrow |\text{BS}_2| * \text{ncell}(\text{BUF}, \text{LEN}, \text{BS}_2) * \text{I} \xrightarrow{\text{F}} \text{BS}_1 \cdot \text{BS}_2 * \text{FD} \xrightarrow{\text{PH}} (\text{I}, \text{O} + |\text{BS}_2|) * E \right\} \\
& \left\{ \begin{array}{l} \text{buf} \Rightarrow \text{BUF} \wedge \text{sz} \Rightarrow \text{LEN} \wedge \text{O} > |\text{BS}| \\ \wedge \text{fd} \rightarrow \text{FD} * \text{c} \rightarrow - * \text{ncells}(\text{BUF}, \text{LEN}, \text{V}) * \text{I} \xrightarrow{\text{F}} \text{BS} * \text{FD} \xrightarrow{\text{PH}} (\text{I}, \text{O}) * E \\ \text{c} := \text{read}(\text{fd}, \text{buf}, \text{sz}) \end{array} \right\} \\
& \left\{ \text{fd} \rightarrow \text{FD} * \text{c} \rightarrow 0 * \text{ncells}(\text{BUF}, \text{LEN}, \text{V}) * \text{I} \xrightarrow{\text{F}} \text{BS} * \text{FD} \xrightarrow{\text{PH}} (\text{I}, \text{O}) \right\} \\
& \left\{ \begin{array}{l} \text{to} \Rightarrow \text{O}_1 \wedge \text{wh} \Rightarrow \text{SEEK\_SET} \wedge \text{O}_1 \geq 0 \\ \wedge \text{fd} \rightarrow \text{FD} * \text{off} \rightarrow - * \text{FD} \xrightarrow{\text{PH}} (\text{I}, \text{O}_2) * E \\ \text{off} := \text{lseek}(\text{fd}, \text{to}, \text{wh}) \end{array} \right\} \\
& \left\{ \text{fd} \rightarrow \text{FD} * \text{off} \rightarrow \text{O}_1 * \text{FD} \xrightarrow{\text{PH}} (\text{I}, \text{O}_1) * E \right\}
\end{aligned}$$

Axioms for featherweight POSIXFS (continued from figure 6.5 on page 256).

$$\begin{array}{c}
\left\{ \begin{array}{l} \text{to} \Rightarrow \text{O}_1 \wedge \text{wh} \Rightarrow \text{SEEK\_CUR} \wedge (\text{O}_1 + \text{O}_2) \geq 0 \\ \wedge \text{fd} \rightarrow \text{FD} * \text{off} \rightarrow - * \text{FD} \xrightarrow{\text{PH}} (\text{I}, \text{O}_2) * E \end{array} \right\} \\
\text{off} := \text{lseek}(\text{fd}, \text{to}, \text{wh}) \\
\left\{ \text{fd} \rightarrow \text{FD} * \text{off} \rightarrow (\text{O}_1 + \text{O}_2) * \text{FD} \xrightarrow{\text{PH}} (\text{I}, \text{O}_1 + \text{O}_2) * E \right\} \\
\left\{ \begin{array}{l} \text{to} \Rightarrow \text{O}_1 \wedge \text{wh} \Rightarrow \text{SEEK\_END} \wedge (|\text{S}| + \text{O}_2) \geq 0 \\ \wedge \text{fd} \rightarrow \text{FD} * \text{off} \rightarrow - * \text{I} \xrightarrow{\text{F}} \text{S} * \text{FD} \xrightarrow{\text{PH}} (\text{I}, \text{O}_2) * E \end{array} \right\} \\
\text{off} := \text{lseek}(\text{fd}, \text{to}, \text{wh}) \\
\left\{ \text{fd} \rightarrow \text{FD} * \text{off} \rightarrow (|\text{S}| + \text{O}_1) * \text{I} \xrightarrow{\text{F}} \text{S} * \text{FD} \xrightarrow{\text{PH}} (\text{I}, |\text{S}| + \text{O}_1) * E \right\} \\
\left\{ \begin{array}{l} \text{fd} \rightarrow \text{FD} * \text{FD} \xrightarrow{\text{PH}} (\text{I}, \text{OFFSET}) \end{array} \right\} \\
\text{close}(\text{fd}) \\
\left\{ \text{fd} \rightarrow \text{FD} \right\} \\
\left\{ \begin{array}{l} \text{path} \Rightarrow \text{PATH} \wedge ((\text{PATH} = \text{P}/\text{A}) \vee (\text{PATH} = \text{T} \wedge \text{P} = \emptyset_p \wedge \text{A} = \text{T})) \\ \wedge \text{t} \rightarrow - * \alpha^{\text{P}} \mapsto \text{A}[\beta] * E \end{array} \right\} \\
\text{t} := \text{stat}(\text{path}) \\
\left\{ \text{t} \rightarrow \text{D} * \alpha^{\text{P}} \mapsto \text{A}[\beta] * E \right\} \\
\left\{ \begin{array}{l} \text{path} \Rightarrow \text{P}/\text{A} \wedge \text{t} \rightarrow - * \alpha^{\text{P}} \mapsto \text{A} : \text{I} * E \end{array} \right\} \\
\text{t} := \text{stat}(\text{path}) \\
\left\{ \text{t} \rightarrow \text{F} * \alpha^{\text{P}} \mapsto \text{A} : \text{I} * E \right\} \\
\left\{ \begin{array}{l} \text{path} \Rightarrow \text{PATH} \\ \wedge \text{PATH} = \text{P}_1/\text{D}/\text{A}/\text{P}_2 \vee \text{PATH} = \text{P}_1/\text{D}/\text{A} \vee ((\text{PATH} = \text{T}/\text{A}/\text{P}_2 \vee \text{PATH} = \text{T}/\text{A}) \wedge \text{P}_1 = \emptyset_p \wedge \text{D} = \text{T}) \\ \wedge \text{t} \rightarrow - * \alpha^{\text{P}_1} \mapsto \text{D}[\text{C} \wedge \neg \text{top\_entry}(\text{A}) \wedge \neg \text{top\_address}] * E \end{array} \right\} \\
\text{t} := \text{stat}(\text{path}) \\
\left\{ \text{t} \rightarrow \text{N} * \alpha^{\text{P}_1} \mapsto \text{D}[\text{C}] * E \right\} \\
\left\{ \begin{array}{l} \text{size} \Rightarrow \text{S} \wedge \text{ptr} \rightarrow - * E \end{array} \right\} \\
\text{ptr} := \text{malloc}(\text{size}) \\
\left\{ \exists \text{P}, \text{V}. \text{ptr} \rightarrow \text{P} * (\text{P} - 1) \mapsto \text{S} * \text{ncells}(\text{P}, \text{S}, \text{V}) * E \right\} \\
\left\{ \begin{array}{l} \text{ptr} \Rightarrow \text{P} \wedge (\text{P} - 1) \mapsto \text{S} * \text{ncells}(\text{P}, \text{S}, \text{V}) * E \end{array} \right\} \\
\text{free}(\text{ptr}) \\
\left\{ E \right\}
\end{array}$$

Axioms for featherweight POSIXFS (continued from figure 6.5 on page 256).

```

{ path ⇒ P/A ∧ ret → - * αP ↦ A : I * E }
ret := remove(path) ≜ local t {
  ret := 0
  t := stat(path);
  { path ⇒ P/A ∧ ret → 0 * t → F * αP ↦ A : I * E }
  if t = F
    { path ⇒ P/A ∧ ret → 0 * αP ↦ A : I * E }
    unlink(path);
    { path ⇒ P/A ∧ ret → 0 * αP ↦ ∅ * E }
  else if t = D
    rmdir(path);
  else ret := -1;
}
{ ret → 0 * αP ↦ ∅ * E }

```

Figure 6.3.: The code for `ret := remove(path)`, with the proof sketch for the case when `path` is a file.

We now consider specifying and reasoning about client programs. Noting that `remove` only removes directories if they are empty, we can also specify a command `rmdirRec` that recursively descends from a given directory, removing it and all its descendants. The axiom for this command states that, when provided with the path to a directory, it will remove that entire subtree from the file system:

$$\left\{ \text{path} \Rightarrow P/N * \alpha^P \mapsto N[C \wedge \text{is\_complete}] * E \right\} \text{rmdirRec}(\text{path}) \left\{ \alpha^P \mapsto \emptyset * E \right\}$$

Another common operation on file systems is the copying of files. Because file sizes can exceed available process memory, they are often copied chunk by chunk. A specification for such an operation is:

$$\left\{ \begin{array}{l} \text{path}(\text{source}, P_1, A) \wedge \text{path}(\text{target}, P_2, C, D) \\ \wedge \alpha^{P_1} \mapsto A : I_1 * \beta^{P_2} \mapsto D[C \wedge \text{can\_create}(A)] * I_1 \xrightarrow{F} SD * E \end{array} \right\} \\ \text{ret} := \text{fileCopy}(\text{source}, \text{target}) \\ \left\{ \exists I_2. \alpha^{P_1} \mapsto A : I * \beta^{P_2} \mapsto D[C + A : I_2] * I_1 \xrightarrow{F} SD * I_2 \xrightarrow{F} SD * E \right\}$$

The pre-condition states that a file exists at the path given by `source`, and that the file we will create at `target` does not yet exist. The disjunction on the value of `TGT` handles the case where we may be creating a file in the root directory. The post-condition ensures that the `target` file has been created, and a corresponding entry `I2` added to the file heap

with identical contents to that of the `source` file. The implementation code and associated proof is in the appendix, section A.1.5.

## 6.4. Software installer example

One type of program that interacts extensively with the file system is a *software installer*. Modern software is typically downloaded by the users in an archive, and is thus initially unusable. The goal of a software installer is to take this archive and place the contents at the correct points in the user's file system, such that the software can run. Most installers also check that the users computer meets certain requirements (e.g. no conflicting software is already installed), remove previous versions of the software, and either remove or alter incompatible configuration files.

Good software installers either successfully complete installation, or make no changes to the machine. Here, we develop an good installer for the fictional software “Widget Version 2”. It supersedes “Widget Version 1”, but is unfortunately incompatible with any V1 configuration files that users may have created. Widget V2 consists of a program executable, `widgProg` and a data file, `widgData`. We follow common conventions for placing these files in the users file system [64]. The two Widget files will be placed in the directory `⊔/opt/widget/`. The program will be made usable by creating a hard link from `⊔/usr/bin/widget` (the system directory for user provided executables) to the file `⊔/opt/widget/widgProg`.

The installer must contend with the possible existence of Widget V1, as well as unrelated files that the user may have chosen to store at the paths it wants to use. For example, a user may have created a directory at `⊔/usr/bin/widget` - our installer should not remove this, as that could damage other components of their system. However, we *will* remove it if it is just a file, as we assume it to be a Widget V1 executable. Similarly, we must remove `⊔/opt/widget` only if it is a directory. Finally, if any user has placed a Widget V1 configuration file in their home directory, e.g. `⊔/home/usern/.wconf`, we choose to remove that as well.

Considering these requirements, the Widget V2 installer must:

- ① Check to see if entries already exist at the locations we wish to place Widget V2 files, and abort installation if they are not Widget V1 entries.
- ② Remove Widget V1 entries, if they were found.
- ③ Check for a Widget V1 configuration file in each users home directory, removing it if found.

- ④ Copy Widget V2 files to the target location on the file system.
- ⑤ Make a link to the Widget V2 executable, so the user can run it.

The installation of a simple, two file program has become quite a complex task. We therefore specify our intuitions about good behaviour and prove that our installer matches them. We build a pre-condition for our installer out of several sub-assertions. In these, the assertion  $\otimes_{x \in E} \phi$  is the iterated version of  $+$ , interpreted as  $\phi_1 + \dots + \phi_{|E|}$  where each  $\phi_i$  has  $x$  bound to a distinct member of  $E$ . We also extend the logical environment to include pairs of logical values, instrumented file systems and process heaps, and extend the heap assertions to include such logical variables. In this way, we can write logical variables that capture invariants about the entire heap.

$$\begin{aligned}
v2Dir_P &\triangleq \text{widgProg} : J + \text{widgData} : K \\
v2Files_P &\triangleq J \xrightarrow{F} \text{PROG} * K \xrightarrow{F} \text{DAT} \\
home_P &\triangleq \text{home} \left[ \otimes_{(N,C) \in \text{HOMES}N} \left[ \begin{array}{l} (C \wedge \neg \text{top\_entry}(.wconf)) \\ + (\emptyset \vee \exists I. .wconf : I) \\ \wedge \neg \text{top\_address} \end{array} \right] \right] \\
bin_P &\triangleq \text{bin} \left[ \begin{array}{l} (B \wedge \neg \text{top\_entry}(\text{widget})) + \\ (\emptyset \vee \text{entry}(\text{widget})) \wedge \neg \text{top\_address} \end{array} \right] \\
opt_P &\triangleq \text{opt} \left[ \begin{array}{l} (T + (\emptyset \vee \text{widget}[T_w \wedge \neg \exists \alpha. \diamond \alpha]) \\ \wedge \neg \text{top\_address} \end{array} \right]
\end{aligned}$$

Each of these fragments describes the states that parts of the file system may be in for the Widget installer to run safely. The directory entries that make up the Widget version 2 program are described by  $v2Dir_P$ ; their corresponding file data by  $v2Files_P$ . We require that these files be in some unspecified place in the file system, with this location is stored in variable `instLoc`.

The  $home_P$  resource captures all the home directories on the system, along with the fact that some of them will contain a ‘`.wconf`’ file containing Widget V1 configuration data for that user. We use a logical variable, `HOMES` to store the pair of all the home directory names and their contents. This allows us to construct a loop invariant as we iterate across each directory looking for the ‘`.wconf`’ file.

The  $bin_P$  resources captures the normal UNIX executables directory, that may or may not contain a “widget” entry. Notice we require that there be no structural addresses present in any widget directory that may exist, as any such directory will be deleted. Also, we require for most of our components that there be no top addresses. This allows

us to assert the absence of a file (the  $\neg top\_entry(\dots)$  assertions) safe in the knowledge that it is not present in another frame. The use of *top\_address* does not stop there being holes deeper within the structures captured by the logical variables, so we are still minimal in the resource we demand.

We combine these descriptions into a pre-condition for the program. The pre-condition makes extensive use of structural addresses, localising the proof to just the parts of the file system touched by the installer. It also snapshots the initial state of resource in a logical variable *w*, to show that nothing changes in the event of failure.

$$P_{ins} \triangleq \text{instLoc} \rightarrow \text{IL} * \text{w} \wedge \delta^{\text{IL}} \mapsto v2Dir_P * v2Files_P * \\ \alpha^\top \mapsto home_P * \gamma^{\top/usr} \mapsto bin_P * \beta^\top \mapsto opt_P$$

If the installer fails, we expect that the file system should be unchanged. If it does not fail, we expect that the program should be successfully installed. There should be no other outcomes. We describe a successful installation with the following predicates:

$$\begin{aligned} v2Dir_Q &\triangleq \text{widgProg} : J' + \text{widgDat} : K' \\ v2Files_Q &\triangleq J' \xrightarrow{F} \text{PROG} * K' \xrightarrow{F} \text{DAT} \\ home_Q &\triangleq \text{home} \left[ \otimes_{(N,C) \in \text{HOMES}^N} \left[ \begin{array}{c} C \\ \wedge \neg top\_entry('wconf') \\ \wedge \neg top\_address \end{array} \right] \right] \\ bin_Q &\triangleq \text{bin}[B + \text{widget} : J'] \\ opt_Q &\triangleq \text{opt}[T + \text{widget}[v2Dir_Q]] \end{aligned}$$

The program post-condition is built from these predicates.

$$Q_{ins} \triangleq \begin{aligned} &\exists \text{RET}, J', K'. \text{ret} \rightarrow \text{RET} * \text{instLoc} \rightarrow \text{IL} * \\ &\neg \text{RET} \Rightarrow \text{w} \wedge \\ &\text{RET} \Rightarrow \left( \begin{array}{l} \delta^{\text{IL}} \mapsto v2Dir_P * \alpha^\top \mapsto home_Q \\ * \beta^\top \mapsto opt_Q * \gamma^{\top/usr} \mapsto bin_Q \\ * v2Files_Q * \text{true} \end{array} \right) \end{aligned}$$

Installation can fail if entries already exist at the paths we wish to use (point ① above). In this case, the program assigns variable *ret* to **false** and we use the *w* to show that the file system is unchanged. If it is true, i.e. the installation was successful, then file system reflects this: the  $\top/opt/widget$  directory exists and is suitably populated by the new

```

{  $P_{ins}$  }
ret := installWidgetV2  $\triangleq$ 
local t1, t2, homeDirH, userDir {
  {
    instLoc  $\rightarrow$  iL
    *  $\delta^{iL} \mapsto v2Dir_P * \alpha^\top \mapsto home_P *$ 
     $\gamma^{\top/usr} \mapsto bin_P * \beta^\top \mapsto opt_P * v2Files_P$ 
  }
  // Check for preexisting files (points ① and ②)
  t1 := stat('T/usr/bin/widget');
  t2 := stat('T/opt/widget');
  {
    (t1  $\rightarrow F \vee$  t1  $\rightarrow D \vee$  t1  $\rightarrow N$ ) * (t2  $\rightarrow F \vee$  t2  $\rightarrow D \vee$  t2  $\rightarrow N$ )
    * instLoc  $\rightarrow$  iL *  $\delta^{iL} \mapsto v2Dir_P * \alpha^\top \mapsto home_P *$ 
     $\gamma^{\top/usr} \mapsto bin_P * \beta^\top \mapsto opt_P * v2Files_P$ 
  }
  if t1 = D  $\vee$  t2 = F
    // Existing files are of the wrong type, so fail
    ret = false;
  else
    if t1 = F
      { t  $\rightarrow F * \gamma^{\top/usr} \mapsto bin[B+widget : I]$  }
      unlink('T/usr/bin/widget');
      { t  $\rightarrow F * \gamma^{\top/usr} \mapsto bin[B]$  }
    if t2 = D
      {  $\beta^\top \mapsto opt[T + widget[T_w \wedge is\_complete]]$  }
      rmdirRec('T/opt/widget');
      {  $\beta^\top \mapsto opt[T]$  }
      {
        instLoc  $\rightarrow$  iL *  $\delta^{iL} \mapsto v2Dir_P * \alpha^\top \mapsto home_P *$ 
         $\gamma^{\top/usr} \mapsto bin[B] * \beta^\top \mapsto opt[T] * v2Files_P$ 
      }
  // Remove any stale Widget configuration files (point ③)
  {
     $\alpha^\top \mapsto home$   $\left[ \otimes_{(N,C) \in HOMES^N} \left[ \begin{array}{l} (C \wedge \neg top\_entry(.wconf)) \\ + (\emptyset \vee \exists I. .wconf : I) \\ \wedge \neg top\_address \end{array} \right] \right]$ 
  }
}

```

Figure 6.4.: Code and proof for Widget V2 software installer (continued on next page)

Widget V2 files, no user specific configuration files exist, and the ‘T/usr/bin/widget’ executable points to the newly installed program. Notice that any Widget V1 files are subsumed by the predicate **true**, awaiting garbage collection, and that the logical variables T, B, ... are witness to the fact that no other resource in our pre-condition has changed.

The code for the installer, and proof that it meets this specification  $\{P_{ins}\} \text{installWidgetV2} \{Q_{ins}\}$ , is given in figure 6.4. As another testament to the compositional properties of our reasoning, notice that we reuse the `rmdirRec`, to delete existing directory subtrees, and `fileCopy` to copy the binary and data files from the installer directory to the target location. Throughout the sketch we implicitly use the frame rule and semantic consequence to localise the resource we are acting on.



```

homeDirH := opendir('T/home');
userDir = readdir(homeDirH);

```

$$\left\{ \begin{array}{l} \text{homeDirH} \rightarrow \text{HDIR} * \text{userDir} \rightarrow \text{UDIR} * \text{HDIR} \stackrel{\text{PH}}{\mapsto} \text{US} \\ * \exists \text{NH. NH} = \text{US} \cup \{\text{UDIR}\} \wedge \\ \alpha^\top \mapsto \text{home} \left[ \otimes_{(N,C) \in \text{HOMES}^N} \left[ \begin{array}{l} (C \wedge \neg \text{top\_entry}(.wconf)) + \\ (N \in \text{NH} \implies (\emptyset \vee \exists I. .wconf : I)) \\ \wedge N \notin \text{NH} \implies \emptyset \\ \wedge \neg \text{top\_address} \end{array} \right] \right] \end{array} \right\}$$

```

while userDir  $\neq$   $\epsilon$ 
  t := stat('T/home'/userDir/'.wconf');
  if t1 = F
    unlink('T/home'/userDir/'.wconf');
  userDir := readdir(homeDirH);
closedir(homeDirH);

```

$$\left\{ \alpha^\top \mapsto \text{home} \left[ \otimes_{(N,C) \in \text{HOMES}^N} \left[ \begin{array}{l} C \wedge \neg \text{top\_entry}(.wconf) \\ \wedge \neg \text{top\_address} \end{array} \right] \right] \right\}$$

```


```

$$\left\{ \begin{array}{l} \text{instLoc} \rightarrow \text{IL} * \delta^{\text{IL}} \mapsto v2Dir_P * \alpha^\top \mapsto \text{home}_Q \\ * \gamma^{\top/usr} \mapsto \text{bin}[B] * \beta^\top \mapsto \text{opt}[T] * J \stackrel{F}{\mapsto} \text{PROG} * K \stackrel{F}{\mapsto} \text{DAT} \end{array} \right\}$$

```

// Copy the new Widget files and link the executable
// (Points ④ and ⑤)
mkdir('T'/opt/widget');
fileCopy(instLoc/'.widgProg', 'T'/opt/widget');
fileCopy(instLoc/'.widgData', 'T'/opt/widget');
link('T'/opt/widget/widgProg', 'T'/usr/bin/widget');
ret := true

```

$$\left\{ \begin{array}{l} \exists J, K. \\ \text{instLoc} \rightarrow \text{IL} * \delta^{\text{IL}} \mapsto v2Dir_P * \alpha^\top \mapsto \text{home}_Q \\ * \beta^\top \mapsto \text{opt} \left[ T + \text{widget} \left[ \begin{array}{l} \text{widgProg} : J' \\ + \text{widgData} : K' \end{array} \right] \right] \\ * \gamma^{\top/usr} \mapsto \text{bin}[B + \text{widget} : J'] * J \stackrel{F}{\mapsto} \text{PROG} \\ * K \stackrel{F}{\mapsto} \text{DAT} * J' \stackrel{F}{\mapsto} \text{PROG} * K' \stackrel{F}{\mapsto} \text{DAT} \end{array} \right\}$$

```


```

$$\left\{ \begin{array}{l} \exists \text{RET}, J', K'. \text{ret} \rightarrow \text{RET} * \text{instLoc} \rightarrow \text{IL} * \\ \neg \text{RET} \implies W \wedge \\ \text{RET} \implies \left( \begin{array}{l} \delta^{\text{IL}} \mapsto v2Dir_P * \alpha^\top \mapsto \text{home}_Q \\ * \beta^\top \mapsto \text{opt}_Q * \gamma^{\top/usr} \mapsto \text{bin}_Q * v2Files_Q * \text{true} \end{array} \right) \\ Q_{\text{ins}} \end{array} \right\}$$

Figure 6.4.: Widget V2 Software Installer program (continued)

## 6.5. Summary

This chapter has introduced a structural separation logic specification for the *POSIX* file system library. This forms the second of our two large case studies, demonstrating the entire process of analysing and formalising a “real world” library using our techniques. Our key contributions are:

1. **Featherweight POSIXFS:** Section 6.1 introduced a novel subset of POSIX, identifying and describing a set of core commands used in file systems. This subset is both accurate with respect to the POSIX specification, and amiable to formal analysis.
2. **Axiomatic specification for POSIX file systems:** Using structural separation logic, section 6.3.3 gives a axiomatic specification of our POSIX subset. The axioms are accurate to POSIX<sup>4</sup>, providing an unambiguous description of the behaviour of the commands.
3. **Demonstration of naturally stable promises:** Our specifications are given as local axioms by using path promises. This demonstrates that the promise technique scales to a real example.
4. **Local reasoning for POSIX file systems programs:** The installer example of section 6.4 demonstrates that our specification can be used to reason about non-trivial file system manipulating programs.

---

<sup>4</sup>Expect for the two cases noted, `readdir` and `malloc`.

## 7. Obligations

The naturally stable promises of chapter 5 allow passive access to information in the surrounding context. However, they restrict the set of commands that can be considered to those which can never break promises. This prevents some useful libraries from being specified, as seen with the list library in section 5.3.2. There, when using promises to state the list index from which an abstract heap cell was abstractly allocated, the `remove` had to be restricted to the *last* element of a list in order to preserve stability.

This chapter introduces *obligations*, which act as the dual of promises. Whereas the promises associated with an abstract heap cell give information about the shape of the context, obligations associated with a cell restrict the shape of the data within the cell. Example obligations might be “This abstract heap cell always contains root tree node N”, or “The number of list elements in this abstract heap cell is at least 3”.

When using obligations, the set of command axioms is unrestricted, subject to the standard requirement of atomic soundness 12. Proving commands atomically sound means showing that the obligations can never be violated. Thus, an `appendChild` command acting on a cell with the obligation “Always contains root tree node N” would have to ensure that, after the command, the cell still contained root node N. A list `remove` command acting on a cell with the obligation “number of list elements is at least 3” could never reduce the number of cells to less than 3. Obligations thus ensure that promises are kept. Whenever a non-naturally stable promise is issued during abstract allocation, an associated obligation is created to ensure the promise is kept. When the promise is destroyed, so is the obligation.

Our work on obligations is less mature than other chapters of these thesis. Here, we develop the initial theory of obligations, demonstrating that they restore full function to the list `remove` command. We show how to reasoning using obligations. We will reexamine our DOM specification (chapter 4) in the light of this work, and demonstrate how the technique could be used to improve the specification, by enabling new behaviours or by tightening the footprints for commands. At the end of this chapter, we speculate on how obligations may allow us to reason about *symbolic links*. Symbolic links are a type of file that *redirect* file system path resolution to continue at another point in the file-system

tree.

## Related work

As discussed in section 5, promises can be seen as both a style of rely-guarantee, deny-guarantee reasoning, and of passivity system. This is similarly true of obligations. Just as a promise associated with a heap cell is something about the environment the cell can rely on, an obligation associated with a heap cell is a *guarantee* to the environment that the given invariant will always hold. As promises and obligations can be split (by splitting the abstract heap cells they are attached to), the work is more similar to Deny-Guarantee than Rely-Guarantee. However, both systems are typically used to enable concurrent update to the *same* resource, with the guarantees and rely (denys) being used to ensure a safe protocol for this update.

Thus, we again prefer to link this work to *permission systems*. Separation logic, traditionally using the Boyland-style fractional permissions [10], means that each heap cell in a footprint can be in two states: write and read, or just read. Promises and obligations offer similar statements, but over *invariants*. As discussed in 5, a promise associated with a heap cell is a “read-only” view of some data. It can be seen as a permission  $\pi < 1$  on that data. However, obligations are more expressive. Rather than the cell that issued the promise also being associated with  $\pi < 1$ , the obligation states only that it must maintain some invariant. Consider a heap cell  $x \mapsto v$ . Using normal permissions, this splits into  $x \xrightarrow{0.5} v * x \xrightarrow{0.5} v$ , rendering both passive. However, if  $v$  is *structured* data using promises and obligations, it can be split  $\exists \alpha. x_{\alpha:I} \mapsto v_1 * \alpha^{\mathfrak{m}:I} \mapsto v_2$ . Here, not only is the data is split, but some invariant  $I$  is shared. Thus,  $x$  can still be updated, as long as  $I$  is still true.

## 7.1. Lists access via indexing

Recall the list indexing promises of section 5.3.2. These promises are naturally stable only if the `remove` command can only remove the final list element. This prevents any issued indexing promises from being broken. Here, we work with the unrestricted `remove` command, and prevent promises being broken with with *obligations*.

We update the two index promises with their associated obligations. Again, one states the index at which an abstract heap cell can be found, whilst the other states the length of the data in a cell. Informally, the promise and obligation pairs are:

- $\alpha \Leftarrow i$  If  $\alpha$  maps to  $\mathfrak{m}$ , this denotes a promise that the data comes from index  $i$  in the list. If  $\alpha$  does not map to  $\mathfrak{m}$ , this denotes *an obligation* to  $\alpha$  to keep the body address  $\mathbf{x}$  at index  $i$  of the list.
- $\alpha \Rightarrow n$  If  $\alpha$  maps to  $\mathfrak{m}$ , this denotes an obligation to keep the length of the data to exactly  $n$ . If  $\alpha$  does not map to  $\mathfrak{m}$ , this denotes a promise that the data contained within the cell at  $\alpha$  will be of length  $n$ .

Using the first as an obligation, we can split the element 4 from the list  $\mathbf{L} \mapsto [3 \otimes 1 \otimes 4 \otimes 2]$  in such a way that the cell  $\mathbf{L}$  still knows that the element must be at index 3. As , we write promises as superscripts on heap addresses, we write obligations as subscripts:

$$\mathbf{L}_{\alpha \Leftarrow 3} \mapsto [3 \otimes 1 \otimes 4 \otimes \alpha] * \alpha^{\mathfrak{m} \Leftarrow 3} \mapsto 2$$

By associating this obligation with  $\mathbf{L}$ , we know we must never alter the data within that cell in any way that would make the body address  $\alpha$  no longer the 3<sup>rd</sup> child. This, by itself, would severely restrict what can be done with the  $\alpha$  cell. One could never safely abstractly allocate part of the  $3 \otimes 1 \otimes 4$  sub-list, as the resultant  $\mathbf{L}$  addressed cell would not have enough information to guarantee that  $\alpha$  is at index 3. This problem can be overcome with the *length promise and obligation*. To allow, say,  $3 \otimes 1$  to be allocated at  $\beta$ , we issue it with a *length obligation*, which grants  $\mathbf{L}$  a *length promise* from  $\beta$ :

$$\mathbf{L}_{\alpha \Leftarrow 3}^{\beta \Rightarrow 2} \mapsto [\beta \otimes 4 \otimes \alpha] * \alpha^{\mathfrak{m} \Leftarrow 3} \mapsto 2 * \beta_{\mathfrak{m} \Rightarrow 2} \mapsto 3 \otimes 1$$

This promise states that the data compressed into the  $\beta$  address of  $\alpha$  will always contain exactly two elements. The promise is given the  $\beta$  name, so that when  $\beta$  is compressed, it can be removed. The promise will be paired with whichever cell has the  $\beta$  body address, allowing it to be transferred as the tree is further cut up. For example, if the cell  $\alpha$  is split via abstract allocation. Now, considered in isolation, the assertion  $\mathbf{L}_{\alpha \Leftarrow 3}^{\beta \Rightarrow 2} \mapsto [\beta \otimes 4 \otimes \alpha]$  is obliged to always keep  $\alpha$  at index 3. It can ensure that this is true because the  $\beta$  cell has promised it will contain data of length 2.

## 7.2. Obligations

Obligations are the *dual* of promises. Whereas promises tell a datum about the shape of context which fits around it, obligations are restrictions on the datum that ensure it always conforms to a specific shape. When data is associated with an unstable promise then, somewhere, a matching *obligation* exists that ensures the promise will always hold.

Figure 7.1 extends a promise-carrying datum into a *promise- and obligation-carrying datum*. Before, in figure 5.5, this datum had three promises: one head promise, and two body promises. Now, it still has these promises, but has gained a *head obligation* and two *body obligations*.

There is strong symmetry here: promises received from super-data are *head promises*, consisting of *head data* (definition 91) with exactly one address ( $\mathfrak{m}$ ). Promises received from sub-data are *body promises*, made up of *body data* (definition 92) containing no holes. Obligations reverse this relationship: obligations to super-data (*head obligations*) consist of *body data*. Obligations made to sub-data (*body obligations*) are made up of head data.

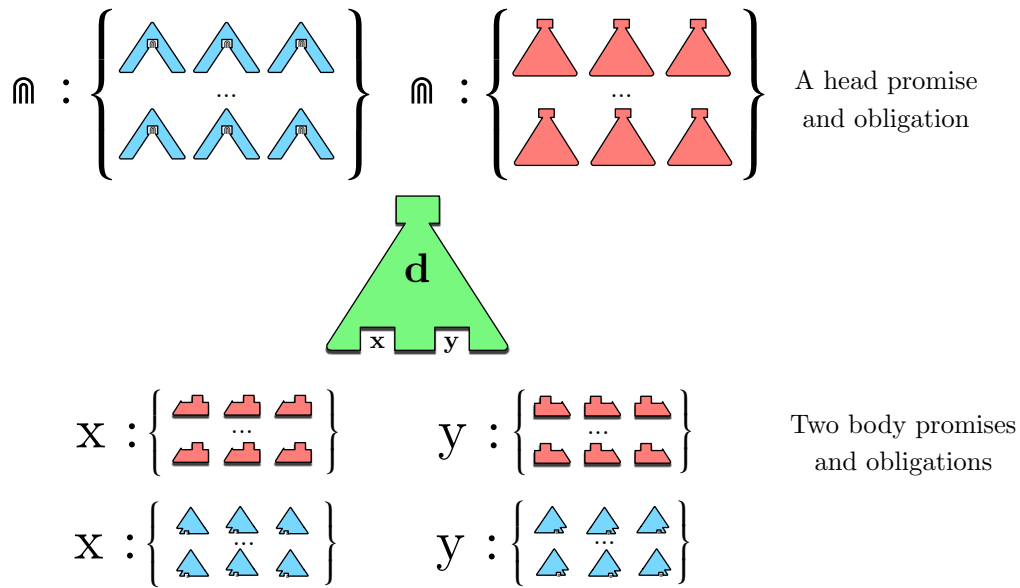


Figure 7.1.: A datum with three promises, and three obligations

Any data carrying obligations must *enforce* them, so that the combination of the data and promises is sufficient to show that the obligation is satisfied. It is not legal to construct data which does not satisfy its obligations. This implies that a datum must have extracted sufficient promises from any sub- and super-data to ensure the obligations remain true.

Compression using naturally stable promises could verify that the promises held by comparing the closure of the data against the promises. This was sufficient, as naturally stable promises can never be broken; as long as the data currently satisfies the promise,

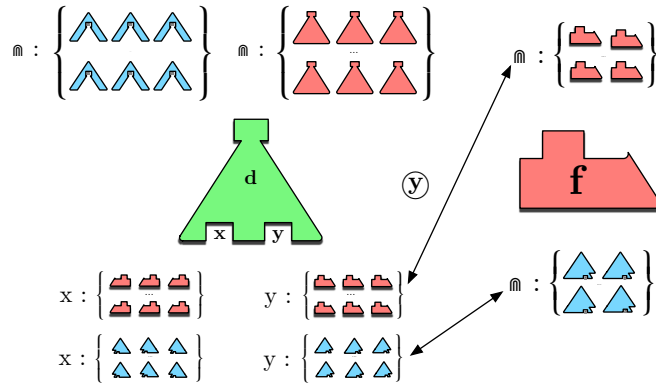


Figure 7.2.: Compression when using obligations

the stability of the reasoning ensures that it must *always* have satisfied it. With unstable promises, we instead check promises against the obligations. As the obligations bound the range of changes that are possible, if the promises are satisfied by the obligations, they must always have been true.

### Index and length promises for lists

As an example of promise- and obligation-carrying data, consider figure 7.3. This considers the promises and obligations for list indexing and lengths for the list library with an `item` command. It shows the combinations of promises and obligations that allow or disallow compression in a variety of cases.

#### 7.2.1. Formalising obligations

We now formalise the intuitions of obligations introduced in the last section. As stated, obligations are, in many ways, symmetric to promises. The notions of head and body data (definitions 91 and 92 respectively) are retained unchanged. However, rather than head obligations used head data, they will use *body data*. This is because, rather than stating what information will be provided *by* the super-data, they state what information is provided *to* the super-data.

**Parameter 22** (Head obligations). Assume a set of **head obligations** `HEADOBLS`, ranged over by  $\omega^H$ , with the type:

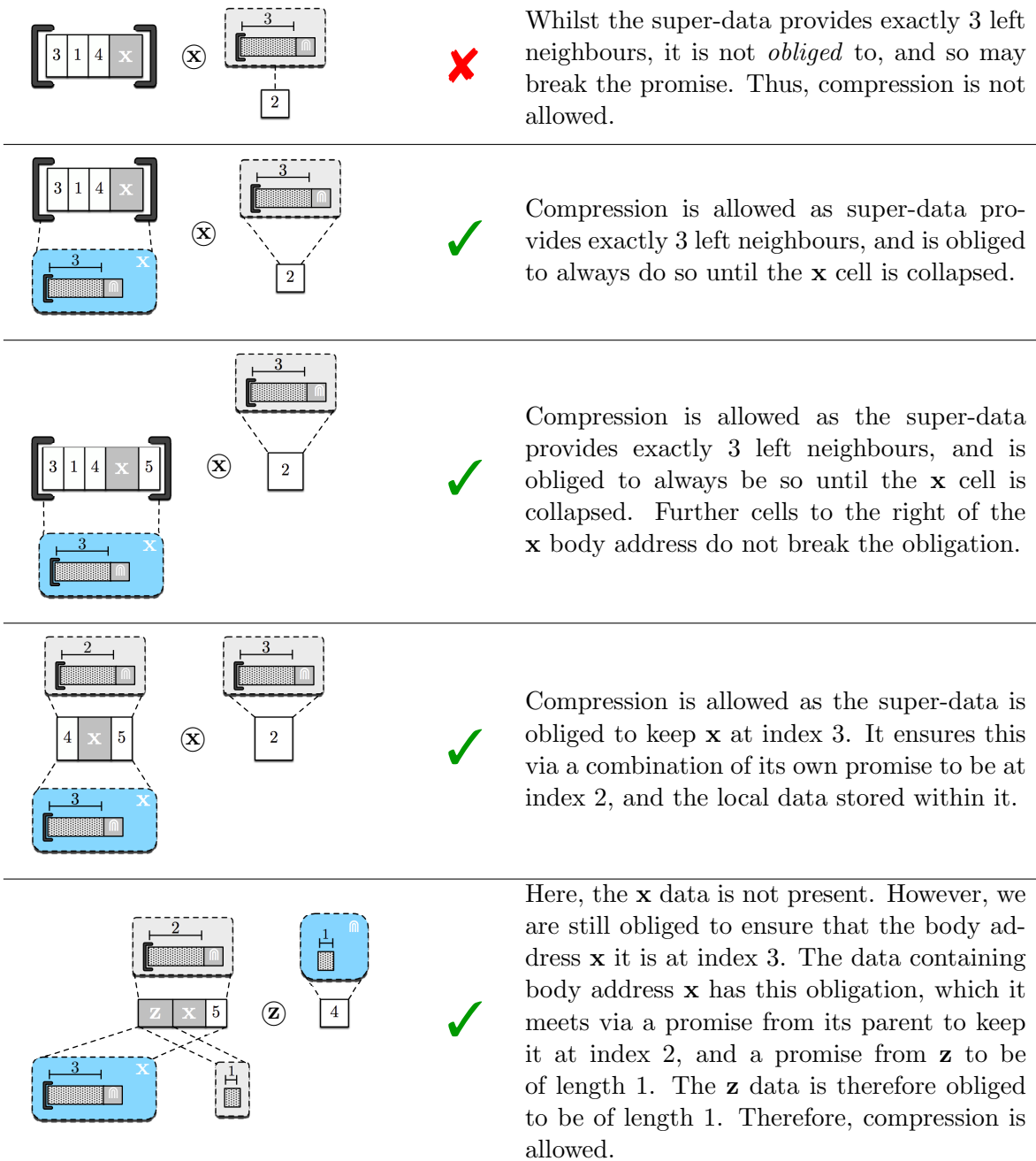






Figure 7.3.: Examples of promises for list indexing. The promise  indices that structural address  $\mathfrak{m}$  is at index 3 of list  $l$ . The associated obligation to  $x$  is . The promise that the data within  $z$  is of length 1 is . The associated obligation to the super-data is .



$$\text{HEADOBLS} \subseteq \{\mathbb{m}\} \times \text{BODYDATA}$$

Similarly, body obligations make guarantees *to* the sub-data that will eventually compress into a body address. These obligations describe the shape of heads they will compress into. Ergo, body obligations use *head data*.

**Parameter 23** (Body obligations). Assume a set of **body obligations** BODYOBLS, ranged over by  $\omega^B$ , with the type:

$$\text{BODYOBLS} \subseteq \text{STRUCTADDRS} \times \text{HEADDATA}$$

The set of obligations is then all groups of body obligations that may include a head obligation. This is exactly as was defined for promises but in reverse.

**Definition 137** (Obligations). Recall that  $\text{HEADDATA} \cup \text{BODYDATA}$  is ranged over by  $\bar{d}, \bar{d}_1, \dots, \bar{d}_n$ . Then, given a set of head and body obligations HEADOBLS and BODYOBLS (parameters 22 and 23), the set of **obligation sets** (or just **obligations**), ranged over by  $\Omega, \Omega_1, \dots, \Omega_n$ , is defined as:

$$\text{OBLIGATIONS} = \left\{ \Omega \in \mathcal{P} \left( \begin{array}{c} \text{HEADOBLS} \\ \cup \\ \text{BODYOBLS} \end{array} \right) \mid \begin{array}{l} \nexists \mathbf{x} \in \text{STRUCTADDRS}. \\ \exists \bar{d}_1, \bar{d}_2. \{(\mathbf{x}, \bar{d}_1), (\mathbf{x}, \bar{d}_2)\} \subseteq \Omega \end{array} \right\}$$

Let  $\Omega \in \text{OBLIGATIONS}$ . We write the removal of the obligation for any address  $\mathbf{x} \in \text{STRUCTADDRS}$  from  $\Omega$  as:

$$\Omega - \mathbf{x} \triangleq \{(\mathbf{y}, \bar{d}) \mid (\mathbf{y}, \Omega) \in \Pi, \mathbf{y} \neq \mathbf{x}\}$$

With obligations, we can extend promise-carrying data to be *promise- and obligation-carrying data*. As with promise-carrying data, each obligation must refer to either the head, or a body address. More strongly, the data and promises in the group must conspire to satisfy the obligations. It is, by construction, impossible to construction obligation-carrying data that does not satisfy its obligations.

**Definition 138** (Promise- and obligation-carrying data). The set of **promise- and obligation-carrying data**  $\text{PROMOBSDATA}$ , ranged over by  $\mathbf{pod}, \mathbf{pod}_1, \dots, \mathbf{pod}_n$  is defined as:

$$\text{PROMOBSDATA} \subseteq \text{DATA}_{\mathfrak{m}} \times \text{PROMISES} \times \text{OBLIGATIONS}$$

where every promise or obligation is either associated with the head, or with a body address in the data. That is, for all  $(d, \Pi, \Omega) \in \text{PROMOBSDATA}$ :

$$\forall(\mathbf{x}, \bar{d}) \in \Pi \cup \Omega. \mathbf{x} = \mathfrak{m} \text{ or } \mathbf{x} \in \text{addrs}(d)$$

Moreover, all obligations must be fulfilled: for all  $\omega \in \Omega$

1. If this is a head obligation, then the body closure (definition 93) of this data satisfies the obligation:  $\omega = (\mathfrak{m}, \underline{d})$  implies  $\downarrow(d, \Pi) \subseteq \underline{d}$ .
2. If this is a body obligation to  $\mathbf{x}$ , then the closure of the data with respect to  $\mathbf{x}$  (definition 96) satisfies the obligation:  $\omega = (\mathbf{x}, \bar{d})$  implies  $\mathcal{C}_{\mathbf{x}}((d, \Pi)) \subseteq \bar{d}$ .

We now defined compression, which no longer checks that the promises hold with respect to the data, instead checking with respect to the *obligations*. This ensures that, no matter what shape the data had been, as long as it was obliged to hold the shapes expected, the compression can proceed. There is no need to check that the obligations hold, as obligation-carrying data is always valid. In section A.3, we prove that obligation-carrying data is a valid structural addressing algebra. The proof is similar to the equivalent proof in chapter 5.

**Definition 139** (Compression for obligation-carrying data). The **compression function** for promise- and obligation-carrying data,  $\text{comp}_O : \text{STRUCTADDRS} \rightarrow \text{PROMOBSDATA} \rightarrow \text{PROMOBSDATA} \rightarrow \text{PROMOBSDATA}$  is defined as: for all  $\mathbf{pod}_1 = (d_1, \Pi_1, \Omega_1)$  and  $\mathbf{pod}_2 = (d_2, \Pi_2, \Omega_2)$

$$\text{comp}_O(\mathbf{x}, \mathbf{pod}_1, \mathbf{pod}_2) = \left( \begin{array}{c} \text{comp}_{\mathfrak{m}}(\mathbf{x}, d_1, d_2), \\ (\Pi_1 - \mathbf{x}) \cup (\Pi_2 - \mathfrak{m}), \\ (\Omega_1 - \mathbf{x}) \cup (\Omega_2 - \mathfrak{m}) \end{array} \right)$$

where:

1. The underlying data compression is defined:  $\text{comp}_{\mathfrak{m}}(\mathbf{x}, d_1, d_2)$  defined.

2. If it exists, the head promise made to  $\mathbf{pd}_2$  by  $\mathbf{pd}_1$  is satisfied by a body obligation on  $\mathbf{pd}_2$ :  $(\mathfrak{m}, \bar{d}_2) \in \Pi_2 \implies \exists(\mathbf{x}, \bar{d}_1) \in \Omega_1. \bar{d}_1 \subseteq \bar{d}_2$ .
3. If it exists, the body promise made to  $\mathbf{pd}_1$  by  $\mathbf{pd}_2$  is satisfied by the head obligation on  $\mathbf{pd}_2$ :  $(\mathbf{x}, \underline{d}_1) \in \Pi_1 \implies \exists(\mathfrak{m}, \underline{d}_2) \in \Omega_2. \underline{d}_1 \subseteq \underline{d}_2$ .

An instance  $\text{comp}_O(\mathbf{x}, \mathbf{pod}_1, \mathbf{pod}_2)$  is written  $\mathbf{pod}_1 \otimes \mathbf{pod}_2$ , overloading the compression notation.

### 7.3. Reasoning with obligations

We now consider reasoning with obligations, applying the technique to the list indexing problem. We also revisit the DOM and file-systems example of chapters 4 and 6.

#### 7.3.1. Obligations for list indexing

We now turn to axiomatising the `item` command for the lists library. We first formalise the promises and obligations we will use.

**Definition 140** (Promises and obligations for index lists). The **list index head data with respect to  $\mathbf{x}$**  and the **list length body data with respect to  $\mathbf{x}$**  are defined as:

$$\begin{aligned} \Leftarrow i &= \{\bar{d} \mid d \in \bar{d}, d = [pl_1 \otimes \mathbf{x} \otimes pl_2], |pl_1| = i\} \\ \Rightarrow i &= \{\underline{d} \mid d \in \underline{d}, |d| = i\} \end{aligned}$$

The **promises for the list library** are defined as:

$$\begin{aligned} \text{PROMISES} &= \bigcup_{i \in \mathbb{N}} \{(\mathfrak{m}, \Leftarrow i)\} && \text{List index promises} \\ &\cup \bigcup_{i \in \mathbb{N}, \mathbf{x} \in \text{STRUCTADDRS}} \{(\mathbf{x}, \Rightarrow i)\} && \text{List length promises} \end{aligned}$$

The **obligations for the list library** are defined as:

$$\begin{aligned} \text{OBLIGATIONS} &= \bigcup_{i \in \mathbb{N}, \mathbf{x} \in \text{STRUCTADDRS}} \mathbf{x} \Leftarrow i && \text{List index obligations} \\ &\cup \bigcup_{i \in \mathbb{N}} \mathfrak{m} \Rightarrow i && \text{Partial list length obligations} \end{aligned}$$

Here, we are using more than just head promises. Many promises and obligations may accrue as abstract allocation occurs. To ease notation, we define logical expressions for denoting sets of promises and obligations.

**Definition 141** (Logical expressions for list promises and obligations). The logical expressions for the list library with item command and associated interpretation function are extended with the following expressions:

$$\begin{aligned}
\langle\langle\emptyset\rangle\rangle(\Gamma) &\triangleq \emptyset \\
\langle\langle A \Leftarrow E \rangle\rangle(\Gamma) &\triangleq \begin{cases} \Gamma(A) \Leftarrow \langle\langle E \rangle\rangle(\Gamma) & \text{if } \Gamma(A) \in \text{STRUCTADDRES}, \langle\langle E \rangle\rangle(\Gamma) \in \mathbb{N} \\ \text{undefined} & \text{otherwise} \end{cases} \\
\langle\langle A \Rightarrow E \rangle\rangle(\Gamma) &\triangleq \begin{cases} \Gamma(A) \Rightarrow \langle\langle E \rangle\rangle(\Gamma) & \text{if } \Gamma(A) \in \text{STRUCTADDRES}, \langle\langle E \rangle\rangle(\Gamma) \in \mathbb{N} \\ \text{undefined} & \text{otherwise} \end{cases} \\
\langle\langle E_1, \dots, E_n \rangle\rangle(\Gamma) &\triangleq \{\langle\langle E_1 \rangle\rangle(\Gamma), \dots, \langle\langle E_n \rangle\rangle(\Gamma)\}
\end{aligned}$$

We then define a promise- and obligation carrying abstract list heap cell assertion.

**Definition 142** (Promise- and obligation carrying heap assertion). The abstract heap assertions of the list library are extended to promise- and obligation-carrying data with:

$$\langle\langle A_{E_O}^{E_P} \mapsto \phi \rangle\rangle^\Gamma = \left\{ \mathbf{a} \mapsto (d, \Pi, \Omega) \mid \begin{array}{l} \mathbf{a} = \Gamma(A), d \in \langle\langle \phi \rangle\rangle^\Gamma, \\ \Pi = \langle\langle E_P \rangle\rangle(\Gamma), \\ \Omega = \langle\langle E_O \rangle\rangle(\Gamma) \end{array} \right\}$$

The assertion  $A_\emptyset^\emptyset \mapsto \phi$  is written  $A \mapsto \phi$ . Similarly,  $A_O^\emptyset \mapsto \phi$  is written  $A_O \mapsto \phi$  and  $A_\emptyset^P \mapsto \phi$  is written  $A^P \mapsto \phi$ .

We can replicate the set of equivalences given in section 5.3.2 using obligations. Notice that in all cases, the **L** cell retains enough information in each case to ensure the obligation.

$$\begin{aligned}
& L \mapsto [1 \otimes 2 \otimes 3] \quad \not\approx_{\Gamma} \quad \exists \alpha. (L \mapsto [1 \otimes \alpha \otimes 3] * \alpha \mapsto 2) \\
& L \mapsto [1 \otimes 2 \otimes 3] \quad \not\approx_{\Gamma} \quad \exists \alpha. (L_{\alpha \leftarrow 1} \mapsto [1 \otimes \alpha \otimes 3] * \alpha^{\mathfrak{m} \leftarrow 1} \mapsto 2) \\
& \quad \quad \quad \exists \alpha. (L_{\alpha \leftarrow 1} \mapsto [1 \otimes \alpha \otimes 3] * \alpha^{\mathfrak{m} \leftarrow 1} \mapsto 2) \\
& \quad \quad \quad \not\approx_{\Gamma} \\
& \quad \quad \quad \exists \alpha, \beta. (L_{\alpha \leftarrow 1}^{\beta \Rightarrow 1} \mapsto [\beta \otimes \alpha \otimes 3] * \alpha^{\mathfrak{m} \leftarrow 1} \mapsto 2 * \beta_{\mathfrak{m} \Rightarrow 1} \mapsto 1) \\
& \quad \quad \quad \exists \alpha, \beta. (L_{\alpha \leftarrow 1}^{\beta \Rightarrow 1, \alpha \Rightarrow 1} \mapsto [\beta \otimes \alpha \otimes 3] * \alpha_{\mathfrak{m} \Rightarrow 1}^{\mathfrak{m} \leftarrow 1} \mapsto 2 * \beta_{\mathfrak{m} \Rightarrow 1} \mapsto 1) \\
& \quad \quad \quad \not\approx_{\Gamma} \\
& \quad \quad \quad \exists \alpha, \beta, \gamma. (L_{\alpha \leftarrow 1, \gamma \leftarrow 2}^{\beta \Rightarrow 1, \alpha \Rightarrow 1} \mapsto [\beta \otimes \alpha \otimes \gamma] * \alpha_{\mathfrak{m} \Rightarrow 1}^{\mathfrak{m} \leftarrow 1} \mapsto 2 * \beta_{\mathfrak{m} \Rightarrow 1} \mapsto 1 * \gamma^{\mathfrak{m} \leftarrow 2} \mapsto 3)
\end{aligned}$$

The axiom for the list indexing command is essentially unchanged from that given with promises. Here, it carries an arbitrary set of obligations, as the command is passive, changing no data and so invalidating nothing.

$$\begin{aligned}
& \{ \alpha_0^{\mathfrak{m} \leftarrow 1} \mapsto J * j \rightarrow - * E \wedge e \Rightarrow I \wedge |J| = 1 \} \\
& \quad j := \mathbf{item}(e) \\
& \{ \alpha_0^{\mathfrak{m} \leftarrow 1} \mapsto J * j \rightarrow J * E \}
\end{aligned}$$

The key contribution of obligations is enabling commands that *could* invalidate promises. The problematic command was the `remove`, which in its most general form could break the item index promise. Here, we can restore the full behaviour by ensuring `remove` have an *empty* set of obligations. It can thus never invalidate a promise.

$$\begin{aligned}
& \{ \alpha_0^{\emptyset} \mapsto I * E \wedge e \Rightarrow I \} \\
& \quad \mathbf{remove}(e) \\
& \{ \alpha_0^{\emptyset} \mapsto \emptyset * E \}
\end{aligned}$$

With this obligation, `remove` is atomically sound.

**Lemma 25** (The `remove` command is atomically sound). *The `remove` command is atomically sound, satisfying 12 of the framework.*

*Proof.* Recall the `remove(e)` action given in definition 28:

$$\llbracket \text{remove}(e) \rrbracket(s) \triangleq \begin{cases} \{s[L \mapsto [pl_1 \otimes pl_2]]\} & \text{if } s(L) = [pl_1 \otimes (e)(s) \otimes pl_2] \\ \{\downarrow\} & \text{otherwise} \end{cases}$$

The pre-condition of `remove`( $e$ ) is  $\alpha_{\emptyset}^{\emptyset} \mapsto \mathbf{I} * E \wedge e \Rightarrow \mathbf{I}$ . After interpretation, this provides views of the form  $\mathbf{x} \mapsto (i, \emptyset, \emptyset) * \text{varHeap}$ , where  $\text{varHeap}$  is the arbitrary variable resource produced by  $E$ . The lack of promises and obligations means we have no information about the frame, but do know that mutations cannot destabilise the reasoning. After noting that the post-condition also has no promises and obligations, atomic soundness of the command then follows via a similar argument to that of lemma 10.  $\square$

More interesting is what would happen if we picked a bad specification for `remove`: not having obligations, using a non-empty set of obligations  $\alpha_{\mathcal{O}}^{\emptyset} \mapsto \dots$ , and changing the obligations in the axiom all result in unsoundness.

1. Without obligations, changes to data within the  $\alpha$  cell could invalidate any promise regarding list lengths, as there would be no way to know what promises had been issued depending on the data.
2. If we included arbitrary obligations, so the post-condition contained  $\alpha_{\mathcal{O}}^{\emptyset} \mapsto \emptyset$ , it would *not* abstract the result of the command, as such a cell represents invalid data, and so a false post-condition.
3. If we had removed the obligations, so the post-condition contained  $\alpha_{\emptyset}^{\emptyset} \mapsto \emptyset$ , then we could propose a frame of the form  $\beta^P \mapsto \alpha$  where  $P$  is the promise associated with the obligation that  $\alpha$  previously had. This frame would be invalidated by the update, and so the command would not be atomically sound.

### 7.3.2. Enhancing the specifications of DOM

Obligations allow us to further tighten DOM axiomatisation we gave in chapter 4. There are two axioms in the DOM library of 4 that arguably involve more data access than is strictly necessary. The first, `item`, acts much like the `item` command discussed with lists. We can thus use list index promises and obligations to reduce the footprint. The second, `appendChild` uses

### 7.3.3. The `item` command footprint

Recall the `item` command of DOM,  $j := \mathbf{f}.\text{item}(e)$  which extracts the node at index  $e$  from the forest identified by  $\mathbf{f}$ . We have already considered a virtually identical situation

example in section 7.1, albeit on a single list. DOM has many forests rather than a single list, but it is straightforward to extend the promises and obligations to describe *which* forest is under consideration. We can extend the list index head data from  $\mathfrak{m} \Leftarrow \mathbf{I}$ , which describes the single list with the head address at index  $\mathbf{I}$ , to  $\mathbf{F} \Leftarrow \mathbf{I}$ , which describes all DOM trees where the  $\mathfrak{m}$  address is the  $\mathbf{I}^{\text{th}}$  child of node with forest identifier  $\mathbf{F}$ . When used as a head promise, provides exactly what is needed for the `item` command. Using promises and obligations of this form, the `f.item(i)` command can be axiomatised as:

$$\left\{ \begin{array}{l} \mathbf{f} \rightarrow \mathbf{FID} * \mathbf{n} \rightarrow - * \alpha_{\mathbf{O}}^{\mathbf{FID} \Leftarrow \mathbf{I}} \mapsto \mathbf{S}_N[\gamma]_{\mathbf{FS}} * \mathbf{E} \wedge e \Rightarrow \mathbf{I} \\ \mathbf{n} := \mathbf{f.item}(e) \\ \mathbf{f} \rightarrow \mathbf{FID} * \mathbf{n} \rightarrow \mathbf{N} * \alpha_{\mathbf{O}}^{\mathbf{FID} \Leftarrow \mathbf{I}} \mapsto \mathbf{S}_N[\gamma]_{\mathbf{FS}} * \mathbf{E} \end{array} \right\}$$

The footprint has been reduced to only the node that will be returned. As well as more concisely statement the effect of the command, it enables more concurrency with respect to the elements of the forest  $\mathbf{FID}$ , as each node can be managed in a different thread.

#### 7.3.4. The `appendChild` command footprint

One example that is not based on the simple examples already presented in this chapter is the `p.appendChild(c)` command. This command, which moves the node identified by  $\mathbf{c}$  to be the last child of the node identified by  $\mathbf{p}$ , currently has the axiom:

$$\left\{ \begin{array}{l} \mathbf{p} \rightarrow \mathbf{P} * \mathbf{c} \rightarrow \mathbf{C} * \mathbf{o} \rightarrow - * \alpha \mapsto \mathbf{S}_P[\gamma]_{\mathbf{FS}_1} * \beta \mapsto \mathbf{S}'_C[\mathbf{T} \wedge \mathit{is\_complete}]_{\mathbf{FS}_2} \wedge \mathbf{S} \neq \#\mathit{text} \\ \mathbf{o} := \mathbf{n.appendChild}(\mathbf{m}) \\ \mathbf{p} \rightarrow \mathbf{P} * \mathbf{c} \rightarrow \mathbf{C} * \mathbf{o} \rightarrow \mathbf{C} * \alpha \mapsto \mathbf{S}_P[\gamma \otimes \mathbf{S}'_C[\mathbf{T}]]_{\mathbf{FS}_2} * \beta \mapsto (\emptyset_f \vee \emptyset_g) \end{array} \right\}$$

Notice that the  $\mathbf{c}$  node must have no body addresses. This states that the sub-tree under  $\mathbf{c}$  is complete, which is needed to ensure that the node  $\mathbf{p}$  is not a descendent of  $\mathbf{c}$ . Unfortunately, it also gives `appendChild` a larger than ideal footprint, ensuing that no concurrent analysis of the nodes under  $\mathbf{c}$  is possible.

We can correct this excessive footprint with a “No descendent with identifier  $n$ ” promise. An abstract heap making this promise states that it *never* contains a node with identifier  $n$ , anywhere in the sub-data. This promise is not naturally stable, as the `appendChild` command can break it by placing a node with the disqualified identifier into the subtree. The other axioms cannot break the obligation, and can just carry it unchanged.

**Definition 143** (No such node promise & obligation). The body data **no such node** is defined as:

$$\neg n = \{d \in \text{DOMDATA} \mid \nexists \mathbf{x}, d_1, d_2. d = d_1 \otimes n[d_2]\}$$

With this, the body promises and head obligations are defined as:

$$\begin{aligned} \text{BODYPROMS} &= \{(\mathbf{x}, \neg n) \mid \mathbf{x} \in \text{STRUCTADDRS}, n \in \text{NODEIDS}\} \\ \text{HEADOBLS} &= \{(\mathbb{m}, \neg n) \mid n \in \text{NODEIDS}\} \end{aligned}$$

There are no body obligations or head promises.

The “no such node” body data  $\neg n$  describes all trees which do not contain node identifier  $n$ . The allowable promises thus describe a context containing *any* tree data without a specific node. As this is not stable, the associated obligation states that a given cell never contains the same node.

Using this promise/obligation pair, we can create an axiom where the children of  $c$  are described with a body address and promise stating that, whatever the children are, they do not contain the target parent node. We can then axiomatise the children of the  $c$  node using body address, retaining on the fact that  $p$  is not one of them. This node is moved to be a child of  $p$ , which by the obligation cannot create a cycle. We must check, however, that the obligations associated with the abstract cell containing  $p$  will allow a child  $c$ , to ensure we are not breaking another obligation that may be in force. The `appendChild` axiom becomes:

$$\left\{ \begin{array}{l} p \rightarrow P * c \rightarrow C * o \rightarrow - * \alpha_{O_1}^{P_1} \mapsto S_P[\gamma]_{FS_1} * \beta_{O_2}^{P_2, \delta: \neg P} \mapsto S'_C[\delta]_{FS_2} \wedge S \neq \#text \wedge (\neg C) \notin O_1 \\ o := n.appendChild(m) \\ p \rightarrow P * c \rightarrow C * o \rightarrow C * \alpha_{O_1}^{P_1, \delta: \neg P} \mapsto S_P[\gamma \otimes S'_C[\delta]_{FS_2}]_{FS_1} * \beta_{O_2}^{P_2} \mapsto (\emptyset_f \vee \emptyset_g) \end{array} \right\}$$

As before, soundness of `appendChild` depends upon the sub-tree under node  $m$  *not* containing the target parent node  $m$ . We assure this via the final conjunct of the pre-condition,  $(\neg C) \notin O_1$ . This states that the obligations associated with  $\alpha$  permit a descendent node with identifier  $C$ . This ensures the stability of the system, as `appendChild` is the only axiom which can add a node to a sub-tree. All other commands need only carry the promises and obligations untouched.

Notice that this axiom is *weaker* than our original, promise free version. Here, we are us-



ing only head obligations; this implies that any abstract cell containing no body addresses must have *no* obligations. Our original axiom mandated  $\beta \mapsto S'_M[\top \wedge \text{is\_complete}]_{\text{FS}_2}$  has no body addresses, beneath the node with identifier  $M$ , and so has no obligations, trivially satisfying  $(\neg C) \notin O_1$ . This weaker axiom allows more flexible use of the data. The following parallel use of `appendChild` and `nodeName` is only possible with the obligations system, in which we elide the program variable resource to avoid clutter:

$$\begin{array}{c}
\{ \alpha \mapsto S_P[\emptyset]_{\text{PFS}} \otimes T_C[U_D[\emptyset_f]_{\text{DFS}}]_{\text{CFS}} \} \\
\left\{ \begin{array}{l} \exists \beta, \gamma, \delta, \epsilon, \zeta. \\ \alpha \mapsto \beta \otimes \gamma * \beta \mapsto S_P[\delta]_{\text{PFS}} * \gamma^{\epsilon: \neg P} \mapsto T_C[\epsilon]_{\text{CFS}} \\ * \delta \mapsto \emptyset_f * \epsilon_{\zeta: \neg P}^{\cap: \neg P} \mapsto U_D[\zeta]_{\text{DFS}} * \zeta^{\cap: \neg P} \mapsto \emptyset_f \end{array} \right\} \\
\left\{ \beta \mapsto S_P[\delta]_{\text{PFS}} * \gamma^{\epsilon: \neg P} \mapsto T_C[\epsilon]_{\text{CFS}} \right\} \quad \parallel \quad \left\{ \epsilon_{\zeta: \neg P}^{\cap: \neg P} \mapsto U_D[\zeta]_{\text{DFS}} \right\} \\
\text{p.appendChild(c)} \quad \parallel \quad \text{s := d.nodeName} \\
\left\{ \beta^{\epsilon: \neg P} \mapsto S_P[\delta \otimes T_C[\epsilon]_{\text{CFS}}]_{\text{PFS}} * \gamma \mapsto \emptyset_f \right\} \quad \parallel \quad \left\{ \epsilon_{\zeta: \neg P}^{\cap: \neg P} \mapsto U_D[\zeta]_{\text{DFS}} \right\} \\
\left\{ \begin{array}{l} \exists \beta, \gamma, \delta, \epsilon, \zeta. \\ \alpha \mapsto \beta \otimes \gamma * \beta^{\epsilon: \neg P} \mapsto S_P[\delta \otimes T_C[\epsilon]_{\text{CFS}}]_{\text{PFS}} * \gamma \mapsto \emptyset \\ * \delta \mapsto \emptyset_f * \epsilon_{\zeta: \neg P}^{\cap: \neg P} \mapsto U_D[\zeta]_{\text{DFS}} * \zeta^{\cap: \neg P} \mapsto \emptyset_f \end{array} \right\} \\
\{ \alpha \mapsto S_P[\otimes T_C[U_D[\emptyset_f]_{\text{DFS}}]_{\text{CFS}} \emptyset]_{\text{PFS}} \}
\end{array}$$

### 7.3.5. Symbolic links

We now briefly outline *symbolic links* for file systems (chapter 6, and speculate on how obligations may be able to reason about these. Whereas hard links point directly to the inode of an entry, a *symbolic link* contains the *path* to an entry. Consider figure 7.4. The directory in the bottom right contains a *symbolic link file*. This symbolic link file references the directory at path  $\top/\text{Music}/\text{Classical}$ . When resolving paths that go “through”  $\top/\text{Documents}/\text{Music}/\text{Classical}/$ , the resolution process will *follow* the symbolic link, and continue at the target of it. Therefore, the path  $\top/\text{Documents}/\text{Music}/\text{Classical}/\text{adagio.mp3}$  resolves to the file with inode 6.

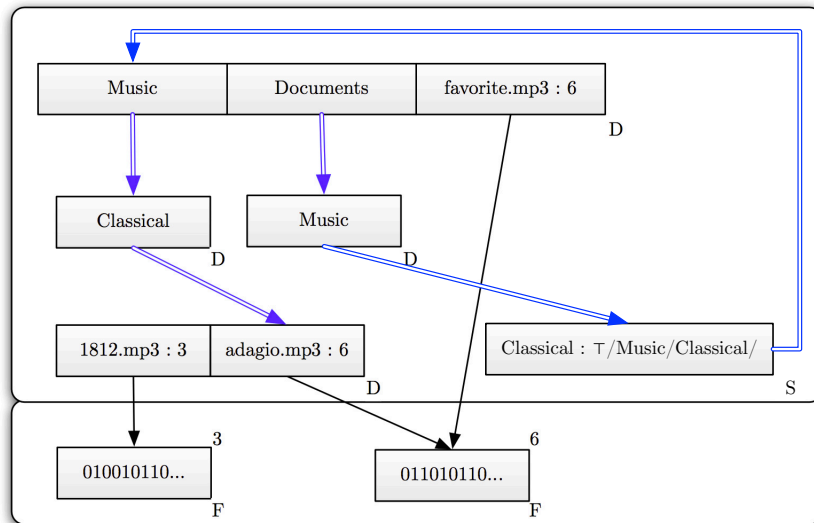


Figure 7.4.: A POSIX file-system tree where the directory in the bottom right contains a symbolic link. The hard links traversed by resolution are highlighted.

There are two major obstacles to reasoning with symbolic links:

1. The addition of symbolic links means that paths need not follow the inductive structure of the file-system tree. Resolving a path may involve traversing symbolic links, and resolving each symbolic link can involve resolving *further* symbolic links, ad-infinitum. POSIX avoids such unbounded path resolution by providing a constant integer `SYMLINK_MAX`. After `SYMLINK_MAX` symbolic links have been encountered whilst resolving a path, resolution fails with an error.
2. Symbolic links also break the intuition that the data used to identify sub-data is *disjoint* from the sub-data itself. With symbolic links, the data to be updated can be involved in the resolution process. For example, a descendent of a directory  $d$  could be a symbolic link pointing to  $d$  itself. By identifying  $d$  via a path using that symbolic link, the contents of  $d$  are directly used in identifying  $d$ . If we wanted to, for example, remove the contents of  $d$  using `rmdirRec(τ/path/to/d/through/symlink)`, at some point during the removal the symbolic link will be deleted, and the path will stop resolving! Such a situation is not possible with the linear paths we are currently using.

Nonetheless, promises and obligations provide some of the machinery needed to manage symbolic links. Linear paths were described using simple head promises, which

provide a sub-datum  $d$  with the shape of super-data  $c$  that resolves the path. Any symbolic links that only use resource in  $c$  can be handled using an enriched notion of our resolve function acting on  $c$ . As symbolic links will use sub-data from  $c$  that is not a direct ancestor of  $d$  (that is, the symbolic links), obligations would be required to ensure the stability of the system.

However, the second obstacle remains difficult. Our framework is based upon *splitting* data. Even promises and obligations split the data, into local data which can be updated, and that which is promised from the frame. To solve this problem, we would seem to need a system that simultaneously allows the *local* view *and* a *global* view of the complete data. Our framework, with promises and obligations, provides all the needed parts to build this global view, but currently has no elegant method for analysing it. We will continue to pursue this problem.

## 7.4. Summary

This chapter has introduced *obligations*, a system for restricting the possible updates to abstract heap cells. Obligations, when combined with promises, combine to give a general theory of *passive access* for structured data, where data can be shared among many abstract heap cells, and updates restricted to ensure this sharing is safe. This allows:

1. **Richer smaller axioms for libraries:** Obligations allow a greater range of commands to be specified using promises, by removing the restriction that promises be naturally stable. We have demonstrated this with the list `item` and `remove` commands, which are not possible with either structural separation logic or promises alone.
2. **Smaller axioms for DOM:** We can provide even smaller axioms for the DOM library, allowing a greater separation of DOM tree data. This will enable a much richer notion of concurrent DOM in future work.
3. **Beginnings of permission system for structured data:** This system is an initial attempt at a *permission system* for structured data. Promises provide one part of this, offering passive read access to data not directly *owned* by a thread. Obligations provide the other. Rather than just use “read” or “read and write” permissions, obligations allow a richer range of updates by stating “Any change is permitted, up to the given obligation invariants”.

As stated, the obligations system is less mature than naturally stable promises and structural separation logic. One obvious issue is the verbose nature of the bookkeeping, requiring significant effort during proof construction. We will continue to develop the obligations system in future work.

## 8. Conclusions

This thesis has primarily been concerned with techniques for, and examples of, *library reasoning*. By developing reasoning that takes an *abstraction first* rather than *implementation first* approach to specifying libraries, we have been able to give novel axiomatic semantics for a range of libraries. We have covered simple data structures (such as lists and trees), updated pre-existing axiomatic specifications for real libraries (with DOM), and introduced entirely new specifications for complex libraries which are not immediately amenable to local reasoning (such as POSIX). By allowing a pragmatic mix of standard heap models and our abstractions, we have enabled easier verification of programs that *use* these libraries. This thesis has touched on many aspects of program verification and has, inevitably, opened many new questions that remain unanswered.

Our primary technical contribution has been *structural separation logic*, a program logic for local reasoning with mixed *structured* and *unstructured* data. We have provided a general presentation of structural separation logic, showing how the technique can be applied to many different *libraries* that use a mix of highly structured data and normal heap manipulation. When verifying client programs that use libraries, this mix of high level abstraction with low level heap manipulation allows easier program verification than would be possible with separation logic alone. We have demonstrated this both with our DOM reasoning example in section 4.4, and more extensively with our featherweight POSIX case study.

By design, the abstract heap cells of our logic inherit the separation afforded by the separating conjunction. Beyond the simple flat heaps used in our examples, this means we can interface easily with the developments of the wider separation logic community. One particular area we wish to interface with is *tooling*. We can represent abstract heap cells within existing separation logic tools where, to the tools proof engine, they are harmless passengers in the assertions and are simply removed by the frame rule. We can then extend the proof engines to be *aware* of the abstract cells, including their creation and destruction via abstract allocation, and update via axioms. We have already begun experiments with this, embedding a simple version of structural separation logic into the Verifast tool [45] with promising results. However, the correct long-term solution to representing our

abstract data remains unclear. We will aggressively pursue automation in future work, focusing on experimentation and proving real examples.

We have shown how structural separation logic can give an axiomatic specification to DOM that has smaller footprints than was previously possible. Using structural separation logic for DOM allows easy composition of DOM reasoning with language reasoning. This will allow reasoning about *web applications*, which are written in JavaScript, interacting with the browser's page representation via the DOM library. There is now significant work on local program reasoning with JavaScript using variants on the standard separation logic heap model [34]. Structural separation logic allows this to be combined with our DOM axioms to obtain a system capable of reasoning about web applications. An initial version of this work appeared in [66], and we hope to work with Smith et al. in extending this to a fuller approach.

As with previous DOM axiomatic semantics, our axioms are justified against a high-level operational semantics. This can be unconvincing, as we authored the semantics specifically to allow verification of the axioms. To obtain a stronger soundness result, it will be necessary to link our approach to an underlying *implementation*. Structural separation logic is well placed to achieve this, as we can mix heap implementation code with our abstraction. We are currently working with Raad on refinement for the DOM library. We have developed an implementation in C, and by using reasoning techniques for refinement being developed by her and Wheelhouse, we can show that this soundly refines our axiomatic specifications. This enables a verified *concurrent* DOM implementation, the first of its kind.

Local reasoning is not always easily applicable to libraries. Our axioms for POSIX show that even well-designed libraries do not always localise their data access. Even in these situations, we believe that the compositionality provided via locality can be a useful concept. As such, we have introduced *promises and obligations* to act as a *permission* system for abstract data. They enable data to be split into *read-only* global data and *read-write* local data, and so allow locality where it would otherwise be impossible. Promises and obligations are only the first step in understanding richer notions of separation and composition on abstract structured data. Our inability to easily specify symbolic links that go *through* abstractly allocated data indicate that it may be helpful to have two simultaneous views of data; one “cut-up” via abstract allocation, and one “complete”, using promises and obligations, to give context to the localised sub-data.

However, the current form has been sufficient to prove interesting programs that use the POSIX file system. Our approach is one of the first POSIX specifications focused entirely on verifying *client programs*, rather than POSIX implementations. Although our subset is already useful, the POSIX standard is large. Gzik is working on extending featherweight

POSIXFS to handle more of the POSIX file-system specification, including additional commands, richer symbolic link and non-linear path support, and (with us) *errors*. We have so far ignored errors without our system, so that any command not correctly used is treated as a faulting case. POSIX typically reports such command invocations as non-fatal *errors*, allowing programs to recover and proceed in another manner. It will be interesting to see the challenges this poses for reasoning.

This thesis is but a single step in local reasoning for client programs that use libraries. As users of libraries vastly outnumber authors, such a focus will help enable more software achieve correctness through verification. We will continue to push this approach in our research, analysing and axiomatising more libraries. We will continue to extend the theory to handle the realities of what we find, rather than work only on idealised libraries designed with verification in mind.

# A. Appendices

In these appendices, we include both some proofs too straightforward for, or similar to, proofs from the main text, and additional program proofs using structural separation logic.

## A.1. Program proofs

This section contains various programs and their associated proofs, demonstrating further uses of structural separation logic.

### A.1.1. Proof for contains

$$\left\{ \begin{array}{l}
 \mathbf{k} \rightarrow \mathbf{K} * \mathbf{b} \rightarrow - * \mathbf{L} \mapsto [\mathbf{A}] \\
 \mathbf{b} := \text{contains}(\mathbf{k}) \triangleq \text{local } \mathbf{i} \{ \\
 \quad \mathbf{i} := \text{getFirst}(); \\
 \quad \left\{ \begin{array}{l}
 (\mathbf{A} = \emptyset \wedge \mathbf{i} \rightarrow 0) \vee (\exists \mathbf{I}, \mathbf{R}. \mathbf{A} = \mathbf{I} \otimes \mathbf{R} * \mathbf{i} \rightarrow \mathbf{I}) * \mathbf{L} \mapsto [\mathbf{A}] \\
 \end{array} \right\} \\
 \quad \mathbf{b} := \text{false}; \\
 \quad \left\{ \begin{array}{l}
 \exists \mathbf{I}, \mathbf{B}, \mathbf{S}, \mathbf{R}. \mathbf{k} \rightarrow \mathbf{K} * \mathbf{b} \rightarrow \mathbf{B} * \mathbf{L} \mapsto [\mathbf{A}] * \mathbf{i} \rightarrow \mathbf{I} \wedge \\
 (\mathbf{A} = \mathbf{S} \otimes \mathbf{I} \otimes \mathbf{R} \wedge \mathbf{I} \neq 0) \vee (\mathbf{A} = \mathbf{S} \wedge \mathbf{I} = 0) \wedge \\
 ((\mathbf{K} \in \mathbf{S} \wedge \mathbf{B} = \text{true}) \vee (\mathbf{K} \notin \mathbf{S} \wedge \mathbf{B} = \text{false})) \\
 \end{array} \right\} \\
 \quad \text{while } (\mathbf{i} \neq 0) \\
 \quad \left\{ \begin{array}{l}
 \mathbf{i} \rightarrow \mathbf{I} * \mathbf{k} \rightarrow \mathbf{K} * \mathbf{b} \rightarrow \mathbf{B} * \exists \mathbf{J}, \beta. (\alpha \mapsto (\mathbf{I} \otimes \mathbf{J} \otimes \beta) \vee \alpha \mapsto [\beta \otimes \mathbf{I}]) \\
 \text{if } (\mathbf{i} = \mathbf{k}) \\
 \quad \mathbf{b} := \text{true}; \\
 \text{else} \\
 \quad \text{skip}; \\
 \quad \mathbf{i} := \text{getRight}(\mathbf{i}) \\
 \end{array} \right\} \\
 \left. \begin{array}{l}
 \\
 \mathbf{k} \rightarrow \mathbf{K} * ((\mathbf{K} \in \mathbf{A} \wedge \mathbf{b} \rightarrow \text{TRUE}) \vee (\mathbf{K} \notin \mathbf{A} \wedge \mathbf{b} \rightarrow \text{FALSE})) * \mathbf{L} \mapsto [\mathbf{A}] \\
 \end{array} \right\}
 \end{array} \right\}$$



### A.1.2. Proof of $l := n.\text{stringLength}$

$$\left\{ \begin{array}{l}
n \rightarrow N * l \rightarrow - * \alpha \mapsto \#text_N[S \wedge is\_complete]_{FS} \\
l := n.\text{stringLength} \triangleq \text{local } r \{ \\
\quad l := 0; \\
\quad r := n.\text{substringData}(l, 1); \\
\quad \left\{ \begin{array}{l}
\exists s1, s2, R, L. n \rightarrow N * l \rightarrow L * r \rightarrow R * \\
\left( \begin{array}{l}
(R = \mathbf{null} \wedge \alpha \mapsto \#text_N[S \wedge s1]_{FS}) \\
\vee \\
(R \neq \wedge \alpha \mapsto \#text_N[S \wedge s1 \cdot R \cdot s2]_{FS})
\end{array} \right) \wedge L = |s1|
\end{array} \right\} \\
\quad \text{while } (r \neq \mathbf{null}) \\
\quad \quad l := l + 1; \\
\quad \quad r := n.\text{substringData}(l, 1); \\
\quad \} \\
\left. \right\} \exists L. n \rightarrow N * l \rightarrow L * \alpha \mapsto \#text_N[S]_{FS} \wedge L = |S| \}
\end{array} \right.$$

### A.1.3. Proof of $s := n.\text{value}$ in the text node case

$$\left\{ \begin{array}{l}
n \rightarrow N * s \rightarrow - * \alpha \mapsto \#text_{ID}[VAL]_{FS} \\
s := n.\text{value} \triangleq \text{local } l, \text{ name } \{ \\
\quad \text{name} := n.\text{nodeName}; \\
\quad \left\{ \begin{array}{l}
n \rightarrow N * s \rightarrow - * l \rightarrow - * \text{name} \rightarrow \#text * \alpha \mapsto \#text_{ID}[VAL]_{FS} \\
\text{if name} = "\#text" \\
\quad \left\{ \begin{array}{l}
n \rightarrow N * s \rightarrow - * l \rightarrow - * \text{name} \rightarrow \#text * \alpha \mapsto \#text_{ID}[VAL]_{FS} \\
l := n.\text{stringLength}; \\
s := n.\text{substringData}(0, l) \\
\left\{ \begin{array}{l}
n \rightarrow N * s \rightarrow VAL * l \rightarrow |VAL| * \text{name} \rightarrow \#text * \alpha \mapsto \#text_{ID}[VAL]_{FS}
\end{array} \right\} \\
\text{else} \\
\quad \left\{ \begin{array}{l}
\mathbf{false}
\end{array} \right\} \\
s := \mathbf{null}
\end{array} \right\} \\
\quad \} \\
\left. \right\} n \rightarrow N * \alpha \mapsto \#text_{ID}[VAL]_{FS} \wedge s \rightarrow VAL \}
\end{array} \right.$$

```

{ n → N * s → - * α ↦ #textID[VAL]FS }
s := n.value  $\triangleq$  local l, name {
  name := n.nodeName;
  { n → N * s → - * l → - * name → #text * α ↦ #textID[VAL]FS }
  if name = "#text"
  { n → N * s → - * l → - * name → #text * α ↦ #textID[VAL]FS }
  l := n.stringLength;
  s := n.substringData(0, l)
  { n → N * s → VAL * l → |VAL| * name → #text * α ↦ #textID[VAL]FS }
else
  { false }
  s := null
}
{ n → N * α ↦ #textID[VAL]FS ∧ s → VAL }

```

Figure A.1.: Proof of  $s := n.value$  in the text node case.

#### A.1.4. Proof of $v := n.childValue$

```

{ n → N * v → - * α ↦ NMID[#textTID[VAL]FS1 ⊗ β]FS2 }
v := n.childValue  $\triangleq$  local cs, c {
  cs := n.childNodes;
  {
    ∃FS3, FID. n → N * v → - * c → - * cs → FID *
    α ↦ NMID[#textTID[VAL]FS1 ⊗ β]FS3 ∧ FID ∈ FS3 ∧ FS2 ⊆ FS3
  }
  c := cs.item(0);
  {
    ∃FS3, FID. n → N * v → - * c → TID * cs → FID *
    α ↦ NMID[#textTID[VAL]FS1 ⊗ β]FS3 ∧ FID ∈ FS3 ∧ FS2 ⊆ FS3
  }
  v := c.value;
}
{ ∃FS3. n → N * v → VAL * α ↦ NMID[#textTID[VAL]FS1]FS3 ∧ FS2 ⊆ FS3 }

```

#### A.1.5. Code for fileCopy(source, target)

The source code for, and associated proof of, the fileCopy command of section 6.3.4 is:

$$\left\{ \begin{array}{l} \text{path}(\text{path}, P_1, A) \wedge \text{path}(\text{target}, P_2, C, D) \\ \wedge \alpha^{P_1} \mapsto A : I_1 * \beta^{P_2} \mapsto D[C \wedge \text{can\_create}(A)] * I_1 \xrightarrow{F} SD * E \end{array} \right\}$$

```

ret := fileCopy(source, target)  $\triangleq$  local s, sourceH, targetH, buffer, c {
  buffer := malloc(4096)
  sourceH := open(source, 0);
  c := read(sourceH, buffer, 4096);
   $\left( \begin{array}{l} \exists SH, TH, I_2, SD_2, SB, SD_3. \\ SD = SD_2 \cdot SB \cdot SD_3 \wedge (|SB| < 4096 \wedge SD_3 = \epsilon) \vee |SB| = 4096 \\ \wedge \text{sourceH} \rightarrow SH * \text{targetH} \rightarrow TH * \text{buffer} \rightarrow \text{BUF} * c \rightarrow |SB| \\ * \text{BUF} - 1 \mapsto 4096 * \text{ncells}(\text{BUF}, |SB|, SB) * \text{ncells}(\text{BUF} + |SB|, 4096 - |SB|, -) \\ * \alpha^P \mapsto A : I_1 * \beta^{P_2} \mapsto D[C + A : I_2] * I_1 \xrightarrow{F} SD \\ * E * I_2 \xrightarrow{F} SD_2 * SH \xrightarrow{PH} (I_1, |SD_2 \cdot SB|) * TH \xrightarrow{PH} (I_2, |SD_2|) \end{array} \right)$ 
  while c = 4096
    write(targetH, buffer, 4096);
    c := read(sourceH, buffer, 4096);
  // We read less than 4096, so it was the last chunk
   $\left( \begin{array}{l} \exists SH, TH, I_2, SD_2, SB, SD_3. \\ SD = SD_2 \cdot SB \cdot SD_3 \wedge |SB| < 4096 \wedge SD_3 = \epsilon \\ \wedge \text{sourceH} \rightarrow SH * \text{targetH} \rightarrow TH * \text{buffer} \rightarrow \text{BUF} * c \rightarrow |SB| \\ * \text{BUF} - 1 \mapsto 4096 * \text{ncells}(\text{BUF}, |SB|, SB) * \text{ncells}(\text{BUF} + |SB|, 4096 - |SB|, -) \\ * \alpha^P \mapsto A : I_1 * \beta^{P_2} \mapsto D[C + A : I_2] * I_1 \xrightarrow{F} SD \\ * E * I_2 \xrightarrow{F} SD_2 * SH \xrightarrow{PH} (I_1, |SD_2 \cdot SB|) * TH \xrightarrow{PH} (I_2, |SD_2|) \end{array} \right)$ 
  s := write(targetH, buffer, c);
  close(sourceH); close(targetH); free(buffer)
} \left\{ \exists I_2. \alpha^{P_1} \mapsto A : I_1 * \beta^{P_2} \mapsto D[C + A : I_2] * I_1 \xrightarrow{F} SD * E * I_2 \xrightarrow{F} SD \right\}

```

## A.2. Unique collapse to heap

Here, we provide the proof of theorem 3, showing that if an abstract heap collapses to a complete heap, the complete heap is unique. This result is *confluence*. It is well known, and typically proven via local confluence and Newman's lemma. Unfortunately, our system does not satisfy local confluence on all pre-abstract heaps. The pre-abstract heap  $\mathbf{x} \mapsto \mathbf{y} * \mathbf{y} \mapsto \mathbf{x}$  has two possible collapses,  $\mathbf{x} \mapsto \mathbf{x}$  and  $\mathbf{y} \mapsto \mathbf{y}$ . Neither can collapse further, but both are different. However, if  $\mathbf{ph} \downarrow^* h$ , then  $\downarrow$  is locally confluent on  $\mathbf{ph}$ . We can thus

create a proof based upon *conditional* local confluence.

**Lemma 26** (Conditional local confluence). *Assume that, for all  $\mathbf{ph}, \mathbf{ph}_2 \in \text{PREABSHEAPS}$  and  $h \in \text{STRUCTHEAPS}$ ,  $\mathbf{ph} \downarrow^* h$  and  $\mathbf{ph} \downarrow \mathbf{ph}_2$ . Then,  $\mathbf{ph}_2 \downarrow^* h$ .*

*Proof.* Since  $\mathbf{ph} \downarrow \mathbf{ph}_2$ , we know that  $\#(\mathbf{ph}) > 0$ . Moreover, since  $\mathbf{ph} \downarrow^* h$ , we know that  $\exists \mathbf{ph}_1. \mathbf{ph} \downarrow \mathbf{ph}_1 \downarrow^* h$ . Moreover, by the definition of single-step collapse,  $\text{dom}(\mathbf{ph}) \setminus \text{dom}(\mathbf{ph}_1) = \{\mathbf{x}\}$  and  $\text{dom}(\mathbf{ph}) \setminus \text{dom}(\mathbf{ph}_2) = \{\mathbf{y}\}$  (that is, the collapse to  $\mathbf{ph}_1$  used abstract address  $\mathbf{x}$ , and the collapse to  $\mathbf{ph}_2$  used abstract address  $\mathbf{y}$ ).

Let  $\#(\mathbf{ph}) = |\text{dom}(\mathbf{ph}) \cap \text{STRUCTADDRS}|$  (the function giving the number of structural addresses in the domain of some pre-abstract heap). We proceed by induction on  $\#(\mathbf{ph})$ .

1.  $\#(\mathbf{ph}) = 0$ : The result follows directly, as  $\mathbf{ph} \not\downarrow$ .
2.  $\#(\mathbf{ph}) = n + 1$ : We know that  $\mathbf{ph} \downarrow \mathbf{ph}_1$  using hole  $\mathbf{x}$  and  $\mathbf{ph} \downarrow \mathbf{ph}_2$  using hole  $\mathbf{y}$ . If  $\mathbf{x} = \mathbf{y}$ , the result follows directly.

Assume then, that  $\mathbf{x} \neq \mathbf{y}$ . We demonstrate that there exists some  $\mathbf{ph}_3$  such that  $\mathbf{ph}_1 \downarrow \mathbf{ph}_3$  and  $\mathbf{ph}_2 \downarrow \mathbf{ph}_3$ . With this result, we have  $\mathbf{ph}_1 \downarrow^* h$ ,  $\mathbf{ph}_1 \downarrow \mathbf{ph}_3$  and  $\#(\mathbf{ph}_1) = n$ . So, by the inductive hypothesis, we have  $\mathbf{ph}_3 \downarrow^* h$ . Hence,  $\mathbf{ph}_2 \downarrow^* h$  as required.

To prove this remaining result, examine the possible ways  $\mathbf{ph}$  can be constructed such that  $\mathbf{ph} \downarrow \mathbf{ph}_1$  and  $\mathbf{ph} \downarrow \mathbf{ph}_2$ . There must two abstract heap cells  $\mathbf{x}, \mathbf{y} \in \text{dom}(\mathbf{ph})$ . There may exist at most two additional heap cells with addresses  $\mathbf{a}_1, \mathbf{a}_2 \in \text{dom}(\mathbf{ph})$  which contain  $\mathbf{x}, \mathbf{y}$  as body addresses.

In figure A.2, we consider all the permutations for the location of the body addresses  $\mathbf{x}$  and  $\mathbf{y}$ . The rows indicate which heap addresses contains  $\mathbf{x}$  and  $\mathbf{y}$  as body addresses, and is labelled by a justification. In each case, this justification either demonstrates the existence of  $\mathbf{ph}_3$  or shows that the case is impossible. The justifications are:

**Quazi-commutivity:** In these cases, construct  $\mathbf{ph}_3$  by collapsing using  $\mathbf{y}$  on  $\mathbf{ph}_1$  and using  $\mathbf{x}$  on  $\mathbf{ph}_2$ . The result is such that both collapses are into exactly one of the cells  $\mathbf{a}_1$  or  $\mathbf{a}_2$ , and the equality of the results follows by quazi-commutativity of structurally addressed data.

**Quazi-associativity:** We construct  $\mathbf{ph}_3$  as in the quazi-commutitive case. However, here, the equality is given by quazi-associativity of structurally addressed data.

Heap cells that may be  $\mathbf{x}$  and  $\mathbf{y}$

	$\mathbf{ph}(\mathbf{a}_1)$	$\mathbf{ph}(\mathbf{a}_2)$	$\mathbf{ph}(\mathbf{x})$	$\mathbf{ph}(\mathbf{y})$	Justification
1	$\mathbf{x}, \mathbf{y}$				$\checkmark$ , Quazi-commutativity
2	$\mathbf{x}$	$\mathbf{y}$			$\checkmark$ , Pick alternate
3	$\mathbf{x}$		$\mathbf{y}$		$\checkmark$ , Quazi-associativity
4	$\mathbf{x}$			$\mathbf{y}$	$\times$ , $\mathbf{ph} \not\downarrow \mathbf{ph}_2$
5	$\mathbf{y}$	$\mathbf{x}$			$\checkmark$ , Pick alternate
6		$\mathbf{x}, \mathbf{y}$			$\checkmark$ , Quazi-commutativity
7		$\mathbf{x}$	$\mathbf{y}$		$\checkmark$ , Quazi-associativity
8		$\mathbf{x}$		$\mathbf{y}$	$\times$ , $\mathbf{ph} \not\downarrow \mathbf{ph}_2$
9	$\mathbf{y}$		$\mathbf{x}$		$\times$ , $\mathbf{ph} \not\downarrow \mathbf{ph}_1$
10		$\mathbf{y}$	$\mathbf{x}$		$\times$ , $\mathbf{ph} \not\downarrow \mathbf{ph}_1$
11			$\mathbf{x}, \mathbf{y}$		$\times$ , $\mathbf{ph} \not\downarrow \mathbf{ph}_1$
12			$\mathbf{x}$	$\mathbf{y}$	$\times$ , $\mathbf{ph} \not\downarrow \mathbf{ph}_1$
13	$\mathbf{y}$			$\mathbf{x}$	$\checkmark$ , Quazi-associativity
14		$\mathbf{y}$		$\mathbf{x}$	$\checkmark$ , Quazi-associativity
15			$\mathbf{y}$	$\mathbf{x}$	$\times$ , No final heap
16				$\mathbf{x}, \mathbf{y}$	$\times$ , $\mathbf{ph} \not\downarrow \mathbf{ph}_2$

Position of  $\mathbf{x}$  and  $\mathbf{y}$  addresses

Figure A.2.: Case analysis for proof of lemma 26. The four central columns indicate the heap addresses in which the given body addresses are found.

**Pick alternate:** In these cases, construct  $\mathbf{ph}_3$  by collapsing using  $\mathbf{y}$  on  $\mathbf{ph}_1$  and using  $\mathbf{x}$  on  $\mathbf{ph}_2$ . The result is such that  $\mathbf{x}$  collapsed into  $\mathbf{a}_1$  and  $\mathbf{y}$  collapsed into  $\mathbf{a}_2$  (or vice versa), and the result follows directly.

$\mathbf{ph} \not\downarrow \mathbf{ph}_2$ : This permutation cannot occur, as  $\mathbf{ph}$  could never use  $\mathbf{y}$  to collapse to  $\mathbf{ph}_2$ .

$\mathbf{ph} \not\downarrow \mathbf{ph}_1$ : This permutation cannot occur, as  $\mathbf{ph}$  could never use  $\mathbf{x}$  to collapse to  $\mathbf{ph}_1$ .

**No final heap:** This permutation cannot occur, as  $\mathbf{ph}_1$  must satisfy  $\mathbf{y} \in \mathbf{ph}_1(\mathbf{y})$ . Evidently, there is exactly one  $\mathbf{y} \in \text{dom}(\mathbf{ph}_1)$ . The only way to remove  $\mathbf{y}$  from the body of  $\mathbf{ph}_1(\mathbf{y})$  is to collapse a  $\mathbf{y}$ -addressed cell into it, yet collapse will never perform this here by definition. Ergo, there is no  $\mathbf{ph}_n$  such that  $\mathbf{ph}_1 \downarrow^* \mathbf{ph}_n$  and  $\nexists \mathbf{a}. \mathbf{y} \in \mathbf{ph}_n(\mathbf{a})$ . Yet,  $h$  satisfies this second property, and  $\mathbf{ph}_1 \downarrow^* h$ . Contradiction.

□

Given this local confluence, we can prove a many-step version.

**Lemma 27** (Conditional confluence). *Assume that, for all  $\mathbf{ph}, \mathbf{ph}_1 \in \text{PREABSHEAPS}$  and  $h \in \text{STRUCTHEAPS}$ ,  $\mathbf{ph} \downarrow^* h$  and  $\mathbf{ph} \downarrow^* \mathbf{ph}_1$ . Then,  $\mathbf{ph}_1 \downarrow^* h$ .*

*Proof.* Again, we proceed by induction on the number of abstract addresses in  $\mathbf{ph}$ ,  $\#(\mathbf{ph})$ .

- $\#(\mathbf{ph}) = 0$ : By definition,  $\mathbf{ph}_1 = h$ , and the result follows.
- $\#(\mathbf{ph}) = n + 1$ : Then, there exists  $\mathbf{ph}'_1$  such that  $\mathbf{ph} \downarrow \mathbf{ph}'_1 \downarrow^* \mathbf{ph}_1$ . Conditional confluence (lemma 26) ensures that  $\mathbf{ph}'_1 \downarrow^* h$ . Now,  $\#(\mathbf{ph}'_1) = n$ ,  $\mathbf{ph}'_1 \downarrow^* h$  and  $\mathbf{ph}'_1 \downarrow^* \mathbf{ph}_2$ . The inductive hypothesis provides the result.

□

**Theorem 5** (Unique collapse). *For all  $\mathbf{ph}_1 \in \text{PREABSHEAPS}$ , if there exists  $h \in \text{STRUCTHEAPS}$  such that  $\mathbf{ph}_1 \downarrow^* h$ , then for all  $\mathbf{ph}_2 \in \text{PREABSHEAPS}$ ,  $\mathbf{ph}_1 \downarrow^* \mathbf{ph}_2$  and  $\mathbf{ph}_2 \not\downarrow$  implies  $\mathbf{ph}_2 = h$ .*

*Proof.* By conditional confluence (lemma 27), if  $\mathbf{ph}_1 \downarrow^* h$  and  $\mathbf{ph}_1 \downarrow^* \mathbf{ph}_2$ , then  $\mathbf{ph}_2 \downarrow^* h$ . We also have that  $\mathbf{ph}_2 \not\downarrow$ . The only way this is possible is if  $\mathbf{ph}_2 = h$ . □

This result allows us to show that abstract heaps are unambiguous and acyclic.

**Lemma 28** (Abstract heaps are unambiguous). *All  $\mathbf{h} \in \text{ABSHEAPS}$  are unambiguous, in that there are no  $\mathbf{x} \in \text{STRUCTADDRS}$  such that there exists  $\mathbf{a}_1, \mathbf{a}_2 \in \text{ADDRS}$  with  $\mathbf{x} \in \mathbf{h}(\mathbf{a}_1)$  and  $\mathbf{x} \in \mathbf{h}(\mathbf{a}_2)$ .*

*Proof.* We proceed by contradiction. Assume  $\mathbf{h}$  is ambiguous. Therefore, there exists  $\mathbf{a}_1$  and  $\mathbf{a}_2$  with  $\mathbf{x} \in \text{addrs}(\mathbf{h}(\mathbf{a}_1))$  and  $\mathbf{x} \in \text{addrs}(\mathbf{h}(\mathbf{a}_2))$ . By the definition of  $\mathbf{h}$ , there exists an extension  $\mathbf{ph} \in \text{PREABSHEAPS}$  and machine heap  $h$  such that  $\mathbf{h} \sqcup \mathbf{ph} \downarrow^* h$ .

By the definition of heaps, there can be no  $\mathbf{a} \in \text{dom}(h)$  such that  $\mathbf{x} \in \text{addrs}(h(\mathbf{a}))$  (as heaps contain no structural addresses). Therefore, there must be  $\mathbf{x} \in \text{dom}(\mathbf{h} \sqcup \mathbf{ph})$ , and some step in the chain of collapses that collapsed the  $\mathbf{x}$  addressed heap cell into a value. However, by the definition of collapse, this is not possible, as there is never a *unique*  $\mathbf{a}$  containing  $\mathbf{x}$ . Ergo, no matter what chain of collapses is used, it must eventually reach a pre-abstract heap  $\mathbf{ph}'$  such that  $\mathbf{h} \sqcup \mathbf{ph} \downarrow^* \mathbf{ph}'$ ,  $\mathbf{ph}' \not\downarrow$  and  $\mathbf{x} \in \text{dom}(\mathbf{ph}')$ . But, by theorem 3, as  $\mathbf{h} \sqcup \mathbf{ph} \downarrow^* h$  so  $\mathbf{ph}' \downarrow^* h$ , which is impossible. Ergo,  $\mathbf{h}$  was not ambiguous. □

**Lemma 29** (Abstract heaps are acyclic). *All  $\mathbf{h} \in \text{ABSHEAPS}$  are acyclic, in that there are no structural addresses  $\mathbf{x} \in \text{STRUCTADDRS}$  and (possibly empty) chain of single-step collapse steps such that  $\mathbf{h} \downarrow^* \mathbf{h}_n$  with  $\mathbf{x} \in \text{addrs}(\mathbf{h}_n(\mathbf{x}))$ .*

*Proof.* We proceed by contradiction. Assume  $\mathbf{h}$  is cyclic. By definition, there is some concrete structured heap  $h$  and pre-abstract heap  $\mathbf{ph}$  such that  $\mathbf{h} \sqcup \mathbf{ph} \downarrow^* h$ , where  $h$  contains no structural addresses. By the definition of cyclicity, there is some  $\mathbf{h}_n$  such that  $\mathbf{x} \in \text{addrs}(\mathbf{h}_n(\mathbf{x}))$ . There must exist  $\mathbf{h}_m$  such that  $\mathbf{h}_n \downarrow^* \mathbf{h}_m$ ,  $\mathbf{h}_m \not\downarrow$ , with  $\mathbf{x} \in \text{addrs}(\mathbf{h}_m(\mathbf{x}))$  as, by definition, collapse will never compress an abstract heap cell into itself and, by construction, the domain can never contain an addition  $\mathbf{x}$  by which collapse can remove the body address  $\mathbf{x}$ . By theorem 3, it must be the  $\mathbf{h}_m = h$ , which cannot be, as  $\mathbf{h}_m$  contains structural addresses. Ergo,  $\mathbf{h}$  was not cyclic.  $\square$

### A.3. Promise- and obligation-carrying data as structural separation algebras

This section provides the proof that promise- and obligation-carrying data is a structural addressing algebra. The proofs proceed as in the promises case given in section 5.2.1.

**Definition 144** (Promise- and obligation-carrying data algebra). Let  $(\text{STRUCTADDRS}, \text{DATA}, \text{addrs}, \text{comp})$  be some structural addressing algebra (definition 42). Let  $(\text{STRUCTADDRS}_{\mathfrak{m}}, \text{DATA}_{\mathfrak{m}}, \text{addrs}_{\mathfrak{m}}, \text{comp}_{\mathfrak{m}})$  be the associated underlying data algebra (definition 90). Let  $\text{PROMOBSDATA}$  be the set of promise- and obligation-carrying data defined using these previous two algebras. Then, the associated *promise- an obligation-carrying data algebra* is defined as:

$$(\text{STRUCTADDRS}_O, \text{PROMOBSDATA}, \text{addrs}_O, \text{comp}_O)$$

where

1. The structural addresses are those of the underlying data:  $\text{STRUCTADDRS}_P = \text{STRUCTADDRS}_{\mathfrak{m}}$ .
2. The addresses function is the addresses function of the underlying data, applied to the underlying data component of a promise-carrying data  $\text{addrs}(\mathbf{pod}) = \text{addrs}_{\mathfrak{m}}(\mathbf{pod} \downarrow_1)$ .

3. The compression function  $\text{comp}_P$  is that defined on  $\text{PROMOBSDATA}$ , given in definition 139.

When unambiguous,  $\text{comp}_O(\mathbf{x}, \mathbf{pod}_1, \mathbf{pod}_2)$  is written  $\mathbf{pod}_1 \otimes \mathbf{pod}_2$ , and  $\text{addrs}_O$  is written  $\text{addrs}$ . The data of the underlying algebra are equivalent to data with no promises and obligations  $d \in \text{DATA}_{\mathfrak{m}} = (d, \emptyset, \emptyset)$ .

This algebra inherits many useful properties from the underlying structural addressing algebra. However, we must first ensure that the compression function preserves the well-formedness of the richer data we are using. For the lemmas, assume a structural addressing algebra  $(\text{STRUCTADDRS}, \text{DATA}, \text{addrs}, \text{comp})$  for the promises and obligations, and the associated underlying data algebra  $((\text{STRUCTADDRS}_O, \text{PROMOBSDATA}, \text{addrs}_O, \text{comp}_O)$ .

**Lemma 30** (Compression is closed on  $\text{PROMOBSDATA}$ ). *Let  $\mathbf{pod}_1, \mathbf{pod}_2 \in \text{PROMOBSDATA}$ ,  $\mathbf{x} \in \text{STRUCTADDRS}$  and  $\mathbf{pod}_1 \otimes \mathbf{pod}_2$  be defined. Then,  $\mathbf{pod}_1 \otimes \mathbf{pod}_2 \in \text{PROMOBSDATA}$ .*

*Proof.* Assume  $\mathbf{pod}_1 = (d_1, \Pi_1, \Omega_1)$  and  $\mathbf{pod}_2 = (d_2, \Pi_2, \Omega_2)$ . The result of  $\mathbf{pod}_1 \otimes \mathbf{pod}_2$  is:

$$(d_1 \otimes d_2, (\Pi_1 - \mathbf{x}) \cup (\Pi_2 - \mathfrak{m}), (\Omega_1 - \mathbf{x}) \cup (\Omega_2 - \mathfrak{m}))$$

As per the definitions of promises, obligations and promise- and obligation-carrying data (definitions 94, 137 and 138 respectively), this will be valid promise- and obligation-carrying data if: 1) there are no duplicate promise or obligation addresses; 2) every promise or obligation is addressed to either  $\mathfrak{m}$  or a body address within the data; and 3) every obligation is fulfilled.

Note first that, by the addressing properties of the underlying structural addressing algebras, the set  $\text{addrs}(d_1 \otimes d_2)$  contains the addresses  $\text{addrs}((d_1 \setminus \{\mathbf{x}\}) \cup \text{addrs}(d_2))$ . Call this the *address containment property*.

1. Proceed by contradiction. Assume an address  $a$  that identifies two promises:

$$\{(a, \bar{d}_1), (a, \bar{d}_2)\} \subseteq (\Pi_1 - \mathbf{x}) \cup (\Pi_2 - \mathfrak{m}).$$

Notice that  $a \neq \mathfrak{m}$ , as by construction  $\mathfrak{m}$  is removed from  $\Pi_2$  in the compression, and  $\Pi_1$  is well-formed. Ergo  $a \in \text{STRUCTADDRS} \setminus \{\mathfrak{m}\}$ . Therefore, by well-formedness of  $\mathbf{pod}_1$  and  $\mathbf{pod}_2$ ,  $a \in \text{addrs}_O(\mathbf{pod}_1)$  or  $a \in \text{addrs}_O(\mathbf{pod}_2)$ . Assume  $a = \mathbf{x}$ . Then it cannot be duplicated, as  $\mathbf{x}$  is removed from  $\Pi_1$  in the union and  $\Pi_2$  is well-formed. However, it is also the case that  $a \neq \mathbf{x}$ . This follows from the address containment properties, which show that  $\text{addrs}(d_1) \cup \text{addrs}(d_2) \setminus \{\mathbf{x}\} = \emptyset$ .



The case for obligations is similar.

2. Proceed by contradiction. Assume there is a promise in  $(a, \bar{d}) \in (\Pi_1 - \mathbf{x}) \cup (\Pi_2 - \mathfrak{m})$  such that  $a \neq \mathfrak{m}$  and  $a \notin \text{addrs}(d_1 \otimes d_2)$ . By definition, either  $a \in \Pi_1$  or  $a \in \Pi_2$ . By the well-formedness of  $\mathbf{pod}_1$  and  $\mathbf{pod}_2$ , either  $a \in \text{addrs}_O(\mathbf{pod}_1)$ , or  $a \in \text{addrs}_O(\mathbf{pod}_2)$ . If  $a \neq \mathbf{x}$ , then by the address containment property, it must be that  $a \in \text{addrs}(d_1 \otimes d_2)$ , which contradicts its existence.

If  $a = \mathbf{x}$ , then by the construction of  $(\Pi_1 - \mathbf{x}) \cup (\Pi_2 - \mathfrak{m})$ ,  $a$  addresses a promise in  $\Pi_2$ . Ergo,  $a \in \text{addrs}_O(\mathbf{pod}_2)$  by well-formedness. By the address preservation property of structural addressing algebras,  $a \in \text{addrs}(d_1 \otimes d_2)$ , which again contradicts its existence.

The case for obligations is similar.

3. Proceed by contradiction. Assume there is an unfulfilled obligation  $(a, \bar{d}) \in (\Omega_1 - \mathbf{x}) \cup (\Omega_2 - \mathfrak{m})$ . Assume first that  $a = \mathfrak{m}$ . Then, by construction, the obligation must be  $(\mathfrak{m}, \underline{d}) \in \Omega_1$ . As the obligation is not fulfilled, it must be that  $\downarrow (d_1 \otimes d_2, (\Pi_1 - \mathbf{x}) \cup (\Pi_2 - \mathfrak{m})) \not\subseteq \underline{d}$ . However, we have  $\downarrow (d_1 \otimes d_2, (\Pi_1 - \mathbf{x}) \cup (\Pi_2 - \mathfrak{m})) \subseteq \downarrow (d_1, \Pi_1)$ . This must be true, as either  $\mathbf{pod}_1$  had no promise from  $\mathbf{x}$  for  $d_2$ , and so the body closure considered *all* possibilities, or  $\mathbf{pod}_1$  did have a promise, which was fulfilled by the obligations of  $\mathbf{pod}_2$ . Moreover,  $\downarrow (d_1, \Pi_1) \subseteq \underline{d}$ , as  $\mathbf{pod}_1$  is well-formed. Ergo the obligation must be met, as  $\subseteq$  is transitive.

Assume now that  $a \neq \mathfrak{m}$ . Then, via the proof of case 2) above, it must be that the obligation is  $(\mathbf{x}, \bar{d})$  for some  $\mathbf{y} \in \text{addrs}(d_1 \otimes d_2)$ . As the obligation is not fulfilled, it must be that  $\cup_{\mathbf{y}} ((d_1 \otimes d_2, (\Pi_1 - \mathbf{x}) \cup (\Pi_2 - \mathfrak{m}))) \not\subseteq \bar{d}$ . However, by reasoning similar to the above, this cannot be:  $\cup_{\mathbf{y}} ((d_1 \otimes d_2, (\Pi_1 - \mathbf{x}) \cup (\Pi_2 - \mathfrak{m}))) \subseteq \cup_{\mathbf{y}} ((d_1, \Pi_1))$ , as the body closure around  $\mathbf{x}$  (even if vacuous) must contain at least the actual data  $d_2$ .

□

Of the underlying data algebra properties, the following are maintained.

**Lemma 31** (Value containment). *Given DATA that is the underlying structural addressing algebra for the promise- and obligation-carrying data algebra, and VALUES that is the set of values underlying DATA,  $\text{VALUES} \subseteq \text{DATA}$ .*

*Proof.* Follows from the lift of data without promises or obligations to be equal to promise-carrying data, and the value containment property of the underlying data. □

**Lemma 32** (Address properties). *For all  $\mathbf{pod}_1, \mathbf{pod}_2 \in \text{PROMOBSDATA}$  and  $\mathbf{x} \in \text{STRUCTADDRS}_O$ , if  $\mathbf{pod}_1 \otimes \mathbf{pod}_2$  is defined then:*

1. Containment:  $\mathbf{x} \in \text{addrs}_O(\mathbf{pod}_1)$ .
2. Non-overlap:  $\text{addrs}_O(\mathbf{pod}_1) \cap \text{addrs}_O(\mathbf{pod}_2) \subseteq \{\mathbf{x}\}$
3. Preservation:  $(\text{addrs}_O(\mathbf{pod}_1) \setminus \{\mathbf{x}\}) \cup \text{addrs}_O(\mathbf{pod}_2) = \text{addrs}_O(\mathbf{pod}_1 \otimes \mathbf{pod}_2)$

*Proof.* These follow directly from the addresses function being defined in terms of the underlying addresses function, which is applied to the underlying data.  $\square$

**Lemma 33** (Compression quasi-associates). *Let  $\mathbf{pod}_1, \mathbf{pod}_2, \mathbf{pod}_3 \in \text{PROMOBSDATA}$  and  $\mathbf{x}, \mathbf{y} \in \text{STRUCTADDRS}$ , with  $\mathbf{pod}_1 = (d_1, \Pi_1, \Omega_1)$ ,  $\mathbf{pod}_2 = (d_2, \Pi_2, \Omega_2)$ ,  $\mathbf{pod}_3 = (d_3, \Pi_3, \Omega_3)$ . If  $\mathbf{y} \in \text{addrs}_O(\mathbf{pod}_2)$  and either  $\mathbf{y} \notin \text{addrs}_O(\mathbf{pod}_1)$  or  $\mathbf{y} = \mathbf{x}$ , then  $\mathbf{pod}_1 \otimes (\mathbf{pod}_2 \otimes \mathbf{pod}_3) = (\mathbf{pod}_1 \otimes \mathbf{pod}_2) \otimes \mathbf{pod}_3$ .*

*Proof.* Assume that  $\mathbf{x} \neq \mathbf{y}$  (the case where  $\mathbf{x} = \mathbf{y}$  is similar). Assume further that the left-hand side,  $\mathbf{pod}_1 \otimes (\mathbf{pod}_2 \otimes \mathbf{pod}_3)$  is defined. Then, it must equal:

$$\left( d_1 \otimes (d_2 \otimes d_3), \begin{array}{l} (\Pi_1 - \mathbf{x}) \cup ((\Pi_2 - \mathbf{y}) \cup (\Pi_3 - \mathfrak{m})) - \mathfrak{m}, \\ (\Omega_1 - \mathbf{x}) \cup ((\Omega_2 - \mathbf{y}) \cup (\Omega_3 - \mathfrak{m})) - \mathfrak{m} \end{array} \right)$$

The removal of a promise or obligation from a set is idempotent. Therefore, we have:

$$\left( d_1 \otimes (d_2 \otimes d_3), \begin{array}{l} (\Pi_1 - \mathbf{x}) \cup ((\Pi_2 - \mathbf{y} - \mathfrak{m}) \cup (\Pi_3 - \mathfrak{m})), \\ (\Omega_1 - \mathbf{x}) \cup ((\Omega_2 - \mathbf{y} - \mathfrak{m}) \cup (\Omega_3 - \mathfrak{m})) \end{array} \right)$$

As  $\mathbf{x} \neq \mathbf{y}$ , there are no promises named  $\mathbf{y}$  in  $\Pi_1$  (similarly for the obligations). Moreover, as standard set union associates, we have:

$$\left( (d_1 \otimes d_2) \otimes d_3, \begin{array}{l} ((\Pi_1 - \mathbf{x}) \cup (\Pi_2 - \mathfrak{m})) - \mathbf{y} \cup (\Pi_3 - \mathfrak{m}), \\ ((\Omega_1 - \mathbf{x}) \cup (\Omega_2 - \mathfrak{m})) - \mathbf{y} \cup (\Omega_3 - \mathfrak{m}), \end{array} \right)$$

which is equal to  $(\mathbf{pod}_1 \otimes \mathbf{pod}_2) \otimes \mathbf{pod}_3$  (data quasi-associativity holds by the definition of structural addressing algebras). Ergo, if the left is defined, the right is defined and equal to it.  $\square$

**Lemma 34** (Compression quasi-commutes). *Let  $\mathbf{pod}_1, \mathbf{pod}_2, \mathbf{pod}_3 \in \text{PROMDATA}$  and  $\mathbf{x}, \mathbf{y} \in \text{STRUCTADDRS}$ , with  $\mathbf{pod}_1 = (d_1, \Pi_1, \Omega_1)$ ,  $\mathbf{pod}_2 = (d_2, \Pi_2, \Omega_2)$ ,  $\mathbf{pod}_3 = (d_3, \Pi_3, \Omega_3)$ . If  $\mathbf{x} \notin \text{addrs}_O(\mathbf{pod}_3)$  and  $\mathbf{y} \notin \text{addrs}_O(\mathbf{pod}_2)$ , then  $(\mathbf{pod}_1 \otimes \mathbf{pod}_2) \otimes \mathbf{pod}_3 = (\mathbf{pod}_1 \otimes \mathbf{pod}_3) \otimes \mathbf{pod}_2$ .*

*Proof.* Assume that the left hand side,  $\mathbf{pod}_1 \otimes \mathbf{pod}_2 \otimes \mathbf{pod}_3$  is defined. Then, it must equal:

$$\left( (d_1 \otimes d_2) \otimes d_3, \begin{array}{l} ((\Pi_1 - \mathbf{x}) \cup (\Pi_2 - \mathfrak{m})) - \mathbf{y} \cup (\Pi_3 - \mathfrak{m}), \\ ((\Omega_1 - \mathbf{x}) \cup (\Omega_2 - \mathfrak{m})) - \mathbf{y} \cup (\Omega_3 - \mathfrak{m}) \end{array} \right)$$

As  $\Pi_2$  has no promise named  $\mathbf{y}$  (and similarly for the obligations), we have:

$$\left( (d_1 \otimes d_2) \otimes d_3, \begin{array}{l} ((\Pi_1 - \mathbf{x} - \mathbf{y}) \cup (\Pi_2 - \mathfrak{m})) \cup (\Pi_3 - \mathfrak{m}), \\ ((\Omega_1 - \mathbf{x} - \mathbf{y}) \cup (\Omega_2 - \mathfrak{m})) \cup (\Omega_3 - \mathfrak{m}) \end{array} \right)$$

As  $\Pi_3$  has no promise named  $\mathbf{x}$ , we then have

$$\left( (d_1 \otimes d_2) \otimes d_3, \begin{array}{l} ((\Pi_1 - \mathbf{y}) \cup (\Pi_3 - \mathfrak{m})) - \mathbf{x} \cup (\Pi_2 - \mathfrak{m}), \\ ((\Omega_1 - \mathbf{y}) \cup (\Omega_3 - \mathfrak{m})) - \mathbf{x} \cup (\Omega_2 - \mathfrak{m}) \end{array} \right)$$

which is  $\mathbf{pod}_1 \otimes \mathbf{pod}_3 \otimes \mathbf{pod}_2$ .  $\square$

The underlying data has a simple left identity property, as  $\mathbf{x} \otimes d = d$  always. The addition of promises and obligations breaks this simple identity, simply by picking any  $d$  with a head promise. However, there are still identities, which can be seen via construction based upon the choice of  $d$ . We show the existence of left identities below; the right identities are demonstrated in a similar manner.

**Lemma 35** (Existence of left identities). *For all  $\mathbf{pod} \in \text{PROMOBSDATA}$  and  $\mathbf{x} \in \text{STRUCTADDRS}$ , there exists an  $\mathbf{pod}_x \in \text{PROMOBSDATA}$  such that  $\mathbf{pod}_x \otimes \mathbf{pod} = \mathbf{pod}$ .*

*Proof.* Assume  $\mathbf{pod} = (d, \Pi, \Omega)$ . We will construct of  $\mathbf{pod}_x = (\mathbf{x}, \Pi_x, \Omega_x)$  by picking  $\Pi_x$  and  $\Omega_x$  based on the contents of  $\Pi$  and  $\Omega$ . Without further information, the compression is:

$$(\mathbf{x} \otimes d, (\Pi_x - \mathbf{x}) \cup (\Pi - \mathfrak{m}), (\Omega_x - \mathbf{x}) \cup (\Omega - \mathfrak{m}))$$

Assume that  $(\mathfrak{m}, \bar{d}) \in \Pi$  and  $(\mathfrak{m}, \underline{d}) \in \Omega$ . In this case, we pick  $\Pi_x = \{(\mathbf{x}, \underline{d}), (\mathfrak{m}, \bar{d})\}$  and  $\Omega = \{(\mathbf{x}, \bar{d}), (\mathfrak{m}, \underline{d})\}$ . This is well-formed by construction. By calculation, using the identity property of data:

$$\begin{aligned} & (\mathbf{x} \otimes d, (\Pi_x - \mathbf{x}) \cup (\Pi - \mathfrak{m}), (\Omega_x - \mathbf{x}) \cup (\Omega - \mathfrak{m})) \\ = & (d, (\{(\mathbf{x}, \underline{d}), (\mathfrak{m}, \bar{d})\} - \mathbf{x}) \cup (\Pi - \mathfrak{m}), (\{(\mathbf{x}, \bar{d}), (\mathfrak{m}, \underline{d})\} - \mathbf{x}) \cup (\Omega - \mathfrak{m})) \\ = & (d, \{(\mathfrak{m}, \bar{d})\} \cup (\Pi - \mathfrak{m}), \{(\mathfrak{m}, \underline{d})\} \cup (\Omega - \mathfrak{m})) \\ = & (d, \Pi, \Omega) \end{aligned}$$

as required. The cases where  $\mathbf{pod}$  has either no head promise or head obligations are similar.  $\square$

The identity properties were used by structural addressing algebras to show that the choice of hole is irrelevant. One could always compress another structural address in, so that  $d(\otimes)\mathbf{y}$  was defined if  $\mathbf{y} \notin \text{addrs}(d)$ . Without the simple identity properties, we cannot prove this directly. However, we can still show that the choice of address does not matter.

**Lemma 36** (Address irrelevance). *For all  $\mathbf{pod}_1, \mathbf{pod}_2 \in \text{PROMOBSDATA}$  and  $\mathbf{x} \in \text{addrs}_O(\mathbf{pod}_1)$ ,  $\mathbf{y} \notin \text{addrs}_O(\mathbf{pod}_2)$ , if  $\mathbf{pod}_1 \otimes \mathbf{pod}_2$  is defined, there exists  $\mathbf{pod}_y$  such that  $\mathbf{pod}_1 \otimes \mathbf{pod}_y \circledast \mathbf{pod}_2 = \mathbf{pod}_1 \otimes \mathbf{pod}_2$ .*

*Proof.* Knowing that  $\mathbf{pod}_1 \otimes \mathbf{pod}_2$ , assume that  $\mathbf{pod}_1 = (d_1, \Pi_1, \Omega_1)$  and  $\mathbf{pod}_2 = (d_2, \Pi_2, \Omega_2)$ . We will consider the case where  $(\mathbf{x}, \underline{d}_1) \in \Pi_1$ ,  $(\mathbb{m}, \underline{d}_2) \in \Omega_2$ ,  $(\mathbb{m}, \bar{d}_2) \in \Pi_2$  and  $(\mathbf{x}, \bar{d}_1) \in \Omega_1$ , (ergo,  $\underline{d}_2 \subseteq \underline{d}_1$ ,  $\bar{d}_1 \subseteq \bar{d}_2$ ). The cases without head or body obligations to one or the other are similar.

We construct  $\mathbf{pod}_x$  in a similar fashion to building the identities for data, by giving it the promises and obligations that match the expectations of  $\mathbf{pod}_1$  and  $\mathbf{pod}_2$ . Pick  $\mathbf{pod}_y = (\mathbf{y}, \Pi_y, \Omega_y)$  where  $\Pi_y = \{(\mathbb{m}, \bar{d}_1), (\mathbf{y}, \underline{d}_1)\}$  and  $\Omega_y = \{(\mathbb{m}, \underline{d}_1), (\mathbf{y}, \bar{d}_1)\}$ . The result then follows by calculation, as in lemma 35.  $\square$

If our promise- and obligation-carrying data is a structural addressing algebra, we know it is safe to use as the data for structural separation logic. We can satisfy the requirements definition 42 as:

1. *Value containment*: True by lemma 31.
2. *Unaddressed values*: Satisfied by the lift of the addresses function in definition 144 and the underlying data.
3. *Address properties*: Satisfied by lift of the addresses function in definition 144, and the underlying data properties.
4. *Identity*: True by lemma 35.
5. *Arbitrary addresses*: True via lemma 36.
6. *Compression left-cancellativity*: True by definition of compression and underlying data.
7. *Compression quasi-associativity*: True by lemma 33.
8. *Compression quasi-commutativity*: True by lemma 34.

Therefore, given any abstract addressing algebra, we can construct a promise- and obligation-carrying structure, which by the lemmas above will be a weak structural addressing algebra. This is then a foundation on which we can build abstract heaps.

**Corollary 2** (Abstract heaps with promises and obligations are sound). *The construction of abstract heaps using promises and obligations on data is sound.*

*Proof.* As results in chapter 3. □

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