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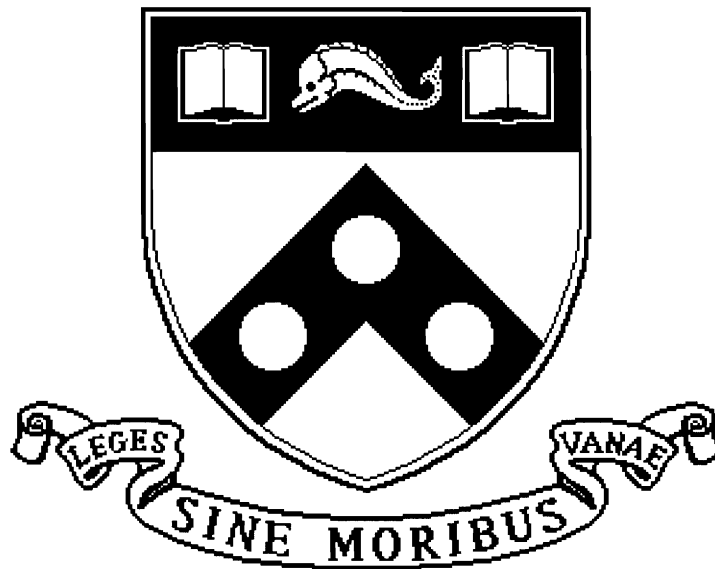
Comments

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The Power Broker: Intelligent Power Management for Mobile Computers

MS-CIS-96-12

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1996

The Power Broker: Intelligent Power Management for Mobile Computers*

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Abstract

Technology has improved processor speed and memory densities at exponential rates. Rapid advances in portable computing have resulted in laptop computers with performance and features comparable to their desktop counterparts. Battery technology has failed to keep pace, decreasing the usefulness of mobile computers and portable wireless devices.

We provide a detailed analysis of power consumption typically encountered in a networked laptop computer and the power management methods currently used. We then show how interaction between independent power consumers results in inefficient use of energy resources and propose the Power Broker as a means for orchestrating energy use with the goal of extending battery life. The Power Broker's resource management algorithms exploit an abundant resource (CPU power) to conserve a scarce one (battery energy).

1 Introduction

Successful engineering is as much art as science, and a key aesthetic is *balance* among components. In designing computer systems, we are faced with the challenge of balancing system elements exhibiting exponential performance improvement with other elements which enjoy much less aggressive improvement. Trading plentiful resources against scarce ones to obtain a balance is a proven engineering principle. For example, the gap between processor speed and DRAM access time has motivated architectural features such as caches and pipelining. In this paper, we apply this principle to improve battery life in mobile computing.

A battery's performance can be characterized by the total amount of energy it can store (i.e. power x duration) and the physical dimensions (weight and size) of the battery. The total energy available from a battery is a design issue and is fixed at design time, along with its weight and size. The only value available for manipulation by the user is duration, or battery life. Short battery life plagues mobile computer users to whom the stark contrast between exponential and non-exponential technology improvement rates are particularly evident. Our research goal is to investigate the possibilities for rethinking system resource usage towards achieving better system balance; we believe that the development, automation and integration of improved power management algorithms can have a major impact on improving mobile computer battery life.

1.1 Portable Computer Technology

In the past laptop computers have served as portable word processors or game machines. Such machines were generally two or more generations behind desktop computers in terms of processing power, features and performance. Limitations in display and miniaturization technology prevented laptops from being able to compete with desktops as "real" computers.

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Recent advances in technology have dramatically improved laptop performance and it is increasingly common to see software development being done on a laptop. Laptops with a 133 MHz Pentium processor, 1.2 Gigabyte hard disk, built-in 6x CD ROM drive and 12.1 inch SVGA display are currently available, albeit at a price premium over comparable desktops. In general, laptops are now near desktops in terms of performance and features. However in addition to a price premium, laptops have another drawback that doesn't affect desktops—limited battery life.

A mobile computer (for the purposes of this paper, we define a mobile computer as a laptop computer with wireless networking capabilities) has severe limits on its electrical power usage, and a frequent complaint about mobile computers is the short lifespan of the battery. Battery life is rarely more than 3 hours for a heavily-used laptop. Additional features, such as larger color displays, larger and faster hard disks, powerful processors, more memory and CD-ROM drives are becoming common, and result in ever increasing electrical power demands. Unfortunately, laptop batteries are not advancing as rapidly as the other subsystems. Each of these new additions, unless managed properly, will only further reduce battery life and inhibit untethered operation.

To optimize battery use, we propose a centralized resource management tool, the Power Broker. The Power Broker is a power management scheme that keeps track of and optimizes the various subsystems of a mobile computer. It is aware of each subsystem – CPU, system memory, hard disk, display, network interface etc. – and selectively slows or shuts down a system component based on the status of the other components. This is to be done without significantly affecting performance from the user point of view.

1.2 Overview

In the next section we will discuss laptop batteries and present evidence that batteries will lack significant improvement in the near future. Section 3 shows the relative power consumption of the major subsystems of a laptop. In Section 4 we examine currently available power management techniques for each of the subsystems, and discuss some of the problems associated with them. Section 5 presents ideas for the Power Broker.

2 The Problem with Batteries

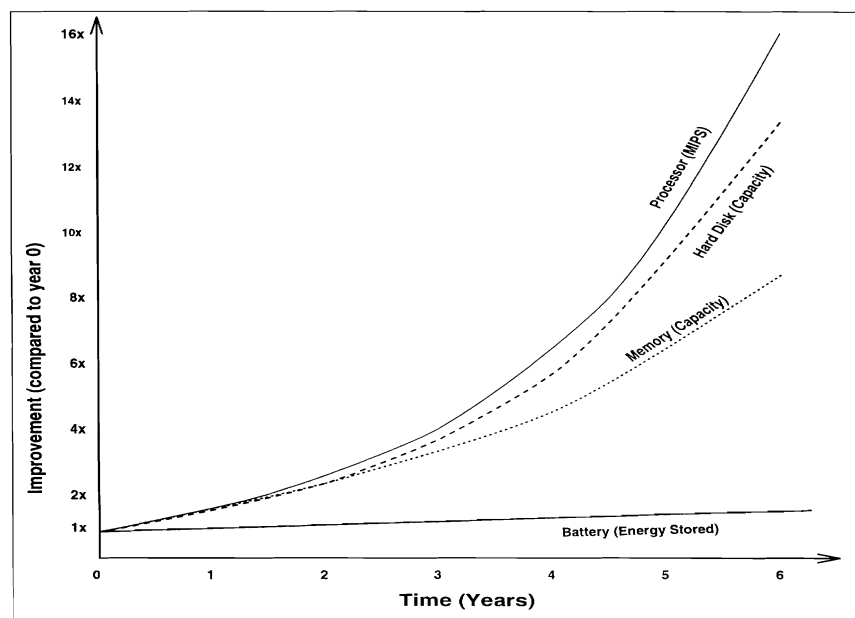


Figure 1: Approximate performance/capacity growth of major laptop components

Figure 1 shows the approximate time it takes for some of the major subsystems of a laptop to double in performance or capacity [2, 20]. In general, an unmanaged performance or capacity increase also indicates some increase in power consumption. Based on current research, the growth rate of battery power output through the year 2000 is expected to be no more than 20% [20].

Advancements in power storage technology are slow in comparison to the other subsystems of a mobile computer. Presently there is a shift from Nickel Cadmium (Ni Cd) and Nickel Metal Hydride (Ni MH) batteries to Lithium Ion (Li ion), which has a better gravimetric energy density (i.e. energy per unit of weight) and longer recharge cycle life. Lithium ion batteries took many years to develop and have some disadvantages compared to Ni Cd batteries—they can require an additional 2-3 hours to reach their maximum charge compared to Ni Cd batteries, and require much stricter voltage regulation when charging [11]. Significant advances in battery technology take many years and are unable to keep pace with the growth of laptop power consumption.

Another important issue in mobile computing is battery weight. One impractical solution to the limited battery life problem would be to carry multiple spare batteries and simply replace them as necessary. Similarly, there are laptops that allow a user to install two batteries in the laptop, extending the laptop's usage but at the expense of additional weight and the loss of a modular bay. The most recent advances in laptop batteries are in the form of better "fuel gauging" of the battery, to give a more precise measure of the charge level and to estimate the time left before a recharge is needed. For example, Intel-Duracell's Smart Battery Specifications [15] propose a common information mechanism for laptop rechargeable batteries. Although this is a useful measure, it does not extend battery life.

Another issue that has been brought up with laptop batteries is that of safety. The current generation of Lithium ion batteries have had mixed reviews in terms of safety. A laptop that is recharged/used in an insufficiently ventilated area may cause the battery to burn out. Dropping the laptop may cause a short-circuit that could start a fire in the laptop [23]. This is not mere speculation – for example, Apple Computer had to recall their Powerbook 5300 laptops [1] because the batteries ignited under certain conditions. Battery manufacturers claim otherwise – documents from them show that the batteries in laptops can survive significant abuse (short-circuit, puncturing, heat etc.) without any danger of fire or explosion [4]. It is not clear whether some of the problems are caused by bad design, or misuse by the user.

We believe that these problems will increase as mobile computer use becomes more prevalent and batteries continue to increase in energy density. Again, the indications are that we must learn to use the available power more efficiently. Thus, unless there is a major advance in power management, the mobility of mobile computers is going to be severely restricted by short battery life.

3 The Balance Of Power

Figure 2 shows the change over the past few years in the fraction of total power consumed by the major subsystems of a laptop computer. The x -axis represents recent years, and the data is for typical laptop¹ computers for that year. The specifications for a typical laptop for that year are included in the bubbles above the graph lines. The values in the graph are mostly experimental values from [9, 16, 18] and measurements by the authors, although some estimations have been made for 1992. The jump in the power consumed by displays (1993 to 1994) is due to the move from grayscale to color displays. It should be noted that although the percentage of total power used by the display for the newest computer has decreased, the actual power used has increased due to the use of active matrix technology.

The reduction in microprocessor power consumption is a result of advanced microprocessors with built-in power management and also the move to lower voltage designs. A more detailed explanation is provided in Section 4.3. Hard disks are consuming an increasing fraction of total system power as manufacturers focus on increasing capacity rather than reducing power consumption. The rest of the components of a typical laptop – keyboard, floppy drive etc. – typically consume less than 15% of the total power and are not shown on the chart. CD ROMs can use a significant amount of power, but are not included in the chart since they are used infrequently.

¹The computers measured were the Zenith MasterSport SLe (1991), Compaq LTE 386 (1993), Compaq 486 (1994) and Toshiba 410 CDT (1995-96)

Figure 3 gives measured values of the power consumed by the major system components of a Toshiba 410 CDT mobile computer (Pentium 90 with 8 MBytes of EDO RAM and AT&T WaveLAN PC Card). While this figure is based on actual measurements, the results are based on estimates of typical usage. The measured instantaneous power with the system idle (display on, HD spinning, WaveLAN receiving) was 14 Watts which is small compared to a light bulb (60 Watts) but large for a system that is powered by a battery.

The conclusion is that even though the Features/Dollar (and in some cases Features/Power Consumed) ratios have increased significantly, the overall power consumption of a laptop has also increased. One solution to this problem would be to decrease the capacity and/or performance of the individual subcomponents. For

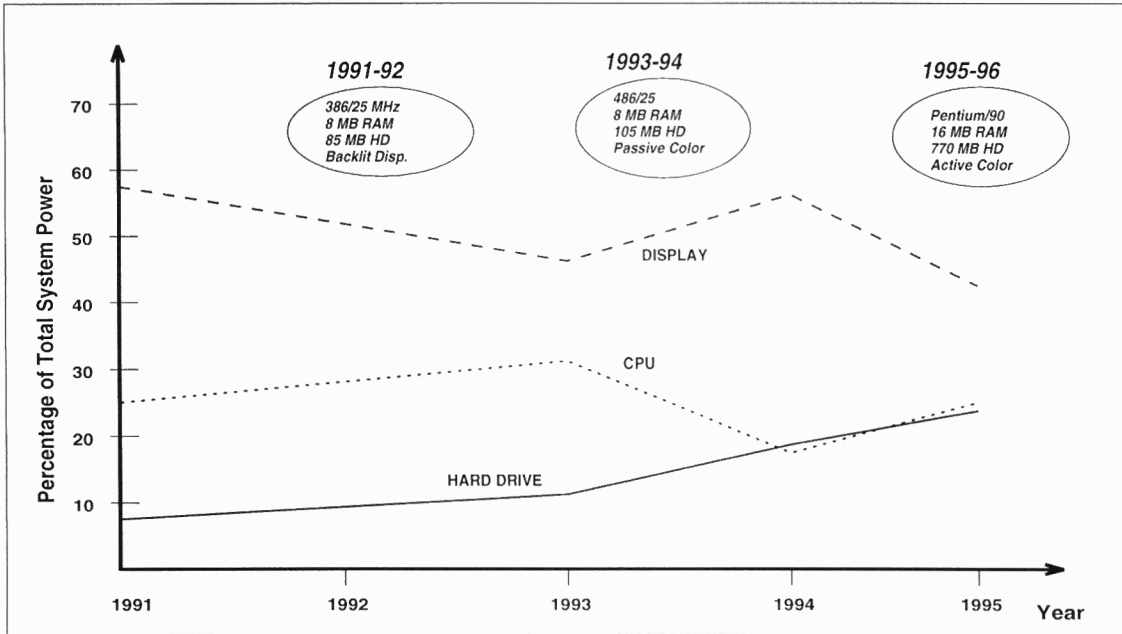


Figure 2: Percentage of total power consumed by major components in a typical laptop computer.

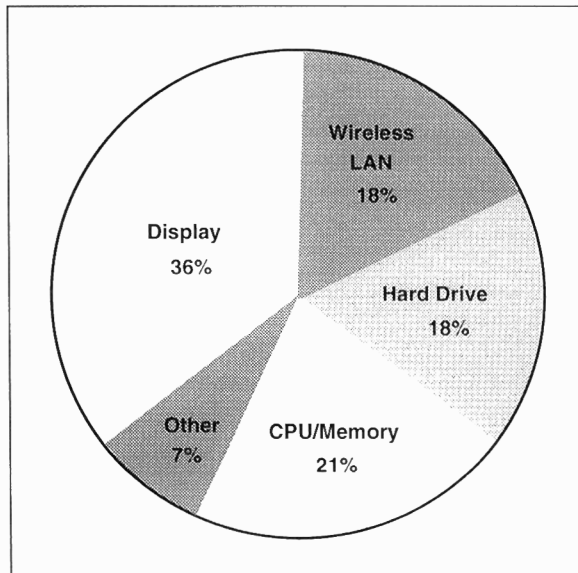


Figure 3: Power consumption by each subsystem of a mobile computer.

example, we could offer a 80286 laptop with 1 MB RAM, small grayscale display and a 10 MB disk that would offer superior battery life, but this machine might not load a full-size operating system, much less be useful in day-to-day laptop-based tasks. In fact, machines similar to this already exist as palmtop computers and have their own niche. Since reducing the features available on a computer is not economically feasible we must intelligently manage system power use.

4 Current Work in Power Management

Currently, power management in laptops is performed in a variety of ways, including custom BIOS implementations, unique device configurations for specific operating systems, and various interpretations of the Advanced Power Management standard [15] (APM - a joint proposal from Intel and Microsoft). The APM BIOS is a layer of software that supports power management in computers with “power manageable” hardware. The APM specification defines the hardware independent software interface between system hardware and an operating system power management policy driver. Unfortunately, most manufacturers incorporate only a small subset of the APM features, and few operating systems actually use the features.

Most laptops have simple power management schemes that allow the CPU to be run in “fast” or “slow” mode to conserve power (described in more detail in Section 4.3). In addition, the display can be blanked after being idle for a set amount of time, and the hard drive can be powered down when idle for several minutes. The user commonly has the option to set each of the parameters individually. The remainder of this section examines each major subsystem of a laptop and discusses the details of currently available management schemes.

4.1 Hard Disk Power Management

The hard disk is one of the three big consumers in a laptop’s power budget. Depending on its state, it can use up anywhere between one and three Watts—approximately 25% of total system power. Although the Power/MByte ratio has fallen rapidly in the past few years, the actual power consumed by a typical drive has remained approximately constant. Since some of the other laptop components have reduced their power consumption, the net effect is that a laptop hard drive is taking an increasing percentage of total system power. Drive manufacturers driven by consumer demands have focused their efforts on increasing drive capacity, rather than decreasing overall power consumption.

Figure 4 is derived from Li, et. al’s [16] measurements and illustrates the dynamic power consumption of a typical laptop-optimized hard drive (a Maxtor MXL-105 III). The total energy consumed is equal to the entire shaded area under the curve (i.e. Energy = Watts*Seconds). The largest power drain occurs during spin up, shown as area **2** in the figure. Spinning up a disk requires overcoming the mechanical inertia of the stationary platters of a disk. Once the platters are spinning, the power required to keep them spinning is much lower, as shown in area **3** of the figure. Disks optimized for laptops have a shorter spin up time than disks intended for ordinary PC’s. This is to allow for frequent spin-downs to conserve energy. Of course, spinning-down and spinning-up a disk *too frequently* can result in higher overall power consumption since the energy required to spin up a disk is much higher than that needed to keep the disk spinning. In theory, the best power conservation happens when a disk is spun down if the energy it would spend being idle (i.e. area **3**) is equivalent to or greater than the additional cost of spinning it back up (the area in **2**). As we will see, this isn’t always feasible in practice.

Research has been done on reducing the overall amount of energy used by a hard drive. This has ranged from simple algorithms that spin down the drive when it is idle for more than a set length of time (currently the most common method), to adaptive spin down techniques where the drive examines past access patterns to determine a dynamic spin down strategy.

The fixed length spin down policy has one big advantage: it is very simple to implement. If the spinning disk is not accessed for *idle_time* minutes, the assumption is that there will be no disk accesses in the near future and the disk is spun down. It spins up again when there is a read/write request. This is the only widely available disk management method at present. Since the user fixes the value of *idle_time* and rarely readjusts it, the savings are very limited. Setting *idle_time* too low results in the user waiting for the drive to spin up too often. Too high a value of *idle_time* results in minimal power savings since the disk will remain spinning most of the time. A study by [16] has shown that the optimal value (strictly from the

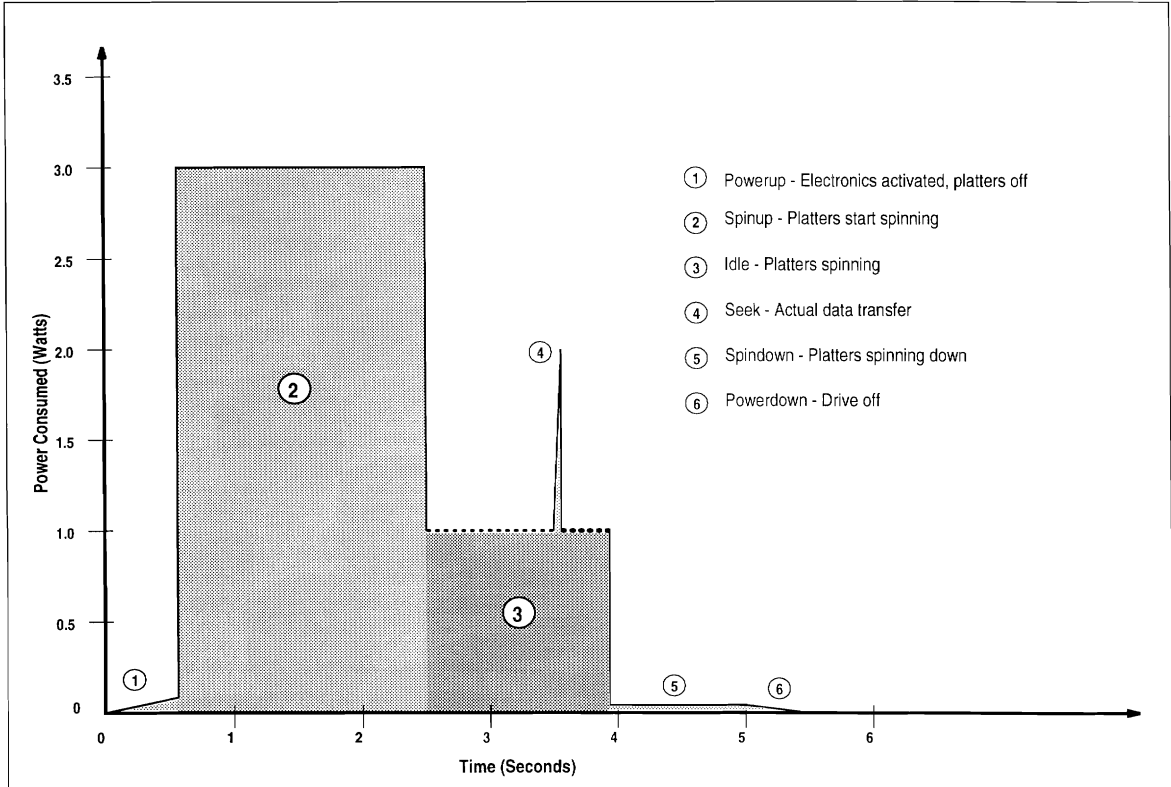


Figure 4: Dynamic power consumption for a typical laptop-optimized hard disk.

power conservation point of view) for *idle_time* is approximately 6 seconds. This may be ideal from the power perspective, but is very inconvenient for the user who will have to wait for the drive to spin up very frequently (a spin up takes 2-6 seconds). In addition, since a hard disk is a mechanical device, it typically has a spin-up/spin-down life expectancy of 40,000-60,000 cycles and overly aggressive spin-down techniques will result in premature drive failure. For example, if *idle_time* is set to 6 seconds, the drive could spin-up over 1000 times on a 5 hour cross-country flight, reducing disk life by about 2% in just one flight.

Adaptive disk spin-down attempts to adjust to the user's access patterns. IBM's Adaptive Battery Life Extender (ABLE) [12] looks for temporal locality of reference in drive accesses to put a hard drive in a special *idle* mode that shuts down most of the electronics of the drive but does not spin-down the platters when accesses are not expected. The drive analyzes the frequency distribution of commands over the previous 10-15 seconds and calculates the probability that the current command is the final one in the burst. This method conserves about 15% more power than a regular idle mode disk and is transparent to host software. Some adaptive spin-down schemes [8] propose to actually spin down the drive completely to maximize energy conservation, but are difficult to implement, and have only been simulated so far. The caveat for each of these schemes is that savings can vary widely with usage. A more detailed analysis of these techniques can be found in [21].

Another technique is increasing the size of the disk cache to reduce the need for spin-ups. Caching can improve performance, while reducing power consumption. Simulations by Douglass et. al. [8] show that using a 32 Kbyte SRAM write-buffer improves average write response by a factor of 20 or more and reduces energy consumption by between 15%-20%. Their simulations show that increasing the buffer beyond 32 Kbytes does not improve write response time, nor does it save additional energy, although this will probably vary with the operating system environment. Thus there is an upper bound on how useful a disk cache can be, and there are also negative consequences for reliability since SRAM is volatile and there is potential for data loss in the event of a system error.

In summary, hard disk management is still not mature. Currently available algorithms (almost exclusively

fixed-length spin down) can help, but are far from optimal. Adaptive algorithms are still in the preliminary research stage, and are difficult to implement. There are other problems inherent to hard disks – since they are mechanical devices, they have limited spin-up/spin-down cycles before drive failure.

4.2 Flash Memory Versus Disks

Flash memory is a form of non-volatile storage that has gained popularity in the past few years. Data is stored in semiconductor memory that is about as compact as DRAM with the added advantage of not needing any refreshing to maintain the data. From the user's point of view, it has the non-volatility of a hard disk (i.e. keeps data even when the power is turned off) and the speed and compactness of DRAM. Flash memory is solid state and thus immune to mechanical shocks, unlike a hard disk. It is about as fast as system memory when doing reads but much slower when doing writes. The other limitations include high cost and a limited number of write cycles [21].

Cost plays a key role in the selection of storage devices in mobile computers. Figure 5 is based on data from [7] and shows the broad range of cost/Mbyte (the y -axis is logarithmic) for the various types of laptop memory. Flash prices are falling as sales volumes increase, but the price of flash relative to hard disk has remained very high, with flash memory costing between 60-100 times more per MByte than hard disks. While we could present a power management scheme that extended battery life to 24 hours and required the use of flash memory to replace a hard disk, it would raise the cost of a laptop (with just 300 MB storage) to about \$18 000, far beyond the reach of most users.

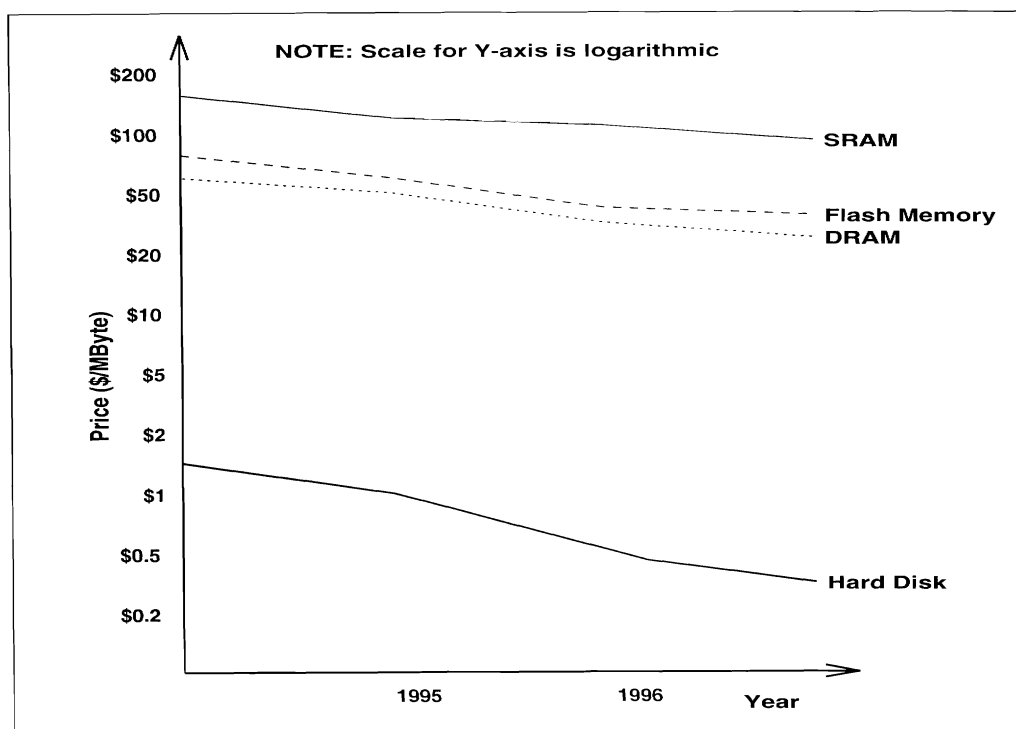


Figure 5: Price comparison of the various laptop storage devices

The fact that software is ever increasing in size makes it clear that mechanical storage devices are an economic necessity in laptops, at least for the foreseeable future. Hard disks may consume significant amounts of power, but they are non-volatile and very low cost. Until the cost of flash memory (or other non-volatile memory) is nearer to that of a hard disk, the problems of spinning mechanical disks must be dealt with.

4.3 CPU Power Management

The Performance/Power ratio of microprocessors has increased tremendously in the past few years, as can be seen in Table 1 which shows the specifications for various Pentium processors. The performance index (Intel’s iCOMP index) and data for Table 1 were obtained from Intel’s home site [14].

CPU Frequency (MHz)	Voltage (Volts)	Typical Power (Watts)	Performance (iCOMP/Watt)
Desktop 66	5.0	~10	57
75	3.3	3.0 - 4.0	174
90	2.9	2.5 - 3.5	245
100	2.9	2.0 - 3.0	326
120	2.9	2.5 - 3.5	333
133	2.9	3.0 - 4.0	317

Table 1: Power requirements of Pentium processors for laptops.

The key physical changes in the design of microprocessors are reduced feature size (smaller transistor size generally results in lower power consumption) and lower operating voltage, from 5 Volts to 2.9 Volts. The amount of power used by a circuit is proportional to the square of the voltage used, so even a small decrease in processor voltage results in a large decrease in the power consumed.

The newer Pentium CPUs also have mechanisms that allow the microprocessor to slow down, suspend, or completely shut down various subunits of the processor when they are not in use. This is transparent to the operating system and application software and explains the dramatic drop in power consumption for the more recent processors, as shown in Table 1. As power management schemes internal to the CPU start reaching their limits, the total power consumed will start rising, as is apparent with the fastest processor in the table.

In addition, there are user selectable options to run the CPU at a slower speed to conserve power—this is the most common user choice in most power managed laptops. The problem with user-selectable “slow” or “fast” CPU modes is that the user may actually end up using **more** power with the “slow” power-saving mode than by not using the power save mode at all. For example, if the user is editing a spreadsheet, having the CPU in its slow state is fine, but if the user is running a calculation in the spreadsheet, having the CPU running slower will result in more power being used since the display and hard disk will be left on longer. It is impractical to expect the user to set the CPU speed manually each time so this inefficiency is common. Our Power Broker will address this inefficiency.

4.4 System Memory

One method to reduce the number of times a hard disk has to be spun up is to have a large amount of system memory (i.e. DRAM). This makes intuitive sense—place the current working set in memory and there will be few page faults to cause the disk to spin up. Unfortunately a study by Li [17] has shown that having as little as 8 MBytes of additional DRAM can use up as much power as a constantly spinning hard disk. To confirm this rather surprising result, we did some calculations based on manufacturers data [6], and found that 8 MBytes of 60ns Extended Data Output (EDO) DRAM uses 2.8 W when active, compared to a typical 500 MByte hard disk which uses about 3 W. We plan to confirm this result by running experiments on our laptops. Newer memory technologies will probably reduce the power consumption of DRAMs, but it will still remain significant.

This indicates that adding system memory solely to reduce disk accesses is not a workable solution. In fact, a user wanting to maximize battery life may need to keep system memory to an absolute minimum. We discuss this further in Section 5.3.

4.5 The Display and Network Interface

The display of a laptop can take up almost half of the total available system power. Active matrix (TFT) screens use more power than the older dual scan displays. Displays are improving rapidly in size and resolution, but not in terms of power consumption. Power management of displays is typically restricted to blanking the display after a period of inactivity. Some newer system management software allows a user to set a low power mode that dims the screen. Blanking the screen after a few minutes is effective in saving power but is not optimal.

Wireless network interface cards are a relatively new addition to the mobile computing field. The wireless Ethernet card (CSMA/CA) we are using is the AT&T PC Card WaveLAN, which has a claimed consumption of 3 W during transmission, 1.5 W when receiving and 0.2 W in sleep mode [22]. Experiments conducted on Personal Digital Assistants (PDAs) by Gauthier et. al. [10] support these numbers. In addition, they also noted that the time the WaveLAN takes to switch from sleep mode to active mode is on the order of 100 ms – sufficiently short that the user would not notice a lag if the card were put in sleep mode frequently.

The wireless LAN standard (IEEE 802.11) is still being defined and will include some form of built-in power management when finalized. In addition, there has been work done on reducing power consumption of wireless network cards, such as [10, 13], but most of it is focused on a particular subsystem, not the entire mobile computer. We plan to characterize the power consumption of these cards and include them in our intelligent power management scheme.

5 The Power Broker

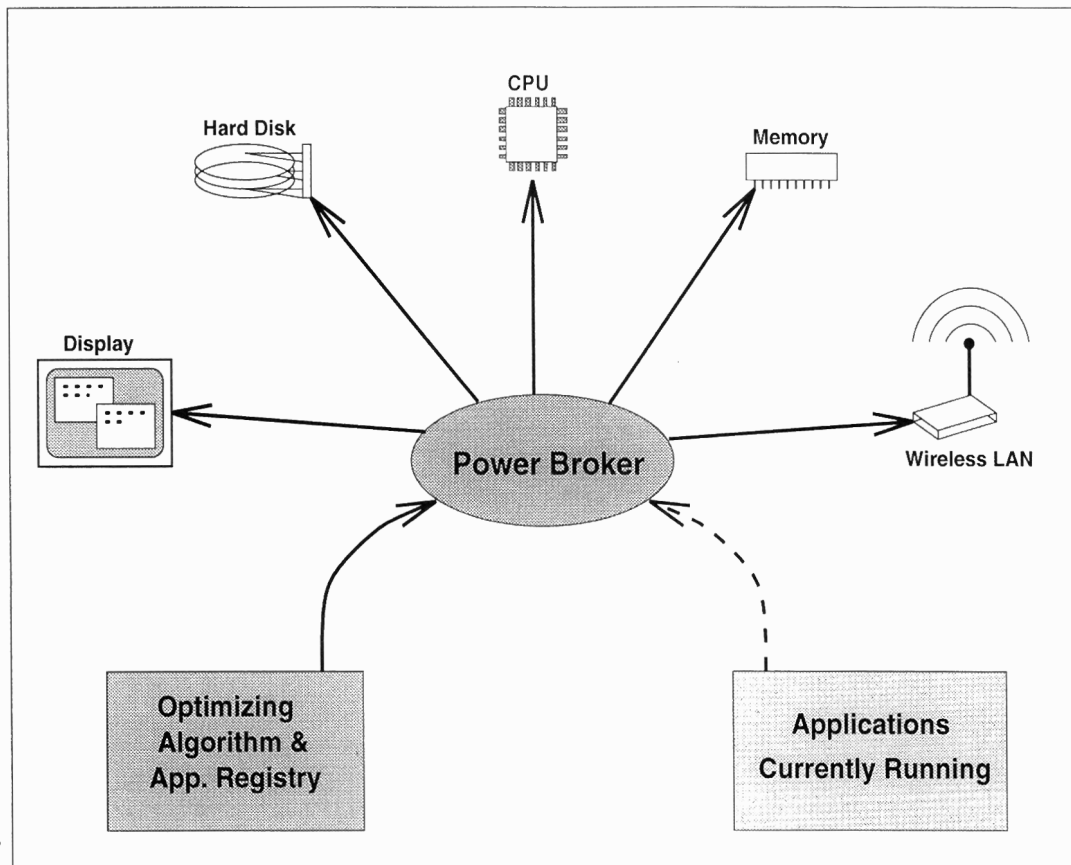


Figure 6: Overview of the Power Broker.

Battery life in a laptop presents a resource management problem; the power consumers (the subsystems) are sharing a single limited resource (the battery) in a relatively uncoordinated and inefficient way. We propose the use of a relatively inexpensive resource with low power requirements and exponential performance growth—the CPU—to manage the allocation of available limited resources to the subsystems in an efficient way.

A broker is defined by the American Heritage Dictionary as “one who acts as an agent for others in negotiating contracts, purchases, or sales in return for a fee or commission.” The *Power Broker* is an agent responsible for negotiating the best overall mobile system performance from the various subsystems for the user, as illustrated in Figure 6. The “commission” charged in this case is the use of CPU cycles. The broker is aware of the general state of the entire system and is in a position to make decisions that will enable the efficient use of resources. The broker paradigm has been successfully explored before in our laboratory for mapping application Quality of Service to shared resource environments such as computer networks [19].

Most applications can be put into general groups (such as editor, browser, compiler etc.), each of which have different resource requirements and priorities. For example, a text editor has very different resource needs from a web browser. We plan to exploit these differences by using the broker to dynamically adjust the subsystem resources for maximum power efficiency. We will also make use of the fact that the number of users on a laptop is typically just one and so it is uncommon for multiple applications to be interacting with the user simultaneously.

5.1 Monitoring Behavior

A simple scenario is to have the broker daemon running in the background observing user activity. Based on predetermined rules, the broker will modify the status of each major system component to minimize power consumption with minimal effect on user performance.

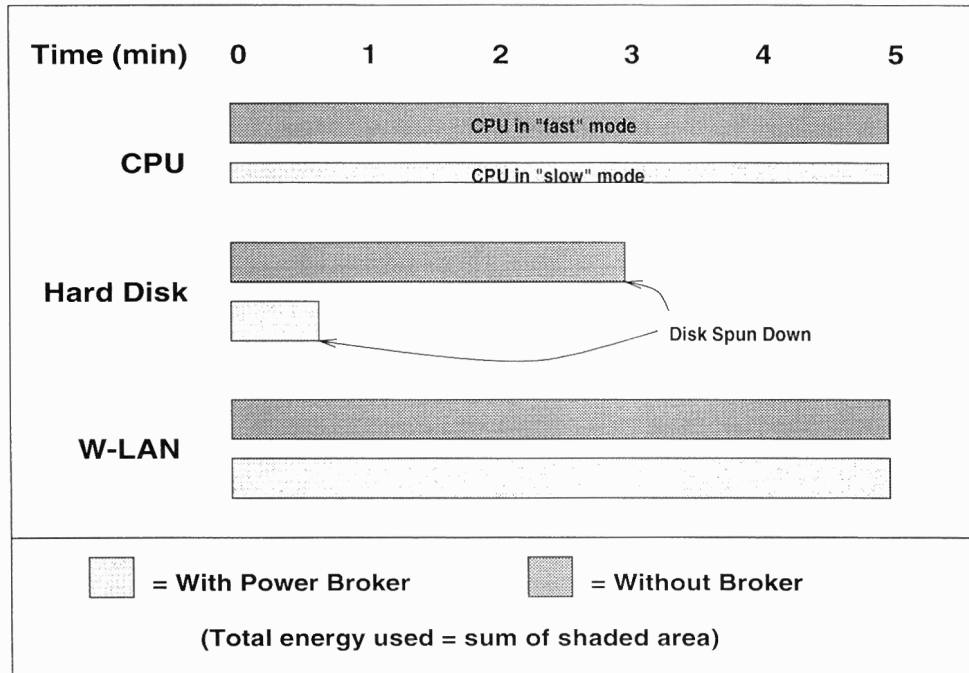


Figure 7: Energy savings comparison for a telnet session.

For example, Figure 7 shows one rule that can save energy during a telnet session. The darker shaded bars show component usage without the Broker, and the lightly shaded bars show usage with the Broker in control. The sum of the shaded area for each case gives the total energy used. The user starts a telnet session (at time = 0) and continues in that session over the 5 minute interval. The rule applied in this case

is that if the user is using a virtual terminal on a remote machine (i.e. no longer regularly accessing the hard disk) and there is sufficient memory, the hard disk will be spun down when idle for only a few seconds instead of the default of a few minutes. The only major components left running continuously will be the network interface, display, CPU (in “slow” mode) and the system memory.

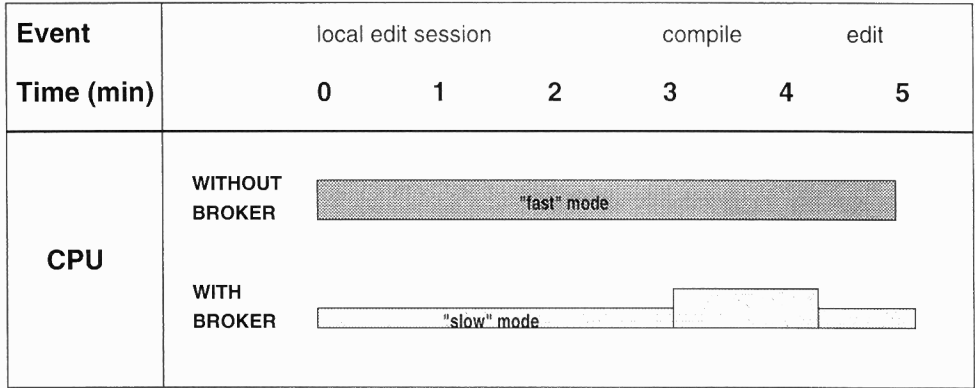


Figure 8: Energy savings comparison for a local edit & compile session.

Figure 8 shows another example where the broker can dynamically adjust the CPU speed based on the active application. When a compiler is running, it sets the CPU speed to high, and then shifts it back to the low speed mode when editing is resumed.

It should be noted that no modifications are required of the application itself – the broker simply detects that a particular application is active and adjusts accordingly. The user would have to register any new applications that are installed on the system, but this is only done once and commonly used applications will already be included in a default rule-file that we will create. Any applications that are not registered are left untouched. In effect there would be no brokered management for them.

The initial version of the broker is a simple one—it will simply look at what application(s) are running and adjust the subsystems accordingly. Later versions might include more complex algorithms where the user could be given the choice to select from multiple rules that would trade off performance versus power conservation.

5.2 Experimental Setup

Our experimental setup consists of a Toshiba 410 CDT Pentium 90 laptop with 8/24 MB EDO DRAM (i.e. a 16 MB removable module), 772 MB HD and a 3600 mAH Lithium Ion battery. It has Linux and Windows 95 installed on it. The wireless Ethernet card is a Type II PC Card from AT&T and uses Direct Sequencing Spread Spectrum technology in the 902-928 MHz frequency range. We took current and voltage measurements directly from the battery instead of the AC adapter or an external constant voltage source as we wanted our readings to reflect actual usage conditions, including any voltage drops.

To measure the power consumption of the WaveLAN card, we are using a PC Card Extender with an external power connection so that we can isolate the power used by the WaveLAN. The initial measurements were performed with two digital multimeters, but subsequent work will use a data acquisition card that will offer more flexibility and allow much higher sampling rates.

5.3 Power-Conscious Memory Management

Since software requires ever increasing amounts of memory it is useful to control just how much of the memory is actually powered up. For example, if a laptop with 40 MBytes of memory were to use only 16 Mbytes and depower the other 24 Mbytes, there would be very significant power savings albeit with a potential performance hit. If a user could set (either at power-up, or dynamically) the amount of memory to be powered down, it would offer a very straightforward way to trade off performance with laptop battery endurance. Since about 8 Mbytes of DRAM can use as much power as a spinning hard disk [17], the

additional page faults (and subsequent drive spin-ups) would be offset by the savings from having reduced DRAM. Intel has released a new Pentium PCI chipset (the 82430MX PCI chipset) that has suspend and standby modes which not only put the CPU in low power mode, but also restrict power to system memory. The challenge here is to find ways to intelligently tradeoff the power savings from reducing system memory with performance penalties. We plan to investigate this tradeoff as part of our work on the Power Broker.

An interesting idea proposed by the Video Electronics Standards Association (VESA) group is the Unified Memory Architecture (UMA) [5]. They propose a scheme where segments of main memory are dynamically allocated for video and graphics, thus eliminating the need for a separate frame buffer. This proposal is presented primarily as a cost saving measure, but can also be viewed from the power management point of view. Instead of having dedicated memory just for graphics (2 MB requires about 0.7 W of power), segments of main memory can be used as needed. This would be more efficient and flexible. For example, a word processing application might need only 512 KBytes whereas photo rendering may need over 2 MBytes. Each of these could be accommodated using the UMA scheme and the memory returned for system use after the application is finished. The claim by VESA is that UMA is transparent to the operating system and is controlled by the core BIOS logic. There are disadvantages however. Since we are using the system bus and system memory for all the traffic a performance hit is expected and estimated to be between 5-15%. For a desktop machine this may not be acceptable, but if it can extend a laptop's battery life by 10% there is strong incentive to use the scheme.

6 Summary

We have discussed the power management schemes that are currently available for mobile computers. We also presented several ideas for an integrated management scheme that we are investigating. It is difficult to predict how well these ideas will work since there are many factors that come into play with the most important, and difficult to quantify, being the user convenience performance tradeoff.

The experimental implementation of the Power Broker is underway, and our evaluation will include human factors issues to gain insight into this convenience/performance tradeoff and the engineering tradeoffs supporting it.

References

- [1] Apple Computer Press Release, URL: <http://www.atcon.com/maccentral/oct/bits.html>
- [2] *Managing Mass Storage*, Byte Magazine, March 1994, pp. 79–83.
- [3] *Communications Week*, January 15, 1996, p.19.
- [4] Personal Communication, Glenn Bernasek, Email: dd314@cleveland.Freenet.Edu
- [5] *Unified Memory Architecture May Change Future PC Designs*, Computer Design, pp. 47–48, December 1995.
- [6] *Product Trends: High Performance DRAMs*, Computer Design, pp. 139–149, December 1995.
- [7] *Computer Shopper*, April 1995 and March 1996.
- [8] F. Douglis, P. Krishnan, and B. Bershad, *Adaptive Disk Spin-down Policies for Mobile Computers*, Second USENIX Symposium on Mobile and Location-Independent Computing, pp. 121–137 Ann Arbor, April 1995.
- [9] G. Forman and J. Zahorjan, *The Challenges of Mobile Computing*, Technical Report UW CSE 93-11-03, University of Washington, March 1994, available from <ftp.cs.washington.edu>.
- [10] P. Gauthier, D. Harada and M. Stemm, *Reducing Power Consumption for the Next Generation of PDAs: It's in the Network Interface!*, URL: <http://http.cs.berkeley.edu/~stemm/cs252/report/main/node1.html>
- [11] D. Freeman, *Lithium-ion Batteries—The Next Wave*, Portable Design, pp.46–47, January 1996.

- [12] *Adaptive Power Management for Mobile Hard Drives*, IBM, August 1995.
URL: <http://eagle.almaden.ibm.com/storage/oem/tech/ableback.htm>
- [13] T. Imielinski, S. Viswanathan and B. Badrinath *Power Efficient Filtering of Data on the Air*
URL: <http://www.cs.purdue.edu:80/homes/uzk/MobileComputing.html>
- [14] URL: <http://pentium.intel.com/procs/pentium/systems/mobile/pp90/sltech.htm>
- [15] URL: <http://www.intel.com/IAL/powermgm/>
- [16] Kester Li, R. Kumpf, P Horton and T. Anderson, *A Quantitative Analysis of Disk Drive Power Management in Portable Computers*, Proceedings of the 1994 Winter USENIX Conference, pp.279–291, San Francisco, January 1994.
- [17] Kester Li, *Towards A Low Power File System*, Technical Report UCB/CSD 94/814, University of California - Berkeley, Masters Thesis, May 1994.
- [18] B. Marsh and B. Zenel, *Power Measurements Of Typical Notebook Computers*, Technical Report MITL-TR-110-94, Matsushita Information Technology Laboratory, May 1994.
- [19] K. Nahrstedt and J. M. Smith, *The QoS Broker*, IEEE Multimedia Magazine, 2(1), pp. 53–67, Spring 1995.
- [20] S. Sheng, A. Chandrasekaran and R. W. Broderson, *A Portable Multimedia Terminal For Personal Communications*, IEEE Communications Magazine, pp. 64-75, December 1992.
- [21] S. Udani, *Power Management of Permanent Storage in Mobile Computers*, Written Preliminary Examination Part II Report, University of Pennsylvania, April 1995.
URL: <http://www.cis.upenn.edu/~udani>
- [22] *AT&T WaveLAN PCMCIA User's Guide*, October 1994.
- [23] Personal Communication, Clinton Winchester, US Navy. Email: winchest@oasys.dt.navy.mil