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PARAMETRIC STUDY ON THE SOLDERABILITY OF ETCHED PWB COPPER

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ABSTRACT

The rapid advancement of interconnect technology has resulted in a more "engineered" approach to designing and fabricating printed wiring board (PWB) surface features. Recent research at Sandia National Laboratories has demonstrated the importance of surface roughness on solder flow. This paper describes how chemical etching was used to enhance the solderability of surfaces that were normally difficult to wet. The effects of circuit geometry, etch concentration, and etching time on solder flow are discussed. Surface roughness and solder flow data are presented. The results clearly demonstrate the importance of surface roughness on the solderability of fine PWB surface mount features.*

KEYWORDS:

Surface Roughness, Chemical Etching, Solder Flow

INTRODUCTION

Fluid flow on rough surfaces has been extensively studied for many years [1-9]. Early work by Wenzel described the conditions necessary for surface wetting to occur [1]. He developed a relationship showing that increases in roughness produced smaller contact angles between a liquid and a solid surface. This relationship has been subsequently investigated by other researchers to better understand the wetting behavior of solder on rough surfaces. Romero & Yost [8] and Yost *et al* [9] recently identified an additional driving force for wetting, namely, flow along open channel capillaries. Solder flow along V-shaped grooves was found to depend on the orientation and physical dimensions of the grooves.

Copper surface finishes, common to most printed wiring boards (PWBs), inherently possess some degree of open capillary roughness that affects solderability. Additional surface roughening can be achieved through selective mechanical or chemical processing. The resulting surface offers an opportunity for improved manufacturability of solder interconnects on electronic assemblies.

The rapid advancement of interconnect technology has provided the impetus for developing a more "engineered" approach toward enhancing PWB solderability. As fine pitch technology becomes the norm in circuit designs, better control of solder delivery to critical surface features will be necessary to assure the formation of reliable joints. The effort to maximize PWB real estate, by increasing circuit density through ever finer pitch size, will certainly

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cause additional solderability problems, since a potentially larger relative percentage of unwetted surface area can result.

The thinning of solder coatings around convex points, particularly at the barrel opening of a PWB plated through hole (PTH), often results in limited solder flow onto the topside land. This phenomenon is commonly referred to as a "weak-knee" and can be observed during typical PWB fabrication and circuit assembly. Solder joints at the "weak-knee" are often poor and can consequently fail prematurely. Chemical etching was recently demonstrated as a solution to this solder flow problem by roughening the barrel edge and enhancing solder flow [10].

There are numerous chemical and mechanical methods, therefore, that can produce the kind of surface features which promote solder wettability. For example, copper electroplating parameters can be adjusted to optimize roughness profiles with improved solderability [9]. Chemical etching can yield similar results [11, 12].

A variety of chemical solutions have been formulated to etch copper [10-16]. Typical uses include metal finishing, the manufacture of semiconductor devices, and the fabrication and assembly of printed circuit boards. A chemically etched surface, however, does not necessarily guarantee good solder wetting behavior, since deposited residues could inhibit flow and must usually be removed prior to solder processing.

This paper describes work recently conducted by Sandia National Laboratories, in collaboration with the National Center for Manufacturing Sciences' PWB Interconnect Systems Surface Finishes project, which characterized the effects of surface roughness on solder flow. The investigation demonstrated the importance of "engineering" the surface topography of fine lines and controlling the baseline surface conditions.

MATERIALS & EXPERIMENTAL PROCEDURE

A 10% HCl baseline pretreatment and two chloride-based etching solutions were selected for the copper roughening study (Table 1). Ferric or cupric chloride (FeCl_3 or CuCl_2) were the basis for the etchant formulations. Hydrogen peroxide (H_2O_2) was added to the CuCl_2 solutions to accentuate the etch process. The peroxide is an oxidizer and promotes preferential etching by the CuCl_2 through an iterative reoxidation and etch process. A third etching procedure used a sequential CuCl_2 and FeCl_3 dual etch.

Preliminary etching experiments were conducted with an etch time of 60 seconds. Subsequent etch times were varied from 30 to 120 seconds. Different FeCl_3 and CuCl_2

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concentrations (127, 253, and 380 g) were investigated. Corresponding H_2O_2 concentrations in the $CuCl_2$ solutions were 7, 14, and 21 ml. Samples were rinsed in ethanol and deionized water before being immersed in the etchant baths. Etched samples were given a final rinse in dilute HCl and deionized water.

Table 1: Surface Treatments of PWB Copper Samples

1. 10% HCl baseline pretreatment, with solvent clean.
2. $CuCl_2/H_2O_2/HCl$ (50 vol. % HCl stock solution with 7-21 ml H_2O_2 and 127-380 g $CuCl_2$ per liter of stock solution).
3. $FeCl_3/HCl$ (50 vol. % HCl stock solution with 127-380 g $FeCl_3$ per liter of stock solution).
4. $CuCl_2/H_2O_2/HCl$ (50 vol. % HCl stock solution with 14 ml H_2O_2 and 253 g $CuCl_2$ per liter of stock solution) for 30 s, followed by $FeCl_3/HCl$ (50 vol. % HCl stock solution with 253 g $FeCl_3$ per liter of stock solution) for 30 s.

Surface profilometry was used to measure the relative roughness of the etched surfaces and map their topography. The instrument, manufactured by WYKO Corporation, uses a white light interferometer source to measure the degree of fringe modulation or coherence. The system is capable of profiling surfaces with root-mean-square roughness ranging from 1 Å to over 20 μm and with steps over 100 μm . Surface area, arithmetic average, root-mean-square, and maximum peak-to-valley roughness measurements are typical outputs available with this technology. ASME 846.1 describes quantitative parameters used to characterize surfaces from measured profiles [17]. Parameters are typically profile-amplitude sensitive, profile-wavelength sensitive, or sensitive to both amplitude and wavelength.

Roughness measurements were performed initially on samples sectioned from a copper-clad, FR-4 panel to calibrate the test instrument. Baseline measurements were then made on the as-received test coupons before conducting the etching experiments. Similar measurements were performed after etching. The measurement area was approximately 58 $\mu m \times 77 \mu m$.

The solder flow test vehicle (TV) was fabricated with conventional PWB materials and based on a layout design consisting of 0.006" (0.15 mm) copper lines attached to individual outer 0.016" (0.4 mm) diameter copper pads (Fig. 1). The substrate was a single-sided, 0.060" (1.52 mm) thick, epoxy resin/fiberglass-reinforced laminate (FR-4) board. Copper patterns were imaged and etched to represent a mini ball grid array (BGA) footprint. The nominal Cu thickness was 17 μm (0.5 oz. Cu).

The line and pad dimensions were based on a relationship developed for surface capillary flow of solder on a PWB

surface [18]. Common to the line-pad geometry is the relationship between the line width, δ , and pad radius, r_c . Solder flow onto a line from a pad was found to depend on certain values of the ratio δ to r_c . These geometric limits generally represent typical solder flow conditions observed during PWB assembly, where solder is either moved or contained along conductor paths. When δ/r_c is less than 0.5, liquid solder normally does not readily flow from a pad onto an attached line. When δ/r_c exceeds 0.5 and sufficient solder is present, the probability of flow onto the line significantly improves. Since experimental results suggested favorable conditions for solder flow with a 0.75 δ/r_c ratio, the BGATV line-pad dimensions were scaled to the 0.75 value. Even with this "ideal" geometric relationship, the effects of an increased edge-to-surface area ratio, greater variability in solder volume control on the smaller circuit, and inherent fabrication differences caused by sizing could influence actual wetting behavior. Considering these factors, surface topography should have a measurable effect on solder flow.

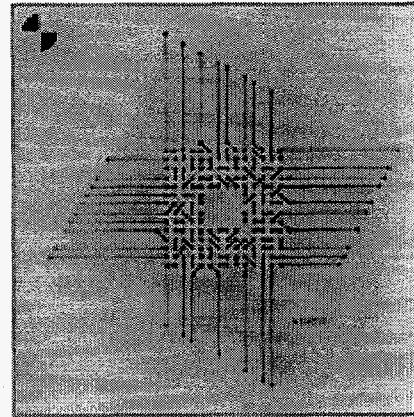


Figure 1: Ball Grid Array Test Vehicle (BGATV)

Solder paste was used in the study. The paste consisted of 85% eutectic 63Sn37Pb (wt. %) metal powder with a rosin mildly activated (RMA) flux vehicle. A 0.012" (0.3 mm) thick stencil was used to screen paste onto the outer pad surfaces. Additional RMA flux, containing 25% solids in an alcohol carrier, was lightly applied to the exposed copper traces to facilitate flow once solder wetted each line. The BGATV was processed through a tabletop solder reflow machine. The unit has four conduction heat zones that were set at 90, 140, 240, and 100°C. The sample transport bar speed was 9 in/min (3.8 mm/s). The thermal profile yielded a ramp rate of approximately 1.5°C/s and a 210-215°C peak temperature on the sample topside. The time spent above the solder melting temperature was typically 30-40 s. The heat zones were fully inerted with technical grade nitrogen flowing at a rate of 30 SCFH. Flux residues were removed ultrasonically in trichloroethylene and rinsed in isopropyl alcohol after each test.

Table 2: Optical Profilometry Measurements on Baseline and Etched BGATV Surfaces

Surface Treatment & Time	Area Path Index R_p ($\mu\text{m}^2/\mu\text{m}^2$)	Peak-to-Valley R_T (μm)	RMS R_Q (nm)	Arithmetic Mean R_A (nm)
Baseline, 10% HCl	1.768 ± 0.102	4.6 ± 0.2	405.1 ± 21.8	311.6 ± 16.9
FeCl ₃ (127 g)*, 30 s	2.453 ± 0.053	7.1 ± 1.5	727.0 ± 214.4	553.6 ± 183.1
FeCl ₃ (127 g)*, 120 s	3.101 ± 0.201	10.0 ± 1.5	1073.3 ± 319.6	806.6 ± 255.3
FeCl ₃ (253 g)*, 75 s	2.564 ± 0.039	7.7 ± 2.0	673.6 ± 140.6	508.4 ± 88.1
FeCl ₃ (380 g)*, 30 s	2.375 ± 0.197	5.7 ± 1.1	555.6 ± 37.7	435.4 ± 27.5
FeCl ₃ (380 g)*, 120 s	2.541 ± 0.071	7.8 ± 0.7	842.5 ± 164.5	666.3 ± 131.9
CuCl ₂ (127 g) + H ₂ O ₂ (7 ml)*, 30 s	2.486 ± 0.166	7.7 ± 1.1	752.8 ± 209.7	556.0 ± 108.2
CuCl ₂ (127 g) + H ₂ O ₂ (7 ml)*, 120 s	2.657 ± 0.064	9.1 ± 2.2	787.0 ± 60.6	602.9 ± 37.8
CuCl ₂ (253 g) + H ₂ O ₂ (14 ml)*, 75 s	2.518 ± 0.073	5.4 ± 0.8	606.1 ± 114.2	479.3 ± 98.9
CuCl ₂ (380 g) + H ₂ O ₂ (21 ml)*, 30 s	1.783 ± 0.299	5.2 ± 0.8	475.8 ± 142.3	368.4 ± 130.9
CuCl ₂ (380 g) + H ₂ O ₂ (21 ml)*, 120 s	2.155 ± 0.275	5.6 ± 0.7	591.1 ± 156.6	455.3 ± 127.8
CuCl ₂ (253 g) + H ₂ O ₂ (14 ml)*, 30 s followed by FeCl ₃ (253 g)*, 30 s * (per liter of 50 vol. % HCl stock solution)	3.062 ± 0.113	9.8 ± 2.9	1207.7 ± 248.6	930.7 ± 197.4

RESULTS AND DISCUSSION

Optical profilometry results for the BGATV copper samples are presented in Table 2. Area path (R_p), maximum peak-to-valley (R_T), root-mean-square (rms, R_Q), and arithmetic mean (R_A) roughness values were calculated from each measured profile. Since it was difficult to conclusively interpret peak-to-valley metrics due to the somewhat rippled surface produced by the underlying FR-4 laminate layers, experimental analysis was concentrated on the normalized surface area metric, R_p , although the other roughness parameters exhibited similar measured trends.

R_p is determined by dividing the measured surface area by the projected measurement area. It generally does a good job in reflecting the roughness ratio in Wenzel's equation, which depends on the mean square slope of a surface, rather than its amplitude [9]. While R_p is not mean square slope, the two metrics convey similar information. This normalized parameter is consequently more characteristic of general surface roughness, as compared to the localized nature of the other roughness parameters. The R_p results are summarized in Fig. 2.

The FeCl₃/HCl etch produced the largest roughness gains on the BGATV copper surfaces. The average roughness increase ranged between 35 and 75% of the baseline value. It should be noted that the baseline condition also exhibited a certain degree of roughness, as indicated by an area path roughness index of 1.768. The lower concentration FeCl₃ solution, with an etch time of 120 s, yielded the roughest surface of the single etch experiments ($R_p = 3.101$). The higher concentration solutions did not necessarily produce the roughest surface.

Metallographic analysis suggested that the thin copper finish was self-limiting in achieving maximum roughness. As the more aggressive etch conditions dissolved the copper material, a state was eventually reached where only a thin layer of copper remained and the etch began to remove some of the rougher surface features originally created by the etch process. This effect was seen in the lower roughness values observed on the higher concentration CuCl₂/H₂O₂/HCl etched samples. Removal of the copper traces down to the FR-4 substrate was a particular problem with the longer 120 s etch. Once the FR-4 was exposed, it became difficult to find continuous paths for measuring roughness. These trace breaks would also limit solder flow and cause electrical opens.

The roughness values of the CuCl₂/H₂O₂/HCl etched samples were slightly lower than the FeCl₃/HCl results, although the general etching trends were similar. The average roughness increase ranged between 0 and 50% of the baseline value. Once again, the lower concentration solution, at an etch time of 120 s, yielded the roughest surface of 2.657.

Three-dimensional images of the measured roughness data were plotted. Typical profiles and their peak-to-valley ranges are presented in Fig. 3 (a - baseline, b - FeCl₃/HCl at low concentration for 120 s, and c - dual etch process). As observed with the measured roughness data, the baseline condition did exhibit a certain degree of roughness. The FeCl₃ etch generally produced a relatively homogeneous surface topography with uniform, moderate scale peaks and valleys, as shown in Fig. 3-b. The CuCl₂/H₂O₂/HCl etch yielded a more heterogeneous surface, with high peaks along the trace edges and deep, broad valleys over the remaining surface area. The dual etch process produced the most interesting results,

yielding a bimodal topography consisting of generally uniform peaks and valleys with deeper interconnected grooves. This "grooved" topography is believed to be caused by preferential etching along the copper grain boundaries (Fig. 3-c) and is common to most chemical etching processes [19]. Under controlled conditions, the "grooved" pattern can enhance solder flow. If overetched, however, wider "grooves" with a shallow slope might inhibit flow.

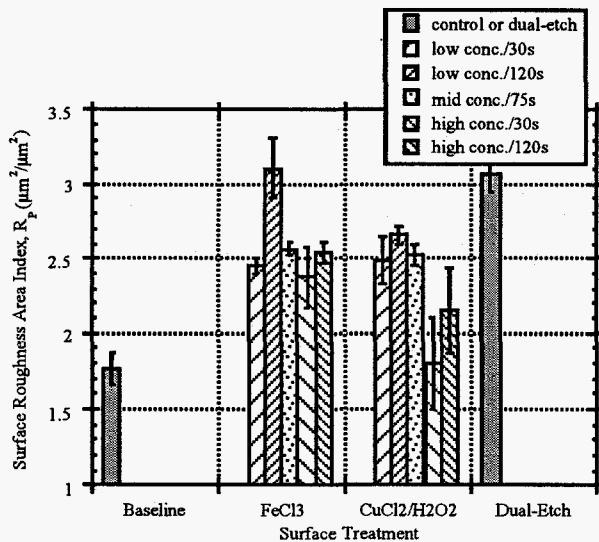
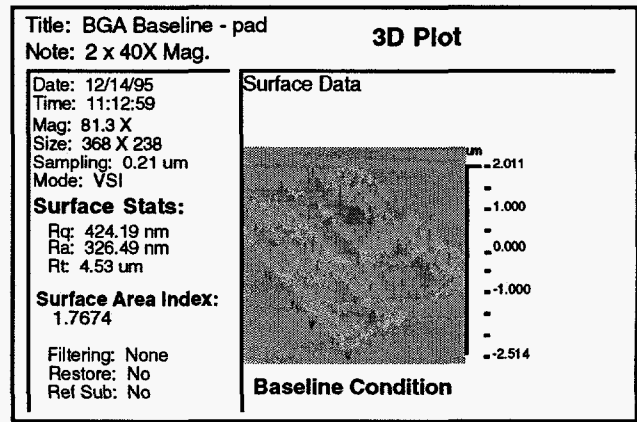


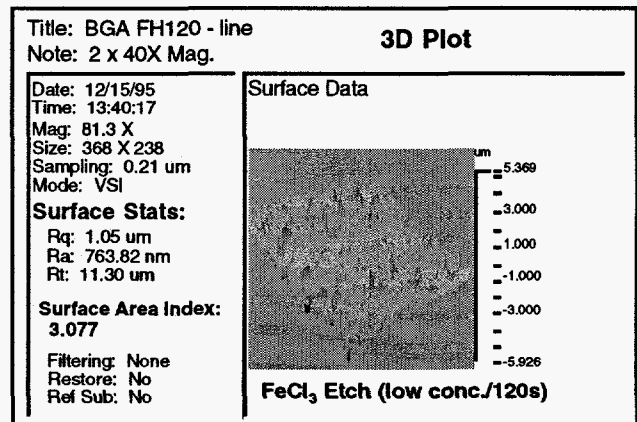
Figure 2: Surface roughness area index, R_p , on copper as a function of etch concentration and time ($R_p = 1.0$ is an ideal, smooth surface).

To complete the surface analysis, etched substrates were cross-sectioned and mounted to measure the remaining copper thickness. Etch rates were then determined with the measured values and the given etch times. The FeCl_3/HCl and dual etch conditions yielded a relative etch rate of $0.1 \mu\text{m/s}$. The higher concentration FeCl_3/HCl chemistry was the exception, with an etch rate closer to $0.25 \mu\text{m/s}$. The higher etch rate resulted in excessive copper attack, especially at 120s. The $\text{CuCl}_2/\text{H}_2\text{O}_2/\text{HCl}$ etch rate was less consistent and varied between 0.15 and $0.25 \mu\text{m/s}$, even across the same trace. As noted with the FeCl_3/HCl etch, long etch times stripped most, if not all, of the copper features on the BGATVs, particularly with the higher etch concentration.

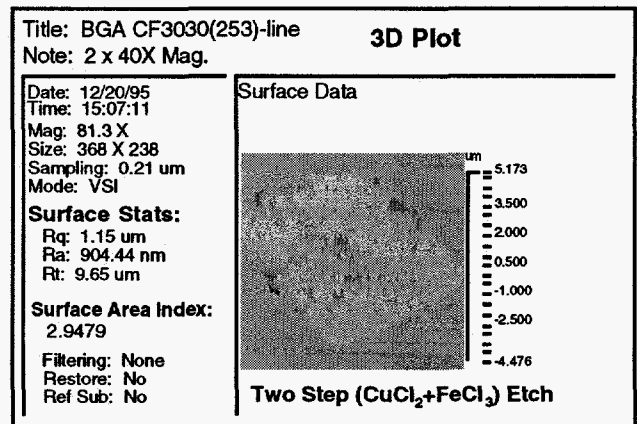
Solderability tests were conducted to characterize the solder flow behavior on the roughened surfaces. Wetting balance tests were performed with mixed results [11]. The wetting forces and times were generally good, but with minimal differences measured between the baseline and etched surfaces. A thin solder film in advance of the bulk solder was observed on the etched surfaces, however, and not on the baseline samples. This solder precursor is similar to the "precursor foot" phenomenon reported by other researchers [20-22] and is usually a good indicator of enhanced solder flow. The actual benefits of etching were likely underestimated, therefore, by the wetting



(a)



(b)



(c)

Figure 3: Mapped surface topography of (a) baseline, (b) FeCl_3/HCl (low concentration, 120 s), and (c) dual $\text{CuCl}_2/\text{H}_2\text{O}_2/\text{HCl}$ and FeCl_3 etched BGATVs.

balance tests, due to the large surface area of the test coupon. The BGATV, on the other hand, provides a more sensitive means for capturing the response of flow to

roughness. The BGATV tests discriminate solder flow from the pads, along the lines, and into the inner circuit array, a potential flow path of at least 7.5 mm.

The results of the solder flow experiments are listed in Table 3 and displayed in Fig. 4. Flow was generally poor on the baseline BGATVs, with wetted lengths averaging less than 1.0 mm. The FeCl₃/HCl and CuCl₂/H₂O₂/HCl etched samples clearly yielded better wetting results. The FeCl₃/HCl etch produced more consistent solder flow under the given boundary condition chemistries and etch times, with wetted lengths of 4.0-5.0 mm. It should be noted that solder tests were not conducted on the higher FeCl₃ concentration, 120 s processed samples because of significant overetching and breaks in the copper lines. The solder results were consistent with the roughness and etch rate measurements reported above. Figure 5 shows a typical cross-section of a FeCl₃/HCl etched and soldered sample, with its "rippled" solder/Cu interface.

Table 3: Etched BGATV Solder Flow Results

Surface Treatment & Time	line flow, (mm)
Baseline, 10% HCl	0.5 ± 0.7
FeCl ₃ (127 g)*, 30 s	5.0 ± 1.9
FeCl ₃ (127 g)*, 120 s	5.3 ± 1.6
FeCl ₃ (253 g)*, 75 s	4.0 ± 1.9
FeCl ₃ (380 g)*, 30 s	4.1 ± 2.1
FeCl ₃ (380 g)*, 120 s	none tested
CuCl ₂ (127 g) + H ₂ O ₂ (7 ml)*, 30 s	5.6 ± 2.0
CuCl ₂ (127 g) + H ₂ O ₂ (7 ml)*, 120 s	5.7 ± 1.6
CuCl ₂ (253 g) + H ₂ O ₂ (14 ml)*, 75 s	3.1 ± 1.8
CuCl ₂ (380 g) + H ₂ O ₂ (21 ml)*, 30 s	4.8 ± 1.8
CuCl ₂ (380 g) + H ₂ O ₂ (21 ml)*, 120 s	3.8 ± 1.9
CuCl ₂ (253 g) + H ₂ O ₂ (14 ml)*, 30 s followed by FeCl ₃ (253 g)*, 30 s	7.0 ± 1.9

* (per liter of 50 vol. % HCl stock solution)

The CuCl₂/H₂O₂/HCl solution also gave good flow results, although the range of wetted lengths was typically greater, varying from 2.6 to 5.7 mm. Some evidence of copper overetching was observed with increasing CuCl₂/H₂O₂ concentration. CuCl₂/H₂O₂ generally created wider "trenches" across the test lines which contributed to a more heterogeneous solder flow. A secondary problem was caused by H₂O₂ depletion in the etch bath, either through use or natural aging.

The dual CuCl₂/H₂O₂/HCl and FeCl₃/HCl etch process yielded the best wetting results. The process produced a uniform peak and valley topography, with additional deeper interconnected channels covering the surface, which further enhanced capillary flow. A low etch rate, with high roughness indicators, correlated into an average solder flow of 7.0 mm. There were several longer lines on

the test pattern where flow exceeded this value and wet the inner circuit array.

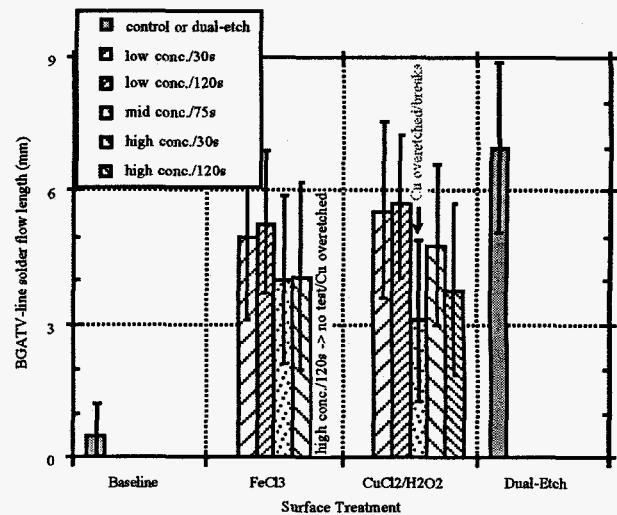


Figure 4: Solder flow on BGATV lines as a function of etch processing (i.e., surface roughness); error bars - ± one standard deviation.

This investigation clearly demonstrates the importance of roughness on solder wettability, regardless of the method used to create a distinctive surface topography. The desire for finer circuit features has made this issue particularly timely, as solder joint producibility and reliability requirements for advanced interconnects grow. Controlling the solder wetting and flow response, whether inhibited or enhanced, by engineering the physical properties of the surface should yield more robust fabrication and assembly processes.

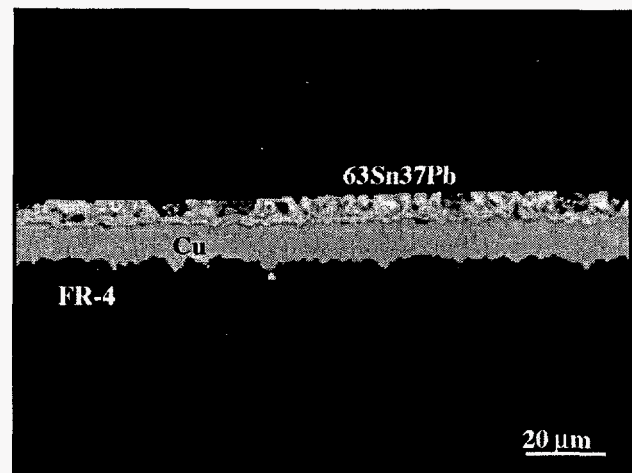


Figure 5: Optical image of a FeCl₃/HCl etched and soldered BGATV line.

Another important outcome of the study was the recognition of the significance of the baseline surface

conditions prior to solder processing. For example, surface roughness is typically ignored as a process variable at the board level. This is usually not a problem with "older" PWB technology, since the surface area and processing window are larger. With more advanced technology, however, the materials and processing limits have shrunk and are less forgiving. As a consequence, the roughness parameter can no longer be treated as a constant. Chemical etching may not be the best way for producing the desired roughness features because of its subtractive nature and the difficulties associated with its incorporation into an existing production line. The essential point, however, is to clearly understand the significant effect that roughness can have on fabrication and assembly level defects.

CONCLUSIONS

Microscopically roughened PWB copper surfaces were produced with different chemical solutions. The solutions were based on FeCl_3/HCl and $\text{CuCl}_2/\text{H}_2\text{O}_2/\text{HCl}$ chemistries. The effects of etch concentration and etch time on surface roughness and solder flow were determined. The FeCl_3/HCl solutions generally yielded a more uniform etched surface than the $\text{CuCl}_2/\text{H}_2\text{O}_2/\text{HCl}$ etchants. Overall, solder wetting and flow were improved when compared to the baseline control condition, especially at the lower etch concentrations. A sequential process, using $\text{CuCl}_2/\text{H}_2\text{O}_2$ and FeCl_3 , was particularly effective in achieving enhanced solder flow along the very fine test lines. The results clearly demonstrated the importance of surface roughness on solder flow, whether produced by chemical or mechanical means. Not understanding or controlling this surface property could lead to serious problems during fine line fabrication and assembly processing.

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REFERENCES

- [1] R. N. Wenzel, Ind. Eng. Chem., 28, 988 (1936).
- [2] E. R. Parker and R. Smoluchowski, Trans. ASM, 35, 362 (1944).
- [3] R. Shuttleworth and G. L. J. Bailey, Disc. Faraday Soc., 3, 16 (1948).
- [4] R. E. Johnson and R. H. Dettre, Contact Angle, Wettability, and Adhesion (edited by R. F. Gould). Advances in Chemistry Series 43, American Chemical Society (1964).
- [5] S. J. Hitchcock, N. T. Carroll, and M. G. Nicholas, J. Mater. Sci., 16, 714 (1981).
- [6] M. G. Nicholas and R. M. Crispin, J. Mater. Sci., 21, 522 (1986).

- [7] A. M. Cazabat and M. A. Cohen Stuart, J. Phys. Chem., 90, 5845 (1986).
- [8] L. Romero and F. G. Yost, "Flow in an Open-Channel Capillary", J. Fluid Mech., accepted for publication.
- [9] F. G. Yost, J. R. Michael, and E. T. Eisenmann, "Extensive Wetting due to Roughness", Acta Metall. Mater., 43, 299 (1995).
- [10] D. M. Tench and D. P. Anderson, "Uniform Solder Coating on Roughened Substrate", Rockwell International Corp., US Patent 5178965 (1993).
- [11] J. O. Stevenson, T. R. Guilinger, F. M. Hosking, F. G. Yost, and N. R. Sorensen, "Solderability Enhancement of Copper Through Chemical Etching", IPC Expo '95 Conf. Proceedings, San Diego, CA, (May 1995).
- [12] F. M. Hosking, J. O. Stevenson, and F. G. Yost, "Capillary Flow of Solder on Chemically Roughened PWB Surfaces", IPC '96 Conf. Procs., San Jose, CA, S6-3/1-5 (March 1996).
- [13] E. Kauczor, "Preparation of Test Pieces for Macroscopic Examination", Praktiker, 30 (5), 82-84 (May 1978).
- [14] "Metallography and Microstructures", ASM Handbook, Vol. 9, ASM International (1985).
- [15] "Surface Engineering", ASM Handbook, Vol. 5, ASM International (1994).
- [16] J. Chiang, "Process of Etching Copper Circuits", FMC Corp., US Patent 3844857 and US Patent 3837945 (1974).
- [17] ASME 846.1 85, American Society of Mechanical Engineers (1992).
- [18] F. M. Hosking, and F. G. Yost., "Characterization of Solder Flow on PWB Surfaces", Surface Mount International Conference Proceedings, 545-550 (August 1995).
- [19] V. E. Fradkov, Scripta Metall. et Mat., 30 (12), 1599-1603 (1994).
- [20] A. Bondi, Chem. Rev., 52, 417 (1953).
- [21] G. C. Smith and C. Lea, Surf. Inter. Analy., 9, 145 (1986).
- [22] T. J. Singler, J. A. Clum, and E. R. Prack, Trans. ASME, 114, 128 (1992).