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Highly efficient, large area, roll coated flexible and rigid OPV modules with geometric fill factors up to 98.5% processed with commercially available materials†

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Highly efficient, large area OPV modules achieving full area efficiencies of up to 93% of the reference small area cells are reported. The way to a no-loss up-scaling process is highlighted: photoelectrical conversion efficiencies of 5.3% are achieved on rigid modules and of 4.2% on flexible, roll coated ones, employing a commercially available photoactive material. Exceptionally high geometric fill factors (98.5%), achieved via structuring by ultrashort laser pulses, with interconnection widths below $100 \mu m$ are demonstrated.

1. Introduction

In recent years, printable photovoltaic technologies have been attracting the attention of the scientific community, due to their potentialities; in fact, solution processability constitutes a major advantage towards large scale, cheap, industrial production of solar cells. Furthermore, the rapid energy payback time of these technologies makes them of particular interest.¹ Despite the high efficiencies reported on lab scale devices, which recently surpassed 10% power conversion efficiency (PCE) for organic photovoltaics $(OPV)²$ and 20%³ for perovskite based ones, very little progress is shown towards up-scaling processing of solar cells. $4-8$ Large area, roll-to-roll (R2R) processed solar cells and modules usually show a quite remarkable drop in efficiencies compared to small area, lab-produced hero cells,⁹ highlighting the importance of the development of a stable, reliable production process which should allow a minimization of the performance losses. Recently we showed what are the guidelines for an optimal upscaling route for large area modules, 10 using a combination of optical simulations (transfer matrix formalism – TMF) and electrical simulations (finite element simulations – FEM): a loss in PCE of

Broader context

The achievement of high performances for solar modules can be considered one of the biggest challenges to be tackled in order to make printed photovoltaics suitable for industrial production and commercialization. Often the scientific community is solely focused on achieving the highest, record performances on small scale devices, disregarding the development of suitable up-scaling strategies to minimize the performance drop between single cells and modules. In this contribution the key factors for an ideal large area processing are addressed: optical and electrical simulations for the design of the geometry of the modules and the choice of the electrode materials; roll-to-roll printing and laser structuring demonstrating that losses between cells and modules can be as low as 7%. The use of halogenfree formulations and materials available in large quantities constitute as well an important step towards industrially sustainable processes. **Published on 11 November 2015.**
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less than 10% should be achievable for opaque devices under optimized conditions. Generally this involves (i) a careful choice and optimization of the printing techniques, (ii) the development of halogen-free formulations allowing the achievement of the right morphology for the active layers, (iii) the development of a reliable, fast, high resolution patterning method for the monolithical interconnection of the cells in a module, the latest being of particular importance to minimize the size of the interconnection area which is not photoactive and thus is accountable as a loss. The ratio between the photoactive area and the total area of the module is usually referred to as the geometric fill factor (GFF).

In this report we performed optical simulations to quantify the losses induced by the change of electrode materials when going to up-scaled modules and compared them with experimental results. Then electrical simulations are carried out to assess the losses due to the monolithic interconnection of the cells in a module and are able to minimize them by choosing the appropriate layout. We will show how theoretical calculations can be matched with experimental results, yielding large area modules with PCEs of up to 90% of the reference hero cells by choosing high precision slot-die coating on a fully R2R compatible printing setup in combination with high precision,

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ultra-fast laser structuring. Exceptionally high GFF, higher than 98%, with an interconnection width of less than 90 μ m are obtained for the first time in the literature.

The relevance of the findings for the community is strengthened by the use of commercially available materials, showing that high PCEs are within reach for a technology which can be defined mature for industrialization.

2. Materials and methods

2.1 OPV devices processing

Inverted structure OPV devices were processed and characterized under ambient conditions. The sequence of layers was electrode1/ ZnO nanoparticles/PBTZT-stat-BDTT-8:¹¹ phenyl-C61-butyric acid methyl ester (PCBM)/poly(3,4-ethylenedioxythiophene):polystyrene sulfonate (PEDOT:PSS)/Ag. Reference cells were processed on a indium-tin oxide (ITO) coated glass (electrode 1, 20 Ω sq⁻¹, OSRAM), laser patterned with a laser fluence of 0.55 J $\rm cm^{-2}$ and 90% overlap at first harmonic wavelength (520 nm) to achieve electrical insulation of the electrode (P1). Large area modules were processed on fluorine-tin oxide (FTO) coated glass substrates (electrode 1, 8 Ω sq⁻¹, Pilkington) and on flexible ITO-Metal-ITO (IMI) sputtered polyethylene terephthalate (PET) substrates (electrode 1, 8 Ω sq⁻¹, Materion). IMI and FTO were laser patterned to achieve P1 with a fluence of 0.41 J $\rm cm^{-2} ,~50\%$ overlap and 0.9 J $\rm cm^{-2}$, 98% overlap respectively. After patterning, the glass substrates were subsequently cleaned in an ultrasonic bath in acetone and isopropyl alcohol for 10 min each. Flexible substrates were instead wiped and rinsed with isopropyl alcohol alone. All layers were processed via doctor blading for glass based devices and via slot-die coating for the flexible ones. Slot-die coating was performed using a 10 cm wide heatable slot-die head, mounted on a mini-roll coater (FOM technologies, Denmark). Coating was performed on a heatable backing roll. The parameters changed were wet film thickness of the layer (W) , coating speed (v) and temperature of the roll (T) . ZnO nanoparticles dispersed in isopropyl alcohol (Nanograde AG) were coated via doctor blading on glass substrates and via slot-die coating on flexible substrates ($W = 5 \mu m$, $\nu = 1 \ m \ min^{-1}$, $T = 40 \ ^\circ\text{C}$), optimizing coating and drying conditions to achieve a film thickness of 50 nm. The layer was dried at 80 $^{\circ}$ C in a convection oven for 90 s. PBTZT-stat-BDTT-8 from the Lisicon $^{\circledR}$ series was purchased from Merck Chemicals Ltd, Germany and PCBM (technical grade 99%) from Solenne BV, The Netherlands, and dissolved at a concentration of 35 mg ml^{-1} in a volume ratio 1:1.5 in o-xylene: tetrahydronaphthalene (9 : 1) and stirred for 3 h at 90 °C before coating. The photoactive layer (PAL) was then coated in air via doctor blading on glass substrates and via slot die coating on flexible substrates ($W = 9.4$ µm, $v = 2$ m min⁻¹, $T = 70$ °C) aiming at a dried film thickness of 220 nm. The film thickness was optimized through optical density measurements (Fig. S1, ESI†). PEDOT:PSS (Clevios P VP Al4083) from Heraeus Germany was diluted in isopropyl alcohol (1:5 volume ratio) before deposition on glass substrates, obtaining a 50 nm thick layer. PEDOT:PSS (HTL Solar) from Heraeus Germany was diluted Communication

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in de-ionized water $(1:1)$ volume ratio) and then coated via slotdie coating (W = 10 µm, $v = 1$ m min⁻¹, T = 40 °C) on flexible substrates, aiming at a dried film thickness of 100 nm. The substrates were then annealed at 120 \degree C for 5 min in a convection oven and afterwards patterned to remove the coated stack (ZnO/PAL/PEDOT:PSS) to achieve P2 (fluence of 0.27 J $\rm cm^{-2}$, 93% overlap for FTO based modules and fluence of 0.1 J cm⁻², 94% overlap, 3 times repeated for IMI based modules). The devices were completed by thermal evaporation of a thin Ag layer (electrode 2) through a shadowing mask for single devices (100 nm) and full area for modules (250 nm) and then patterned to achieve electrical separation (P3 - fluence of 1.25 J cm⁻² and 66% overlap). The layout used for the modules was reported elsewhere.¹²

2.2 Laser patterning

Laser patterning was achieved using an LS 7xxP setup built by LS Laser Systems GmbH (München, Germany). The heart of the system is a femto REGENTM UC-1040–8000 fs Yb SHG from High Q Laser GmbH (Rankweil, Austria) emitting at 1040 nm (fundamental wavelength) and 520 nm (first harmonic wavelength) with a pulse duration of $<$ 350 fs at repetition rates up to 960 kHz. Maximum pulse energies of 16 μ J (1040 nm) or 6.4 μ J (520 nm) are reached at 500 kHz. The beam quality of the laser system is M2 < 1.25 (1040 nm), M2 < 1.5 (520 nm), and the pulse-to-pulse stability is $<2\%$ for both wavelengths. A variable beam expander expands the beam from two to eight times. A galvanometer-mounted mirror deflects the beam over the sample surface with a speed of up to 4000 mm s^{-1} . The scanner's objective has a focal length of 330 mm and a focused spot diameter of 32 \pm 1 µm (at 1/e² intensity). The power of the laser was measured using the VEGA DISPLAY and sensor 30A-BB-SH-18 ROHS from Ophir Optronics (Jerusalem, Israel).

2.3 Dark lock-in thermography

For the DLIT measurements an Equus 327k NM Infrared (IR)-camera (IRCAM GmbH, Erlangen,Germany) controlled by a computer for real-time lock-in calculations was used. The IR camera is equipped with a cooled indium-antimonite (InSb) and a focal plane array detector (640 \times 512 pixels) with a noise equivalent temperature difference less than 20 mK. The IR detector is highly responsive in the spectral range between $1.5-5 \mu m$ and provides a frame rate of 100 Hz. The camera was equipped with a 25 mm focal lens imaging system featuring a spectral transparency > 90% (IRCAM GmbH, Erlangen, Germany). For all measurements the lock-in frequency was set to 10 Hz in order to minimize implications due to the heat diffusion length. As power supply for the pulsed excitation a source measure unit made by Agilent (B2900) was used.

3. Results and discussion

A transition to large area, flexible substrates involves, as a matter of fact, the substitution of the brittle, expensive ITO with mechanically resistant, high performance electrodes, with sheet resistance below 10 Ω sq⁻¹ and high transmission in the

visible range ($T > 85\%$). For glass modules we chose FTO and for flexible ones IMI. However, the substitution of ITO causes important variations in the optical response of the stack 10 and thus affects the performances of the devices. Small area solar cells were prepared as explained in section 2 on ITO, FTO and IMI substrates; the results reported in Table 1 show that the PCE losses of IMI and FTO based devices compared to the ITO ones occur mainly in current. In order to understand the reasons leading to such a prominent loss, we performed optical simulations using the transfer matrix method $\text{(TMF)}^{13,14}$ on the following structures: (a) ITO (360 nm)/ZnO (50 nm)/PBTZT-stat-BDTT-8:PCBM/PEDOT:PSS (50 nm)/Ag (100 nm), (b) FTO (350 nm)/ZnO (50 nm)/PBTZT-stat-BDTT-8:PCBM/PEDOT:PSS (100 nm)/Ag (200 nm) and (c) IMI (ITO 40 nm – Ag 10 nm – ITO 40 nm)/ZnO (50 nm)/PBTZT-stat-BDTT-8:PCBM/PEDOT:PSS (100 nm)/Ag (250 nm). We assumed a constant IQE of the PAL of 80%,¹⁰ a fill factor of 60% and V_{oc} of 0.77 V. The results are shown in Fig. 1a and summarized in Table 1.

The simulations show a substantial agreement with the experimental results. The substitution of the ITO electrode with IMI causes, according to the TMF simulations (Fig. 1a) and in agreement with the experimental results, a significant loss in short circuit current (about 3.4 mA cm^{-2}) and thus a predicted PCE loss of more than 24% compared to the reference device. In order to further investigate the reasons for the diminished $J_{\rm sc}$ in the IMI based device, we calculated the electric field intensity distribution in the stack^{15,16} at λ = 650 nm, which is the absorption maximum of the PAL (Fig. S1b, ESI†). Furthermore, we calculated the spatial distribution of the Poynting vector,¹⁵ depicting the energy flux in the stack and its spatial derivative, and the absorption rate. This quantifies the absorption in each layer composing the stack. In Fig. 1b the electric field intensity $(|E|^2)$, the Poynting vector and the absorption rate are shown as functions of the position in the stack for ITO, FTO and IMI based electrodes. The parasitic absorption of the silver layer in the IMI electrode reduces the photogenerated current in the PAL by around 0.9 mA cm^{-2} in the wavelength range 350-900 nm, according to the TMF calculations.

In an ideal up-scaling process, independently of the chosen substrate (flexible or rigid) and the processing technique (slot-die coating or doctor blading), it should be possible to obtain the above optically simulated efficiencies (Table 1) also for large area modules, provided the electrical interconnection of the cells does not lead to additional resistive losses and does not cause photoactive area losses. However, this is physically not possible, as shown in Fig. 2a: in fact the interconnection region (distance

Fig. 1 (a) Optical simulation of the resulting $J_{\rm sc}$ as a function of the active layer thickness for different electrode materials: ITO, FTO, IMI and (b) calculations on Poynting vector, absorption rate and intensity of the electric field ($|E|^2$) as a function of the stack position.

between P1 and P3, called patterning length ℓ) is not photoactive and furthermore may induce an additional resistance in the P2 line, where the top and bottom electrodes are in contact (interconnect resistance, R_{int}). High sheet resistances of top $(R_{\text{s,top}})$ and bottom $(R_{s, bottom})$ electrodes are as well causes of additional resistive losses when the cell length exceeds the dimensions of the single cells which are used as the references. The ratio between the active area length (L) and the sum of active area length and the patterning length $(L + \ell)$ is usually referred to as geometric fill factor (GFF). According to finite element simulations,¹⁷ the previously mentioned losses can be minimized if (i) L is kept between 3 to 5 mm, (ii) $R_{\rm int}$ is well below 100 m Ω cm², and (iii) ℓ is as small as technically possible. Satisfying these three conditions can be considered the main challenge to be faced: as

Table 1 Results of the optical simulations performed on single small area cells based on the architectures and materials chosen for the upscaling process. Experimental results give average values on more than 6 cells with 10.4 mm² active area

| | $J_{\rm sc}$ (mA cm ⁻²) at 220 nm PAL thickness | | $V_{\rm oc}$ (V) | | FF(%) | | PCE(%) | |
|--------------------------|---|----------------|------------------------------------|--------------|--------------------------------------|-----------|------------------------------------|--------------|
| Bottom electrode | Exp. | Sim. | Exp. | a^a | Exp. | a^{μ} | Exp. | Sim. |
| ITO FTO | 13.51 ± 0.70 11.81 ± 0.44 | 14.39 13.75 | 0.77 ± 0.01 0.78 ± 0.03 | 0.77 0.77 | 59.83 ± 1.72 62.19 ± 2.55 | 60 60 | 6.22 ± 0.15 5.77 ± 0.48 | 6.64 6.35 |
| IMI | 10.65 ± 0.22 | 10.98 | 0.75 ± 0.01 | 0.77 | 55.00 ± 1.47 | 60 | 4.47 ± 0.11 | 5.07 |

 a a. stands for assumption used for the optical simulations.

Fig. 2 (a) Scheme of the cross section and (b) top view of monolithically interconnected cells in a module, with L active cell length and ℓ patterning length. The modules prepared in this work have dimensions $L + \ell = 5$ mm, $D = 50$ mm and 14 cells in series. (c) Interconnection region of a flexible module on an IMI electrode with P1, P2 and P3 visible. The total width of the interconnection region is 80 um.

previously shown,¹⁰ complying with these requirements allows the PCE loss from a single cell to a module to be as low as 4–6%. However, commonly reported patterning methods, such as onedimensional slot-die coating, screen printing or spray coating 10 do not allow us to achieve $\ell < 1$ mm and reported modules^{6,18-20} show active cell lengths in the order of 0.8–1 cm, leading to prominent losses in the overall PCE of the modules compared to the single cells. Laser structuring by fs-lasers has already proven to be a successful candidate for monolithic integration of thin film solar cells with patterning lengths as small as $150 \mu m$.^{21,22} In this work, we have managed to reduce this value to as little as 80 µm.

Following the prescriptions (i) to (iii), a suitable module layout was chosen (Fig. 2b) for the fabrication of modules, using the following parameters: $L + \ell = 5$ mm, $\ell = 80$ µm, P2 width = $20 \mu m$. Subsequently, FEM simulations were performed to determine the potential of modules with this layout, inserting the experimental values of $J_{\rm sc}$, FF, and $V_{\rm oc}$ of the best performing single cells and $R_{\rm s,top}$ = 0.1 Ω sq⁻¹ (evaporated silver) and $R_{\rm s, bottom}$ = 8 Ω sq⁻¹ (IMI,¹² FTO, ITO) for the sheet resistances of the materials employed for top and bottom electrodes, respectively, and using the literature value $R_{int} = 11 \text{ m}\Omega \text{ cm}^{223}$ for the interconnect resistance. Bus bar induced losses were not accounted for in this simulation. Fig. 3a provides a scheme of electrical resistances added in the simulation to the experimental *JV* curves of the single cells as reported in Table 2.

The FEM simulations show that efficiencies of 6.2% for ITO based modules, 5.6% for FTO modules and 4.5% for IMI modules are achievable. The slightly lower fill factors in the module with

Fig. 3 (a) Electrical resistances of bottom $(R_{s, bottom})$ and top $(R_{s, top})$ electrodes and interconnect resistance (R_{int}) added to the experimental JV curves of single cells (cell1, cell2,...) in the FEM simulations. (b) Slot-die coating process and (c) dried film of the photoactive layer on a ZnO coated, pre-patterned substrate; (d) 35 $cm²$ active area glass module and (e) 35 cm^2 flexible module.

respect to the single cells are mainly due to the additional series resistance introduced by the interconnection. According to the simulations, $J_{\rm sc}$ should not be affected at all.

Finally, modules were prepared according to the layout described above. Modules on glass/FTO substrates were coated by doctor-blading whereas PET/IMI modules were slot-die coated on a roll coater. A scheme illustrating the working principle of doctor-blading and slot-die coating is provided in Fig. S2 (ESI†) and the roll coating of the active layer is shown in Fig. 3b and c. A finished glass/FTO module and a PET/IMI module are provided in Fig. 3d and e. Experimental IV curves of FTO and IMI based modules are provided in Fig. 4.

Table 2 Achievable performances of the different module types (a)–(c) chosen for the upscaling process, according to electrical simulations (FEM). The experimental performances (exp.) of single cells (best experimental results) were used as input for the simulations

| | $J_{\rm sc}$ (mA cm ⁻²) | | | FF(%) | | PCE(%) | |
|------------|-------------------------------------|------------------|-----------------------|-----------------|------------------|------------------------|------------------|
| | Cells (exp.) | Modules (FEM) | $V_{\alpha e}$ (V) | Cells (exp.) | Modules (FEM) | Cells (exp.) | Modules (FEM) |
| ITO | 13.45 | 13.44 | 0.77 | 64 | 59 | 6.6 | 6.2 |
| FTO | 11.42 | 11.42 | 0.81 | 66 | 61 | 6.1 | 5.6 |
| IMI | 11.50 | 11.46 | 0.77 | 62 | 58 | 4.9 | 4.5 |

Fig. 4 (a) Light and dark (b) experimental IV curves of the modules prepared in this work.

The performances of the prepared modules are summarized in Table 3. Efficiencies as high as 5.3% are obtained for hero glass modules, corresponding to a negligible PCE loss of 3.5% compared to the reference hero device (see Table 2). Flexible hero modules on IMI achieve efficiencies higher than 4%, with a PCE loss of only 7% compared to the reference device, which is mainly due to a reduced Jsc. Interestingly, the average PCE values for FTO, doctor-bladed modules are significantly lower compared to the hero module due to the fluctuations in $J_{\rm sc}$ from module to module. The average values for the slot die coated modules on IMI, are instead consistent with the hero module, showing a variation in $J_{\rm sc}$ of only 2%. This shows that slot-die coating, with a continuous, precise ink supply while coating, is a suitable candidate for upscaling.

High quality, highly uniform coatings are necessary to have a uniform photogenerated current across the photoactive area. In order to verify the homogeneity of the printed active layer, we measured the optical density and calculated the dry film thickness of the slot die coated layer as shown in Fig. S3 (ESI†). The spatially resolved map obtained shows that variations in thickness in the order of ± 20 nm can occur in the region of the printed layer corresponding to the active area of the module. According to TMF simulations this corresponds to the changes in the predicted $J_{\rm sc}$ of ± 0.5 –0.6 mA cm⁻² (Fig. S2, ESI†), thus reducing the current delivered by the modules. Variations in the thickness of the electron injection layer and the hole injection layer can cause further oscillations of the $J_{\rm sc}$ and account for the losses of PCE as in Table 3.

The high quality of the slot die coatings is also confirmed by dark lock-in thermography (DLIT) measurements in the injection regime. A DLIT image of a slot-die coated 35 cm^2 module is shown in Fig. S4 (ESI†). The slot-die coated module (PCE 3.95% as prepared) shows a high homogeneity of the locally dissipated power, indicating a homogeneous current and voltage distribution in the cells.²⁴ From Fig. S3 (ESI[†]) and Table 3 it can be seen that the few hot spots do not affect the cells and performances of the module.²⁵ Furthermore no interconnection issues between P1 and P3 could be detected.

Doctor-blading is instead affected by both batch-to-batch variations in the deposition procedure (for example, to the manual positioning of each substrate under the blade) as well as an intrinsic gradient of the thickness of the doctor-bladed layer.

4. Conclusions

In summary we have identified a successful way towards the minimization of the losses between hero cells, usually prepared on glass substrates with ITO electrodes, and roll to roll produced modules on PET/IMI. The optimization route starts necessarily from a quantitative evaluation of the optically induced losses in the upscaling process, mainly due to the choice of the electrodes and thicknesses of the electron-injection layers and hole-injection layers. The short circuit current of the devices can be reduced quite significantly upon replacing ITO by IMI electrodes, due to the lower transparency and interference effects of the latter, which in case of PBTZT-stat-BDTT-8:PCBM photoactive layer can be as high as 30% compared to a reference ITO based electrode (Table 1). FEM simulations then allow us to assess the impact of the electrical

Table 3 Performances of printed modules on glass/FTO and PET/IMI substrates. The last column shows the PCE drop * in comparison to the best small area devices (0.1 cm²) and ** in comparison to the average PCE of the small area devices (0.1 cm²)

| | Active area $\text{ (mm}^2\text{)}$ | I/module active area (mA cm ⁻²) and ($J_{\rm sc}$ (mA cm ⁻²) of the single cells in the module) | $V_{\rm oc}$ (V) | FF(%) | PCE(% | PCE loss to ref. cell |
|----------------------------|-------------------------------------|--|------------------|-------|-------|--------------------------|
| FTO best module | 3500 | 0.820(11.48) | 10.50 | 62 | 5.28 | 3.5% * |
| Average on 6 modules (FTO) | 3500 | 0.775(10.85) | 10.38 | 56 | 4.49 | 16% ** |
| IMI best module | 3500 | 0.630(8.82) | 10.70 | 62 | 4.18 | 7.0% * |
| Average on 6 modules (IMI) | 3500 | 0.619(8.66) | 10.70 | 60 | 3.97 | 11% ** |
| IMI Module | 9800 | 0.613(8.58) | 10.70 | 60 | 3.95 | $12\%*$ |

layout of modules consisting of monolithically connected solar cells. The use of high resolution, ultra-fast laser structuring allows achieving record geometric fill factors up to 98.5% and dead areas below 80 μ m, with P2 line width in the range of 20 μ m yielding to simulated PCE losses of less than 8% compared to the hero devices (Table 2). Furthermore, the use of high precision coating techniques, such as slot-die coating, allows for a very good control of the thickness of the coated layers yielding to small differences in performance between modules and reference cells. The combination of information from optical and electrical simulations allows a very precise estimation of the achievable efficiencies in experimental trials, allowing to maximize the PCE of the modules and obtain, to the best of our knowledge, the highest PCE (4.2%) reported in the literature for large area, roll coated, flexible OPV modules and an efficiency higher than 5.3% for large area coated rigid modules. The exclusive use of commercially available materials and the minimization of the efficiency drop between hero cells and large area modules finally open the way to a successful production of large volumes of highly efficient modules. Communication

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