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Citation: *Applied Physics Letters* **103**, 173108 (2013); doi: 10.1063/1.4826640

View online: <http://dx.doi.org/10.1063/1.4826640>

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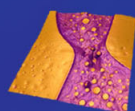
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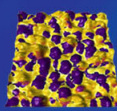
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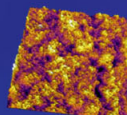


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Transport properties of single TiO₂ nanotubes

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(Received 20 August 2013; accepted 9 October 2013; published online 22 October 2013)

We investigated the electric transport properties of single TiO₂ nanotubes separated from an anodic titania nanotube array. The temperature dependence of the resistance measured with the conventional four point method of all investigated samples show a Mott variable range hopping behavior. The results obtained with two contacts indicate the existence of a potential barrier between the Cr/Au contacts and samples surfaces, which influence is clearly observable at temperatures <150 K. Impedance spectroscopy in the frequency range of 40 Hz to 1 MHz carried out at room temperature indicates that the electronic transport of these polycrystalline tubes is dominated by the grain cores. © 2013 AIP Publishing LLC. [<http://dx.doi.org/10.1063/1.4826640>]

In the last decade, titanium dioxide nanotube arrays, formed by self-organizing anodization, have attracted considerable attention. Mainly these 1D structures are expected to replace titanium dioxide nanostructured particulate electrodes in catalysis,¹ photocatalysis,² dye-synthesized solar cells,³ gas sensors,⁴ or batteries,⁵ due to improved electron transfer properties. Electronic, chemical, and ionic properties of TiO₂ depend considerably on the specific phase of TiO₂.^{6,7} Rutile and anatase are the most used phases in many applications of TiO₂ based on charge transfer reactions (solar cells, photocatalysis).^{8–12} Nevertheless, the general mechanism for electron transport, particularly in nanostructures, is not well studied and understood. The transport properties can be strongly influenced by the scattering as well as through the possible increase of the carrier density at the grain boundaries, namely, in polycrystalline nanotube arrays.¹³ Additionally, the sample surfaces containing intergranular boundaries may play an extra role in the conductivity. In the present work, we investigated the conductivity of single TiO₂ nanotubes separated from anodic TiO₂ nanotube arrays. The relatively simple way to produce such self-organized TiO₂ nanotubes on Ti substrates makes this approach cheap and efficient compared to other fabrication techniques. These TiO₂ tubes are usually amorphous after growing but can be converted to anatase by annealing at temperatures (*T*) higher than 300 °C.^{14,15} After annealing, the nanotubes are polycrystalline with an average grain size of 10 to 50 nm.^{16,17} We have used impedance spectroscopy in order to discern between the mechanisms of conduction given by the possible electron pathways in the sample.¹⁸ To characterize the contact resistance we have also studied the current-voltage (*I*-*V*) characteristics using two points measurements as recently reported for single TiO₂ nanotubes.¹⁹

Prior to anodization the titanium foils (Advent Ltd., 99,6%, 0.1 mm) were cleaned by sonicating them in acetone, ethanol, and deionized (DI) water and were then dried in a nitrogen stream. The TiO₂ nanotubes were grown by anodization using a potentiostat (Jaissle IMP 88 PC) in a

three electrode setup, where a platinum sheet acts as counter, a platinum wire as reference, and the Ti foil as working electrode. The anodization was carried out at room temperature in an ethylene glycol (Sigma Aldrich 99.5%) 0.15 mol/l ammonium fluoride electrolyte (Sigma Aldrich) with 1 M H₂O at 60 V for 10 h. After anodization, the grown layers were immersed in ethanol for 1 h and dried in a nitrogen stream. To transfer the grown amorphous TiO₂ nanotubes into a crystalline structure annealing at 350 °C and 450 °C was carried out in a tube furnace in air. After annealing, the tubes were removed from the Ti foil by sonication in DI water.

The nanotubes were then transferred on the top of a SiN_x/SiO₂ substrate by a printing method, which means to press a TiO₂ bundle between the two above mentioned substrates. After this step, the substrate is blown gently with air to remove big pieces and dirt, followed by an identification of suitable nanotubes. For this, we examined the nanotubes in a Dual Beam Microscope (FEI NanoLab XT 200) and selected a particular one, see example in Fig. 1(a). To fix the nanotube on the substrate, we used electron beam induced deposition (EBID),²⁰ a technique based on decomposing gaseous molecules by electron beam, leading to deposition of non-volatile fragments of a given material, in our case WC_x. Using the electron beam, instead of the ion beam induced deposition (IBID) usually with Ga⁺ ions, we preserve the sample structure and intrinsic characteristics. The WC_x material deposited on four different places along the nanotube length, see Fig. 1(b), is nearly insulating²¹ and helps to fix the sample on the substrate and also facilitates the preparation of the electrical contacts at different positions along the sample, see Fig. 1(c). To do the electrical contacts, the substrate is covered with a resist (PMMA 950 K, AR-P 671-05) for the electron beam lithography process. After developing the electrode shapes, Cr/Au thin bilayer films of thickness of 5 nm and 35 nm, respectively, were deposited by sputtering on the sample/PMMA/substrate arrangement. In the last step and after removing the rest of the PMMA, the sample is contacted to a chip carrier for the transport measurements.

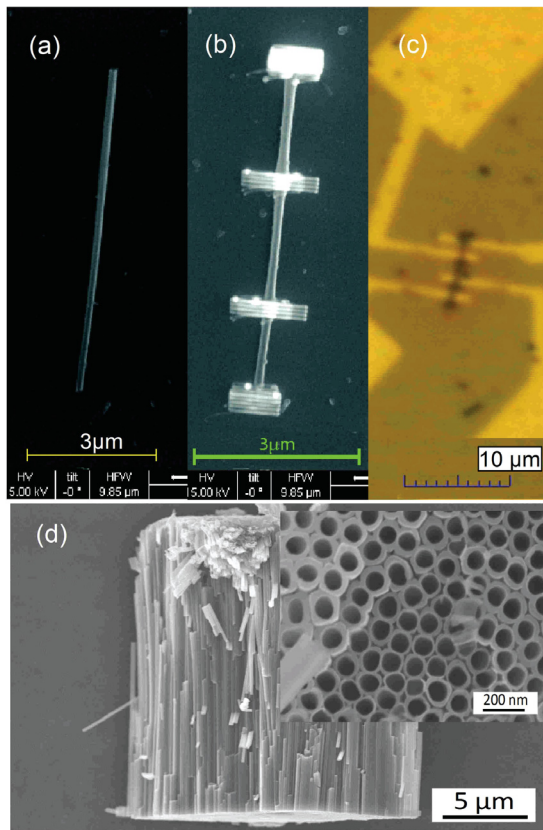


FIG. 1. (a) One TiO_2 nanotube before fixing it on the substrate. (b) The nanotube fixed with WC_x . (c) After producing the electrical contacts. (d) Scanning electron microscope side and top views (inset) of a bundle of TiO_2 nanotubes.

The carrier is mounted on the cold head of a standard closed cycle refrigerator inside a vacuum bell, with a minimal $T \simeq 25$ K. The resistance measurements were performed using a current source (Keithley 6221) and a nanovoltmeter (Keithley 2182). Impedance measurements were carried out using an Agilent 4294A impedance analyzer, in the frequency range of 40 Hz to 1 MHz at room temperature and in darkness to avoid changes due to the photoconductivity of the sample. We measured the resistance and reactance using alternating signals of 20 mV amplitude. The measurements were done in two-contacts mode, taking the two outer electrodes used for the DC resistance measurements with the sample in the same chip-carrier. The necessary calibration of the equipment to avoid spurious cables and sample holder contribution was done prior to the measurements and without the sample. Table I describes the studied samples, their length, resistance at 300 K, and the annealing treatment.

First, we discuss the results obtained using the conventional four point measurements in order to avoid possible contributions from the contact barriers between the

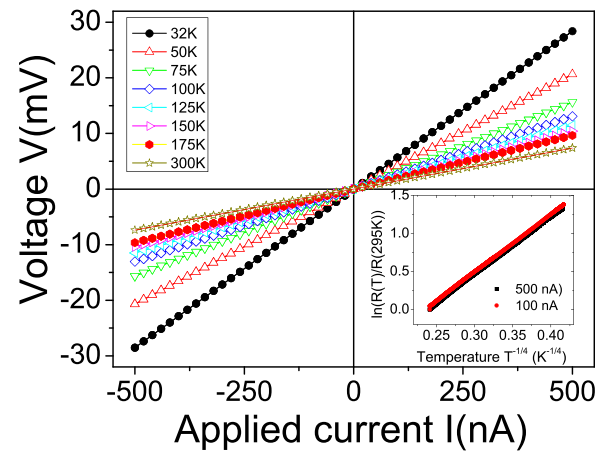


FIG. 2. Current-voltage (I - V) curves at different temperatures for sample TiO_2a measured with four contacts. Inset: Logarithm of the normalized resistance vs. $T^{-1/4}$ for sample TiO_2a at two currents, 100 nA and 500 nA. The observed linearity in the used scale indicates a 3D variable range hopping mechanism.

electrodes and the oxide surface. In this contact configuration, we observe that the I - V curves are linear over all the temperature range, see Fig. 2. The T -dependence of the resistance $R(T)$, shown in the inset of Fig. 2, indicates that $R(T) = R_0 \exp\left[\left(\frac{T_0}{T}\right)^{0.25}\right]$, with R_0 an arbitrary prefactor and T_0 a characteristic temperature coefficient. This dependence is compatible with the three dimensional (3D) variable range hopping (VRH) conduction mechanism known as the Mott law, a model that describes conduction in strongly disordered systems with localized charge-carrier states.²² According to this model, $T_0 = 18/k_B \xi^3 N(E_F)$, where ξ is the localization length, $N(E_F)$ is the density of states at the Fermi level E_F , and k_B the Boltzmann constant. From the observed dependence, see inset in Fig. 2, we obtain $T_0 \simeq 3250$ K. Assuming that the localization length is of the order of ≈ 1 nm,²³ we estimate $N(E_F) \simeq 6.4 \times 10^{28} \text{ eV}^{-1} \text{ m}^{-3}$, a value comparable with those from literature.²⁴

Figure 3 shows the results of the impedance spectroscopy measurements of sample TiO_2a at room temperature. Similar results were obtained for other samples. In general, for a semiconductor, different resistances and capacitances (R, C) due mainly to grain boundaries (R_{gb}, C_{gb}) and grain interior (R_g, C_g) are assumed. Each conduction process is modeled by a parallel R/C equivalent circuit. Such contributions can be in parallel or in series.²⁵ The process-related contributions can be recognized through a cole-cole plot by two different arcs related to the grain boundary, at low frequencies, and grain interior at higher frequencies. In the present case, see Fig. 3(a), a single arc is observed, which can be fitted with the following equation:

TABLE I. Specifications of the investigated nanotubes. The shown lengths were measured between the voltage electrodes using scanning electron microscopy. Resistivity values range between 10^{-3} and $1 \Omega \text{cm}$.

	TiO1a	TiO1b	TiO2a	TiO2b	TiO2c	TiO3b
Length (μm)	1.5 ± 0.1	1.4 ± 0.1	0.8 ± 0.1	0.5 ± 0.1	1.5 ± 0.1	0.8 ± 0.1
Annealed (t, air)	350°C	350°C	450°C	450°C	450°C	450°C
$R_{300\text{K}}$ (k Ω)	101.3 ± 0.1	27330 ± 200	14.7 ± 0.1	11.1 ± 0.1	116.7 ± 0.1	160 ± 1

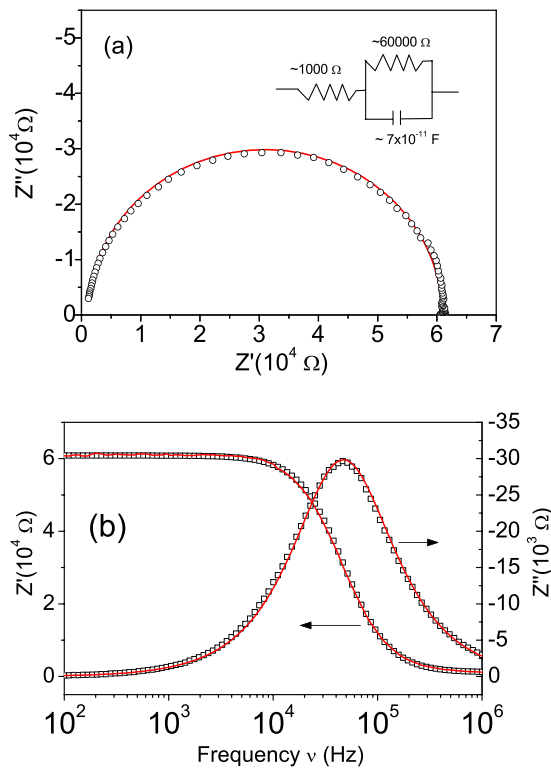


FIG. 3. (a) Nyquist plot of a single TiO_2 of sample TiO2a. The red line is the fitting to the experimental data following Eq. (1) assuming the equivalent circuit shown in the inset. (b) Frequency dependence of Z' and Z'' with their corresponding fitting curves.

$$Z^* = Z' + iZ'' = R_0 + \left(\frac{1}{R_{g,gb}} + i\omega C_{g,gb} \right)^{-1}, \quad (1)$$

where R_0 is the total resistance due to external component as wiring, $R_{g,gb}$ is the grain or grain boundary resistance, and $C_{g,gb}$ is the grain or grain boundary capacitance. From the fit of the data to Eq. (1), we obtained $R_0 = (10^3 \pm 50)\Omega$, $R_g = (6 \pm 02) \times 10^4 \Omega$, $C_g = (7 \pm 1) \times 10^{-11}F$. The value of the capacitance indicates a conduction process through the grain cores,^{26,27} in agreement with recently reported measurements in pellets of TiO_2 nanotubes.²⁸

Furthermore and for completeness, Fig. 3(b) shows the frequency dependence of Z' and Z'' of the studied sample. The observed behavior can be well fitted with the same equivalent circuit, as expected. It can be seen that there is a single mechanism of conduction since it is possible to see a single change of slope and a single minimum for the frequency dependence of Z' and Z'' .

Note that the resistances measured in all the samples indicate resistivity values below $\sim 1 \Omega cm$ at room temperature. These values are much smaller than the intrinsic resistivity of TiO_2 , very probably due to the large density of intragrain defects such as oxygen vacancies, known to influence the electrical properties of this oxide.²⁸ These defects can facilitate the conduction within the grain, in spite of the existence of grain boundaries that do not have an appreciable contribution in the conduction process in our polycrystalline samples at room temperature.

As mentioned in the experimental section, the contacts were done by sputtering of chromium and gold. When chromium is sputtered on TiO_2 , an oxide is formed²⁹ on the TiO_2

surface such that an extra potential barrier is formed. Each of the nanotubes has been measured using the two-point method. An example can be seen for the TiO3b sample in Fig. 4. We get a clear linear ohmic behavior at room temperature whereas at temperatures below $\sim 150 K$ the contribution of an energy barrier is obvious and it becomes larger decreasing temperature. All other samples show similar $I-V$ curves, a fact that speaks for the general formation of a potential barrier and the inadequacy of the two points method to obtain the intrinsic transport properties of several oxides. The observed behavior in the $I-V$ curves can be understood taking into account the existence of a potential barrier and thermally activated process and the contribution of a temperature dependent ohmic term, given by the equation

$$V = a(T) \tanh(I/I_0) + b(T)I, \quad (2)$$

where $a(T)$, $b(T)$, and I_0 are fitting parameters. In Fig. 4, we show the fits of the $I-V$ curves following Eq. (2). From these fits, we obtain that $a(T) \simeq 26[VK]/T$, $I_0 \simeq 1 \mu A$, and $b(30K) \simeq 4.6 \times 10^5 \Omega$, decreasing slightly with T .

The results of the temperature dependence of the resistance at different constant currents are shown in the inset of Fig. 4. At low input currents, the normalized resistance follows roughly an inverse temperature dependence due to the influence of the potential barrier, which is the reason for the non-linear $I-V$ characteristics shown in Fig. 4. Obviously, this influence decreases with temperature and current; it remains negligible at $T > 150 K$, temperature range where the resistance shows an ohmic behavior.

Concluding, we have isolated TiO_2 nanotubes within the micro- and nanometer scale and we were able to contact them using a combination between electron beam induced deposition and electron beam lithography for electronic transport measurements. The temperature dependent of the resistance is described by the Mott variable range hopping mechanism. The usage of Cr/Au contacts for transport measurement at temperatures below 150 K does produce a measurable potential barrier. Impedance spectroscopy results

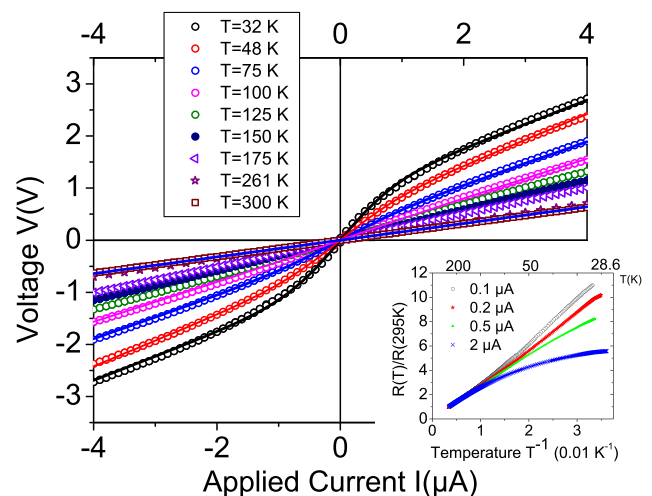


FIG. 4. Current-voltage characteristics for sample TiO3b. The continuous lines through the points at five different temperatures (32 K, 48 K, 75 K, 100 K, and 300 K) follow Eq. (2). Inset: Normalized resistance vs. inverse temperature for sample TiO3b at different constant currents.

indicate that a single transport through the grain cores is active.

This work is being supported by the collaborative project SFB 762 “Functionality of Oxide Interfaces” and the DFG cluster of excellence “Engineering of Advanced Materials.”

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