Boise State University ScholarWorks

College of Engineering Presentations

2014 Undergraduate Research and Scholarship Conference

4-21-2014

Thermo-Electric Generator Evaluation Board

Jeanette Brooks College of Engineering, Boise State University

Cody Sullivan College of Engineering, Boise State University

Adrian Rothenbuhler Project sponsor for the College of Engineering, Boise State University



THERMO-ELECTRIC GENERATOR EVALUATION BOARD

Jeanette Brooks, Cody Sullivan, and Adrian Rothenbuhler **Department of Electrical and Computer Engineering**

THEORY

BACKGROUND

THERMAL ENERGY:

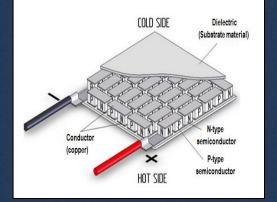
- Renewed and increased interest in harvesting thermal energy
- Potential power harvested is comparable to solar
- "Free" ambient energy Less than 2°C temperature difference will generate power

	Energy Source	Harvested Power
	Vibration/Motion	
	Human	4 µW/cm ²
	Industry	100 µW/cm ²
	Temperature Difference	
j	Human	25 µW/cm ²
	Industry	1-10 mW/cm ²
	Light	
	Indoor	10 µW/cm ²
	Outdoor	10 mW/cm ²
	RF	
	GSM	0.1 µW/cm2

From: http://www.ti.com

WiFi

0.001 µW/cm²



THERMO-ELECTRIC GENERATORS (TEG):

- Highly reliable
- Low Maintenance
- No moving parts Perfect for remote applications

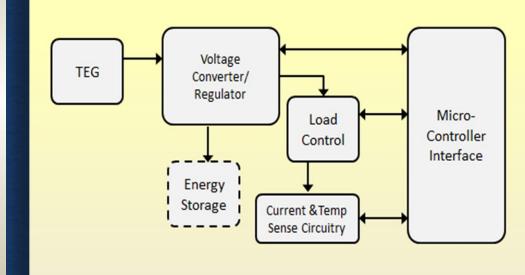
From: http://www.engineeringagenda.com/thermoelectric-cooling/

PROJECT OBJECTIVE:

To build a circuit board for evaluation of different TEGs, as well as the quantification of the maximum power that can be harvested within a certain environment.

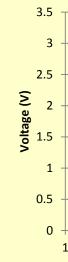
PROJECT REQUIREMENTS:

- Use a micro-power harvesting regulator for power conversion
- Load simulation through electronic load control
- Measure output voltage and current
- Measure ambient temperature as well as the temperature on both sides of the TEG
- Log data at a frequency of at least 10 seconds
- Microcontroller interface for process automation



The block diagram above shows the individual blocks implemented to meet the project requirements listed above. The five left-most blocks are used to convert, control, and measure the generated power. The microcontroller interface block on the right is a digital interface to a microprocessor for data processing and logging. Note the optional "Energy Storage" block allowing the user to experiment with a high storage battery or capacitor.

The graph below shows experimental results of the TEG evaluation board. Of note is that the power peaks right around when the system output voltage becomes unstable, which is due to the power transfer being maximized and the TEG driven to its limit.

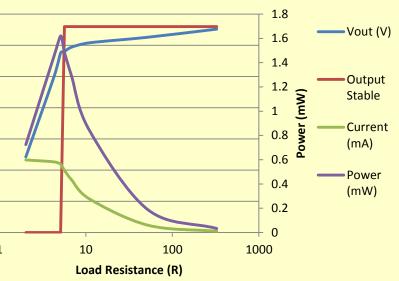


PCB SPECIFICATIONS:

- cost
- Converter
- BJT Load Control with adjustable range



APPLICATION



2-layer board for reduced

Hardware interface fits an Arduino or STM32 Discovery board for easy prototyping LTC3109 Micro Harvesting MAX9611 Current Sensing

