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IB-DFE Receiver Techniques for CP-Assisted Block Transmission within DS-CDMA and MC-CDMA Systems

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Abstract - MC-CDMA (MultiCarrier Code Division Multiple Access), currently regarded as a promissing multiple access scheme for broadband communications, is known to combine the advantages of an OFDM-based (Orthogonal Frequency Division Multiplexing), CP-assisted (Cyclic Prefix) block transmission with those of CDMA systems. Recently, it was recognised that DS-CDMA (Direct Sequence) implementations can also take advantage of the beneficts of the CP-assisted block transmission approach, therefore enabling an efficient use of FFT-based (Fast Fourier Transform), chip level FDE (Frequency-Domain Equalisation) techniques.

In this paper we consider the use of IB-DFE (Iterative Block Decision Feedback Equalisation) FDE techniques within both CP-assisted MC-CDMA systems with frequency-domain spreading and DS-CDMA systems. Our simulation results show that an IB-DFE receiver with moderate complexity is suitable in both cases, with excellent performances that can be close to the single-code matched filter bound (especially for the CP-assisted DS-CDMA alternative), even with full code usage.

I. Introduction

It is widely known that a CP-assisted block transmission approach, allowing low-complexity FDE receiver techniques, is suitable for high data rate transmission over severely time-dispersive channels. This approach can be employed with either MC (MultiCarrier) or SC (Single-Carrier) modulations [1], [2]. When adopted in CDMA systems, it leads to MC-CDMA implementations [3], [4], [5], and also, as recently recognized, quite efficient DS-CDMA implementations [6]. These CP-assisted schemes are especially interesting for multicode and/or downlink transmission, since all codes are synchronised, which simplifies the receiver implementation.

Conventional, linear FDE techniques are known to lead to a significant noise enhancement when a ZF (Zero Forcing) criterion is adopted in channels with deep in-band notches, which can lead to significant performance degradation. For this reason, an MMSE (Minimum Mean-Squared Error) FDE equaliser is usually preferable [7]. However, an MMSE FDE does not perform an ideal channel inversion. Therefore, when this type of equaliser is employed within CP-assisted CDMA systems, we are not able to fully orthogonalise the different spreading codes. This means severe interference levels, especially when different powers are assigned to different codes.

It is well-known that nonlinear equalisers can significantly outperform linear equalisers. For this reason, a promising IB-DFE (Iterative Block Decision Feedback Equalisation) approach proposed for CP-assisted SC schemes [8], with both the feedforward and the feedback parts implemented in the frequency domain (a similar concept was also proposed in [9]). Since the feedback loop takes into account not just the hard-decisions for each block, but also the overall block reliability, the error propagation problem is significantly reduced. Consequently, the IB-DFE receivers offer much better performances than the linear, non-iterative FDE receivers [8], [10]; moreover, their implementation is much less complex than that of frequency-domain turbo-equalisation [11].

In this paper, we consider the use of IB-DFE techniques for CP-assisted block transmission within MC-CDMA systems with frequency-domain spreading and DS-CDMA systems.

This paper is organized as follows: Sec. II describes the CP-assisted CDMA schemes. The IB-DFE receiver is described in sec. III. A set of performance results is presented in sec. IV, and sec. V is concerned with the conclusions and complementary remarks of this paper.

II. CP-Assisted CDMA Schemes

In this section we describe the CP-assisted DS-CDMA and MC-CDMA schemes to be considered, involving a multicode transmission with constant spreading factor (the extension to VSF schemes (Variable Spreading Factor) is straightforward). In both cases, the receiver can be based on a linear FDE, as depicted in fig. 1A. As with other CP-assisted techniques, after removing the cyclic extension, the received time-domain block $\{y_n; n=0,1,\ldots,N-1\}$ is passed to the frequency domain, leading to the block $\{Y_k; k=0,1,\ldots,N-1\}$. When the cyclic extension is longer than the overall channel impulse response, the samples Y_k can be written as

$$Y_k = H_k S_k + N_k, (1)$$

where H_k and N_k denote the channel frequency response and the noise term for the kth frequency, respectively, and $\{S_k; k = 0, 1, ..., N - 1\} = DFT \{s_n; n = 0, 1, ..., N - 1\},$

with $\{s_n; n=0,1,\ldots,N-1\}$ denoting the transmitted time-domain block. For a linear FDE, the frequency-domain samples at its output are given by

$$\tilde{S}_k = F_k Y_k,\tag{2}$$

where $\{F_k; k=0,1,\ldots,N-1\}$ denote the FDE coefficients. If these coefficients are optimised under the MMSE criterion (Minimum Mean Squared Error), then

$$F_k = \frac{H_k^*}{\alpha + |H_k|^2},\tag{3}$$

where $\alpha = \sigma_n^2/\sigma_s^2$, with σ_n^2 and σ_s^2 denoting the variance of the noise and data symbols, respectively.

A. MC-CDMA

The frequency-domain block to be transmitted is $\{S_k; k=0,1,\ldots,N-1\}$, where N=KM, with K denoting the spreading factor and M the number of data symbols per spreading code. The frequency-domain symbols are given by $S_k = \sum_{p=1}^P \xi_p S_{k,p}$, where ξ_p is an appropriate weighting coefficient for power control purposes (the power associated to the pth spreading code is proportional to ξ_p^2). $\{S_{k,p}; k=0,1,\ldots,N-1\}$ is an interleaved version of $\{S'_{k,p}; k=0,1,\ldots,N-1\}$ (rectangular $K\times M$ interleaver, so that different chips associated with a given data symbol are spaced by M subcarriers).

$$S'_{k,p} = C_{k,p} A_{\lfloor k/K \rfloor,p} \tag{4}$$

is the kth chip for the pth spreading code ($\lfloor x \rfloor$ denotes 'larger integer not higher that x'). $\{A_{m,p}; m=0,1,\ldots,M-1\}$ denotes the block of data symbols associated to the pth spreading code and $\{C_{k,p}; k=0,1,\ldots,N-1\}$ is the corresponding spreading sequence. An orthogonal spreading is assumed throughout this paper, with $C_{k,p}$ belonging to a QPSK constellation (Quaternary Phase Shift Keying). Without loss of generality, it is assumed that $|C_{k,p}|=1$. At the receiver side, the $A_{k,p}$ coefficients are estimated from

$$\tilde{A}_{m,p} = \sum_{k' \in \Psi_m} \tilde{S}_{k'} C_{k',p}^*, \tag{5}$$

with $\Psi_m = \{m, m+M, \dots, m+(K-1)M\}$ denoting the set of frequencies employed to transmit the mth data symbol of each spreading code and \tilde{S}_k given by (2).

B. DS-CDMA

Let us consider now a DS-CDMA scheme. The transmitted block of chips is $\{s_n; n=0,1,\ldots,N-1\}$, where N=KM, with K denoting the spreading factor and M denoting the number of data symbols for each user. The overall "chip" symbols s_n are given by $s_n = \sum_{p=1}^P \xi_p s_{n,p}$, where ξ_p is an weighting coefficient, proportional to the transmitted power for the pth user, and

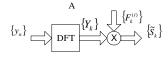
$$s_{n,p} = c_{n,p} a_{\lfloor n/K \rfloor,p} \tag{6}$$

is the nth chip for the pth user. $\{a_m; m=0,1,\ldots,M-1\}$ denotes the block of data symbols associated to the pth user and $\{c_{n,p}; n=0,1,\ldots,N-1\}$ denotes the corresponding spreading sequence. As with MC-CDMA, an orthogonal spreading and $|c_{n,p}|=1$ are also assumed.

In this case, the FDE receiver could estimate the data symbols from

$$\tilde{a}_{m,p} = \sum_{n'=mK}^{mK+K-1} \tilde{s}_{n'} c_{n',p}^*, \tag{7}$$

with $\{\tilde{s}_n; n = 0, 1, \dots, N-1\} = \text{IDFT } \{\tilde{S}_k; k = 0, 1, \dots, N-1\}$ (see fig. 1C).



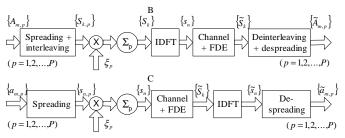


Fig. 1. FDE structure (A) and transmission models for MC-CDMA (B) and DS-CDMA (C).

III. Iterative Block Decision Feedback Equalization for Prefix-Assisted CDMA

Fig. 2 presents the receiver structures that we are considering in this paper, where the linear FDE is replaced by an IB-DFE. In both cases, for a given iteration i, the output samples are given by

$$\tilde{S}_{k}^{(i)} = F_{k}^{(i)} Y_{k} - B_{k}^{(i)} \hat{S}_{k}^{(i-1)} \tag{8}$$

where $\{F_k^{(l,i)}; k=0,1,\ldots,N-1\}$ $(l=1,2,\ldots,L)$ and $\{B_k^{(i)}; k=0,1,\ldots,N-1\}$ denote the feedforward and the feedback coefficients, respectively. The block $\{\hat{S}_k^{(i-1)}; k=0,1,\ldots,N-1\}$ is an etimate of the transmitted block $\{S_k^{(i-1)}; k=0,1,\ldots,N-1\}$, obtained form the data estimates of the (i-1)th iteration.

It can be shown that the optimum feedback and feedforward coefficients are given by

$$B_k^{(i)} = \rho^{(i-1)} \left(F_k^{(i)} H_k - \gamma^{(i)} \right) \tag{9}$$

and

$$F_k^{(i)} = \frac{H_k^*}{\alpha + (1 - (\rho^{(i-1)})^2)|H_k|^2},\tag{10}$$

respectively, where

$$\gamma^{(i)} = \frac{1}{N} \sum_{k=0}^{N-1} F_k^{(i)} H_k. \tag{11}$$

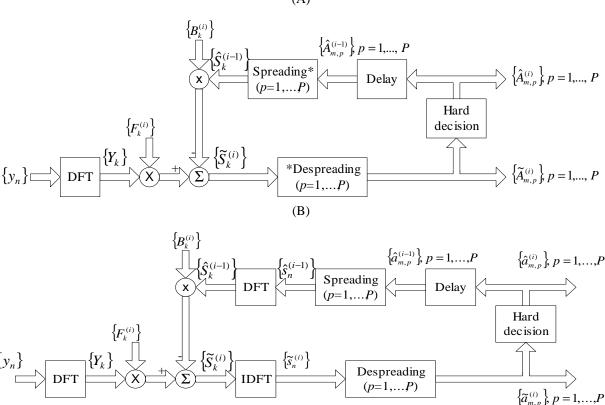


Fig. 2. IB-DFE receiver for MC-CDMA (A) (* denotes the complementary interleaving/deinterleaving) and DS-CDMA (B).

The coefficient $\rho^{(i-1)}$, which can be regarded as the blockwise reliability of the decisions used in the feedback loop (from the previous iteration), is given by

$$\rho^{(i-1)} = E[\hat{S}_k^{(i-1)} S_k^*] / E[|S_k|^2]. \tag{12}$$

This correlation factor is crucial for the good performance of the proposed receivers. Assuming uncorrelated data blocks, it can be easily shown that

$$\rho^{(i-1)} = \sum_{p=1}^{P} \xi_p^2 \rho_p^{(i-1)},\tag{13}$$

with

$$\rho_p^{(i-1)} = \frac{E[\hat{A}_{k,p}A_{k,p}^*]}{E[|A_{k,p}|^2]} = \frac{E[\hat{a}_{n,p}a_{n,p}^*]}{E[|a_{n,p}|^2]}.$$
 (14)

Clearly, for the first iteration (i=0), no information exists about S_k and the correlation coefficient is zero; therefore, $B_k^{(0)}=0$ and $F_k^{(0)}$ equals the right-hand side of (3). After that first iteration, and if the residual BER is not too high (at least for the spreading codes with higher transmit power), we can use the feedback coefficients to eliminate a significant part of the residual interference. When $\rho\approx 1$ (after several iterations and/or moderate-to-high SNRs), we have an almost full cancellation of the "inter-code" interference through these coefficients, while the feedforward coefficients perform an approximate matched filtering.

IV. Performance Results

In this section we present a set of performance results concerning the proposed receiver structure. We consider the downlink transmission, with each spreading code intended to a given user. It is assumed that N=K=256 (similar results could be obtained for other values of N) and the data symbols are selected from a QPSK constellation under a Gray mapping rule. For both DS-CDMA and MC-CDMA, we considered an orthogonal spreading (Hadamard-Walsh sequences plus pseudo-random scrambling sequences with the same chip rate) and the power amplifier at the transmitter is assumed to be linear. We consider a channel characterized by the power delay profile type C for the HIPERLAN/2 (HIgh PERformance Local Area Network) [13], with uncorrelated Rayleigh fading on the different paths. The subcarrier separation is 0.2MHz. Perfect synchronisation and channel estimation are assumed in all cases. The number of users is K, i.e., we are assuming a fully loaded system. For the sake of comparisons, we included the MFB performance (Matched Filter Bound).

Figs. 3 and 4 concern MC-CDMA and DS-CDMA schemes, respectively. Clearly, the iterative procedure allows a significant improvement relatively to the conventional linear FDE (first iteration). Moreover, the achievable performances are close to the MFB after three iterations. It was also observed that the performances are very similar, slightly better for DS-CDMA.

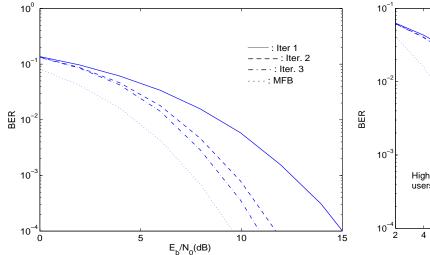


Fig. 3. MC-CDMA BER performance when $K=256\ (M=1)$ and P=256 users, with the same assigned power.

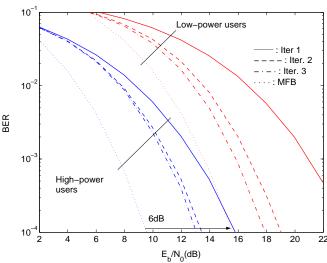


Fig. 5. MC-CDMA BER performance with K/2=128 low-power users and K/2=128 high-power users.

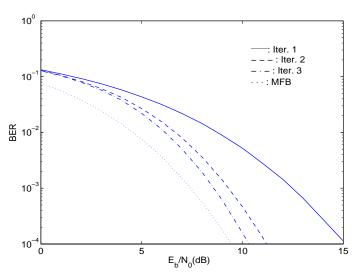


Fig. 4. DS-CDMA BER performance when $K=256\ (M=1)$ and P=256 users, with the same assigned power.

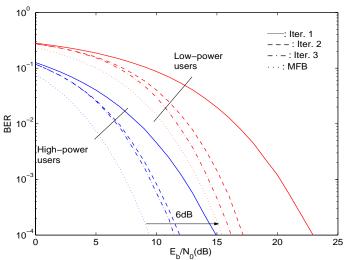


Fig. 6. DS-CDMA BER performance with K/2=128 low-power users and K/2=128 high-power users.

Let us consider now that the power assigned to K/2=128 users is 6dB below the power assigned to the other K/2=128 users. Clearly, the low-power users face strong interference levels. Figs. 5 and 6 present the BER performances, expressed as a function of the E_b/N_0 of high-power users, 6dB below the E_b/N_0 of low-power users). Once again, the iterative receiver allows significant performance improvements. From these figures, it is clear that gains associated to the performance of the iterative procedure are higher for low-power users and the BERs are closer to the MFB than for the high-power users. Moreover, the performances of high-power users are still a few dB from the MFB after three iterations, especially for the MC-CDMA schemes. This is explained as follows: the BER is much lower for high-power users, allowing an

almost perfect interference cancelation of their effects on low-power users; the higher BERs for the low-power users preclude an appropriate interference cancelation when we detect high-power users, where the BERs are expressed as a function of the E_b/N_0 of high-power users, 6dB below the E_b/N_0 of low-power users). Once again, the MC-CDMA schemes have worse performances than the DS-CDMA schemes.

V. Conclusions and Complementary Remarks

In this paper we considered the use of IB-DFE techniques for multicode CP-assisted DS-CDMA and MC-CDMA systems. The proposed receiver has excellent performance, that can be close to the MFB performance, especially for DS-CDMA schemes.

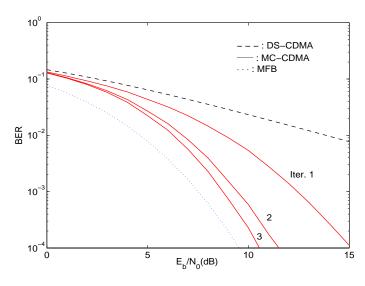


Fig. 7. BER performances for a Fourier spreading (with no scrambling), when N=K=256 and P=256 spreading codes, with the same assigned power.

It should be noted that the type of spreading adopted can have a significant impact on the performance of CP-assisted CDMA schemes. As an extreme example (see Fig. 7), for M=1, full code usage under equal power conditions and a Fourier spreading/despreading with no complementary scrambling, the MC-CDMA scheme considered in this paper is equivalent to a CP-assisted SC scheme [12], and our receiver reduces to the IB-DFE receiver described in [8], [10]. On the other hand, for M=1, full code usage under equal power conditions and a Fourier spreading/despreading with no complementary scrambling, the DS-CDMA scheme considered in this paper is equivalent to an OFDM scheme (then there is no advantage in using the IB-DFE receiver).

It should also be noted that, for K < N, the performances of MC-CDMA schemes are worse since just a fraction 1/M of the frequencies is used for the transmission of a given data symbol. This is not the case of DS-CDMA, where all frequencies can be used for transmitting each data symbol, regardless of the spreading factor. However, it should be noted that this does not mean necessarily an weakness of the MC-CDMA schemes with small spreading factors (small K). The comparison between DS-CDMA and MC-CDMA schemes should take into account other aspects, such as the envelope fluctuations of the transmitted signals and the impact of the channel coding (one might expect larger coding gains for MC-CDMA schemes, especially when a small K is combined with interburst interleaving).

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