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Three-Phase Unbalanced Load Flow Tool for **Distribution Networks**

Erhan Demirok, Søren B. Kjær, Dezso Sera, and Remus Teodorescu

Abstract—This work develops a three-phase unbalanced load flow tool tailored for radial distribution networks based on Matlab[®]. The tool can be used to assess steady-state voltage variations, thermal limits of grid components and power losses in radial MV-LV networks with photovoltaic (PV) generators where most of the systems are single phase. New ancillary service such as static reactive power support by PV inverters can be also merged together with the load flow solution tool and thus, the impact of the various reactive power control strategies on the steady-state grid operation can be simply investigated. Performance of the load flow solution tool in the sense of resulting bus voltage magnitudes is compared and validated with IEEE 13-bus test feeder.

Index Terms-Load flow, LV network, PV integration, voltage unbalance.

I. INTRODUCTION

Power quality are two are true power quality are two essential targets on the overall electrical power system to be continuously maintained within the most economical way of delivery. Assuming that power balance is guaranteed by the central power generators under varying power demand, network components will be exposed to certain amount of current and voltage stresses and will generate losses in the network. Load flow study here plays an important role as a tool to assess these stresses in the steady-state domain.

Minimum requirements of a load flow solution will vary depending on whether power system under study is transmission or distribution network. Exemplifying this, power flow in transmission networks is usually balanced and the network structures likely contain meshed or looped lines. Therefore, it is sufficient to represent transmission systems as single phase components. On the other hand, distribution networks typically accommodate single/two/three-phase loads and four-wire cables/lines so that load flow solutions shall handle unbalanced power flow with 3-phase modeling of network components. Since distribution networks mostly operate in radial structure, more straightforward and convergence guaranteed load flow solutions can be employed by taking advantage of this structure

In general, this work was inspired by MatDyn tool [1]-[2] which focuses on dynamical analysis of power systems, MATPOWER [3] and PSAT [4] that were developed to run balanced load flow/optimal load flow on single-phase equivalent circuits especially for transmission networks. However, in reality, rooftop PV installations have usually widespread usage in residential areas through single-phase connections. Therefore, regarding of more realistic network analysis, a 3-phase load flow script can allow more precise estimation of PV hosting capacity in unbalanced cases.

Most of the load flow solution methods proposed so far in the literature can be generalized in three groups:

- Gauss-like methods [5]-[8],
- Newton-Raphson (NR) based methods [9]-[14], and
- Backward-forward sweep (BFS) based methods [15]-[17].

Concerning precise estimation of PV hosting capacity of distribution networks, primary requirements can be summarized as

- 1. All class of loads including 2-phase, single-phase and constant impedance, constant power, constant current types or their combination shall be represented in a load flow study for more realistic investigation,
- 2. Load flow solver shall take into account of asymmetric layouts and mutual coupling situation of lines, cables and transformers,
- 3. Time series simulation is necessary for daily, monthly and yearly assessments. Therefore, the solver shall be fast enough to run load flow for one year period with certain time steps, e.g. every 15 minutes.
- 4. Any user-defined functions and ancillary services of PV inverters should be integrated into the load flow solver without need of another program,
- 5. Load flow solver should be flexible to enable statistical study on the resulting data.

Although the first three requirements can be provided by some commercial power system simulation packages (PowerFactory, NetBas, CYMDIST, etc.), a MATLAB[®] based unbalanced load flow solver has been developed in this work based on the various state of the art publications from the literature and thus, it can be used for the assessment of maximum PV hosting capacity of distribution networks.

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A generalized functional block diagram is depicted in Fig. 1. In the following sections, the load flow solution concept is given firstly and a bus and branch numbering technique based on breadth-first search algorithm is described. Next, power systems modeling of some components (lines/cables, transformers, shunt capacitors, loads) are briefly provided as 3-phase representations. Load flow solver developed in Matlab[®] is validated on IEEE 13-bus test networks.

II. LOAD FLOW SOLUTION CONCEPT

BFS method is selected here as the most suitable solver due to its simplicity and better convergence performance compared to Gauss-like and NR based methods under the assumption of radial network structures. The main advantage of BFS algorithm is the straightforward implementation of Kirchoff's current and voltage laws on the feeders. In this way, branch currents and bus voltages are updated by traversing between the root (source or slack) bus and end buses in iterative way (Fig. 2). Specifying initial bus voltages and nominal ratings of shunt components (loads, capacitor banks, and generators), backward sweep updates branch currents by summation of child branches and shunt currents from end nodes toward the root node. Similarly, starting from the specified root bus voltage and knowing the branch currents from the previous backward sweep, bus voltages are updated from the root node towards end nodes by means of voltage drops along the branches. Thus single iteration of backward-forward sweep is completed. If line charging capacitances are neglected, two general equations as referred to Fig. 2 for backward and forward sweep can be written as (1)-(2) respectively.

$$J_{i}^{ibus} = J_{i}^{fbus} = \underbrace{I_{i}}_{shunt} + \underbrace{\sum J_{subbranch}^{fbus}}_{sub-branch}$$
(1)
$$V_{i} = V_{i-1} - J_{i}^{fbus} \cdot Z_{branch}$$
(2)

It should be noticed that BFS procedure must follow the branch current and bus voltage updates in a proper sequence. Therefore, bus and branch ordering will play important role to obtain a correct network solution.

III. BUS AND BRANCH NUMBERING TECHNIQUE

BFS concept as shown above briefly converges to a unique and accurate solution as long as bus-branch connection structure for the whole network is provided. Given (m-1) branches will form an *m*-bus network. Each branch located between two predefined buses ("from bus" and "to bus") is the only relevant input data to the load flow solver. The aim is to assign unique numbers to buses in such a way that backward and forward sweeps can follow the sequential branches systematically between the root bus and end buses. Breadth-first and depth-first search methods are the simplest graph algorithms to achieve this goal [19] and they are both classified as uninformed search methods. As distinct from depth-first method, breadth-first search method explores all the nodes reachable from the root node in a graph. Accordingly, it is applicable on mesh structured distribution networks because of producing inherently "breadth-first tree" form from the looped graphs.

Bus and branch numbering can be integrated into the breadth-first search algorithm. As proposed in [18], a threeindex scheme that assigns branch level (l), branch index (m) and bus index (n) to each bus is systematically introduced in (l,m,n) order. The main idea behind the bus numbering technique is that buses located on the same feeder will be identified with the same branch level l, and the buses on the other child sub-branches will have level of l+1. If multiple sub-branches within the same level exist in the network, then buses on the same level of sub-branches can be uniquely identified by lateral index m. Lastly, the third index of n refers to the n^{th} bus on the branch (l,m). An example of bus numbering is illustrated in Fig. 3.

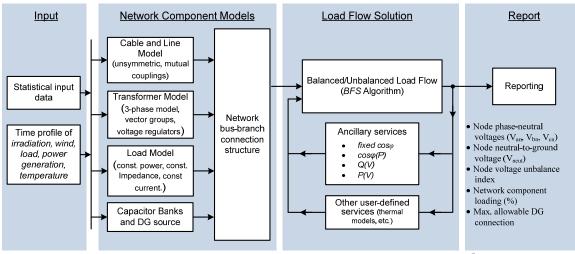


Fig. 1. Generalized functional block diagram of the load flow method developed in Matlab®

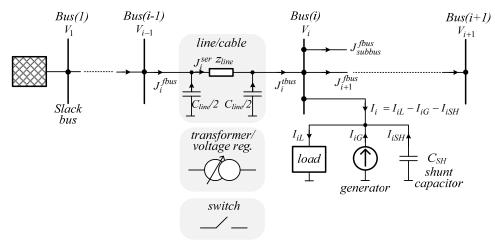


Fig. 2. Two-bus and single-line representation of distribution networks for the solution of BFS algorithm

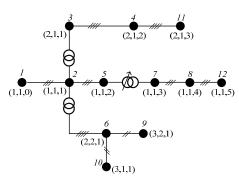


Fig. 3. An example of bus numbering for a radial structure

IV. COMPONENT MODELING

A. 3-Phase Distribution Line/Cable Model

Unsymmetrical spacing between phase conductors without transposition is the most common property of distribution lines. It is likely to see unequal voltage drops among the phases, even though power flow is balanced along a distribution line. For this reason, unbalanced threephase load flow solution will require accurate modeling of distribution lines for the steady-state voltage analysis. Additionally, in reality, loads and distributed generators are usually connected between line and neutral terminals for 3phase 4-wire systems. However, most of the distribution network analysis disregards the phase to neutral voltages so that voltage unbalance is miscalculated. Therefore, neutralto-earth voltage (NEV) at each node can be estimated here for multi-grounded neutral system. In case of unbalanced load flow condition, the return current will be divided into neutral and earth circuit. Share amount of return currents among neutral and earth circuit mainly depends on the neutral conductor and grounding electrode impedances. On account of this, unlike Kersting's phase impedance matrix [20], neutral conductors can be explicitly represented here in the series impedance of lines/cables. Since the equivalent shunt impedance of lines/cables (e.g. line charging capacitor) has negligible impact on voltage drop in distribution networks, only series impedances will be considered here.

Carson's line equations have been used to generate a series impedance matrix for the given line/cable configuration in this work (Appendix). Fig. 4 depicts typical 3-phase 4-wire overhead line and its equivalent circuit. Voltage drop along a line based on the primitive impedance matrix (A.7) as provided in Appendix can be calculated as:

$$\begin{bmatrix} V_{a}(i) \\ V_{b}(i) \\ V_{c}(i) \\ V_{n}(i) \end{bmatrix} = \begin{bmatrix} V_{a}(i-1) \\ V_{b}(i-1) \\ V_{c}(i-1) \\ V_{n}(i-1) \end{bmatrix} - \begin{bmatrix} z_{aa} & z_{ab} & z_{ac} & z_{an} \\ z_{ba} & z_{bb} & z_{bc} & z_{bn} \\ z_{ca} & z_{cb} & z_{cc} & z_{cn} \\ z_{na} & z_{nb} & z_{nc} & z_{nn} \end{bmatrix} \cdot \begin{bmatrix} J_{a}(i) \\ J_{b}(i) \\ J_{c}(i) \\ -J_{n}(i) \end{bmatrix}$$
(3)

where

 $V_{a,b,c,n}(i)$ are the phase and neutral voltages of the i^{th} node as referenced to their own local earth,

 $J_{a,b,c,n}(i)$ are the incoming branch phase and neutral currents of the *i*th node.

In order to solve voltage drop in (3), phase and neutral

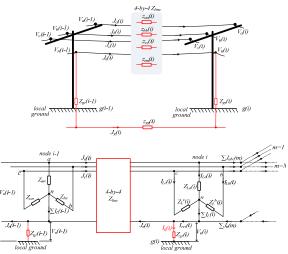


Fig. 4. Typical 3-phase 4-wire overhead line segment (upper); and its generalized circuit representation (bottom)

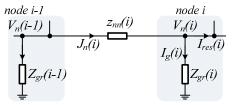


Fig. 5. Closer look on the neutral and grounding circuit. Current directions are matched with Carson's circuit

branch currents have to be determined accurately during backward sweep. For the computation of neutral branch current J_n , three constraints are introduced as in (4)-(6) and as referred to Fig. 5 [22].

$$I_g(i) = V_n(i) / Z_{gr}(i) \tag{4}$$

$$J_n(i) = I_g(i) + I_{res}(i)$$
⁽⁵⁾

and the last constraint is defined by the last row of (3):

$$V_{n}(i-1) - V_{n}(i) = \begin{bmatrix} z_{an} & z_{bn} & z_{cn} \end{bmatrix} \cdot \begin{bmatrix} J_{a} \\ J_{b} \\ J_{c} \end{bmatrix} + z_{nn} \cdot J_{n}$$

$$= \begin{bmatrix} z_{in} \end{bmatrix} \begin{bmatrix} J_{abc} \end{bmatrix} + z_{nn} \cdot J_{n}$$
(6)

By combining (4)-(6), the branch neutral current is provided in terms of neutral voltage of parent node, grounding impedance of local node, incoming branch phase currents, and residual current that is calculated by summation of all sub-branches:

$$J_{n}(i) = \frac{V_{n}(i-1) - [z_{in}(i)] \cdot [J_{abc}(i)] + I_{res}(i) \cdot Z_{gr}(i)}{Z_{gr}(i) + z_{nn}(i)}$$
(7)

where I_{res} is the residual current and computed as:

$$I_{res}(i) = \sum_{k=\{a,b,c\}} I_{L,k}(i) + \sum_{k=\{a,b,c\}} I_{sh,k}(i) + \sum_{m\in M} J_n(m)$$
(8)

M is the set of sub-branches that branch off the i^{th} node, $I_L(i)$ and $I_{sh}(i)$ denote load and shunt capacitor currents in respectively; absorbed by the i^{th} node.

Phase currents along the branch are determined as given in (9) during backward sweep and finally, the required branch currents will be completed to compute voltage drops in (3).

$$\begin{bmatrix} J_{a}(i) \\ J_{b}(i) \\ J_{c}(i) \end{bmatrix} = \begin{bmatrix} I_{L,a}(i) + I_{sh,a}(i) \\ I_{L,b}(i) + I_{sh,b}(i) \\ I_{L,c}(i) + I_{sh,c}(i) \end{bmatrix} - \begin{bmatrix} \left(\frac{S_{gen,a}}{V_{a}(i) - V_{n}(i)}\right)^{*} \\ \left(\frac{S_{gen,b}}{V_{b}(i) - V_{n}(i)}\right)^{*} \\ \left(\frac{S_{gen,c}}{V_{c}(i) - V_{n}(i)}\right)^{*} \end{bmatrix} + \begin{bmatrix} \sum_{m \in M} J_{a}(m) \\ \sum_{m \in M} J_{b}(m) \\ \sum_{m \in M} J_{c}(m) \end{bmatrix}$$
(9)

 $S_{gen}(i)$ is the apparent power of constant PQ generator connected at i^{th} node.

B. Load Model

Compared to transmission networks, various balanced and unbalanced load types exist in distribution networks according to number of phases (1- or 3-phase) and connection types (delta or star). Moreover, in the sense of electricity consumption characteristics, constant power, constant current, constant admittance or any combination must be performed for the realistic load models. If a measured load profile is available with certain time intervals, then the class of constant power may be preferred selection among the other load characteristics. For some cases, only load density along a line is specified in terms of kVA/km and usually assumed to be uniformly distributed for the simplicity. In accordance with this, loads can be further classified as spot and uniformly distributed loads. Fig. 6 shows wye- and delta-connected spot loads while Table I summarizes model equations where I^k , V^k represent load phase currents and voltages at kth iteration, respectively. From the specified rated power (S_L) and rated voltage levels (V_{rated}) of loads, the nominal current (I_{nom}) and nominal admittance (y^{nom}) values per phase are determined to be used in Table I:

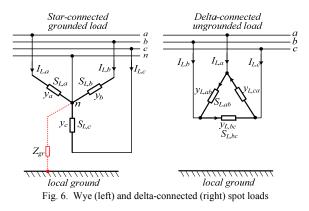
$$I_{L,a}^{nom} = \left(\frac{S_{L,a}}{V_a^{rated}}\right)^*, \quad y_{L,a}^{nom} = \frac{\left(S_{L,a}\right)^*}{\left|V_a^{rated}\right|^2} \to \text{for } Y_g$$

$$I_{L,ab}^{nom} = \left(\frac{S_{L,ab}}{V_a^{rated} - V_b^{rated}}\right)^*, \quad y_{L,ab}^{nom} = \frac{\left(S_{L,ab}\right)^*}{\left|V_a^{rated} - V_b^{rated}\right|^2} \to \text{for } \Delta$$
(10)

Matrix *T* used in Table I provides transformation of the phase currents into the line currents which are eventually necessary to compute branch currents.

$$T = \begin{bmatrix} 1 & 0 & -1 \\ -1 & 1 & 0 \\ 0 & -1 & 1 \end{bmatrix}$$

Without loss of generality, generator models considered in this work will be equivalent to negative constant power loads. Since most of the generators connected to MV and LV distribution networks operate in constant power mode.

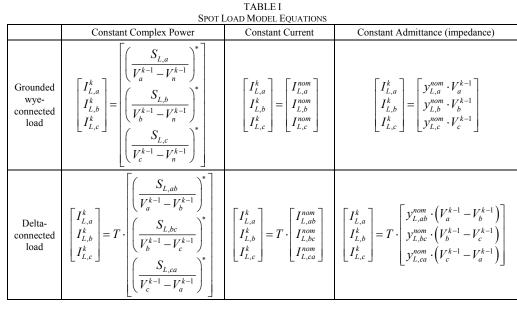


C. Shunt Capacitor Model

Shunt capacitors are located on the distribution networks in order to help regulating voltage levels and compensate reactive power demand. From the modeling point of view, their implementation in load flow simulation is similar to the constant admittance load model.

D. 3-Phase Two-Winding Transformer Model

Regarding of European distribution networks, 20-kV or 10-kV MV distribution networks are commonly engaged to 60-kV or 110-kV sub-transmission networks through the substations that usually contain delta-delta (*D-D*) connection type of transformers with on-load tap changers.



On the other hand, 400-V 3-phase 4-wire secondary distribution feeders branch off MV feeders along delta - grounded wye (D-Yg) transformers with off-load tap changers.

Modeling of 3-phase distribution transformers in phase coordinates requires more attention due to the possibility of having ill-conditioned matrices for certain type of transformers. Basically, two modeling approaches exist in the literature. Direct approach is based on application of Kirchoff's voltage and current loops for both primary and secondary circuit of the transformer [23]. This transformer modeling approach is developed only for BFS load flow algorithm. The other method generates nodal admittance matrices that represent 3-phase transformer configurations [24]-[27]. The operability of nodal admittance matrix-based modeling approach is more extensive and it is able to support any load flow algorithm. However, matrix singularity problem of some transformer configurations leads to limited implementation of nodal admittance matrix approach.

In this work, nodal admittance matrix-based transformer modeling tailored for *BFS* load flow algorithm has been developed. Matrix singularity problem is stressed and then fixed by means of introducing additional constraints on the node admittance matrix of transformer. Although modeling of D-Yg type step-down transformer will be presented here, the same modeling methodology can be further applied to the other connection types of two-winding transformers.

It will be assumed that the secondary side line-to-line voltages are lagging the primary side line-to-line voltages by 30° which represents the prevalent class of transformers (*D*-*Yg1*) in Europe (Fig. 7). In this respect, secondary side terminal currents will be also lagging the primary side terminal currents by 30° . As another assumption, magnetizing impedance of the transformer is sufficiently high to be neglected as compared to leakage impedance. Furthermore, each primary-secondary phase windings are assumed to be formed by separate and identical single-phase

transformers (this is why it is also called as 3-phase transformer bank). This makes cross couplings between the primary and secondary windings zero and simplifies mathematical modeling of the transformers with tolerable errors. Additionally, mutual admittance (m) between each phase will be assumed to equal to winding leakage admittances (y=m).

As a first step to computation of node admittance matrix, primitive admittance matrix (Y_{prim}) that represents relationship between phase voltages and currents at primary and secondary circuits is formed. Then, connection matrices (C and D) transforms the phase quantities into the node quantities, thus, the resulted node voltages and currents can be directly used in load flow solution.

$$\underbrace{C^{T} \cdot I_{ph}}_{I_{node}} = \underbrace{D \cdot Y_{prim} \cdot C}_{Y_{node}} \cdot V_{node}$$
(11)

Each transformer configuration will have its own primitive admittance and connection matrices. For this reason, it will be necessary to store the resulting node admittance matrices for various transformer configurations

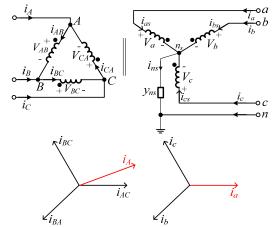


Fig. 7. D-Yg1 transformer bank connection and its positive-sequence current phasor diagram based on [23]

in load flow solution tool. The primitive impedance matrix Y_{prim} , the connection matrices (C, D) and the node admittance matrix Y_{node} are given in Appendix based on Fig. 7.

Concerning the *BFS* load flow algorithm, phase currents at primary side are computed in terms of secondary side currents and voltages during backward step. Similarly, secondary side terminal voltages are expressed in terms of the primary side voltages and currents during forward step. This is summarized in Table II based on (A.14).

However, it can be investigated from (A.14) that submatrices of \overline{Y}_{pp} and \overline{Y}_{ps} are not invertible and only two of the three equations in these sub-matrices are linearly independent. For this reason, additional constraints must be introduced in such a way that singularities of \overline{Y}_{pp}^{-1} and \overline{Y}_{ps}^{-1} are avoided.

The first constraint is introduced by non-existence of zero sequence voltage at delta side of the transformer so that the sum of primary side line-to-line voltages will be zero. The other constraint comes with a relationship between the currents and voltages at grounded-wye side of the transformer using (A.14). Thus, as referred to (A.14), if secondary currents are sum up,

$$\sum I_{s} = i_{a} + i_{b} + i_{c} = -y \underbrace{(V_{AB} + V_{BC} + V_{CA})}_{-y (V_{ag} + V_{bg} + V_{cg})}$$
(12)

then the forward sweep equation in Table II is updated by imposing (12) into the its third row as:

$$\begin{bmatrix} V_{ag} \\ V_{bg} \\ V_{cg} \end{bmatrix} = \begin{bmatrix} -\overline{\Sigma} \overline{i_s} \\ V_{p} \end{bmatrix} - \begin{bmatrix} 0 & 0 & 0 \\ \overline{Y}_{pp} \end{bmatrix} \cdot V_p$$
(13)

and new updated \overline{Y}_{ps} becomes invertible matrix at all. On the other hand, regarding of the backward step; \overline{Y}_{sp} was already invertible so there is no need of introducing constraints for the backward sweep.

TABLE II TRANSFORMER MODEL VALID FOR ALL CONFIGURATIONS: CURRENT

Backward step	Forward step
Step 1. $V_{p} = \left(\overline{Y}_{sp}\right)^{-1} \cdot \left(I_{s} - \overline{Y}_{ss}V_{s}\right)$	Step 1. $V_{s} = \left(\overline{Y}_{ps}\right)^{-1} \cdot \left(I_{p} - \overline{Y}_{pp}V_{p}\right)$
Step 2.	's (*ps) (*p *pp'p)
$I_p = \overline{Y}_{pp}\overline{V}_p + \overline{Y}_{ps}\overline{V}_s$	

E. Computation of Network Active Power Losses

Regardless of what computation method is used, total branch losses of distribution networks can be calculated once bus voltages and branch currents all over the network are obtained accurately after running a load flow simulation. Although branch impedance can be used to estimate active power losses by means of I^2R , this method gives rise to

inaccurate solution due to phase mutual couplings in 3phase systems. In general, loss dissipated along a branch is equivalent to the difference between branch entering and branch outgoing power.

$$S_{loss,k} = S_{fbus,k} - S_{tbus,k}$$

$$= \begin{bmatrix} V^{a}_{fbus,k} \cdot (I^{a}_{fbus,k})^{*} \\ V^{b}_{fbus,k} \cdot (I^{b}_{fbus,k})^{*} \\ V^{c}_{fbus,k} \cdot (I^{c}_{fbus,k})^{*} \end{bmatrix} - \begin{bmatrix} V^{a}_{tbus,k} \cdot (I^{a}_{tbus,k})^{*} \\ V^{b}_{tbus,k} \cdot (I^{b}_{tbus,k})^{*} \\ V^{c}_{tbus,k} \cdot (I^{c}_{fbus,k})^{*} \end{bmatrix}$$

$$(14)$$

where $S_{fbus,k}$ denotes apparent power entering into the k^{th} branch and $S_{tbus,k}$ refers to the apparent power leaving the k^{th} branch.

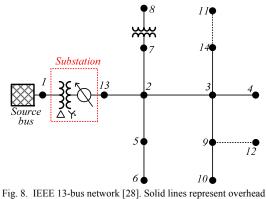
V. VALIDATION OF THE DEVELOPED LOAD FLOW TOOL

IEEE 13-bus test network is used to examine modeling performance of various components such as cables and lines with diverse configurations, all kind of loads, shunt capacitors, voltage regulator and transformers. Fig 8 illustrates the 13-bus test network. Minor modification has been applied on the original test network as following:

- 1)Delta-connected, constant power (Y-PQ) uniformly distributed load situated between nodes 2 and 3 has been removed from the original network. Since most of the distributed load had been connected to *phase c*, new result will overestimate the *phase c* voltage.
- 2)In the original test network, a switch and an additional node exist between node 3 and 11. Since load flow results published by IEEE consider only "on" state of the switch, it doesn't make sense to include it in this work.

Modeling data can be found in [28]. The on load tap changer (OLTC) is positioned at [10 8 11] for phase a, b, c respectively in such a way that minimum voltage level in the network becomes higher than 0.95 p.u. Fig. 9 shows the 3phase line-to-ground voltage profile of the network provided by the developed load flow tool. However, in this case, the nearest nodes to OLTC will experience severe voltage boosting. Therefore, increasing tap positions further levels cannot be accepted although maximum tap position of the OLTC is 16 per phase. Therefore, connecting shunt capacitors at far end nodes can help mitigating this problem for long distribution feeders. The results obtained by the developed load flow simulation have been compared with the results from Radial Distribution Analysis Package (RDAP) [29] as shown in Fig. 9. Phase c voltages from both results are overestimated due to removal of the uniformly distributed load in the modified network. Overall voltage mismatches take place in the acceptable level of 0.1-2 % as referenced to IEEE and RDAP simulation results.

Fig. 10 illustrates comparison of resulting node voltages as line-to-ground and line-to-neutral together. Considering line-to-neutral voltages, lightly loaded phase b moves to emergency condition and relevant tap setting of OLTC or the rating of shunt capacitors connected to phase b must be re-adjusted to prevent this overvoltage situation. Another point is the effect of grounding impedance on the neutral voltage. As depicted in Fig. 11, neutral voltages do not vary



(1g. 8. IEEE 13-bus network [28]. Solid lines represent overhead lines; dashed lines refer to underground cables

substantially as grounding impedance is increased to 100 ohms. It is obvious that slightly shifting up of neutral voltages is observed due to the fact that higher grounding impedance forces more return currents to flow through the neutral conductor.

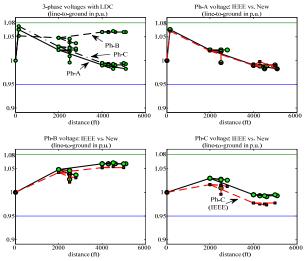


Fig. 9. Line-to-ground voltage profile of IEEE 13-bus network with OLTC

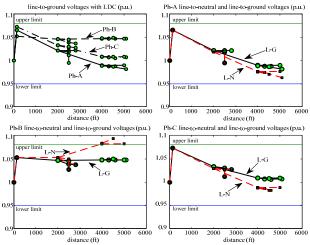
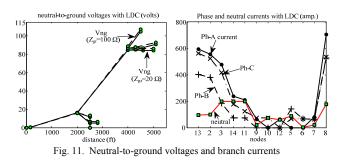


Fig. 10. Line-to-ground and line-to-neutral voltages



VI. CONCLUSION

Since the load flow solution methods are well documented in the literature, one of the attentions here is to investigate and establish appropriate technics special for the distribution networks. Owing to Matlab[®]'s powerful computational performance and ready-to-use functions, the tool becomes flexible by allowing users to develop their own special model functions (thermal model of components, ancillary services, detailed generator models, etc.) and any special network analysis (statistical load flow solution, yearly energy and loss analysis, etc.). The tool will be practiced on a realistic LV network in order to estimate its maximum PV hosting capacity with different unbalanced scenarios.

New load flow tool has been successfully implemented on 3-phase 4-wire circuits. Thus, besides on phase voltage unbalance, neutral-to-ground voltages and neutral currents can also be assessed for multi-grounded neutral systems.

VII. APPENDIX

A. Carson's Equations for Line Modeling

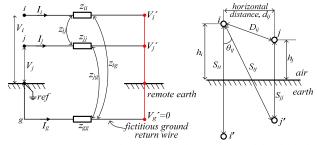


Fig. A. Carson's two-conductor circuit with ground return (left); physical layout of two conductors with ground return (right)

Fig. A illustrates Carson's line circuit and two-conductor line spacing layout with their image conductors. For unbalanced power flow situation, it is obvious that current injections will return to the source through the earth (fictitious ground wire). When receiving end nodes are shorted at the remote earth together, self- and mutualimpedances of conductors can be determined by knowing sending node voltages (V_i, V_j) and currents flowing along the conductors (I_i, I_j).

$$\begin{bmatrix} \Delta V_i \\ \Delta V_j \\ \Delta V_g \end{bmatrix} = \begin{bmatrix} V_i - V_i' \\ V_j - V_j' \\ V_g - V_g' \end{bmatrix} = \begin{bmatrix} z_{ii} & z_{ij} & z_{ig} \\ z_{ij} & z_{jj} & z_{jg} \\ z_{ig} & z_{jg} & z_{gg} \end{bmatrix} \cdot \begin{bmatrix} I_i \\ I_j \\ I_g \end{bmatrix}$$
(A.1)

where $V'_i = V'_j = V'_g$, $V_g = 0$ and $I_i + I_j + I_g = 0$. Thus reduced voltage equation for the *i*th conductor becomes:

$$V_{i} = \left(z_{ii} - 2 \cdot z_{ig} + z_{gg}\right) \cdot I_{i} + \left(z_{ij} - z_{ig} + z_{gg} - z_{gj}\right) \cdot I_{j}$$

$$= \overline{z}_{ii} \cdot I_{i} + \overline{z}_{ij} \cdot I_{j}$$
(A.2)

If simplified Carson's equations are used based on [20]-[21], then self- and mutual impedance of conductor including ground effects thus becomes:

$$\overline{z}_{ii} = r_i + \pi^2 fG$$

$$+j4\pi fG \cdot \left(\ln \left(S_{ii} / GMR_i \right) + 5.7974 + \ln \sqrt{\rho/f} / h_i \right)$$

$$\overline{z}_{ij} = \pi^2 fG$$

$$+j4\pi f \cdot G \cdot \left(\ln \left(S_{ij} / D_{ij} \right) + 6.4906 + \ln \sqrt{\rho/f} / S_{ij} \right)$$
(A.3)

where

 \overline{z}_{ii} is the self-impedance of i^{th} conductor in Ω/km or Ω/mile , \overline{z}_{ij} is the mutual-impedance between i^{th} and j^{th} conductor in Ω/km or Ω/mile ,

 r_i is the resistance of i^{th} conductor in Ω /km or Ω /mile,

f is the system frequency in Hz,

 h_i is the height of i^{th} conductor in meter $(S_{ii}=2h_i)$,

 GMR_i is the geometric mean radius of i^{th} conductor in meter, ρ is the resistivity of earth in Ω -meters,

 S_{ij} is the distance between conductor *i* and conductor *j*' (image of conductor *j*) in meter,

 D_{ij} is the distance between conductor *i* and conductor *j* in meter

 $G=10^{-4}$ in Ω/km or $0.1609347 \cdot 10^{-3}$ in Ω/mile and

$$P_{ij} \cong \pi/8$$

$$Q_{ij} \cong -0.0386 + \frac{1}{2} \left(\ln \left[\frac{2}{2} \cdot (2.8099 \cdot 10^{-3}) \right] + \ln \sqrt{\rho/f} / h_i \right)$$

$$r_{ij} = 2.8099 \cdot 10^{-3} \cdot S_{ij} \cdot \sqrt{f/\rho}$$
(A.6)

After this point, the primitive series impedance matrix can be formed. It should be noticed that ground-related terms are already merged into the self- and mutualimpedances of phase and neutral conductors if Carson's equations are directly implemented [20]. Accordingly, the primitive series impedance matrix is given as:

$$z_{prim} = \begin{bmatrix} \overline{z}_{aa} & \overline{z}_{ab} & \overline{z}_{ac} & \overline{z}_{an} \\ \overline{z}_{ba} & \overline{z}_{bb} & \overline{z}_{bc} & \overline{z}_{bn} \\ \overline{z}_{ca} & \overline{z}_{cb} & \overline{z}_{cc} & \overline{z}_{cn} \\ \overline{z}_{na} & \overline{z}_{nb} & \overline{z}_{nc} & \overline{z}_{nn} \end{bmatrix}$$
(A.7)

B. Admittance Matrices for D-Yg1 Transformer

Based on Fig. 7, the primitive admittance matrix is written as following:

$$\begin{bmatrix} i_{CA} \\ i_{AB} \\ i_{BC} \\ i_{as} \\ i_{bs} \\ i_{ns} \end{bmatrix} = \begin{bmatrix} y & -m & 0 \\ y & -m & 0 \\ -m & y & -m & 0 \\ -m & y & 0 \\ -m & y & 0 \\ 0 & 0 & 0 & 0 & 0 \\ y_{prim} \end{bmatrix} \begin{bmatrix} V_{CA} \\ V_{AB} \\ V_{BC} \\ V_{an} \\ V_{bn} \\ V_{bn} \\ V_{cn} \\ V_{ns} \end{bmatrix}$$
(A.8)

where subscripts in big letters denote the primary side phase quantities and subscripts in small letters represent the quantities in secondary circuit. v_{ns} refers neutral grounding admittance. It should be noticed that ungrounded side terminal voltages are represented in line-to-line whereas grounded side terminal voltages are given in terms of line-to-ground voltages. The connection matrices *C* and *D* are:

$$\begin{bmatrix} V_{CA} \\ V_{AB} \\ V_{BC} \\ V_{an} \\ V_{bn} \\ V_{bn} \\ V_{cn} \\ V_{ng} \end{bmatrix} = \begin{bmatrix} 0 & 0 & 1 & & & & 0 \\ 0 & 1 & 0 & & & 0 & 0 \\ 0 & 1 & 0 & & & 0 & 0 \\ 0 & 1 & 0 & & & 0 & 0 \\ 0 & -1 & 1 & 1 & & \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix} \begin{bmatrix} V_{AB} \\ V_{BC} \\ V_{CA} \\ V_{ag} \\ V_{bg} \\ V_{cg} \\ V_{cg} \\ V_{ng} \end{bmatrix}$$

$$\begin{bmatrix} i_{A} \\ i_{B} \\ i_{C} \\ i_{a} \\ i_{b} \\ i_{b} \\ i_{c} \\ i_{n} \end{bmatrix} = \begin{bmatrix} -1 & 1 & 0 & & & 0 \\ 0 & -1 & 1 & 0 & & 0 \\ 0 & -1 & 1 & 0 & & 0 \\ 1 & 0 & -1 & & & 0 \\ 0 & & 1 & 0 & & 0 \\ 0 & & -1 & -1 & -1 & 1 \end{bmatrix} \begin{bmatrix} i_{CA} \\ i_{AB} \\ i_{BC} \\ i_{as} \\ i_{bs} \\ i_{cs} \\ i_{ns} \end{bmatrix}$$
(A.10)

and, 7-by-7 node admittance matrix becomes: $\begin{bmatrix} v & 0 & -v \\ -v & v & 0 \end{bmatrix} = \begin{bmatrix} 0 & -v \\ 0 & -v \end{bmatrix}$

$$Y_{node} = \begin{bmatrix} y & 0 & y & y & y & 0 & 0 \\ -y & y & 0 & 0 & -y & y & 0 \\ 0 & -y & y & y & 0 & -y & 0 \\ 0 & 0 & -y & -y & y & y \\ -y & 0 & 0 & -y & y \\ 0 & -y & 0 & -y & y \\ y & y & y & y & y & y & y_{ns} - 3y \end{bmatrix}$$
(A.11)
$$= \begin{bmatrix} \frac{Y_{pp}}{Y_{ps}} & \frac{Y_{ps}}{Y_{ss}} & \frac{Y_{pn}}{Y_{np}} \\ \frac{Y_{sp}}{Y_{ns}} & \frac{Y_{ss}}{Y_{sn}} & \frac{Y_{nn}}{Y_{nn}} \end{bmatrix}$$

Further reduction can be applied on (A.11) since current entering into the internal neutral node (ns) is only supplied by the internal sum of phase currents and there is no external current injection to the internal neutral node. Therefore, the last row of Y_{node} can be rewritten as:

$$\begin{bmatrix} \vdots \\ i_n = 0 \end{bmatrix} = \begin{bmatrix} \vdots \\ Y_{np} \\ Y_{ns} \\ Y_{ns} \\ Y_{ns} \end{bmatrix} \begin{bmatrix} V_p \\ V_s \\ V_n \end{bmatrix}, V_n = -Y_{nn}^{-1} \left(Y_{np} V_p - Y_{ns} V_s \right) (A.12)$$

 V_p denotes $\begin{bmatrix} V_{AB} & V_{BC} & V_{CA} \end{bmatrix}^T$ and V_s is $\begin{bmatrix} V_{ag} & V_{bg} & V_{cg} \end{bmatrix}^T$. For example, when (A.12) is applied to the primary side current (top row of Y_{node}):

$$I_{p} = Y_{pp}V_{p} + Y_{ps}V_{s} + Y_{pn}V_{n} = \underbrace{\left(Y_{pp} - Y_{pn}Y_{nn}^{-1}Y_{np}\right)}_{Y_{11}} \cdot V_{p} + \underbrace{\left(Y_{ps} - Y_{pn}Y_{nn}^{-1}Y_{ns}\right)}_{Y_{12}} \cdot V_{s}$$
(A.13)

And if the same is repeated for the secondary currents, then generalized 6-by-6 reduced node admittance matrix can be obtained as

$$Y_{node} = \begin{bmatrix} \frac{Y_{11} | Y_{12}}{Y_{21} | Y_{22}} \end{bmatrix} = \begin{bmatrix} \frac{Y_{pp} - Y_{pn}Y_{nn}^{-1}Y_{np}}{Y_{sp} - Y_{sn}Y_{nn}^{-1}Y_{ns}} \end{bmatrix} \begin{bmatrix} y & 0 & -y & -y & y & 0 \\ -y & y & 0 & 0 & -y & y \\ 0 & -y & y & y & 0 & 0 & -y & y \\ 0 & -y & y & y & 0 & -y \\ \frac{0 & -y & y & y & 0 & -y \\ m & m & n & n & m \\ n & m & m & m & n & m \\ m & n & m & m & m & n \end{bmatrix}$$

where $m=y^2/(3y-y_{ns})$ and $n=y^2/(3y-y_{ns})-y$. For solidly grounded D-Yg1 transformer $(y_{ns} \rightarrow \infty)$, the node admittance matrix becomes:

$$\lim_{y_{m}\to\infty}Y_{node} = \begin{bmatrix} y & 0 & -y & -y & y & 0\\ -y & y & 0 & 0 & -y & y\\ 0 & -y & y & y & 0 & -y\\ \hline 0 & 0 & -y & -y & -y\\ -y & 0 & 0 & -y\\ 0 & -y & 0 & -y \end{bmatrix} = \begin{bmatrix} \overline{Y}_{pp} & \overline{Y}_{ps}\\ \overline{\overline{Y}_{sp}} & \overline{\overline{Y}_{ss}} \end{bmatrix}$$
(A.14)

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