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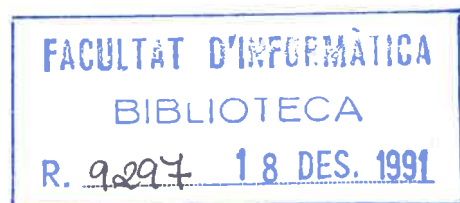
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The power of the middle bit

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The Power of the Middle Bit

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Extended Abstract

Abstract

We study the class of languages that can be recognized in polynomial time with the additional information of one bit from a $\#P$ function. In particular we show that every MOD_k class and every class contained in PH are low for this class. We translate these results to the area of circuit complexity using MidBit (middle bit) gates. A MidBit gate over w inputs x_1, \dots, x_w is a gate which outputs the value of the $\lfloor \log(w)/2 \rfloor^{\text{th}}$ bit in the binary representation of the number $\sum_{i=1}^w x_i$. We show that every language in ACC can be computed by a family of depth-2 deterministic circuits of size $2^{(\log n)^c}$ with a MidBit gate at the root and AND-gates of fan-in $(\log n)^c$ at the leaves. This result improves the known upper bounds for the class ACC .

1 Introduction

The complexity classes PP (probabilistic polynomial time [Gi 77]) and $\oplus P$ (parity P , [PaZa 83, GoPa 86]) have received much attention since the well known result by Toda [Tod 89] proving that the polynomial time hierarchy (PH) is Turing reducible to PP . These classes are closely related to the class of counting functions $\#P$ [Va 79] that count the number of accepting paths on nondeterministic Turing machines. Observe that sets in PP and $\oplus P$ can be respectively decided with the information of the leftmost and rightmost bit of a $\#P$ function. Toda's proof combines two

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important results; on one side he shows that PH is randomly reducible to $\oplus P$, and in a second part he proves that $PP^{\oplus P}$ is included in $P^{\#P}$. A careful observation of the proof of the last result shows that for this inclusion the whole power of $P^{\#P}$ is not needed. To decide an input x , a function $f \in \#P$ has to be queried just once, and more interestingly, just one bit of information of f is needed, as in the case of PP or $\oplus P$. It is natural to ask what other problems can be computed by looking at just one bit of a $\#P$ function. This question has been independently considered by Schwentick [Scw 91] and Regan [Re 91] who define the class MidBitP (middle bit P) of languages that can be computed with the information of just one bit from a $\#P$ function.¹

Definition 1.1 [Re 91, Scw 91] *A language L is in MidBitP if there exists a function f in $\#P$ and a function g in FP such that for all x , x is in L iff there is a 1 at position $g(x)$ in the binary representation of $f(x)$.*

At first sight it looks like the definition depends heavily on the base in which the representation of the $\#P$ function f is taken. It is clear that $\oplus P$ is in MidBitP since the parity of a $\#P$ function in base 2 can be obtained by looking at just one bit, but how about other modular classes? For example, in order to decide whether a number written in base 2 is congruent to 0 modulo 3, one needs the information of each one of its bits. By constructing suitable $\#P$ functions we prove however that for each k the class $MOD_k P$ [BeGiHe 90] is included in MidBitP. Moreover we show that for every k , $MOD_k P$ is low for MidBitP. Intuitively this means that a language in $MOD_k P$ does not give any additional help when used as an oracle in a MidBitP computation and therefore this is stronger than a containment result. We also show that the classes BPP and PH are low for MidBitP.

In section 3 we give an application of the previous results improving the known upper bound for the circuit class ACC. This class was defined by Barrington [Ba 89] as the class of languages accepted by bounded depth polynomial-size circuits with AND, OR, NOT and a finite set of MOD_k gates. Clearly ACC contains AC_0 and is contained in TC_0 . Since the PARITY function cannot be computed in AC_0 the first inclusion is proper; Barrington [Ba 89] conjectured that the second inclusion is also proper i.e., $TC_0 \not\subseteq ACC$, but no proof of this fact has been obtained.

Using Toda's result [Tod 89] and building on some work on AC_0 by Allender and Hertrampf [Al 89], [AlHe 90], Yao [Yao 90] proved the first non-trivial upper bound for ACC. He showed that every language in ACC is recognized by a family of depth-2 probabilistic circuits of size $2^{(\log n)^c}$ with a symmetric gate at the root and AND-gates of fan-in $(\log n)^c$ at the leaves. Recently Beigel and Tarui [BeTa 91] have improved this result showing that the circuits given by Yao can be made deterministic without increasing their size. However in both cases the symmetric gate at the root depends on the type of the modular gates used in the ACC circuit. It is therefore very hard

¹Independently, Regan [Re 91] and Schwentick [Scw 91] have also observed that Toda's proof implies that $PP^{\oplus P}$ and PP^{PH} are contained in MidBitP.

to prove that a certain function cannot be computed by depth-2 circuits of the type given in [Yao 90] or [BeTa 91] since all that can be said about the gates in the root is that they belong to an infinite subfamily of the symmetric functions. We improve the above upper bounds showing that the mentioned circuits can be restricted to have a symmetric gate of type MidBit at the root. A MidBit gate over w inputs x_1, \dots, x_w is a gate which outputs the value of the $\lfloor \log(w)/2 \rfloor^{\text{th}}$ bit in the binary representation of the number $\sum_{i=1}^w x_i$. We prove that ACC can be computed by a family of depth-2 deterministic circuits of size $2^{(\log n)^c}$ with a MidBit gate at the root and AND-gates of fan-in $(\log n)^c$ at the leaves. We believe that there are TC_0 languages which cannot be computed by circuits of this kind, and that the study of these circuits can therefore provide a way to show that TC_0 is not contained in ACC.

2 Lowness of Mod Classes for the Class MidBitP

The concept of lowness in the context of computational complexity theory was first introduced by Schöning [Sch 83] and was first studied in counting classes by Torán [Tor 88]. A class \mathcal{A} is low for a relativizable complexity class \mathcal{C} if the sets in \mathcal{A} , when used as an oracle for \mathcal{C} , do not help, i.e., $\mathcal{C}^{\mathcal{A}} = \mathcal{C}$. In this section we prove that for any k , Mod_kP is low for MidBitP.

Toda has shown that for every function f in $\#\text{P}^{\oplus\text{P}}$ and every polynomial p there is a function $g \in \#\text{P}$ such that $f(x)$ and $g(x)$ agree in the last $p(|x|)$ bits.

Theorem 2.1 [Tod 89] *For all functions f in $\#\text{P}^{\oplus\text{P}}$ and for every polynomial t there exists a function h in $\#\text{P}$ such that*

$$h(x) \equiv f(x) \pmod{2^{t(|x|)}}.$$

By this result, $\oplus\text{P}$ is low for $\oplus\text{P}$ (which is proved by Papadimitriou and Zachos [PaZa 83] using a different technique) and MidBitP.

Corollary 2.2 $\oplus\text{P}$ is low for MidBitP.

It will follow from the next theorem that for every function $f \in \#\text{P}^{\text{BPP}}$ and every polynomial p there is a function $g \in \#\text{P}$ such that $f(x)$ and $g(x)$ agree in the first $p(|x|)$ bits where the first bit of a binary number is the most significant bit which is 1.

Theorem 2.3 *For every function $f \in \#\text{P}^{\text{BPP}}$ there exist a polynomial t and a function $g \in \#\text{P}$ such that*

$$f(x) = \lfloor g(x)/2^{t(|x|)} \rfloor.$$

Proof. Let f be in $\#\text{P}^{\text{BPP}}$. Since BPP is closed under Turing reductions, there exists a language L in BPP and a polynomial q such that

$$f(x) = \sum_{y \in \Sigma^{q(|x|)}} \chi_L(\langle x, y \rangle).$$

Furthermore, by the probability amplification lemma for BPP, there exists a function $h \in \#P$ and a polynomial t such that

$$\begin{aligned}\langle x, y \rangle \in L &\Rightarrow h(\langle x, y \rangle) \geq 2^{t(|x|)} - 2^{t(|x|)-q(|x|)-2}, \\ \langle x, y \rangle \notin L &\Rightarrow h(\langle x, y \rangle) \leq 2^{t(|x|)-q(|x|)-2},\end{aligned}$$

and therefore h fulfills the following inequalities:

$$\chi_L(\langle x, y \rangle) 2^{t(|x|)} - 2^{t(|x|)-q(|x|)-2} \leq h(\langle x, y \rangle) \leq \chi_L(\langle x, y \rangle) 2^{t(|x|)} + 2^{t(|x|)-q(|x|)-2}.$$

Since $\#P$ is closed under addition, $h'(\langle x, y \rangle) = h(\langle x, y \rangle) + 2^{t(|x|)-q(|x|)-2}$ is also a $\#P$ function fulfilling the inequalities

$$\chi_L(\langle x, y \rangle) 2^{t(|x|)} \leq h'(\langle x, y \rangle) \leq \chi_L(\langle x, y \rangle) 2^{t(|x|)} + 2^{t(|x|)-q(|x|)-1}.$$

Now we can define the $\#P$ function

$$g(x) = \sum_{y \in \Sigma^{q(|x|)}} h'(\langle x, y \rangle),$$

and since

$$2^{q(n)} 2^{t(n)-q(n)-1} < 2^{t(n)},$$

it follows that

$$\lfloor g(x) / 2^{t(|x|)} \rfloor = f(x).$$

□

By this result, BPP is low for BPP [Za 82], PP, [KöScToTo 89], and MidBitP.

Corollary 2.4 *BPP is low for MidBitP.*

Since all the proofs in this section relativize we immediately obtain the lowness of $BPP^{\oplus P}$ and since PH is contained in $BPP^{\oplus P}$ [Tod 89], also of PH for MidBitP.

Corollary 2.5 *$BPP^{\oplus P}$ and therefore PH are low for MidBitP.*

Using relativized versions of Theorem 2.3 and Theorem 2.1, we can easily prove the following theorem which states that for every function f in $\#P^{BPP^{\oplus P}}$ a $\#P$ function h can be constructed such that the binary representation of f is a substring of the binary representation of h . Note that Theorem 2.6 additionally allows us to “isolate” the binary representation of f inside h , i.e. for each polynomial p we can construct h such that there are at least $p(|x|)$ 0's in the binary representation of $h(x)$ to the left and to the right of the binary representation of $f(x)$. A similar result was used in [TodWa 91] to show that every function in the counting version of PH is metric reducible to a $\#P$ function.

Theorem 2.6 For all functions f in $\#P^{\text{BPP}^{\oplus P}}$ there exists a polynomial q such that for every polynomial t there exists a function h in $\#P$ such that

$$\lfloor h(x)/2^{q(|x|)} \rfloor \equiv f(x) \pmod{2^{t(|x|)}}.$$

Proof of Theorem 2.6. Let f be in $\#P^{\text{BPP}^{\oplus P}}$. Since Theorem 2.3 relativizes, there exist a polynomial q and a function $g \in \#P^{\oplus P}$ such that

$$f(x) = \lfloor g(x)/2^{q(|x|)} \rfloor.$$

By Theorem 2.1, there exists a function $h \in \#P$ such that

$$h(x) \equiv g(x) \pmod{2^{q(|x|)+t(|x|)}},$$

and therefore

$$\lfloor h(x)/2^{q(|x|)} \rfloor \equiv f(x) \pmod{2^{t(|x|)}}.$$

□

Next we prove the main result of this section, namely that every $\#P^{\text{Mod}_k P}$ function can be isolated inside some $\#P$ function.

Theorem 2.7 Let k be prime. For all functions b in $\#P^{\text{Mod}_k P}$ and for every polynomial t there exist a polynomial q and a function h in $\#P$ such that

$$\lfloor h(x)/2^{q(|x|)} \rfloor \equiv b(x) \pmod{2^{t(|x|)}}.$$

Because the proof of Theorem 2.7 relativizes, we can state the following corollaries.

Corollary 2.8 For any k , $\text{Mod}_k P$ is low for MidBitP .

Proof of Corollary 2.8. By the representation theorem of Hertrampf [He 90], it follows that if $k = p^{e_1} q$ for a prime number p and $\text{gcd}(p, q) = 1$, then

$$\text{Mod}_k P \subseteq \text{Mod}_p P^{\text{Mod}_q P}.$$

The result follows iterating this argument for all the prime factors of k and using the relativized version of the previous theorem. □

The result stated in theorem 2.7 works also for every complexity class in ModPH , a generalization of the polynomial time hierarchy that includes also ModP classes. ModPH can be considered as the polynomial time analogue to the circuit class ACC .

Definition 2.9 ModPH is the smallest family of languages containing the class P and satisfying that for any class K in ModPH the classes NP^K , co-NP^K and $\text{Mod}_n P^K$ (for any positive integer n) also belong to ModPH .

Corollary 2.10 For all functions f in $\#P^{\text{ModPH}}$ and for every polynomial t there exist a polynomial q and a function h in $\#P$ such that

$$\lfloor h(x)/2^{q(|x|)} \rfloor \equiv f(x) \pmod{2^{t(|x|)}}.$$

Proof of Theorem 2.7. Let b be in $\#P^{\text{Mod}_k P}$. Since k is prime, $\text{Mod}_k P$ is closed under Turing reductions [BeGiHe 90]. Thus there exists a language L in $\text{Mod}_k P$ and a polynomial r such that

$$b(x) = \sum_{y \in \Sigma^{r(|x|)}} \chi_L(\langle x, y \rangle).$$

Let p be a polynomial such that $k^{p(n)} > 2^{r(n)+t(n)+2}$. Adapting results from Toda [Tod 89] and Beigel, Gill and Hertrampf [BeGiHe 90] we can assume that there is a function c in $\#P$ s.t.

$$c(\langle x, y \rangle) \equiv \chi_L(\langle x, y \rangle) \pmod{k^{p(|x|)}}.$$

Consider the function

$$f(x) := \sum_{y \in \Sigma^{r(|x|)}} c(\langle x, y \rangle).$$

Then

$$f(x) = a(x)k^{p(|x|)} + b(x)$$

where $b(x) \leq 2^{r(|x|)} < \frac{k^{p(|x|)}}{2^{t(|x|)+2}}$. The proof of Theorem 2.7 is now completed by the following lemma. \square

Lemma 2.11 If $f \in \#P$ is of the form $f(x) = a(x)k^{p(|x|)} + b(x)$, where

$$b(x) < \frac{k^{p(|x|)}}{2^{t(|x|)+2}},$$

then there exist a function h in $\#P$ and a polynomial q such that

$$h(x) = a'(x)2^{q(|x|)+t(|x|)} + b(x)2^{q(|x|)} + c(x),$$

where $c(x) < 2^{q(|x|)}$.

Proof of Lemma 2.11. Since f is in $\#P$ there exists a polynomial s such that $f(x) < 2^{s(|x|)}$ for all x . We first prove the following claim.

Claim. There exist a polynomial q and a function g in $\#P$ such that

$$g(x) = a(x)2^{q(|x|)} + b'(x) \text{ and } b'(x) < 2^{q(|x|)-t(|x|)-1}.$$

Proof of Claim. Define

$$g(x) = f(x) \left\lceil \frac{2^{q(|x|)}}{k^{p(|x|)}} \right\rceil.$$

Then it follows that

$$\begin{aligned} a(x)2^{q(|x|)} \leq g(x) &= (a(x)k^{p(|x|)} + b(x)) \left\lceil \frac{2^{q(|x|)}}{k^{p(|x|)}} \right\rceil \\ &< a(x)2^{q(|x|)} + b(x) \frac{2^{q(|x|)}}{k^{p(|x|)}} + a(x)k^{p(|x|)} + b(x). \\ &< a(x)2^{q(|x|)} + 2^{q(|x|)-t(|x|)-2} + a(x)k^{p(|x|)} + b(x). \\ &< a(x)2^{q(|x|)} + 2^{q(|x|)-t(|x|)-1}. \end{aligned}$$

The last inequality can be achieved by choosing $q > t + s + 2$. □

To complete the proof of Lemma 2.11 we define

$$h(x) = f(x)2^{q(|x|)} + g(x)i(|x|),$$

where

$$i(n) \equiv -k^{p(n)} \pmod{2^{t(n)}} \text{ and } i(n) < 2^{t(n)}.$$

Then it follows that

$$\begin{aligned} h(x) &= a(x)k^{p(|x|)}2^{q(|x|)} + b(x)2^{q(|x|)} + a(x)2^{q(|x|)}i(|x|) + b'(x)i(|x|) \\ &= 2^{q(|x|)}a(x)(k^{p(|x|)} + i(|x|)) + b(x)2^{q(|x|)} + b'(x)i(|x|), \end{aligned}$$

where

$$k^{p(n)} + i(n) \equiv 0 \pmod{2^{t(n)}}$$

and

$$b'(x)i(|x|) < 2^{q(|x|)-1}. \quad \square$$

From corollary 2.10 we know that ModPH is the largest class of languages known to be low for MidBitP. It is an open question whether the class PP is also low for MidBitP. A positive answer to this problem would imply that the polynomial time counting hierarchy, [Wa 86], collapses to the class MidBitP.

3 A New Upper Bound for ACC

The methods of the preceding section relativize. It is thus not surprising that there are analogous circuit results. In this section we prove them directly.

Yao [Yao 90] showed that ACC circuits can be simulated by probabilistic depth-2 circuits consisting of some symmetric gate over subexponentially many AND gates of polylogarithmic fan-in. Beigel and Tarui [BeTa 91] subsequently improved his construction and showed that depth-2 *deterministic* circuits of a symmetric gate over small AND's also can simulate ACC. Note that in both cases it was proved that *some* symmetric function over small AND's is sufficient. This leaves open the possibility that the symmetric function needed will be different for different input sizes. However our main result in this section is that there is *one particular* symmetric function which, together with AND gates of small fan-in, can capture all of ACC: namely, the symmetric function which outputs the middle bit of the sum of the inputs.

Definition 3.1 A *MidBit gate* over w inputs x_1, \dots, x_w is a gate which outputs the value of the $\lfloor \log(w)/2 \rfloor^{\text{th}}$ bit in the binary representation of the number $\sum_{i=1}^w x_i$. A *MidBit-of-AND circuit* of order r is a circuit consisting of a MidBit gate over AND-gates where each AND-gate has fan-in at most r . A family of functions $\{f_n\}$ is computable by a family of MidBit⁺ circuits if there is a polynomial p such that for each n there is a MidBit-of-AND circuit of order $p(\log(n))$ and size $2^{p(\log(n))}$ which computes $f_n(x_1, \dots, x_n)$.

A Mod_k gate over w inputs x_1, \dots, x_w is defined to output 1 if $\sum_{i=1}^w x_i \not\equiv 0 \pmod{k}$ and 0 otherwise. We similarly define Mod_k -of-AND circuits and families of Mod_k^+ circuits. Note that we will always speak of families of MidBit⁺ or Mod_k^+ circuits. Even when we refer to a MidBit⁺ or Mod_k^+ circuit individually, it should be understood that what is meant is a member of a particular family of such circuits.

The following theorems give the circuit analogue of the lowness result $\text{MidBitP}^{\text{Mod}_k\text{P}} = \text{MidBitP}$.

Theorem 3.2 Let k be prime and let $\{C_n\}$ be a family of circuits such that there exists a polynomial r where for each n , C_n consists of a MidBit gate over at most $2^{r(\log(n))}$ Mod_k -gates. Then $\{C_n\}$ is computable by a family of MidBit⁺-circuits.

Proof. Similar to the proof of Theorem 2.7. Let t be any polynomial. We have that C_n outputs 1 if and only if the $\lfloor \log(w)/2 \rfloor^{\text{th}}$ bit in the binary representation of $b(x)$ equals 1, where

$$b(x) = \sum_{i=1}^w \text{Mod}_k(x_{i_1}, \dots, x_{i_{s_i}}),$$

$s_i \leq n$ (i.e., the Mod gates can have any number of the n inputs) and $w \leq 2^{r(\log(n))}$. Using techniques of Toda [Tod 89] and Beigel, Gill and Hertrampf [BeGiHe 90], there is a polynomial Q_d of degree d which has the property that if $X \not\equiv 0 \pmod{k}$ then $Q_d(X) \equiv 1 \pmod{k^d}$, and if $X \equiv 0 \pmod{k}$ then $Q_d(X) \equiv 0 \pmod{k^d}$. Thus

$$b(x) = \sum_{i=1}^w [Q_d(\sum_{l=1}^{s_i} x_{j_l}) \pmod{k^d}].$$

Suppose we choose $d = p(\log(n))$ where p is a polynomial such that $k^p > \max \{2^{r+t+2}, 2^{t+2}\}$. (To simplify notation, in the remainder of the proof, where it is not confusing we use p, q, r and t for $p(\log(n)), q(\log(n)), r(\log(n))$ and $t(\log(n))$, respectively.) Then $b(x) \leq 2^r < k^p$. Now the outer sum in the equation above for b is less than k^p , so the "mod" can be moved outside:

$$b(x) = \left[\sum_{i=1}^w Q_p \left(\sum_{l=1}^{s_i} x_{j_l} \right) \right] \pmod{k^p}.$$

We write

$$f(x) = \sum_{i=1}^w Q_p \left(\sum_{l=1}^{s_i} x_{j_l} \right).$$

Then

$$f(x) = ak^p + b.$$

Note that f is a polynomial in the variables x_1, \dots, x_n of degree $p(\log(n))$. If we multiply f by $\lceil 2^q/k^p \rceil$ we still have such a polynomial. Following the proof of Lemma 2.11, we find that $\lceil 2^q/k^p \rceil f(x) = a2^q + b'$ where $b' < 2^{q-t-1}$. Setting $i = -k^p \pmod{2^t}$ as in the proof of Lemma 2.11,

$$2^q f(x) + i \lceil 2^q/k^p \rceil f(x) = 2^q b + ib' \pmod{2^{q+t}} \quad (*).$$

Now if we reduce the left hand side of the above equation mod 2^{q+t} , we obtain a polynomial of degree $p(\log(n))$ with all positive coefficients (none larger than 2^{q+t-1}). Replace any term with coefficient > 1 by a sum of identical terms with unit coefficients, and substitute multiplication with logical AND. The result is a sum σ of $\leq 2^{\text{polylog}}$ AND gates each of polylog fan-in. Reducing the right hand side of eq. (*), we obtain $2^q(b \bmod t) + ib'$, where $ib' \leq 2^{q-1}$. Thus the output bit of C_n is the bit in position $q + \lfloor \log(w)/2 \rfloor$ in the binary expansion of σ . We can add constant inputs or multiply the sum of AND's so that this is precisely the middle bit. \square

It is not hard to see that if the inputs to the MOD gates in the previous theorem are AND gates of polylog fan-in, that the resulting depth-3 circuits can still be simulated by MidBit⁺ circuits. The reason is that we are composing the polylog degree polynomial Q_p with another polylog degree polynomial.

Corollary 3.3 *Let k be prime and let $\{C_n\}$ be a family of depth-3 circuits such that there exists a polynomial s where for each n , C_n consists of a MidBit gate over at most $2^{s(\log(n))}$ Mod _{k} ⁺ circuits. Then $\{C_n\}$ is computable by a family of MidBit⁺-circuits.*

We now turn our attention to MidBit gates at the root and pure ACC subcircuits [Yao 90] (families of constant-depth polynomial size circuits which consist only of Mod _{m} gates for some natural number m).

Theorem 3.4 *Let $\{C_n\}$ be a family of depth- d circuits consisting of a MidBit gate at the root and Mod_m gates at remaining levels. Then $\{C_n\}$ is computable by a family of MidBit^+ -circuits.*

Proof. Beigel and Tarui [BeTa 91] have shown that a Mod_m gate can be simulated by a “stratified” circuit of $\text{Mod}_{k_1}, \text{Mod}_{k_2}, \dots, \text{Mod}_{k_l}$ gates where k_1, k_2, \dots, k_l are the prime divisors of m , on levels $1, 2, \dots, l$, respectively, and polylog fan-in AND gates on the lowest level. Using the previous theorem and an inductive argument as in the proof of Theorem 6 in [BeTa 91], each layer of Mod_{k_i} gates can be “absorbed” in the MidBit gate, and the resulting polylog AND gates “pushed” down to the leaves. The resulting circuit is a MidBit^+ circuit. \square

The following main theorem uses a combination of the above results, techniques of Valiant and Vazirani [ValVaz 86], Toda [Tod 89], Allender [Al 89], and Allender and Hertrampf [AlHe 90], and the technique by which we showed that BPP is low for MidBitP. It says that circuits consisting of a MidBit gate over ACC subcircuits can be simulated by MidBit^+ circuits. The proof is similiar to those given in Corollaries 8 and 9 of [BeTa 91].

Theorem 3.5 *Let $\{C_n\}$ be a family of depth- d circuits of size $2^{\text{poly}(\log(n))}$ consisting of a MidBit gate at the root and Mod_m , AND, and OR gates at remaining levels. Then $\{C_n\}$ is computable by a family of MidBit^+ -circuits.*

Proof. Let $C_n = 1$ iff the $\lfloor \log(s)/2 \rfloor^{\text{th}}$ bit of S is 1, where $S = \sum_{i=1}^s c_i$, with each subcircuit c_i consisting of AND, OR, and Mod_m gates, and without loss of generality, $s = 2^{q(\log(n))}$ where q is a polynomial. The AND and OR gates in each c_i can be replaced by probabilistic Mod_m^+ circuits with polylogarithmically many random bits, using the techniques of [ValVaz 86], [Al 89], and [AlHe 90]. By pushing the AND-gates to the leaves, as in the preceding theorem, c_i can be simulated by a probabilistic circuit c'_i comprised of Mod_m gates and AND gates of polylog fan-in at the lowest level, so that $\Pr(c'_i \neq c_i) \leq 2^{-q(\log(n)) - 2}$. It is possible to simulate c_i with such a c'_i using $t(\log(n))$ bits where t is a polynomial such that $t > q + 2$. Let c'_i denote the sum of c'_i over all possible settings of the random bits of c'_i , and let $S' := \sum_{i=1}^s (c'_i + 2^{t(\log(n)) - q(\log(n)) - 2})$. One can show that $S' = 2^{t(\log(n))} S + r$ where $r < 2^{t(\log(n))}$. The output of the desired MidBit^+ circuit is the bit in position $\lfloor \log(s)/2 \rfloor + t(\log(n))$ of S' . \square

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